

Load PACS DPU OBS in instrument RESCUE mode  
File: H\_FCP\_OBS\_4112.xls  
Author: Liviu Stefanov



## Procedure Summary

### Objectives

This Herschel OBSM nominal procedure is used to execute the PACS DPU OBS full image upload in instrument RESCUE mode (OBS upload from Boot SW). It is called by the FOP PACS procedures H\_FCP\_PAC\_NRDm and H\_FCP\_PAC\_RRDm.

The OBS image is loaded into the PACS DPU DRAM memory. Note that memory dump and check commands cannot be executed by BSW, therefore image verification after load can only be done after DM to PM-Low copy.

The copying of the OBS image from DM to PM-Low and OBS restart is executed in the calling procedure H\_FCP\_PAC\_NRDm or H\_FCP\_PAC\_RRDm. The checksums for PM-low and updated OBS version numbers are also verified in the calling procedure.

This procedure assumes that the memory load command stack has already been generated using the OBSM system and is ready for loading on the Manual Stack. The command stack generation activity is not covered by this procedure.

Note: The PACS DPU OBS upload in instrument INIT mode can be conducted via procedure H\_FCP\_OBS\_4110.

### Summary of Constraints

CDMU in Operational Mode  
- PACS in RESCUE mode (DPU BSW running)

No memory load command shall patch across a DM page boundary.

Memory areas are Loaded through TC(6,2); this TC will be delayed when there is an ongoing:

- TC(6,2) Load Memory Using Absolute Addresses
- TC(6,5) Dump Memory Using Absolute Addresses
- TC(6,9) Check Memory Using Absolute Addresses
- TC(8,4,1,1) Copy Memory

### Spacecraft Configuration

#### Start of Procedure

CDMU in Operational Mode  
- PACS in RESCUE mode (DPU BSW running)

#### End of Procedure

Same as start except:  
- New PACS DPU OBS image loaded in DPU DRAM memory

### Reference File(s)

#### Input Command Sequences

#### Output Command Sequences

OFCP4112

### Referenced Displays

Status : Version 3 - Unchanged  
Last Checkin: 19/07/09

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ANDs      GRDs      SLDs

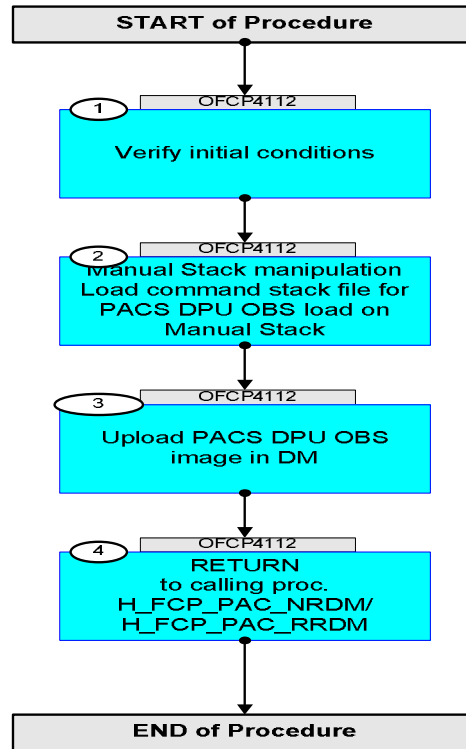
**Configuration Control Information**

DATE	FOP ISSUE	VERSION	MODIFICATION DESCRIPTION	AUTHOR	SPR REF
30/01/08	1	1	Created	Istefanov-hp	
17/06/09		2	1. step 2 and sub-steps updated to for PACS DPU OBS v.9.03 2. step 3 updated: replaced TM(1,1, and TM(1,7) by TM(5,1), in line with PACS's comment 3. added current step 4 to include return to calling procedure and instruct for OBS image dump from PM-Low	Istefanov-hp	
19/07/09	2.5	3	1. step 2.2 updated to indicate the latest PACS DPU OBS version 2. step 4 updated to include start addresses and lengths for PACS OBS v.9.04 image dump from PM-Low	Istefanov-hp	

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## Procedure Flowchart Overview



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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch	AIT Comment
<b>Beginning of Procedure</b>					
OFCP4112		TC Seq. Name :OFCP4112 ( ) Load PACS DPU OBS from BSW and check image  TimeTag Type: B Sub Schedule ID:  <input type="checkbox"/>			
1		Verify initial conditions		Next Step: 2	
		Check PACS instrument in <b>RESCUE mode</b> (DPU BSW running)			
		Instrument SOE to confirm PACS instrument mode			
		<b>Note:</b> Initial conditions are verified in calling procedure H_FCP_PAC_NRDM or H_FCP_PAC_RRDM.			
2		Manual Stack manipulation Load command stack file for PACS DPU OBS load on Manual Stack		Next Step: 3	
2.1		Load memory load command stack			
		<b>NOTE:</b> The current procedure assumes that the memory load is performed using commands with immediate execution.			
		Select the File -> <b>LoadStack</b> option from the main menu of the Manual Stack window			
2.1.1		IF PACS Nominal			
		Select file  <b>PADPRMDA_PI_XXXXYYY_N_NoModel_NoModel_YYYY_DDDThhmmss.machine</b>  from directory  <a href="#">/home/hmcsops/HPMCS/SESSION/current/data/CMD/STACKS/OBSM/PADPRMDA</a>  as indicated by the OBSM engineer			
		<b>IMPORTANT:</b>  <b>XXXXYYY</b> = Image ID(X) and Version(Y) - depend on image used for stack generation  <b>YYYY_DDD hhmmss</b> - depend on stack generation time  <b>machine</b> - depends on the name of the machine used for stack generation			

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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch	AIT Comment
		File name <b>example</b>  PADPRMDA_PI_0002001_N_NoModel_NoModel_2007_254T123300.sun043			
2.1.2		ELSE PACS Redundant			
		Select file  <b>PADPRMDR_PI_XXXXYYY_N_NoModel_NoModel_YYYY_DDDThhmmss.machine</b>  from directory  <a href="#">/home/hmcsops/HPMCS/SESSION/current/data/CMD/STACKS/OBSM/PADPRMDR</a>  as indicated by the OBSM engineer			
		IMPORTANT:  <b>XXXXYYY</b> = Image ID(X) and Version(Y) - depend on image used for stack generation  <b>YYYY_DDD hhmmss</b> - depend on stack generation time  <b>machine</b> - depends on the name of the machine used for stack generation			
		File name <b>example</b>  PADPRMDR_PI_0002001_N_NoModel_NoModel_2007_254T123300.sun043			
2.2		Check memory load command stack loaded			
		For PACS DPU OBSW <b>v.9.04</b> :  The start address of the PADPRMDA memory image used for memory load command stack generation is <b>00.4000 hex</b> , and the last address in the image is <b>01.7FFF hex</b> .  <b>NO offset</b> has to be applied to the memory image for OBS upload in DM.  Consequently, the first address to be loaded is <b>00.4000 hex</b> , and the last address is <b>01.7FFF hex</b> :  <b>Start Address = 00.4000 hex</b> <b>End Address = 01.7FFF hex</b> <b>Length = 14000 hex</b>			
		IMPORTANT: <b># of TCs, Address and Length</b> values in the following sub-steps are <b>applicable to PACS DPU OBS v.9.04</b>			
2.2.1		Check number of memory load commands in the stack			
		Check that loaded stack contains <b>1440 TCs XC001998</b>			

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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch	AIT Comment															
2.2.2		Check Memory ID																		
		Display the Manual Stack in 'Full mode' and check that the <b>Memory ID</b> parameter in the XC001998 commands is set to <b>11 hex</b> :  <b>Memory ID = 11 hex</b>  <b>Note:</b> The Memory ID of the target memory device is stored in the MSB of the 16-bit long Mem ID TC parameter. The LSB of the same parameter carries the most significant 8 bits of the Start Address.																		
		Execute Telecommand <div style="text-align: right; margin-left: 20px;"><b>PACS Memory Load</b></div> Command Parameter(s) : <table style="margin-left: 20px; border: none;"> <tr><td style="padding-right: 20px;">Memory ID</td><td style="padding-right: 20px;">XH000998</td><td style="padding-right: 20px;">11xx hex</td></tr> <tr><td>Start Address</td><td>XH001998</td><td>&lt;hex&gt; (Def)</td></tr> <tr><td>Length of Block</td><td>XH003998</td><td>&lt;dec&gt; (Def)</td></tr> <tr><td>Var length octet string</td><td>XH004998</td><td>&lt;hex&gt; (Def)</td></tr> <tr><td>Checksum</td><td>XH005998</td><td>&lt;hex&gt; (Def)</td></tr> </table> TC Control Flags : <div style="margin-left: 20px;">GBM IL DSE --Y -- ---</div> Subsch. ID : 30 Det. descr. : Load PACS Memory Using Absolute Addresses  This Telecommand will not be included in the export	Memory ID	XH000998	11xx hex	Start Address	XH001998	<hex> (Def)	Length of Block	XH003998	<dec> (Def)	Var length octet string	XH004998	<hex> (Def)	Checksum	XH005998	<hex> (Def)	XC001998	TC	
Memory ID	XH000998	11xx hex																		
Start Address	XH001998	<hex> (Def)																		
Length of Block	XH003998	<dec> (Def)																		
Var length octet string	XH004998	<hex> (Def)																		
Checksum	XH005998	<hex> (Def)																		
2.2.3		Check start address and length of first command in the stack																		
		With the Manual Stack in 'Full mode', check the <b>Start Address</b> and <b>Length</b> in the <b>first</b> XC001998 command:  <b>Start Address = 00.4000 hex</b> <b>Length = 57 dec</b>  <b>Note:</b> The Memory ID of the target memory device is stored in the MSB of the 16-bit long Mem ID TC parameter. The LSB of the same parameter carries the most significant 8 bits of the Start Address.																		
		Execute Telecommand <div style="text-align: right; margin-left: 20px;"><b>PACS Memory Load</b></div> Command Parameter(s) : <table style="margin-left: 20px; border: none;"> <tr><td style="padding-right: 20px;">Memory ID</td><td style="padding-right: 20px;">XH000998</td><td style="padding-right: 20px;">1100 &lt;hex&gt;</td></tr> <tr><td>Start Address</td><td>XH001998</td><td>4000 &lt;hex&gt;</td></tr> <tr><td>Length of Block</td><td>XH003998</td><td>57 &lt;dec&gt;</td></tr> <tr><td>Var length octet string</td><td>XH004998</td><td>patch data</td></tr> <tr><td>Checksum</td><td>XH005998</td><td>calculated by OBSM</td></tr> </table> TC Control Flags : <div style="margin-left: 20px;">GBM IL DSE --Y -- ---</div> Subsch. ID : 30 Det. descr. : Load PACS Memory Using Absolute Addresses  This Telecommand will not be included in the export	Memory ID	XH000998	1100 <hex>	Start Address	XH001998	4000 <hex>	Length of Block	XH003998	57 <dec>	Var length octet string	XH004998	patch data	Checksum	XH005998	calculated by OBSM	XC001998	TC	
Memory ID	XH000998	1100 <hex>																		
Start Address	XH001998	4000 <hex>																		
Length of Block	XH003998	57 <dec>																		
Var length octet string	XH004998	patch data																		
Checksum	XH005998	calculated by OBSM																		

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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch	AIT Comment																	
2.2.4		Check start address and length of last command in the stack																				
		With the Manual Stack in 'Full mode', check the <b>Start Address</b> and <b>Length</b> in the last XC001998 command:  <b>Start Address = 1.7FC9 hex</b> <b>Length = 55 dec</b>  <b>Note:</b> The Memory ID of the target memory device is stored in the MSB of the 16-bit long Mem ID TC parameter. The LSB of the same parameter carries the most significant 8 bits of the Start Address.																				
		Execute Telecommand  <div style="text-align: center;"><b>PACS Memory Load</b></div> <div style="text-align: center;"><b>XC001998</b></div> <i>Command Parameter(s) :</i> <table style="width: 100%; border: none;"> <tr> <td style="padding-left: 40px;">Memory ID</td> <td style="padding-left: 20px;">XH000998</td> <td style="padding-left: 20px;">1101 &lt;hex&gt;</td> </tr> <tr> <td style="padding-left: 40px;">Start Address</td> <td style="padding-left: 20px;">XH001998</td> <td style="padding-left: 20px;">7FC9 &lt;hex&gt;</td> </tr> <tr> <td style="padding-left: 40px;">Length of Block</td> <td style="padding-left: 20px;">XH003998</td> <td style="padding-left: 20px;">55 &lt;dec&gt;</td> </tr> <tr> <td style="padding-left: 40px;">Var length octet string</td> <td style="padding-left: 20px;">XH004998</td> <td style="padding-left: 20px;">patch data</td> </tr> <tr> <td style="padding-left: 40px;">Checksum</td> <td style="padding-left: 20px;">XH005998</td> <td style="padding-left: 20px;">calculated by OBSM</td> </tr> </table> <i>TC Control Flags :</i> <table style="width: 100%; border: none;"> <tr> <td style="padding-left: 40px;">GBM IL DSE</td> <td style="padding-left: 20px;">--Y -- ---</td> </tr> </table> <i>Subsch. ID : 30</i> <i>Det. descr. : Load PACS Memory Using Absolute Addresses</i>  This Telecommand will not be included in the export	Memory ID	XH000998	1101 <hex>	Start Address	XH001998	7FC9 <hex>	Length of Block	XH003998	55 <dec>	Var length octet string	XH004998	patch data	Checksum	XH005998	calculated by OBSM	GBM IL DSE	--Y -- ---	XC001998	TC	
Memory ID	XH000998	1101 <hex>																				
Start Address	XH001998	7FC9 <hex>																				
Length of Block	XH003998	55 <dec>																				
Var length octet string	XH004998	patch data																				
Checksum	XH005998	calculated by OBSM																				
GBM IL DSE	--Y -- ---																					
2.2.5		Check DM page boundaries not violated by the memory load commands																				
		<b>IMPORTANT:</b> Check that the OBSM generated memory load commands respect the <b>DM page boundaries</b> .  A <b>DM page</b> is 1024 words (400 hex words) large, where a <b>DM word</b> is 32-bit long.  The <b>DM starts</b> at address 00.0000 hex.																				
3		Upload PACS DPU OBS image in DM		Next Step: 4																		
		<b>Uplink</b> the XC001998 memory load commands with <b>ARM-GO</b>																				
		For each TC XC001998 successfully executed on-board, a <b>TM(5,1)</b> packet from PACS shall be received on ground.																				
4		RETURN to calling proc. H_FCP_PAC_NRDH/H_FCP_PAC_RRDM		Next Step: END																		

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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch	AIT Comment
		<b>IMPORTANT:</b> After OBS image copy from DM to PM-Low executed in calling procedure H_FCP_PAC_NRD/ H_FCP_PAC_RRDM, dump the PACS DPU <b>new OBS image</b> from <b>PM-Low</b> using FOP procedure <b>H_FCP_OBS_4143</b> .			
		<b>Note:</b> For PACS DPU OBS v.9.04, the memory area to be dumped in <b>H_FCP_OBS_4143</b> is:  <b>Start Address</b> = 00.0000 hex <b>End Address</b> = 01.0CB4 hex <b>Length</b> = 10CB5 hex			
		<b>Note:</b> For PACS DPU OBS v.9.04, the 2 TCs <b>PC028380</b> used in <b>H_FCP_OBS_4143</b> to dump from <b>PM-Low</b> will have the following parameters:  <b>First TC PC028380:</b> <b>Start Address</b> = 03.0000 hex <b>Length</b> = FFFF hex  <b>Second TC PC028380:</b> <b>Start Address</b> = 03.FFFF hex <b>Length</b> = CB6 hex			
End of Sequence					
<b>End of Procedure</b>					