

Read TTR CROME register
File: H_FCP_DHS_3010.xls
Author: S. Manganelli



Procedure Summary

Objectives

The ASW function "TTR Management" handles the CDMU RM and CROME commanding.

This procedure describes the steps needed to read a selected CROME register.

Summary of Constraints

PROCEDURE TO BE USED ONLY UNDER INDUSTRY APPROVAL
For normal register dumps use H_FCP_DHS_1031 instead

Default status of the function: "Started".

When the function is stopped, it does not accept any other telecommands than the:

- Start Function TC(8,1,109);
- Report Function Status TC(8,5,109).

Thus, if the function is stopped this procedure cannot be executed.

Spacecraft Configuration

Start of Procedure

- CDMU in default configuration, that is:
- PM A or B ON (nominally A)
 - TM Encoder/OBT A or B active (nominally A)
 - RM A and B enabled
 - MM A and B ON

End of Procedure

- CDMU in default configuration, that is:
- PM A or B ON (nominally A)
 - TM Encoder/OBT A or B active (nominally A)
 - RM A and B enabled
 - MM A and B ON

Reference File(s)

Input Command Sequences

Output Command Sequences

Referenced Displays

ANDs GRDs SLDs
ZAZAI999

Configuration Control Information

Read TTR CROME register
 File: H_FCP_DHS_3010.xls
 Author: S. Manganelli

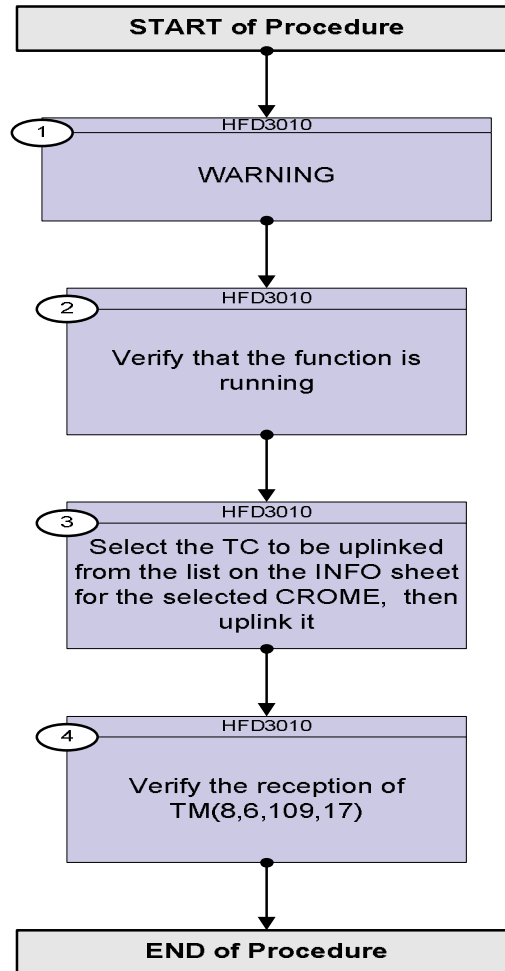


DATE	FOP ISSUE	VERSION	MODIFICATION DESCRIPTION	AUTHOR	SPR REF
09/01/08		1	Created	cmevi-hp	
09/01/08		2	Formal parameters introduced.	cmevi-hp	
16/01/08	1	3	Batch update of TC flags	S. Manganelli	
01/12/08		4	Procedure updated according to latest version received from industry on 24/10/2008	cmevi-hp	
20/01/09	2	5	Added WARNING following TAS comments	S. Manganelli	
22/03/09	2.2	6	Modified as per TAS-I inputs 3 march 09	S. Manganelli	

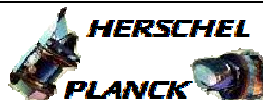
Read TTR CROME register
File: H_FCP_DHS_3010.xls
Author: S. Manganelli



Procedure Flowchart Overview



Read TTR CROME register
 File: H_FCP_DHS_3010.xls
 Author: S. Manganelli



Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
Beginning of Procedure				
<p><i>TC Seq. Name :HFD3010 (Read TTR CROME Reg)</i></p> <p><i>TimeTag Type: N</i> <i>Sub Schedule ID:</i></p> <p style="text-align: center;">□</p>				
1		WARNING		Next Step: 2
		Only the register listed in the info 1 and info 2 sheets can be read from the related CROME.		
2		Verify that the function is running		Next Step: 3
		Verify Telemetry TtrSts DEL17170	= Running	AND=ZAZAI999
3		Select the TC to be uplinked from the list on the INFO sheet for the selected CROME, then uplink it		Next Step: 4
4		Verify the reception of TM(8,6,109,17)		Next Step: END
		Verify Packet Reception TM 8-6-109-17 TTR Management - Crome Register Report <i>Packet Details:</i> <div style="text-align: right; margin-right: 20px;"> APID: 16 Type: 8 Subtype: 6 PI1: 27921 PI2: 0 </div>	CromeRegRpt	
		Verify Packet Telemetry (Pkt = CromeRegRpt) Function_ID DE008170	= TTR_Manag	(None)
		Verify Packet Telemetry (Pkt = CromeRegRpt) TtrManRptActId DE368170	= CromeRegRpt	(None)
		Verify Packet Telemetry (Pkt = CromeRegRpt) SID DE010170	= 0 <dec>	(None)
		Verify Packet Telemetry (Pkt = CromeRegRpt) CromeId DE285170		(None)
		Verify Packet Telemetry (Pkt = CromeRegRpt) CromeAddr DE329170		(None)
		Verify Packet Telemetry (Pkt = CromeRegRpt) CromeData DE367170		(None)
End of Procedure				

Read TTR CROME register
 File: H_FCP_DHS_3010.xls
 Author: S. Manganelli



CROME A registers

DCW00159	CRMA_CAR_PwrOnResetReg	CROME A: Read CAR Power On Reset Register
DCW01159	CRMA_PIMwriteDisStsReg	CROME A: Read PIM Write Disable Status Register
DCW02159	CRMA_PIM_ScrubCfgReg	CROME A: Read PIM Scrubber Configuration Register
DCW03159	CRMA_PIM_ScrubStartAddr	CROME A: Read PIM Scrubber Start Address
DCW04159	CRMA_PIM_ScrubEndAddr	CROME A: Read PIM Scrubber End Address
DCW05159	CRMA_PIM_StatusReg	CROME A: Read PIM Status Register
DCW06159	CRMA_PDECcmdLnkCtrlWrd	CROME A: Read PDEC Command Link Control Word
DCW07159	CRMA_PDECmapIF1StsRegPMA	CROME A: Read PDEC MAP Interface 1 Status Reg (MAP I/F to PMA)
DCW08159	CRMA_PDEC_MapIF5StsReg	CROME A: Read PDEC MAP Interface 5 Status Register
DCW09159	CRMA_PDEC_Monitor	CROME A: Read PDEC Monitor
DCW0A159	CRMA_PDEC_FAR_Copy	CROME A: Read PDEC Frame Analysis Report (Copy)
DCW0B159	CRMA_TME_InCfgRegA-VC2	CROME A: Read TME VC Input Configuration Registers A (i.e. VC 2)
DCW0C159	CRMA_TME_InCfgRegB-VC3	CROME A: Read TME VC Input Configuration Registers B (i.e. VC 3)
DCW0D159	CRMA_TME_InCfgRegC-VC0	CROME A: Read TME VC Input Configuration Registers C (i.e. VC 0)
DCW0E159	CRMA_TME_InCfgRegD-VC1	CROME A: Read TME VC Input Configuration Registers D (i.e. VC 1)
DCW0F159	CRMA_TME_InCfgRegE-VC4	CROME A: Read TME VC Input Configuration Registers E (i.e. VC 4)
DCW0G159	CRMA_TME_InCfgRegF-VC5	CROME A: Read TME VC Input Configuration Registers F (i.e. VC 5)
DCW0H159	CRMA_TME_InCfgRegG-VC6	CROME A: Read TME VC Input Configuration Registers G (i.e. VC 6)
DCW0J159	CRMA_TME_InCfgRegH-VC7	CROME A: Read TME VC Input Configuration Registers H (i.e. VC 7)
DCW0K159	CRMA_TME_TM_EncCfgReg0	CROME A: Read TME TM Encoding Configuration Register 0
DCW0L159	CRMA_TME_TM_EncCfgReg1	CROME A: Read TME TM Encoding Configuration Register 1
DCW0M159	CRMA_TME_TFMsubCdivReg	CROME A: Read TME TFM SubC Divisor Register
DCW0N159	CRMA_TME_TFMbitRteDivReg	CROME A: Read TME TFM Bit Rate Divisor Register
DCW0P159	CRMA_CSEL_StatusReg	CROME A: Read CSEL Status Register
DCW0R159	CRMA_CPDM_StsReportReg	CROME A: Read CPDM Status Report Register
DCW0S159	CRMA_CPDM_StatusReg	CROME A: Read CPDM Status Register
DCW0T159	CRMA_RMH_StatusReg	CROME A: Read RMH Status Register
DCW0U159	CRMA_RMH_AlarmStsReg	CROME A: Read RMH Alarm Status Register
DCW0V159	CRMA_RMH_MonitorStsReg	CROME A: Read RMH Monitor Status Register
DCW0W159	CRMA_RMH_FiltAlrmStsReg	CROME A: Read RMH Filtered Alarm Status Register
DCW0X159	CRMA_RMH_LOGPointerReg	CROME A: Read RMH LOG Pointer Register
DCW0Y159	CRMA_RMH_AT_pointerReg	CROME A: Read RMH AT Pointer Register
DCW0Z159	CRMA_RMH_PAPpointerReg	CROME A: Read RMH PAP Pointer Register
DCW10159	CRMA_RMH_PKTpointerReg	CROME A: Read RMH PKT Pointer Register
DCW11159	CRMA_RMH_PLTpointerReg	CROME A: Read RMH PLT Pointer Register
DCW12159	CRMA_RMH_ToggldlayReg0	CROME A: Read RMH Toggle Delay Register 0
DCW13159	CRMA_RMH_ToggldlayReg1	CROME A: Read RMH Toggle Delay Register 1
DCW14159	CRMA_RMH_ActivDelayReg	CROME A: Read RMH Activation Delay Register
DCW15159	CRMA_RMH_RetryDelayReg	CROME A: Read RMH Retry Delay Register
DCW16159	CRMA_RMH_TogDlaySelReg	CROME A: Read RMH Toggle Delay Select Register
DCW17159	CRMA_RMH_AlmPlrityReg	CROME A: Read RMH Alarm Polarity Register
DCW18159	CRMA_RMH_TemporModeReg	CROME A: Read RMH Temporisation Mode Register
DCW19159	CRMA_RMH_VoteEnableReg	CROME A: Read RMH Vote Enable Register
DCW1A159	CRMA_RMH_AlarmEnSetReg	CROME A: Read RMH Alarm Enable Set Register
DCW1B159	CRMA_RMH_TemporDlayReg0	CROME A: Read RMH Temporisation Delay Register 0
DCW1C159	CRMA_RMH_TemporDlayReg1	CROME A: Read RMH Temporisation Delay Register 1
DCW1D159	CRMA_RMH_TemporDlayReg2	CROME A: Read RMH Temporisation Delay Register 2
DCW1E159	CRMA_RMH_TemporDlayReg3	CROME A: Read RMH Temporisation Delay Register 3
DCW1F159	CRMA_RMH_TemporDlayReg4	CROME A: Read RMH Temporisation Delay Register 4
DCW1G159	CRMA_RMH_TemporDlayReg5	CROME A: Read RMH Temporisation Delay Register 5
DCW1H159	CRMA_RMH_TemporDlayReg6	CROME A: Read RMH Temporisation Delay Register 6
DCW1J159	CRMA_RMH_TemporDlayReg7	CROME A: Read RMH Temporisation Delay Register 7
DCW1K159	CRMA_RMH_TemporDlayReg8	CROME A: Read RMH Temporisation Delay Register 8
DCW1L159	CRMA_RMH_AttmpCntReg0	CROME A: Read RMH Attempt Count Register 0
DCW1M159	CRMA_RMH_AttmpCntReg1	CROME A: Read RMH Attempt Count Register 1
DCW1N159	CRMA_RMH_AttmpCntReg2	CROME A: Read RMH Attempt Count Register 2
DCW1P159	CRMA_RMH_AttmpCntReg3	CROME A: Read RMH Attempt Count Register 3
DCW1R159	CRMA_RMH_AttmpCntReg4	CROME A: Read RMH Attempt Count Register 4
DCW1S159	CRMA_RMH_AttmpCntReg5	CROME A: Read RMH Attempt Count Register 5
DCW1T159	CRMA_RMH_AttmpCntReg6	CROME A: Read RMH Attempt Count Register 6
DCW1U159	CRMA_PIO_IO_StatusReg	CROME A: Read PIO IO Status Register
DCW1V159	CRMA_SPW_LinkStsReg	CROME A: Read SPW Link Status Register

Status : Version 6 - Unchanged

Last Checkin: 22/03/09

Read TTR CROME register
 File: H_FCP_DHS_3010.xls
 Author: S. Manganelli



CROME A registers

DCW1V159	CRMA_SPW_LinkStatusReg	CROME A: Read SPW Link Status Register
DCW1W159	CRMA_OBT_Control_Req	CROME A: Read OBТ Control Register
DCZ13159	ReadCrome_A_AENCLR	Read CROME A RMH_AENCLR Register [TC(8,4,109,17)]
DCZ14159	ReadCrome_A_AENSET	Read CROME A RMH_AENSET Register [TC(8,4,109,17)]

Read TTR CROME register
 File: H_FCP_DHS_3010.xls
 Author: S. Manganelli



CROME B registers

DCW1Y159	CRMB_CAR_PwrOnResetReg	CROME B: Read CAR Power On Reset Register
DCW1Z159	CRMB_PIMwriteDisStsReg	CROME B: Read PIM Write Disable Status Register
DCW20159	CRMB_PIM_ScrubCfgReg	CROME B: Read PIM Scrubber Configuration Register
DCW21159	CRMB_PIM_ScrubStartAdd	CROME B: Read PIM Scrubber Start Address
DCW22159	CRMB_PIM_ScrubEndAddr	CROME B: Read PIM Scrubber End Address
DCW23159	CRMB_PIM_StatusReg	CROME B: Read PIM Status Register
DCW24159	CRMB_PDECcmdLnkCtrlWrd	CROME B: Read PDEC Command Link Control Word.
DCW25159	CRMB_PDECmapIF1StsRegPM	CROME B: Read PDEC MAP Interface 1 Status Reg (MAP I/F to
DCW26159	CRMB_PDEC_MapIF5StsReg	CROME B: Read PDEC MAP Interface 5 Status Register
DCW27159	CRMB_PDEC_Monitor	CROME B: Read PDEC Monitor
DCW28159	CRMB_PDEC_FAR_Copy	CROME B: Read PDEC Frame Analysis Report (Copy)
DCW29159	CRMB_TME_InCfgRegA-VC2	CROME B: Read TME VC Input Configuration Registers A (i.e. VC
DCW2A159	CRMB_TME_InCfgRegB-VC3	CROME B: Read TME VC Input Configuration Registers B (i.e. VC
DCW2B159	CRMB_TME_InCfgRegC-VC0	CROME B: Read TME VC Input Configuration Registers C (i.e. VC
DCW2C159	CRMB_TME_InCfgRegD-VC1	CROME B: Read TME VC Input Configuration Registers D (i.e. VC
DCW2D159	CRMB_TME_InCfgRegE-VC4	CROME B: Read TME VC Input Configuration Registers E (i.e. VC
DCW2E159	CRMB_TME_InCfgRegF-VC5	CROME B: Read TME VC Input Configuration Registers F (i.e. VC
DCW2F159	CRMB_TME_InCfgRegG-VC6	CROME B: Read TME VC Input Configuration Registers G (i.e. VC
DCW2G159	CRMB_TME_InCfgRegH-VC7	CROME B: Read TME VC Input Configuration Registers H (i.e. VC
DCW2H159	CRMB_TME_TM_EncCfgReg0	CROME B: Read TME TM Encoding Configuration Register 0
DCW2J159	CRMB_TME_TM_EncCfgReg1	CROME B: Read TME TM Encoding Configuration Register 1
DCW2K159	CRMB_TME_TFMsubCdivReg	CROME B: Read TME TFM SubC Divisor Register
DCW2L159	CRMB_TME_TFMbitRteDivReg	CROME B: Read TME TFM Bit Rate Divisor Register
DCW2M159	CRMB_CSEL_StatusReg	CROME B: Read CSEL Status Register
DCW2N159	CRMB_CPDM_StsReportReg	CROME B: Read CPDM Status Report Register
DCW2P159	CRMB_CPDM_StatusReg	CROME B: Read CPDM Status Register
DCW2R159	CRMB_RMH_StatusReg	CROME B: Read RMH Status Register
DCW2S159	CRMB_RMH_AlarmStsReg	CROME B: Read RMH Alarm Status Register
DCW2T159	CRMB_RMH_MonitorStsReg	CROME B: Read RMH Monitor Status Register
DCW2U159	CRMB_RMH_FiltAlrmStsReg	CROME B: Read RMH Filtered Alarm Status Register
DCW2V159	CRMB_RMH_LOGPointerReg	CROME B: Read RMH LOG Pointer Register
DCW2W15	CRMB_RMH_AT_pointerReg	CROME B: Read RMH AT Pointer Register
DCW2X159	CRMB_RMH_PAPpointerReg	CROME B: Read RMH PAP Pointer Register
DCW2Y159	CRMB_RMH_PKTpointerReg	CROME B: Read RMH PKT Pointer Register
DCW2Z159	CRMB_RMH_PLTpointerReg	CROME B: Read RMH PLT Pointer Register
DCW30159	CRMB_RMH_TogglDlayReg0	CROME B: Read RMH Toggle Delay Register 0
DCW31159	CRMB_RMH_TogglDlayReg1	CROME B: Read RMH Toggle Delay Register 1
DCW32159	CRMB_RMH_ActivDelayReg	CROME B: Read RMH Activation Delay Register
DCW33159	CRMB_RMH_RetryDelayReg	CROME B: Read RMH Retry Delay Register
DCW34159	CRMB_RMH_TogDlaySelReg	CROME B: Read RMH Toggle Delay Select Register
DCW35159	CRMB_RMH_AlrmPlrityReg	CROME B: Read RMH Alarm Polarity Register
DCW36159	CRMB_RMH_TemporModeReg	CROME B: Read RMH Temporisation Mode Register
DCW37159	CRMB_RMH_VoteEnableReg	CROME B: Read RMH Vote Enable Register
DCW38159	CRMB_RMH_AlarmEnSetReg	CROME B: Read RMH Alarm Enable Set Register
DCW39159	CRMB_RMH_TemporDlayReg0	CROME B: Read RMH Temporisation Delay Register 0
DCW3A159	CRMB_RMH_TemporDlayReg1	CROME B: Read RMH Temporisation Delay Register 1
DCW3B159	CRMB_RMH_TemporDlayReg2	CROME B: Read RMH Temporisation Delay Register 2
DCW3C159	CRMB_RMH_TemporDlayReg3	CROME B: Read RMH Temporisation Delay Register 3
DCW3D159	CRMB_RMH_TemporDlayReg4	CROME B: Read RMH Temporisation Delay Register 4
DCW3E159	CRMB_RMH_TemporDlayReg5	CROME B: Read RMH Temporisation Delay Register 5
DCW3F159	CRMB_RMH_TemporDlayReg6	CROME B: Read RMH Temporisation Delay Register 6
DCW3G159	CRMB_RMH_TemporDlayReg7	CROME B: Read RMH Temporisation Delay Register 7
DCW3H159	CRMB_RMH_TemporDlayReg8	CROME B: Read RMH Temporisation Delay Register 8
DCW3J159	CRMB_RMH_AttmpCntReg0	CROME B: Read RMH Attempt Count Register 0
DCW3K159	CRMB_RMH_AttmpCntReg1	CROME B: Read RMH Attempt Count Register 1
DCW3L159	CRMB_RMH_AttmpCntReg2	CROME B: Read RMH Attempt Count Register 2
DCW3M159	CRMB_RMH_AttmpCntReg3	CROME B: Read RMH Attempt Count Register 3
DCW3N159	CRMB_RMH_AttmpCntReg4	CROME B: Read RMH Attempt Count Register 4
DCW3P159	CRMB_RMH_AttmpCntReg5	CROME B: Read RMH Attempt Count Register 5
DCW3R159	CRMB_RMH_AttmpCntReg6	CROME B: Read RMH Attempt Count Register 6
DCW3S159	CRMB_PIO_IO_StatusReg	CROME B: Read PIO IO Status Register
DCW3T159	CRMB_PIO_Link_StatusReg	CROME B: Read PIO Link Status Register

Status : Version 6 - Unchanged
 Last Checkin: 22/03/09

Read TTR CROME register
 File: H_FCP_DHS_3010.xls
 Author: S. Manganelli



CROME B registers

DCW30159	CRMB_T10_T0_StatusReg	CROME B: Read T10 T0 Status Register
DCW3T159	CRMB_SPW_LinkStatusReg	CROME B: Read SPW Link Status Register
DCW3U159	CRMB_OBT_Control_Reg	CROME B: Read OB T Control Register
DCW3V159	CRMB_M1553_Status_Reg	CROME B: Read M1553 Status Register
DCW82159	CRMA_RMH_LastChTblMskClr	CROME A: Read RMH Last Chance Table Mask Clear Reg
DCW83159	CRMA_RMH_LastChTblMskSet	CROME A: Read RMH Last Chance Table Mask Set Reg
DCW84159	CRMB_RMH_LastChTblMskClr	CROME B: Read RMH Last Chance Table Mask Clear Reg
DCW85159	CRMB_RMH_LastChTblMskSet	CROME B: Read RMH Last Chance Table Mask Set Reg
DCZ15159	ReadCrome_B_AENCLR	Read CROME B RMH_AENCLR Register [TC(8,4,109,17)]
DCZ16159	ReadCrome_B_AENSET	Read CROME B RMH_AENSET Register [TC(8,4,109,17)]