

SGM maintenance
File: H_CRP_DHS_3014.xls
Author: cmevi-hp



Procedure Summary

Objectives

This procedure describes the steps needed to write, dump and check the selected Absolute addresses of the Safe Guard Memory, SGM.

A priori there is no need to write to SGM. Nevertheless this procedure can be used on a case-by-case basis for possible contingencies requiring to write to SGM.

Summary of Constraints

SGM is:
loaded through TC(6,2);
dumped through TC(6,5);
checked through TC(6,9).

Read/Write operations in SGM are performed in tight sequence and ruled by a dedicated semaphore. No other particular precautions need to be taken. The logical addresses are to be used with TCs, the 16 MSBs being the MemoryID and the 16 LSBs the Start Address (32 bits aligned). All accesses to SGM memory must be 32-bit transfers, aligned to 32-bit boundaries.

These TCs will be delayed when there is an ongoing:
- TC(6,2) Load Memory
- TC(6,5) Dump Memory
- TC(6,9) Check Memory
- TC(8,4,1,1) Copy Memory

Spacecraft Configuration

Start of Procedure
n/a

End of Procedure
n/a

Reference File(s)

Input Command Sequences

Output Command Sequences
HRD3014B
HRD3014C
HRD3014D

Referenced Displays

ANDs **GRDs** **SLDs**
(None)

Configuration Control Information

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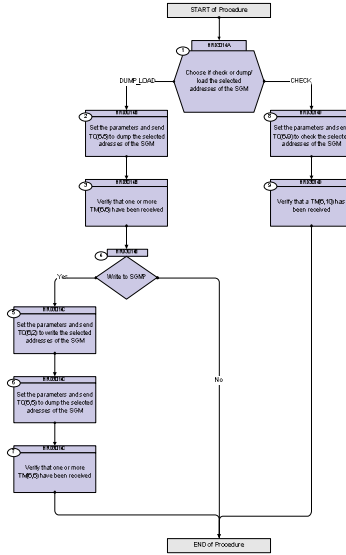


DATE	FOP ISSUE	VERSION	MODIFICATION DESCRIPTION	AUTHOR	SPR REF
25/01/08	1	1	Created	cmevi-hp	
14/11/08	2	2	Procedure updated according to latest version received from industry on 12/09/2008	cmevi-hp	

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Procedure Flowchart Overview



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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
Beginning of Procedure				
<p><i>TC Seq. Name :HRD3014A (Dummy sequence)</i></p> <p><i>TimeTag Type:</i> <i>Sub Schedule ID:</i></p> <p style="text-align: center;"><input type="checkbox"/></p>				
1		Choose if check or dump/load the selected addresses of the SGM		Next Step: DUMP_LOAD 2 CHECK 8
1.1		DUMP_LOAD		<input type="checkbox"/>
1.2		CHECK		<input type="checkbox"/>
<p><i>TC Seq. Name :HRD3014B (Dump SGM addresses)</i></p> <p><i>TimeTag Type:</i> <i>Sub Schedule ID:</i></p> <p style="text-align: center;"><input type="checkbox"/></p>				
2		Set the parameters and send TC(6,5) to dump the selected addresses of the SGM		Next Step: 3
		When the CDMU receives this request it shall read the memory block, generate one or several TMs(6,6) containing the contents of this area and send them to the downlink.		
		<p>In the TC(6,5) it is necessary to set the following parameters:</p> <ul style="list-style-type: none"> - Memory ID: identifier of the memory block of the on-board user from which data shall be dumped. - Start Address: start address (in SAUs, with the count starting from zero) within the memory block for dumping the data (see SGM allocation). - N: number of SAUs to be dumped. 		
		The fields Memory ID and Start Address are treated as one 32-bit field where the 16 least significant bits of the address is stored in Start Address and the 16 most significant bit in the Memory ID field.		
		The length Smallest Addressable Unit (SAU) is 8-bit. However all accesses to SGM memory must be 32-bit transfers, aligned to 32-bit boundaries.		

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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch										
		<p>WARNING: The specified address range must not span over several types of memory.</p> <p>Protected and unprotected parts of a memory are counted as separate memory types.</p> <p>SGM areas reserved for ASW and BSW are counted as separate memory types.</p>												
		<p>SGMA Memory ID is 0x00B0. SGMB Memory ID is 0x00E0.</p>												
		<p>Execute Telecommand</p> <p style="text-align: right;">DumpMem_AbsAddr</p> <p>DC602180</p> <p>Command Parameter(s) :</p> <table style="width: 100%; border: none;"> <tr> <td style="width: 30%;">Memory_ID</td> <td style="width: 30%;">DH003180</td> <td style="width: 30%;">Memory_ID</td> </tr> <tr> <td>Start_Address</td> <td>DH004180</td> <td>Start_Address</td> </tr> <tr> <td>N</td> <td>DH105180</td> <td>Number_of_SAU</td> </tr> </table> <p>TC Control Flags :</p> <p style="text-align: right;">GBM IL DSE --Y -- --</p> <p>Subsch. ID : 10 Det. descr. : Dump Memory Using Absolute Addresses This Telecommand will not be included in the export</p>	Memory_ID	DH003180	Memory_ID	Start_Address	DH004180	Start_Address	N	DH105180	Number_of_SAU			
Memory_ID	DH003180	Memory_ID												
Start_Address	DH004180	Start_Address												
N	DH105180	Number_of_SAU												
3		Verify that one or more TM(6,6) have been received		Next Step: 4										
		<p>Verify Packet Reception</p> <p style="text-align: center;">Memory Dump - Absolute Addresses - SAU 8</p> <p>Packet Details:</p> <table style="width: 100%; border: none;"> <tr> <td style="width: 60%;">APID:</td> <td style="width: 40%;">16</td> </tr> <tr> <td>Type:</td> <td>6</td> </tr> <tr> <td>Subtype:</td> <td>6</td> </tr> <tr> <td>PI1:</td> <td></td> </tr> <tr> <td>PI2:</td> <td></td> </tr> </table>	APID:	16	Type:	6	Subtype:	6	PI1:		PI2:		MemDmpAbsAdd	
APID:	16													
Type:	6													
Subtype:	6													
PI1:														
PI2:														
		Each TM packet contains the following parameters:												
		<p>Verify Telemetry</p> <table style="width: 100%; border: none;"> <tr> <td style="width: 30%; text-align: right;">Memory_ID</td> <td style="width: 30%;">DE060180</td> <td style="width: 40%;">(None)</td> </tr> </table>	Memory_ID	DE060180	(None)									
Memory_ID	DE060180	(None)												
		<p>Verify Telemetry</p> <table style="width: 100%; border: none;"> <tr> <td style="width: 30%; text-align: right;">Start_Address</td> <td style="width: 30%;">DE061180</td> <td style="width: 40%;">(None)</td> </tr> </table>	Start_Address	DE061180	(None)									
Start_Address	DE061180	(None)												
		The Memory ID and Start Address identify the absolute memory address of the first reported SAU in the current TM packet. Thus can be different from the ones commanded in case several TM packets are required for the Dump.												
		<p>Verify Telemetry</p> <table style="width: 100%; border: none;"> <tr> <td style="width: 30%; text-align: right;">N</td> <td style="width: 30%;">DE062180</td> <td style="width: 40%;">(None)</td> </tr> </table>	N	DE062180	(None)									
N	DE062180	(None)												
		The following parameter is repeated N times												

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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
		Verify Telemetry Dumped_Byte DE063180		(None)
		Verify Telemetry Checksum DE064180		(None)
4		Write to SGM?		Next Step: Yes 5 No END
<p>TC Seq. Name :HRD3014C (Write to SGM)</p> <p>TimeTag Type: Sub Schedule ID: <input type="checkbox"/></p>				
5		Set the parameters and send TC(6,2) to write the selected addresses of the SGM		Next Step: 6
		When the CDMU receives this Telecommand, it shall store the checksum of the received data, write the data block to the memory at the specified start address and re-read the memory area just written to, calculate and compare the checksum and report successful execution, TM(1,7), or an error report TM(1,8). In case of an error the loaded data shall be discarded.		
		<p>In the TC(6,2) it is necessary to set the following parameters:</p> <ul style="list-style-type: none"> - Memory ID: identifier of the destination memory block. - Start Address: start address (in SAUs, with the count starting from zero) within the memory block for loading the data (see SGM allocation). - N: number of SAUs to be loaded. As the overall length of a TC packet cannot exceed 248 octets, the maximum length of the field "Data" will be 228 octets. N, expressed as number of SAUs, must be compatible with this boundary. - Data: repeated N times, data block to be loaded (in increasing order of SAU). - Checksum: CRC checksum that is used by the on-board user to verify the integrity of the data being loaded. This checksum is generated over the unpadded Data block to be loaded, (i.e. : excluding the optional spare octet) and is additional and different to the CRC word at the end of each packet. 		

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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
		The fields Memory ID and Start Address are treated as one 32-bit field where the 16 least significant bits of the address is stored in Start Address and the 16 most significant bit in the Memory ID field.		
		The length Smallest Addressable Unit (SAU) is 8-bit. However all accesses to SGM memory must be 32-bit transfers, aligned to 32-bit boundaries.		
		<u>WARNING:</u> The specified address range must not span over several types of memory. Protected and unprotected parts of a memory are counted as separate memory types. SGM areas reserved for ASW and BSW are counted as separate memory types.		
		WARNING: the following TC is a variable length TC therefore does not allow the definition of a generic procedure and it is intended to be just an example.		
		Execute Telecommand <p style="text-align: center;">LoadMem_AbsAdd_EvenByte</p> <pre> Command Parameter(s) : Memory_ID DH003180 Start_Address DH004180 N DH005180 Data DH006180 Data DH006180 Data DH006180 Data DH006180 CheckSum DH007180 </pre> <p>TC Control Flags :</p> <p style="text-align: center;">GBM IL DSE</p> <p style="text-align: center;">--Y -- ---</p> <p>Subsch. ID : 10 Det. descr. : Load Even number of bytes inMemory Using Absolute Addresses This Telecommand will not be included in the export</p>	DC601180	
		Note: DC600180 LoadMem_AbsAdd_OddByte should never be used as all accesses to SGM memory must be 32-bit transfers.		
6		Set the parameters and send TC(6,5) to dump the selected addresses of the SGM		Next Step: 7
		When the CDMU receives this request it shall read the memory block, generate one or several TMs(6,6) containing the contents of this area and send them to the downlink.		

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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
		<p>In the TC(6,5) it is necessary to set the following parameters:</p> <ul style="list-style-type: none"> - Memory ID: identifier of the memory block of the on-board user from which data shall be dumped. - Start Address: start address (in SAUs, with the count starting from zero) within the memory block for dumping the data (see SGM allocation). - N: number of SAUs to be dumped. 		
		The fields Memory ID and Start Address are treated as one 32-bit field where the 16 least significant bits of the address is stored in Start Address and the 16 most significant bit in the Memory ID field.		
		The length Smallest Addressable Unit (SAU) is 8-bit. However all accesses to SGM memory must be 32-bit transfers, aligned to 32-bit boundaries.		
		<p>WARNING: The specified address range must not span over several types of memory.</p> <p>Protected and unprotected parts of a memory are counted as separate memory types.</p> <p>SGM areas reserved for ASW and BSW are counted as separate memory types.</p>		
		<pre> Execute Telecommand DumpMem_AbsAddr DC602180 Command Parameter(s) : Memory_ID DH003180 Memory_ID Start_Address DH004180 Start_Address N DH105180 Number_of_SAUs TC Control Flags : GBM IL DSE --Y -- --- Subsch. ID : 10 Det. descr. : Dump Memory Using Absolute Addresses This Telecommand will not be included in the export </pre>		
7		Verify that one or more TM(6,6) have been received		Next Step: END
		<pre> Verify Packet Reception Memory Dump - Absolute Addresses - SAU 8 Packet Details: APID: 16 Type: 6 Subtype: 6 PI1: PI2: </pre>	MemDmpAbsAdd	
		Each TM packet contains the following parameters:		

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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
		Verify Telemetry Memory_ID DE060180		(None)
		Verify Telemetry Start_Address DE061180		(None)
		The Memory ID and Start Address identify the absolute memory address of the first reported SAU in the current TM packet. Thus can be different from the ones commanded in case several TM packets are required for the Dump.		
		Verify Telemetry N DE062180		(None)
		The following parameter is repeated N times		
		Verify Telemetry Dumped_Byte DE063180		(None)
		Verify Telemetry Checksum DE064180		(None)
<p><i>TC Seq. Name :HRD3014D (Check addresses into)</i></p> <p><i>TimeTag Type:</i> <i>Sub Schedule ID:</i></p> <p><input type="checkbox"/></p>				
8		Set the parameters and send TC(6,9) to check the selected addresses of the SGM		Next Step: 9
		When the CDMU receives this request it shall read and compute the checksum value of the indicated area of the memory using the CRC checksum algorithm. It then generates a report containing the checksum value computed.		
		In the TC(6,9) it is necessary to set the following parameters: - Memory ID: identifier of the memory block of the on-board user which data shall be checked. - Start Address: start address (in SAUs, with the count starting from zero) within the memory block for data to be checked (see SGM allocation). - N: number of SAUs on which the CRC checksum algorithm shall be applied.		
		The fields Memory ID and Start Address are treated as one 32-bit field where the 16 least significant bits of the address is stored in Start Address and the 16 most significant bit in the Memory ID field.		

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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch															
		The length Smallest Addressable Unit (SAU) is 8-bit. However all accesses to SGM memory must be 32-bit transfers, aligned to 32-bit boundaries.																	
		WARNING: The specified address range must not span over several types of memory. Protected and unprotected parts of a memory are counted as separate memory types. SGM areas reserved for ASW and BSW are counted as separate memory types.																	
		Execute Telecommand <p style="text-align: right;">ChkMem_AbsAdd</p> <p>Command Parameter(s) :</p> <table style="width: 100%; border: none;"> <tr> <td style="width: 30%;">Memory_ID</td> <td style="width: 30%;">DH003180</td> <td style="width: 30%;">Memory_ID</td> </tr> <tr> <td>Start_Address</td> <td>DH004180</td> <td>Start_Address</td> </tr> <tr> <td>N</td> <td>DH105180</td> <td>Number_of_SAUs</td> </tr> </table> <p>TC Control Flags :</p> <table style="width: 100%; border: none;"> <tr> <td style="width: 30%;"></td> <td style="width: 30%;">GBM IL DSE</td> <td style="width: 30%;"></td> </tr> <tr> <td></td> <td>--Y --</td> <td></td> </tr> </table> <p>Subsch. ID : 10 Det. descr. : Check Memory Using Absolute Addresses This Telecommand will not be included in the export</p>	Memory_ID	DH003180	Memory_ID	Start_Address	DH004180	Start_Address	N	DH105180	Number_of_SAUs		GBM IL DSE			--Y --		DC603180	
Memory_ID	DH003180	Memory_ID																	
Start_Address	DH004180	Start_Address																	
N	DH105180	Number_of_SAUs																	
	GBM IL DSE																		
	--Y --																		
9		Verify that a TM(6,10) has been received		Next Step: END															
		Verify Packet Reception Memory Check Report - Absolute Addresses Packet Details:	MemChkRepAbs																
		<table style="width: 100%; border: none;"> <tr> <td style="width: 30%;"></td> <td style="width: 30%;">APID:</td> <td style="width: 30%;">16</td> </tr> <tr> <td></td> <td>Type:</td> <td>6</td> </tr> <tr> <td></td> <td>Subtype:</td> <td>10</td> </tr> <tr> <td></td> <td>PI1:</td> <td></td> </tr> <tr> <td></td> <td>PI2:</td> <td></td> </tr> </table>		APID:	16		Type:	6		Subtype:	10		PI1:			PI2:			
	APID:	16																	
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	PI1:																		
	PI2:																		
End of Procedure																			