

Set RM Activation and Toggle Delays
 File: H_CRP_AOC_D2AD.xls
 Author: dsalt-hp



Procedure Summary

Objectives

USAGE:

The procedure may be applied for restoring the value of one of the activation delay or toggle delay registers after a bit flip.

NOTES:

All RM registers are 32 bits wide. The storage of temporisation values requires only 16 bits. The most significant 16 bits of all temporisation registers are always set to 0.

Summary of Constraints

N/A

Spacecraft Configuration

Start of Procedure

N/A

End of Procedure

N/A

Reference File(s)

Input Command Sequences

Output Command Sequences

HRAD2AD1
 HRAD2AD2
 HRAD2AD3
 HRAD2AD4
 HRAD2ADA
 HRAD2ADB
 HRAD2ADC
 HRAD2ADD

Referenced Displays

ANDs	GRDs	SLDs
ZAAM3999		
ZAA07999		

Configuration Control Information

DATE	FOP ISSUE	VERSION	MODIFICATION DESCRIPTION	AUTHOR	SPR REF
Status : Version 2 - Unchanged					
Last Checkin: 09/01/09					

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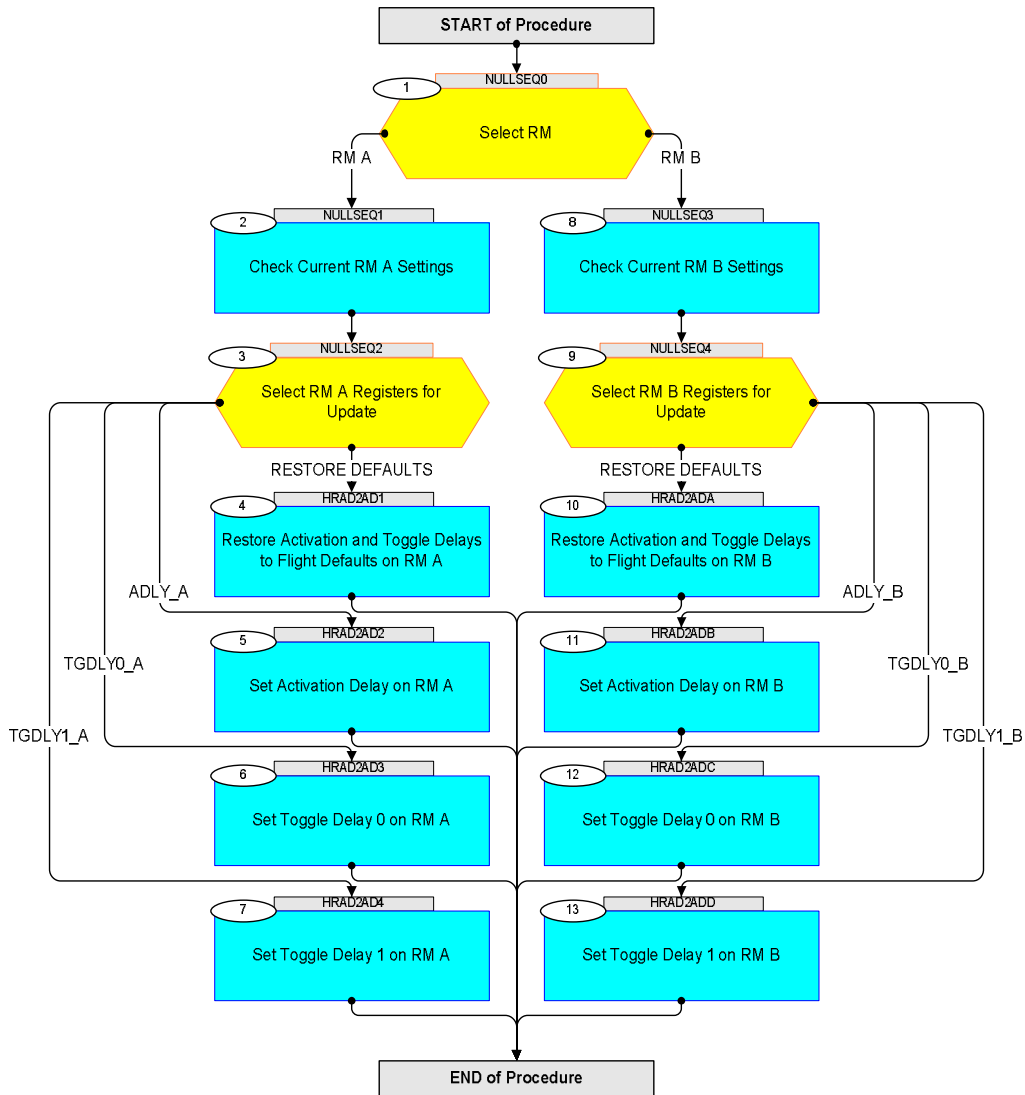


09/01/09		1	Created	dsalt-hp	
09/01/09	2	2	Redundant sequence references removed from end of procedure	dsalt-hp	

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Procedure Flowchart Overview

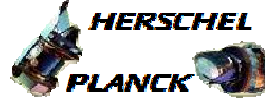


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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
Beginning of Procedure				
TC Seq. Name : NULLSEQ0 () TimeTag Type: N Sub Schedule ID: <input type="checkbox"/>				
1		Select RM		Next Step: RM A 2 RM B 8
		Select the reconfiguration module that needs change of alarm temporisation delay settings: RM A -> GO TO STEP 2 RM B -> GO TO STEP 13		
TC Seq. Name : NULLSEQ1 () TimeTag Type: Sub Schedule ID: <input type="checkbox"/>				
2		Check Current RM A Settings		Next Step: 3
		This step verifies if the current activation and toggle delay settings are as expected. The temporisation delay settings can be read as part of the telemetry packet returned by the TC_GET_RM_STATUS command.		
2.1		Uplink Sequence HFADRMR1		<input type="checkbox"/>
		Execute Sequence HFADRMR1 GetRmAstatusReport		
2.2		Check Toggle Delay and Activation Delay Settings		<input type="checkbox"/>
		Verify Telemetry RMH_TGDLY0_MSB AEXU0109 <to be read>		AND=ZAAM3999
		Verify Telemetry RMH_TGDLY0_LSB AEXU1109 <to be read>		AND=ZAAM3999
		Verify Telemetry RMH_TGDLY0 AD001109 <to be read>		AND=ZAAM3999

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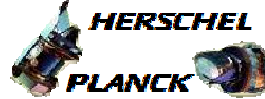
Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
		Verify Telemetry RMH_TGDLY1_MSB AEXU2109	<to be read>	AND=ZAAM3999
		Verify Telemetry RMH_TGDLY1_LSB AEXU3109	<to be read>	AND=ZAAM3999
		Verify Telemetry RMH_TGDLY1 AD002109	<to be read>	AND=ZAAM3999
		Verify Telemetry RMH_ADLY_MSB AEXU4109	<to be read>	AND=ZAAM3999
		Verify Telemetry RMH_ADLY_LSB AEXU5109	<to be read>	AND=ZAAM3999
		Verify Telemetry RMH_ADLY AD003109	<to be read>	AND=ZAAM3999
<p>TC Seq. Name : NULLSEQ2 ()</p> <p>TimeTag Type: Sub Schedule ID:</p> <p style="text-align: center;">□</p>				
3		Select RM A Registers for Update		Next Step: RESTORE DEFAULTS 4 ADLY_A 5 TGDLY0_A 6 TGDLY1_A 7
		Options are to restore the default temporisation delay settings for flight or to set a user-defined temporisation delay for the individual alarms:		
		Restore flight defaults -> GO TO STEP 4		
		Activation Delay -> GO TO STEP 5		
		Toggle Delay 0 -> GO TO STEP 6		
		Toggle Delay 1 -> GO TO STEP 7		
<p>TC Seq. Name : HRAD2AD1 (SetRmAadlyTgdlyDef)</p> <p>TimeTag Type: N Sub Schedule ID:</p> <p style="text-align: center;">□</p>				
4		Restore Activation and Toggle Delays to Flight Defaults on RM A		Next Step: END

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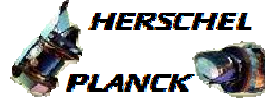
Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
		<i>This step restores the default activation delay and toggle delay settings for flight. Default configuration is as highlighted in the table attached at the back of this procedure.</i>		
4.1		Uplink Sequence HRAD2AD1		<input type="checkbox"/>
4.1.1		Disable RM A		<input type="checkbox"/>
		Execute Telecommand Ext_ACC_RM_A_Disable TC Control Flags : Subsch. ID : 10 Det. descr. : External ACC RM A Disable - Mission Specific	DCM22170	
		Verify Telemetry RMA_fromTTR-RMA AEE91050	= DISABLED	AND=ZAA07999
		Verify Telemetry RMA_fromTTR-RMB AEE92050	= DISABLED	AND=ZAA07999
4.1.2		Load Activation Delay and Toggle Delay Flight Defaults		<input type="checkbox"/>
		Execute Telecommand Load Memory Command Parameter(s) : Memory ID AH6M0109 Start Address AH6M1109 Length SAU AH6M2109 Memory Data 32 AH6M6109 Memory Checksum AH6M7109 TC Control Flags : Subsch. ID : 20 Det. descr. : TC(6,2) Load Memory Using Absolute Addresses	AC062109	

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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
		Execute Telecommand <div style="text-align: right;">Load Memory</div> Command Parameter(s) : Memory ID AH6M0109 Start Address AH6M1109 Length SAU AH6M2109 Memory Data 32 AH6M6109 Memory Checksum AH6M7109 TC Control Flags : GBM IL DSE --Y -- --- Subsch. ID : 20 Det. descr. : TC(6,2) Load Memory Using Absolute Addresses	AC062109 00C0 <hex> 5880 <hex> 4 <dec> (Def) 0000FA01 <hex> 68EB <hex>	
		Execute Telecommand <div style="text-align: right;">Load Memory</div> Command Parameter(s) : Memory ID AH6M0109 Start Address AH6M1109 Length SAU AH6M2109 Memory Data 32 AH6M6109 Memory Checksum AH6M7109 TC Control Flags : GBM IL DSE --Y -- --- Subsch. ID : 20 Det. descr. : TC(6,2) Load Memory Using Absolute Addresses	AC062109 00C0 <hex> 5884 <hex> 4 <dec> (Def) 00009602 <hex> 16CF <hex>	
4.1.3		Verify Update via RM A Status Report		<input type="checkbox"/>
		Execute Telecommand <div style="text-align: right;">Get RM-A status</div> Command Parameter(s) : RMStat DF86Cmd AH841001 RMStat DD86Cmd AH842001 TC Control Flags : GBM IL DSE --Y -- --- Subsch. ID : 20 Det. descr. : TC(8,1) - Get RM-A status	ACZZ4109 Enable 86 Enable 86	
		Verify Packet Reception <div style="text-align: center;">TM 8-6 for RM Status parametrized</div> Packet Details: APID: 512 Type: 8 Subtype: 6 PI1: 41600 PI2: 1	A86_RMStatus	
		Verify Telemetry <div style="text-align: right;">RMH_TGDLY0_MSB</div> <div style="text-align: right;">AEXU0109</div> <div style="text-align: right;">= 250 <dec></div>		AND=ZAAM3999

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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
		Verify Telemetry RMH_TGDLY0_LSB AEXU1109	= 1 <dec>	AND=ZAAM3999
		Verify Telemetry RMH_TDLY0 AD004109	50.1 ms	AND=ZAAM3999
		Verify Telemetry RMH_TGDLY1_MSB AEXU2109	= 150 <dec>	AND=ZAAM3999
		Verify Telemetry RMH_TGDLY1_LSB AEXU3109	= 2 <dec>	AND=ZAAM3999
		Verify Telemetry RMH_TGDLY1 AD002109	60.1 ms	AND=ZAAM3999
		Verify Telemetry RMH_ADLY_MSB AEXU4109	= 156 <dec>	AND=ZAAM3999
		Verify Telemetry RMH_ADLY_LSB AEXU5109	= 7 <dec>	AND=ZAAM3999
		Verify Telemetry RMH_ADLY AD003109	1996.9 ms	AND=ZAAM3999
4.1.4		Enable RM A		<input type="checkbox"/>
		Execute Telecommand Ext_ACC_RM_A_Enable TC Control Flags : GBM IL DSE --Y -- --- Subsch. ID : 10 Det. descr. : External ACC RM A Enable - Mission Specific	DCM21170	
		Verify Telemetry RMA_fromTTR-RMA AEE91050	= ENABLED	AND=ZAA07999
		Verify Telemetry RMA_fromTTR-RMB AEE92050	= ENABLED	AND=ZAA07999
TC Seq. Name : HRAD2AD2 (SetRmAActivDelay)				
TimeTag Type: N Sub Schedule ID: Formal Parameter List : Memory Data 32 MemData= <dec> Memory Checksum MemCrc= <dec>				
5		Set Activation Delay on RM A		Next Step: END

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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
		<i>This step allows the user to set a user-defined activation delay.</i>		
5.1		<i>Uplink Sequence HRAD2AD2</i>		<input type="checkbox"/>
5.1.1		<i>Disable RM A</i>		<input type="checkbox"/>
		Execute Telecommand <div style="text-align: right;">Ext_ACC_RM_A_Disable</div> <i>TC Control Flags :</i> <div style="text-align: right;">GBM IL DSE</div> <div style="text-align: right;">--Y -- ---</div> <i>Subsch. ID : 10</i> <i>Det. descr. : External ACC RM A Disable - Mission Specific</i>	DCM22170	
		Verify Telemetry <div style="text-align: right;">RMA_fromTTR-RMA AEE91050</div>	= DISABLED	AND=ZAA07999
		Verify Telemetry <div style="text-align: right;">RMA_fromTTR-RMB AEE92050</div>	= DISABLED	AND=ZAA07999
5.1.2		<i>Load Activation Delay Settings</i>		<input type="checkbox"/>
		<i>When loading this command sequence on the Manual Stack, it will ask you to enter values for the formal parameters inside the sequence. The formal parameters are:</i> <ul style="list-style-type: none"> - MemData = Alarm temporisation delay in the timing format prescribed at the back of this procedure. Storage of timing data uses two integer numbers: V, in the range 0 .. 255 and exponent E, in the range 0 .. 15. Time is then equal to (V * 2^E + 1) * 0.1 msec. For timing data stored in registers, the datastring to be loaded into the 32-bit register can be obtained as follows: register content = '0000' & V [hex] & E [hex], with V and E expressed as 1 byte hex strings. 		

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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch															
		- MemCrc = CRC-CCITT (0xFFFF) checksum calculated over the entire datastring described above. Do not forget the leading four zeros (!). To verify your own checksum calculation you could use the following webpage: http://www.lammertbies.nl/comm/info/crc-calculation.html?crc=0000b70e&method=hex																	
		Execute Telecommand <p style="text-align: right;">Load Memory</p> Command Parameter(s) : <table style="width: 100%; border: none;"> <tr> <td style="padding-left: 40px;">Memory ID</td> <td style="padding-left: 40px;">AH6M0109</td> <td style="padding-left: 40px;">00C0 <hex></td> </tr> <tr> <td style="padding-left: 40px;">Start Address</td> <td style="padding-left: 40px;">AH6M1109</td> <td style="padding-left: 40px;">5888 <hex></td> </tr> <tr> <td style="padding-left: 40px;">Length SAU</td> <td style="padding-left: 40px;">AH6M2109</td> <td style="padding-left: 40px;">4 <dec> (Def)</td> </tr> <tr> <td style="padding-left: 40px;">Memory Data 32</td> <td style="padding-left: 40px;">AH6M6109</td> <td style="padding-left: 40px;">MemData</td> </tr> <tr> <td style="padding-left: 40px;">Memory Checksum</td> <td style="padding-left: 40px;">AH6M7109</td> <td style="padding-left: 40px;">MemCrc</td> </tr> </table> TC Control Flags : <p style="text-align: right;">GBM IL DSE ---Y -- ---</p> Subsch. ID : 20 Det. descr. : TC(6,2) Load Memory Using Absolute Addresses	Memory ID	AH6M0109	00C0 <hex>	Start Address	AH6M1109	5888 <hex>	Length SAU	AH6M2109	4 <dec> (Def)	Memory Data 32	AH6M6109	MemData	Memory Checksum	AH6M7109	MemCrc	AC062109	
Memory ID	AH6M0109	00C0 <hex>																	
Start Address	AH6M1109	5888 <hex>																	
Length SAU	AH6M2109	4 <dec> (Def)																	
Memory Data 32	AH6M6109	MemData																	
Memory Checksum	AH6M7109	MemCrc																	
5.1.3		Verify Update via RM A Status Report		□															
		Execute Telecommand <p style="text-align: right;">Get RM-A status</p> Command Parameter(s) : <table style="width: 100%; border: none;"> <tr> <td style="padding-left: 40px;">RMStat DF86Cmd</td> <td style="padding-left: 40px;">AH841001</td> <td style="padding-left: 40px;">Enable 86</td> </tr> <tr> <td style="padding-left: 40px;">RMStat DD86Cmd</td> <td style="padding-left: 40px;">AH842001</td> <td style="padding-left: 40px;">Enable 86</td> </tr> </table> TC Control Flags : <p style="text-align: right;">GBM IL DSE ---Y -- ---</p> Subsch. ID : 20 Det. descr. : TC(8,1) - Get RM-A status	RMStat DF86Cmd	AH841001	Enable 86	RMStat DD86Cmd	AH842001	Enable 86	ACZZ4109										
RMStat DF86Cmd	AH841001	Enable 86																	
RMStat DD86Cmd	AH842001	Enable 86																	
		Verify Packet Reception TM 8-6 for RM Status parametrized Packet Details: <table style="width: 100%; border: none;"> <tr> <td style="padding-left: 40px;">APID:</td> <td style="padding-left: 40px;">512</td> </tr> <tr> <td style="padding-left: 40px;">Type:</td> <td style="padding-left: 40px;">8</td> </tr> <tr> <td style="padding-left: 40px;">Subtype:</td> <td style="padding-left: 40px;">6</td> </tr> <tr> <td style="padding-left: 40px;">PI1:</td> <td style="padding-left: 40px;">41600</td> </tr> <tr> <td style="padding-left: 40px;">PI2:</td> <td style="padding-left: 40px;">1</td> </tr> </table>	APID:	512	Type:	8	Subtype:	6	PI1:	41600	PI2:	1	A86_RMStatus						
APID:	512																		
Type:	8																		
Subtype:	6																		
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		Verify Telemetry <table style="width: 100%; border: none;"> <tr> <td style="padding-left: 40px;">RMH_ADLY_MSB</td> <td style="padding-left: 40px;">AEXU4109</td> <td style="padding-left: 40px;"><user defined setting></td> </tr> </table>	RMH_ADLY_MSB	AEXU4109	<user defined setting>		AND=ZAAM3999												
RMH_ADLY_MSB	AEXU4109	<user defined setting>																	
		Verify Telemetry <table style="width: 100%; border: none;"> <tr> <td style="padding-left: 40px;">RMH_ADLY_LSB</td> <td style="padding-left: 40px;">AEXU5109</td> <td style="padding-left: 40px;"><user defined setting></td> </tr> </table>	RMH_ADLY_LSB	AEXU5109	<user defined setting>		AND=ZAAM3999												
RMH_ADLY_LSB	AEXU5109	<user defined setting>																	

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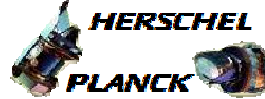
Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
		Verify Telemetry RMH_ADLY AD003109	<user defined setting>	AND=ZAAM3999
5.1.4		Enable RM A		<input type="checkbox"/>
		Execute Telecommand Ext_ACC_RM_A_Enable TC Control Flags : Subsch. ID : 10 Det. descr. : External ACC RM A Enable - Mission Specific	DCM21170 GBM IL DSE --Y -- --	
		Verify Telemetry RMA_fromTTR-RMA AEE91050	= ENABLED	AND=ZAA07999
		Verify Telemetry RMA_fromTTR-RMB AEE92050	= ENABLED	AND=ZAA07999
<p>TC Seq. Name : HRAD2AD3 (SetRmAtoggleDelay0)</p> <p>TimeTag Type: N Sub Schedule ID: Formal Parameter List : Memory Data 32 MemData= Memory Checksum MemCrc=</p>				
6		Set Toggle Delay 0 on RM A		Next Step: END
		This step allows the user to set a user-defined toggle delay 0.		
6.1		Uplink Sequence HRAD2AD3		<input type="checkbox"/>
6.1.1		Disable RM A		<input type="checkbox"/>
		Execute Telecommand Ext_ACC_RM_A_Disable TC Control Flags : Subsch. ID : 10 Det. descr. : External ACC RM A Disable - Mission Specific	DCM22170 GBM IL DSE --Y -- --	

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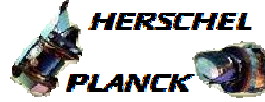
Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
		Verify Telemetry RMA_fromTTR-RMA AEE91050	= DISABLED	AND=ZAA07999
		Verify Telemetry RMA_fromTTR-RMB AEE92050	= DISABLED	AND=ZAA07999
6.1.2		Load Toggle Delay 0 Settings		<input type="checkbox"/>
		<p>When loading this command sequence on the Manual Stack, it will ask you to enter values for the formal parameters inside the sequence. The formal parameters are:</p> <ul style="list-style-type: none"> - MemData = Alarm temporisation delay in the timing format prescribed at the back of this procedure. Storage of timing data uses two integer numbers: V, in the range 0 .. 255 and exponent E, in the range 0 .. 15. Time is then equal to $(V * 2^E + 1) * 0.1$ msec. For timing data stored in registers, the datastring to be loaded into the 32-bit register can be obtained as follows: register content = '0000' & V [hex] & E [hex], with V and E expressed as 1 byte hex strings. 		
		<ul style="list-style-type: none"> - MemCrc = CRC-CCITT (0xFFFF) checksum calculated over the entire datastring described above. Do not forget the leading four zeros (!). To verify your own checksum calculation you could use the following webpage: http://www.lammertbies.nl/comm/info/crc-calculation.html?crc=0000b70e&method=hex 		
		Execute Telecommand <div style="text-align: right;">Load Memory</div> Command Parameter(s) : Memory ID AH6M0109 Start Address AH6M1109 Length SAU AH6M2109 Memory Data 32 AH6M6109 Memory Checksum AH6M7109 TC Control Flags : <div style="text-align: right;">GBM IL DSE --Y -- --</div> Subsch. ID : 20 Det. descr. : TC(6,2) Load Memory Using Absolute Addresses	AC062109	
6.1.3		Verify Update via RM A Status Report		<input type="checkbox"/>

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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
		Execute Telecommand Get RM-A status Command Parameter(s) : RMStat DF86Cmd AH841001 Enable 86 RMStat DD86Cmd AH842001 Enable 86 TC Control Flags : GBM IL DSE --Y -- --- Subsch. ID : 20 Det. descr. : TC(8,1) - Get RM-A status	ACZZ4109	
		Verify Packet Reception TM 8-6 for RM Status parametrized Packet Details: APID: 512 Type: 8 Subtype: 6 PI1: 41600 PI2: 1	A86_RMStatus	
		Verify Telemetry RMH_TGDLY0_MSB AEXU0109	<user defined setting>	AND=ZAAM3999
		Verify Telemetry RMH_TGDLY0_LSB AEXU1109	<user defined setting>	AND=ZAAM3999
		Verify Telemetry RMH_TGDLY0 AD001109	<user defined setting>	AND=ZAAM3999
6.1.4		Enable RM A		<input type="checkbox"/>
		Execute Telecommand Ext_ACC_RM_A_Enable TC Control Flags : GBM IL DSE --Y -- --- Subsch. ID : 10 Det. descr. : External ACC RM A Enable - Mission Specific	DCM21170	
		Verify Telemetry RMA_fromTTR-RMA AEE91050	= ENABLED	AND=ZAA07999
		Verify Telemetry RMA_fromTTR-RMB AEE92050	= ENABLED	AND=ZAA07999

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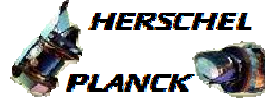
Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
<i>TC Seq. Name : HRAD2AD4 (SetRmAtoggleDelay1)</i> <i>TimeTag Type: N</i> <i>Sub Schedule ID:</i> <i>Formal Parameter List :</i> Memory Data 32 MemData= <dec> Memory Checksum MemCrc= <dec>				
7		Set Toggle Delay 1 on RM A		Next Step: END
		<i>This step allows the user to set a user-defined toggle delay 1.</i>		
7.1		Uplink Sequence HRAD2AD4		<input type="checkbox"/>
7.1.1		Disable RM A		<input type="checkbox"/>
		Execute Telecommand <p style="text-align: right;">Ext_ACC_RM_A_Disable</p> <i>TC Control Flags :</i> <p style="text-align: right;">GBM IL DSE --Y -- ---</p> <i>Subsch. ID : 10</i> <i>Det. descr. : External ACC RM A Disable - Mission Specific</i>	DCM22170	
		Verify Telemetry <p style="text-align: center;">RMA_fromTTR-RMA AEE91050</p>	= DISABLED	AND=ZAA07999
		Verify Telemetry <p style="text-align: center;">RMA_fromTTR-RMB AEE92050</p>	= DISABLED	AND=ZAA07999
7.1.2		Load Toggle Delay 1 Settings		<input type="checkbox"/>

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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch															
		<p>When loading this command sequence on the Manual Stack, it will ask you to enter values for the formal parameters inside the sequence. The formal parameters are:</p> <ul style="list-style-type: none"> - MemData = Alarm temporisation delay in the timing format prescribed at the back of this procedure. Storage of timing data uses two integer numbers: V, in the range 0 .. 255 and exponent E, in the range 0 .. 15. Time is then equal to $(V * 2^E + 1) * 0.1$ msec. For timing data stored in registers, the datastring to be loaded into the 32-bit register can be obtained as follows: register content = '0000' & V [hex] & E [hex], with V and E expressed as 1 byte hex strings. 																	
		<ul style="list-style-type: none"> - MemCrc = CRC-CCITT (0xFFFF) checksum calculated over the entire datastring described above. Do not forget the leading four zeros (!). To verify your own checksum calculation you could use the following webpage: http://www.lammertbies.nl/comm/info/crc-calculation.html?crc=0000b70e&method=hex 																	
		<p>Execute Telecommand</p> <p style="text-align: right;">Load Memory</p> <p>Command Parameter(s) :</p> <table style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 30%;">Memory ID</td> <td style="width: 30%;">AH6M0109</td> <td style="width: 40%;">00C0 <hex></td> </tr> <tr> <td>Start Address</td> <td>AH6M1109</td> <td>5884 <hex></td> </tr> <tr> <td>Length SAU</td> <td>AH6M2109</td> <td>4 <dec> (Def)</td> </tr> <tr> <td>Memory Data 32</td> <td>AH6M6109</td> <td>MemData</td> </tr> <tr> <td>Memory Checksum</td> <td>AH6M7109</td> <td>MemCrc</td> </tr> </table> <p>TC Control Flags :</p> <p style="text-align: right;">GBM IL DSE --Y -- ---</p> <p>Subsch. ID : 20 Det. descr. : TC(6,2) Load Memory Using Absolute Addresses</p>	Memory ID	AH6M0109	00C0 <hex>	Start Address	AH6M1109	5884 <hex>	Length SAU	AH6M2109	4 <dec> (Def)	Memory Data 32	AH6M6109	MemData	Memory Checksum	AH6M7109	MemCrc	AC062109	
Memory ID	AH6M0109	00C0 <hex>																	
Start Address	AH6M1109	5884 <hex>																	
Length SAU	AH6M2109	4 <dec> (Def)																	
Memory Data 32	AH6M6109	MemData																	
Memory Checksum	AH6M7109	MemCrc																	
7.1.3		Verify Update via RM A Status Report		□															

Set RM Activation and Toggle Delays
 File: H_CRP_AOC_D2AD.xls
 Author: dsalt-hp



Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
		Execute Telecommand Get RM-A status Command Parameter(s) : RMStat DF86Cmd AH841001 Enable 86 RMStat DD86Cmd AH842001 Enable 86 TC Control Flags : GBM IL DSE --Y -- --- Subsch. ID : 20 Det. descr. : TC(8,1) - Get RM-A status	ACZZ4109	
		Verify Packet Reception TM 8-6 for RM Status parametrized Packet Details: APID: 512 Type: 8 Subtype: 6 PI1: 41600 PI2: 1	A86_RMStatus	
		Verify Telemetry RMH_TGDLY1_MSB AEXU2109	<user defined setting>	AND=ZAAM3999
		Verify Telemetry RMH_TGDLY1_LSB AEXU3109	<user defined setting>	AND=ZAAM3999
		Verify Telemetry RMH_TGDLY1 AD002109	<user defined setting>	AND=ZAAM3999
7.1.4		Enable RM A		<input type="checkbox"/>
		Execute Telecommand Ext_ACC_RM_A_Enable TC Control Flags : GBM IL DSE --Y -- --- Subsch. ID : 10 Det. descr. : External ACC RM A Enable - Mission Specific	DCM21170	
		Verify Telemetry RMA_fromTTR-RMA AEE91050	=	AND=ZAA07999
		Verify Telemetry RMA_fromTTR-RMB AEE92050	= ENABLED	AND=ZAA07999

TC Seq. Name : NULLSEQ3 ()

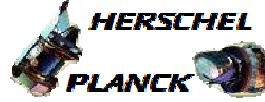
TimeTag Type:
Sub Schedule ID:

Set RM Activation and Toggle Delays
 File: H_CRP_AOC_D2AD.xls
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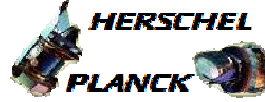
Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
8		Check Current RM B Settings		Next Step: 9
		<i>This step verifies if the current activation and toggle delay settings are as expected. The temporisation delay settings can be read as part of the telemetry packet returned by the TC_GET_RM_STATUS command.</i>		
8.1		Uplink Sequence HFADRM2		<input type="checkbox"/>
		Execute Sequence HFADRM2 GetRmBstatusReport		
8.2		Check Toggle Delay and Activation Delay Settings		<input type="checkbox"/>
		Verify Telemetry RMH_TGDLY0_MSB AEXU0109	<to be read>	AND=ZAAM3999
		Verify Telemetry RMH_TGDLY0_LSB AEXU1109	<to be read>	AND=ZAAM3999
		Verify Telemetry RMH_TGDLY0 AD001109	<to be read>	AND=ZAAM3999
		Verify Telemetry RMH_TGDLY1_MSB AEXU2109	<to be read>	AND=ZAAM3999
		Verify Telemetry RMH_TGDLY1_LSB AEXU3109	<to be read>	AND=ZAAM3999
		Verify Telemetry RMH_TGDLY1 AD002109	<to be read>	AND=ZAAM3999
		Verify Telemetry RMH_ADLY_MSB AEXU4109	<to be read>	AND=ZAAM3999
		Verify Telemetry RMH_ADLY_LSB AEXU5109	<to be read>	AND=ZAAM3999
		Verify Telemetry RMH_ADLY AD003109	<to be read>	AND=ZAAM3999
TC Seq. Name : NULLSEQ4 ()				
TimeTag Type: Sub Schedule ID: <input type="checkbox"/>				

Set RM Activation and Toggle Delays
 File: H_CRP_AOC_D2AD.xls
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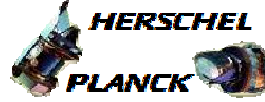
Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
9		Select RM B Registers for Update		Next Step: TGDLY1_B 13 RESTORE DEFAULTS 10 ADLY_B 11 TGDLY0_B 12
		Options are to restore the default temporisation delay settings for flight or to set a user-defined temporisation delay for the individual alarms: Restore flight defaults -> GO TO STEP 10 Activation Delay -> GO TO STEP 11 Toggle Delay 0 -> GO TO STEP 12 Toggle Delay 1 -> GO TO STEP 13		
<p>TC Seq. Name : HRAD2ADA (SetRmBadlyTgdlyDef)</p> <p>TimeTag Type: N Sub Schedule ID: <input type="checkbox"/></p>				
10		Restore Activation and Toggle Delays to Flight Defaults on RM B		Next Step: END
		This step restores the default activation delay and toggle delay settings for flight. Default configuration is as highlighted in the table attached at the back of this procedure.		
10.1		Uplink Sequence HRAD2AD5		<input type="checkbox"/>
10.1.1		Disable RM B		<input type="checkbox"/>
		Execute Telecommand Ext_ACC_RM_B_Disable TC Control Flags : Subsch. ID : 10 Det. descr. : External ACC RM B Disable - Mission Specific	DCM25170	

Set RM Activation and Toggle Delays
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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
		Verify Telemetry RMB_fromTTR-RMA AEE93050	= DISABLED	AND=ZAA07999
		Verify Telemetry RMB_fromTTR-RMB AEE94050	= DISABLED	AND=ZAA07999
10.1.2		Load Activation Delay and Toggle Delay Flight Defaults		□
		Execute Telecommand Load Memory AC062109 Command Parameter(s) : Memory ID AH6M0109 00F0 <hex> Start Address AH6M1109 5888 <hex> Length SAU AH6M2109 4 <dec> (Def) Memory Data 32 AH6M6109 00009C07 <hex> Memory Checksum AH6M7109 A9A1 <hex> TC Control Flags : GBM IL DSE --Y -- --- Subsch. ID : 20 Det. descr. : TC(6,2) Load Memory Using Absolute Addresses		
		Execute Telecommand Load Memory AC062109 Command Parameter(s) : Memory ID AH6M0109 00F0 <hex> Start Address AH6M1109 5880 <hex> Length SAU AH6M2109 4 <dec> (Def) Memory Data 32 AH6M6109 00008902 <hex> Memory Checksum AH6M7109 0582 <hex> TC Control Flags : GBM IL DSE --Y -- --- Subsch. ID : 20 Det. descr. : TC(6,2) Load Memory Using Absolute Addresses		
		Execute Telecommand Load Memory AC062109 Command Parameter(s) : Memory ID AH6M0109 00F0 <hex> Start Address AH6M1109 5884 <hex> Length SAU AH6M2109 4 <dec> (Def) Memory Data 32 AH6M6109 00008902 <hex> Memory Checksum AH6M7109 0582 <hex> TC Control Flags : GBM IL DSE --Y -- --- Subsch. ID : 20 Det. descr. : TC(6,2) Load Memory Using Absolute Addresses		

Set RM Activation and Toggle Delays
 File: H_CRP_AOC_D2AD.xls
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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
10.1.3		Verify Update via RM B Status Report		<input type="checkbox"/>
		Execute Telecommand Get RM-B status Command Parameter(s) : RMStat DF86Cmd AH841001 Enable 86 RMStat DD86Cmd AH842001 Enable 86 TC Control Flags : GBM IL DSE --Y -- --- Subsch. ID : 20 Det. descr. : TC(8,1) - Get RM-B status	ACZZ5109	
		Verify Packet Reception TM 8-6 for RM Status parametrized Packet Details: APID: 512 Type: 8 Subtype: 6 PI1: 41600 PI2: 1	A86_RMStatus	
		Verify Telemetry RMH_TGDLY0_MSB AEXU0109	= 137 <dec>	AND=ZAAM3999
		Verify Telemetry RMH_TGDLY0_LSB AEXU1109	= 2 <dec>	AND=ZAAM3999
		Verify Telemetry RMH_TDLY0 AD004109	54.9 ms	AND=ZAAM3999
		Verify Telemetry RMH_TGDLY1_MSB AEXU2109	= 137 <dec>	AND=ZAAM3999
		Verify Telemetry RMH_TGDLY1_LSB AEXU3109	= 2 <dec>	AND=ZAAM3999
		Verify Telemetry RMH_TGDLY1 AD002109	54.9 ms	AND=ZAAM3999
		Verify Telemetry RMH_ADLY_MSB AEXU4109	= 156 <dec>	AND=ZAAM3999
		Verify Telemetry RMH_ADLY_LSB AEXU5109	= 7 <dec>	AND=ZAAM3999
		Verify Telemetry RMH_ADLY AD003109	1996.9 ms	AND=ZAAM3999
10.1.4		Enable RM B		<input type="checkbox"/>

Set RM Activation and Toggle Delays
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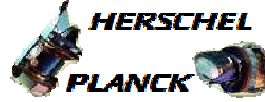
Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
		Execute Telecommand Ext_ACC_RM_B_Enable TC Control Flags : Subsch. ID : 10 Det. descr. : External ACC RM B Enable - Mission Specific GBM IL DSE --Y -- --	DCM24170	
		Verify Telemetry RMB_fromTTR-RMA AEE93050	= ENABLED	AND=ZAA07999
		Verify Telemetry RMB_fromTTR-RMB AEE94050	= ENABLED	AND=ZAA07999
<p>TC Seq. Name : HRAD2ADB (SetRmBactivDelay)</p> <p>TimeTag Type: N Sub Schedule ID: Formal Parameter List : Memory Data 32 MemData= Memory Checksum MemCrc=</p> <p style="text-align: right;"><dec> <dec></p>				
11		Set Activation Delay on RM B		Next Step: END
		This step allows the user to set a user-defined activation delay.		
11.1		Uplink Sequence HRAD2AD6		<input type="checkbox"/>
11.1.1		Disable RM B		<input type="checkbox"/>
		Execute Telecommand Ext_ACC_RM_B_Disable TC Control Flags : Subsch. ID : 10 Det. descr. : External ACC RM B Disable - Mission Specific GBM IL DSE --Y -- --	DCM25170	
		Verify Telemetry RMB_fromTTR-RMA AEE93050	= DISABLED	AND=ZAA07999
		Verify Telemetry RMB_fromTTR-RMB AEE94050	= DISABLED	AND=ZAA07999

Set RM Activation and Toggle Delays
 File: H_CRP_AOC_D2AD.xls
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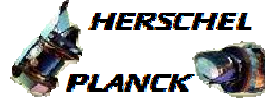
Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch															
11.1.2		Load Activation Delay Settings		<input type="checkbox"/>															
		<p>When loading this command sequence on the Manual Stack, it will ask you to enter values for the formal parameters inside the sequence. The formal parameters are:</p> <ul style="list-style-type: none"> - MemData = Alarm temporisation delay in the timing format prescribed at the back of this procedure. Storage of timing data uses two integer numbers: V, in the range 0 .. 255 and exponent E, in the range 0 .. 15. Time is then equal to $(V * 2^E + 1) * 0.1$ msec. For timing data stored in registers, the datastring to be loaded into the 32-bit register can be obtained as follows: register content = '0000' & V [hex] & E [hex], with V and E expressed as 1 byte hex strings. 																	
		<ul style="list-style-type: none"> - MemCrc = CRC-CCITT (0xFFFF) checksum calculated over the entire datastring described above. Do not forget the leading four zeros (!). To verify your own checksum calculation you could use the following webpage: http://www.lammertbies.nl/comm/info/crc-calculation.html?crc=0000b70e&method=hex 																	
		<p>Execute Telecommand</p> <p style="text-align: right;">Load Memory</p> <p>Command Parameter(s) :</p> <table style="width: 100%; border-collapse: collapse;"> <tr> <td style="text-align: right;">Memory ID</td> <td style="text-align: left;">AH6M0109</td> <td style="text-align: left;">00F0 <hex></td> </tr> <tr> <td style="text-align: right;">Start Address</td> <td style="text-align: left;">AH6M1109</td> <td style="text-align: left;">5888 <hex></td> </tr> <tr> <td style="text-align: right;">Length SAU</td> <td style="text-align: left;">AH6M2109</td> <td style="text-align: left;">4 <dec> (Def)</td> </tr> <tr> <td style="text-align: right;">Memory Data 32</td> <td style="text-align: left;">AH6M6109</td> <td style="text-align: left;">MemData</td> </tr> <tr> <td style="text-align: right;">Memory Checksum</td> <td style="text-align: left;">AH6M7109</td> <td style="text-align: left;">MemCrc</td> </tr> </table> <p>TC Control Flags :</p> <p style="text-align: right;">GBM IL DSE</p> <p style="text-align: right;">--Y -- ---</p> <p>Subsch. ID : 20 Det. descr. : TC(6,2) Load Memory Using Absolute Addresses</p>	Memory ID	AH6M0109	00F0 <hex>	Start Address	AH6M1109	5888 <hex>	Length SAU	AH6M2109	4 <dec> (Def)	Memory Data 32	AH6M6109	MemData	Memory Checksum	AH6M7109	MemCrc	AC062109	
Memory ID	AH6M0109	00F0 <hex>																	
Start Address	AH6M1109	5888 <hex>																	
Length SAU	AH6M2109	4 <dec> (Def)																	
Memory Data 32	AH6M6109	MemData																	
Memory Checksum	AH6M7109	MemCrc																	
11.1.3		Verify Update via RM B Status Report		<input type="checkbox"/>															

Set RM Activation and Toggle Delays
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 Author: dsalt-hp



Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
		Execute Telecommand Get RM-B status Command Parameter(s) : RMStat DF86Cmd AH841001 Enable 86 RMStat DD86Cmd AH842001 Enable 86 TC Control Flags : GBM IL DSE --Y -- --- Subsch. ID : 20 Det. descr. : TC(8,1) - Get RM-B status	ACZZ5109	
		Verify Packet Reception TM 8-6 for RM Status parametrized Packet Details: APID: 512 Type: 8 Subtype: 6 PI1: 41600 PI2: 1	A86_RMStatus	
		Verify Telemetry RMH_ADLY_MSB AEXU4109	<user defined setting>	AND=ZAAM3999
		Verify Telemetry RMH_ADLY_LSB AEXU5109	<user defined setting>	AND=ZAAM3999
		Verify Telemetry RMH_ADLY AD003109	<user defined setting>	AND=ZAAM3999
11.1.4		Enable RM B		<input type="checkbox"/>
		Execute Telecommand Ext_ACC_RM_B_Enable TC Control Flags : GBM IL DSE --Y -- --- Subsch. ID : 10 Det. descr. : External ACC RM B Enable - Mission Specific	DCM24170	
		Verify Telemetry RMB_fromTTR-RMA AEE93050	= ENABLED	AND=ZAA07999
		Verify Telemetry RMB_fromTTR-RMB AEE94050	= ENABLED	AND=ZAA07999

Set RM Activation and Toggle Delays
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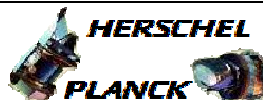
Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
TC Seq. Name : HRAD2ADC (SetRmBtoggleDelay0) TimeTag Type: N Sub Schedule ID: Formal Parameter List : Memory Data 32 MemData= <dec> Memory Checksum MemCrc= <dec>				
12		Set Toggle Delay 0 on RM B		Next Step: END
		This step allows the user to set a user-defined toggle delay 0.		
12.1		Uplink Sequence HRAD2AD7		<input type="checkbox"/>
12.1.1		Disable RM B		<input type="checkbox"/>
		Execute Telecommand <div style="text-align: right;">Ext_ACC_RM_B_Disable</div> TC Control Flags : <div style="text-align: right;">GBM IL DSE --Y -- ---</div> Subsch. ID : 10 Det. descr. : External ACC RM B Disable - Mission Specific	DCM25170	
		Verify Telemetry <div style="text-align: right;">RMB_fromTTR-RMA AEE93050</div>	= DISABLED	AND=ZAA07999
		Verify Telemetry <div style="text-align: right;">RMB_fromTTR-RMB AEE94050</div>	= DISABLED	AND=ZAA07999
12.1.2		Load Toggle Delay 0 Settings		<input type="checkbox"/>

Set RM Activation and Toggle Delays
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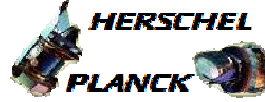
Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch															
		<p>When loading this command sequence on the Manual Stack, it will ask you to enter values for the formal parameters inside the sequence. The formal parameters are:</p> <ul style="list-style-type: none"> - MemData = Alarm temporisation delay in the timing format prescribed at the back of this procedure. Storage of timing data uses two integer numbers: V, in the range 0 .. 255 and exponent E, in the range 0 .. 15. Time is then equal to $(V * 2^E + 1) * 0.1$ msec. For timing data stored in registers, the datastring to be loaded into the 32-bit register can be obtained as follows: register content = '0000' & V [hex] & E [hex], with V and E expressed as 1 byte hex strings. 																	
		<ul style="list-style-type: none"> - MemCrc = CRC-CCITT (0xFFFF) checksum calculated over the entire datastring described above. Do not forget the leading four zeros (!). To verify your own checksum calculation you could use the following webpage: http://www.lammertbies.nl/comm/info/crc-calculation.html?crc=0000b70e&method=hex 																	
		<p>Execute Telecommand</p> <p style="text-align: right;">Load Memory</p> <p>Command Parameter(s) :</p> <table style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 30%;">Memory ID</td> <td style="width: 20%;">AH6M0109</td> <td style="width: 50%;">00F0 <hex></td> </tr> <tr> <td>Start Address</td> <td>AH6M1109</td> <td>5880 <hex></td> </tr> <tr> <td>Length SAU</td> <td>AH6M2109</td> <td>4 <dec> (Def)</td> </tr> <tr> <td>Memory Data 32</td> <td>AH6M6109</td> <td>MemData</td> </tr> <tr> <td>Memory Checksum</td> <td>AH6M7109</td> <td>MemCrc</td> </tr> </table> <p>TC Control Flags :</p> <p style="text-align: right;">GBM IL DSE --Y -- ---</p> <p>Subsch. ID : 20 Det. descr. : TC(6,2) Load Memory Using Absolute Addresses</p>	Memory ID	AH6M0109	00F0 <hex>	Start Address	AH6M1109	5880 <hex>	Length SAU	AH6M2109	4 <dec> (Def)	Memory Data 32	AH6M6109	MemData	Memory Checksum	AH6M7109	MemCrc	AC062109	
Memory ID	AH6M0109	00F0 <hex>																	
Start Address	AH6M1109	5880 <hex>																	
Length SAU	AH6M2109	4 <dec> (Def)																	
Memory Data 32	AH6M6109	MemData																	
Memory Checksum	AH6M7109	MemCrc																	
12.1.3		Verify Update via RM B Status Report		□															

Set RM Activation and Toggle Delays
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 Author: dsalt-hp



Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
		Execute Telecommand <p style="text-align: right;">Get RM-B status</p> <i>Command Parameter(s) :</i> RMStat DF86Cmd AH841001 Enable 86 RMStat DD86Cmd AH842001 Enable 86 <i>TC Control Flags :</i> GBM IL DSE --Y -- --- <i>Subsch. ID : 20</i> <i>Det. descr. : TC(8,1) - Get RM-B status</i>	ACZZ5109	
		Verify Packet Reception <p style="text-align: center;">TM 8-6 for RM Status parametrized</p> <i>Packet Details:</i> APID: 512 Type: 8 Subtype: 6 PI1: 41600 PI2: 1	A86_RMStatus	
		Verify Telemetry <p style="text-align: center;">RMH_TGDLY0_MSB AEXU0109</p>	<user defined setting>	AND=ZAAM3999
		Verify Telemetry <p style="text-align: center;">RMH_TGDLY0_LSB AEXU1109</p>	<user defined setting>	AND=ZAAM3999
		Verify Telemetry <p style="text-align: center;">RMH_TGDLY0 AD001109</p>	<user defined setting>	AND=ZAAM3999
12.1.4		<i>Enable RM B</i>		<input type="checkbox"/>
		Execute Telecommand <p style="text-align: right;">Ext_ACC_RM_B_Enable</p> <i>TC Control Flags :</i> GBM IL DSE --Y -- --- <i>Subsch. ID : 10</i> <i>Det. descr. : External ACC RM B Enable - Mission Specific</i>	DCM24170	
		Verify Telemetry <p style="text-align: center;">RMB_fromTTR-RMA AEE93050</p>	= ENABLED	AND=ZAA07999
		Verify Telemetry <p style="text-align: center;">RMB_fromTTR-RMB AEE94050</p>	= ENABLED	AND=ZAA07999

Set RM Activation and Toggle Delays
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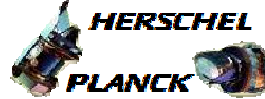
Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
TC Seq. Name : HRAD2ADD (SetRmBtoggleDelay1) TimeTag Type: N Sub Schedule ID: Formal Parameter List : Memory Data 32 MemData= <dec> Memory Checksum MemCrc= <dec>				
13		Set Toggle Delay 1 on RM B		Next Step: END
		This step allows the user to set a user-defined toggle delay 1.		
13.1		Uplink Sequence HRAD2AD8		<input type="checkbox"/>
13.1.1		Disable RM B		<input type="checkbox"/>
		Execute Telecommand <div style="text-align: right;">Ext_ACC_RM_B_Disable</div> TC Control Flags : <div style="text-align: right;">GBM IL DSE --Y -- ---</div> Subsch. ID : 10 Det. descr. : External ACC RM B Disable - Mission Specific	DCM25170	
		Verify Telemetry <div style="text-align: right;">RMB_fromTTR-RMA AEE93050</div>	= DISABLED	AND=ZAA07999
		Verify Telemetry <div style="text-align: right;">RMB_fromTTR-RMB AEE94050</div>	= DISABLED	AND=ZAA07999
13.1.2		Load Toggle Delay 1 Settings		<input type="checkbox"/>

Set RM Activation and Toggle Delays
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 Author: dsalt-hp



Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch															
		<p>When loading this command sequence on the Manual Stack, it will ask you to enter values for the formal parameters inside the sequence. The formal parameters are:</p> <ul style="list-style-type: none"> - MemData = Alarm temporisation delay in the timing format prescribed at the back of this procedure. Storage of timing data uses two integer numbers: V, in the range 0 .. 255 and exponent E, in the range 0 .. 15. Time is then equal to $(V * 2^E + 1) * 0.1$ msec. For timing data stored in registers, the datastring to be loaded into the 32-bit register can be obtained as follows: register content = '0000' & V [hex] & E [hex], with V and E expressed as 1 byte hex strings. 																	
		<ul style="list-style-type: none"> - MemCrc = CRC-CCITT (0xFFFF) checksum calculated over the entire datastring described above. Do not forget the leading four zeros (!). To verify your own checksum calculation you could use the following webpage: http://www.lammertbies.nl/comm/info/crc-calculation.html?crc=0000b70e&method=hex 																	
		<p>Execute Telecommand</p> <p style="text-align: right;">Load Memory</p> <p>Command Parameter(s) :</p> <table style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 30%;">Memory ID</td> <td style="width: 30%;">AH6M0109</td> <td style="width: 40%;">00F0 <hex></td> </tr> <tr> <td>Start Address</td> <td>AH6M1109</td> <td>5884 <hex></td> </tr> <tr> <td>Length SAU</td> <td>AH6M2109</td> <td>4 <dec> (Def)</td> </tr> <tr> <td>Memory Data 32</td> <td>AH6M6109</td> <td>MemData</td> </tr> <tr> <td>Memory Checksum</td> <td>AH6M7109</td> <td>MemCrc</td> </tr> </table> <p>TC Control Flags :</p> <p style="text-align: right;">GBM IL DSE --Y -- ---</p> <p>Subsch. ID : 20 Det. descr. : TC(6,2) Load Memory Using Absolute Addresses</p>	Memory ID	AH6M0109	00F0 <hex>	Start Address	AH6M1109	5884 <hex>	Length SAU	AH6M2109	4 <dec> (Def)	Memory Data 32	AH6M6109	MemData	Memory Checksum	AH6M7109	MemCrc	AC062109	
Memory ID	AH6M0109	00F0 <hex>																	
Start Address	AH6M1109	5884 <hex>																	
Length SAU	AH6M2109	4 <dec> (Def)																	
Memory Data 32	AH6M6109	MemData																	
Memory Checksum	AH6M7109	MemCrc																	
13.1.3		Verify Update via RM B Status Report		□															

Set RM Activation and Toggle Delays
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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
		Execute Telecommand Get RM-B status Command Parameter(s) : RMStat DF86Cmd AH841001 RMStat DD86Cmd AH842001 TC Control Flags : Subsch. ID : 20 Det. descr. : TC(8,1) - Get RM-B status GBM IL DSE --Y -- ---	ACZZ5109 Enable 86 Enable 86	
		Verify Packet Reception TM 8-6 for RM Status parametrized Packet Details: APID: 512 Type: 8 Subtype: 6 PI1: 41600 PI2: 1	A86_RMStatus	
		Verify Telemetry RMH_TGDLY1_MSB AEXU2109	<user defined setting>	AND=ZAAM3999
		Verify Telemetry RMH_TGDLY1_LSB AEXU3109	<user defined setting>	AND=ZAAM3999
		Verify Telemetry RMH_TGDLY1 AD002109	<user defined setting>	AND=ZAAM3999
13.1.4		Enable RM B		<input type="checkbox"/>
		Execute Telecommand Ext_ACC_RM_B_Enable TC Control Flags : Subsch. ID : 10 Det. descr. : External ACC RM B Enable - Mission Specific GBM IL DSE --Y -- ---	DCM24170	
		Verify Telemetry RMB_fromTTR-RMA AEE93050	= ENABLED	AND=ZAA07999
		Verify Telemetry RMB_fromTTR-RMB AEE94050	= ENABLED	AND=ZAA07999
End of Procedure				

Set RM Activation and Toggle Delays
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Tables & Figures

Register name	Register address RMA (hex)	Register address RMB (hex)	Usage	Default time (msec)	Binary representation (hex)
<i>ADLY</i>	00C05888	00F05888	Activation delay	1996.9	9C07
<i>TGDLY0</i>	00C05880	00F05880	Toggle delay #1	50.1 (RMA) 54.9 (RMB)	FA01 (RMA) 8902 (RMB)
<i>TGDLY1</i>	00C05884	00F05884	Toggle delay #2	60.1 (RMA) 54.9 (RMB)	9602 (RMA) 8902 (RMB)
<i>TDLY0</i>	00C05900	00F05900	Temporisation delay #1 (WD toggle)	998.5	9C06
<i>TDLY1</i>	00C05904	00F05904	Temporisation delay #2 (CRS1)	249.7	9C04
<i>TDLY2</i>	00C05908	00F05908	Temporisation delay #3 (CRS2)	249.7	9C04
<i>TDLY3</i>	00C0590C	00F0590C	Unused (CRS3 on Planck)	249.7	9C04
<i>TDLY4</i>	00C05910	00F05910	Temporisation delay #5 (AAD1)	249.7	9C04
<i>TDLY5</i>	00C05914	00F05914	Temporisation delay #6 (AAD2)	249.7	9C04
<i>TDLY6</i>	00C05918	00F05918	Temporisation delay #7 (Sep. strap 1)	29900.9	920B
<i>TDLY7</i>	00C0591C	00F0591C	Temporisation delay #8 (Sep. strap 2)	299827.3	B70E

Timing format

The storage of timing data always uses two integer numbers: a value, V, in the range 0 .. 255 and an exponent, E, in the range 0 .. 15. The actual timing is derived from these two numbers using the formula:

$$\text{Time} = (V * 2^E + 1) * 0.1 \text{ msec.}$$

This format allows the representation of timing values in the range from 0.1 msec to 836 seconds.

For timing data stored in registers, the integer number loaded into the register can be obtained as follows:

$$\text{Register} = (V \ll 8) + E,$$

where \ll represents a bit-wise left shift. The bit layout for retry delays is different to allow more efficient packing of bits in PAP tables.

Conversion to RM format

For any value T, representing time in milliseconds, the value and exponent for the representation used by the RM can be obtained as follows:

$$E = \max(\text{floor}(\log_2((T / 0.1 - 1) / 255)), 0)$$

$$V = \text{floor}((T / 0.1 - 1) / 2^E)$$

A check must obviously be applied that the numbers remain in the ranges specified above. The choice of value and exponent is not unique, and the algorithm above is optimised to achieve the best resolution (lowest exponent).