

Set/Clear Power-on-reset Register  
File: H\_CRP\_AOC\_2POR.xls  
Author: dsalt-hp



## Procedure Summary

### Objectives

The procedure specifies the steps to necessary to either set of clear the contents of the Power-On Reset Register of the CROME ASIC. The register is used as memory external to the PM in which the cold/warm start status of the ACC is stored. The contents are cleared when the ACC is powered on. This allows the BSW to recognise a cold start of the ACC during its initialisation which in turn causes the BSW to execute a destructive check of the SGM and initialise the contents of all SGM location with the default value (0x5A5A5A5A). This operation is carried out independently for each RM. In the nominal case, the BSW sets the PORR LSB at the end of its initialisation so that during successive initialisations will be executed as warm starts without the destructive SGM check. If a write error is encountered during the SGM check, the BSW does not set the PORR bit on the RM for which the error has occurred. the procedure below can then be executed from ground in order to set the PORR bit and make sure the BSW will init

#### USAGE

The procedure can be used to set the PORR bit, which has remained cleared during the cold start because of a write error found by the BSW during the initial destructive check of the SGM. The option to clear PORR is included for completeness, but is not currently used in any contingency recoveries.

### Summary of Constraints

### Spacecraft Configuration

Start of Procedure

End of Procedure

### Reference File(s)

#### Input Command Sequences

HFADRM1  
HFADRM2

#### Output Command Sequences

NULLSEQ0  
HRA2POR1  
HRA2POR2

### Referenced Displays

ANDs      GRDs      SLDs

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ZAA07999  
 ZAAM0999

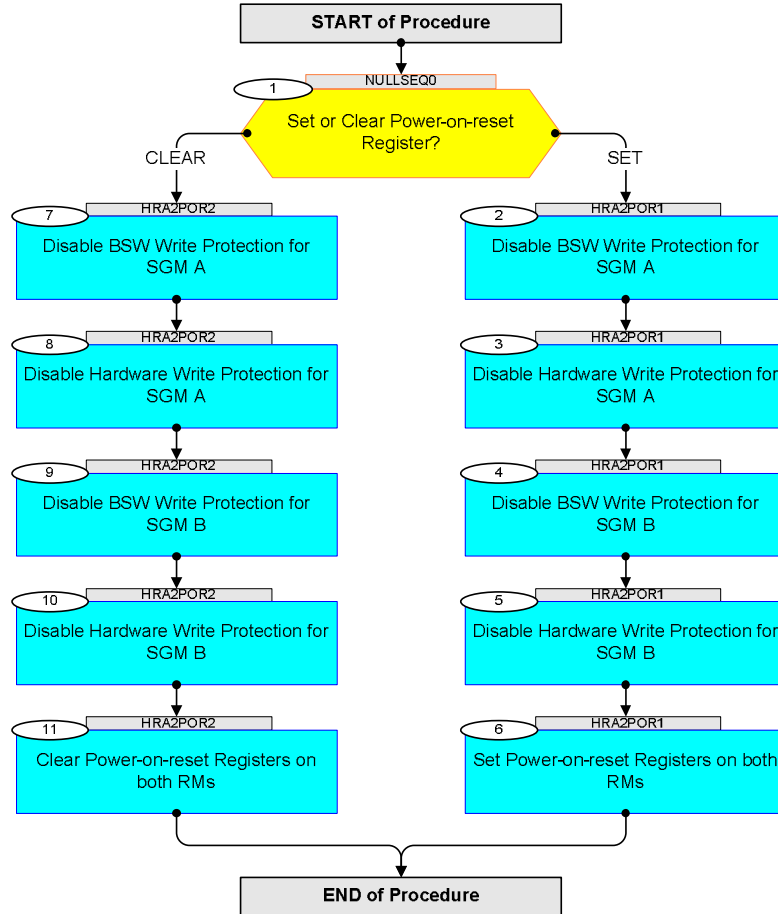
**Configuration Control Information**

DATE	FOP ISSUE	VERSION	MODIFICATION DESCRIPTION	AUTHOR	SPR REF
24/03/09		1	Created	dsalt-hp	
24/03/09	2.2	2	Sequence name correction	dsalt-hp	
07/10/09	2.5	3	Updated to include TCs to disable SGM write protection (ASW4.0 only)	dsalt-hp	

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## Procedure Flowchart Overview



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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
<b>Beginning of Procedure</b>				
TC Seq. Name : NULLSEQ0 ( )				
TimeTag Type: B				
Sub Schedule ID:				
□				
1		Set or Clear Power-on-reset Register?		Next Step: SET 2 CLEAR 7
1.1		Verify current register setting in ASW "Power On" means CLEAR (= 0 <raw>) "Warm Start" means SET (= 1 <raw>)		□
		Verify Telemetry <b>PwrOnResetRegA</b> <b>AEG41050</b>	<to be read>	AND=ZAA07999
		Verify Telemetry <b>PwrOnResetRegB</b> <b>AEG42050</b>	<to be read>	AND=ZAA07999
1.2		Load sequence HFADRM1 on the Manual Stack and uplink the single command inside the sequence		□
		Execute Sequence <b>HFADRM1 GetRmAstatusReport v02</b> Sequence Grouping = -  This Sequence Reference is not included in the generated sequence SSID : 0		SEQ
1.3		Verify current register setting in RM A		□
		Verify Telemetry <b>PwrOnResetRegA</b> <b>AEW3Y109</b>	<to be read>	AND=ZAAM0999
		Verify Telemetry <b>PwrOnResetRegB</b> <b>AEW3Z109</b>	<to be read>	AND=ZAAM0999
1.4		Load sequence HFADRM2 on the Manual Stack and uplink the single command inside the sequence		□
		Execute Sequence <b>HFADRM2 GetRmBstatusReport v02</b> Sequence Grouping = -  This Sequence Reference is not included in the generated sequence SSID : 0		SEQ

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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
1.5		Verify current register setting in RM B		<input type="checkbox"/>
		Verify Telemetry PwrOnResetRegA                      AEW3Y109	<to be read>	AND=ZAAM0999
		Verify Telemetry PwrOnResetRegB                      AEW3Z109	<to be read>	AND=ZAAM0999
1.6		Set or Clear Power-on-reset Register?		<input type="checkbox"/>
		SET     -> GO TO STEP 2  CLEAR  -> GO TO STEP 3		
<p>TC Seq. Name :HRA2POR1 (SetPowerOnResetRegs)</p> <p>TimeTag Type: N Sub Schedule ID:</p> <p><input type="checkbox"/></p>				
2		Disable BSW Write Protection for SGM A		Next Step: 3
2.1		Uplink next block of commands (commands valid for ASW V4B002 only)		<input type="checkbox"/>
		Command below sets the write protection words for both SGM's to 0 (disabled). Only the first word will retain its value, since the status of SGMB, which remains in use by the ASW, is autonomously modified during the updating of the Sun vector. Note that the instantiated command is currently valid only for ASW V4B002		
	ET=+ UT=+00.00.00	Execute Telecommand  TC Control Flags :  Subsch. ID : 20 Det. descr. : TC(6,2) Load Memory Using Absolute Addresses - Write SGM Locked	WritesGM_Locked  GBM IL DSE --Y -- --	ACZ80109

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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch										
		<p>Use generic TC(6,5) to dump variable SgmLocked from RAM. Address of SgmLocked depends on the software version, and can be found in image.syms file of the software build (under \ACMS\ASW_3.4_b2\Code\OBSP_3_4\B002\AAE\image.syms). The length should be 8 bytes.</p> <p>The 1st 32bit word in that memory area is the write protection status of SGM A, and the 2nd 32 word is that of SGM B</p>												
	ET=+ UT=+00.00.05	<p>Execute Telecommand</p> <p style="text-align: center;"><b>Dump Memory</b></p> <p>Command Parameter(s) :</p> <table style="margin-left: auto; margin-right: auto;"> <tr> <td style="padding-right: 20px;">Memory ID</td> <td>AH6M0109</td> <td style="padding-right: 20px;">020E &lt;hex&gt;</td> </tr> <tr> <td>Start Address</td> <td>AH6M1109</td> <td>7A90 &lt;hex&gt;</td> </tr> <tr> <td>Length SAU</td> <td>AH6M3109</td> <td>8 &lt;dec&gt;</td> </tr> </table> <p>Subsch. ID : 20          Det. descr. : TC(6,5) Dump Memory Using Absolute Addresses</p>	Memory ID	AH6M0109	020E <hex>	Start Address	AH6M1109	7A90 <hex>	Length SAU	AH6M3109	8 <dec>	AC063109		
Memory ID	AH6M0109	020E <hex>												
Start Address	AH6M1109	7A90 <hex>												
Length SAU	AH6M3109	8 <dec>												
2.2		Verify write protection disable (the first 32 bit word in the dump area should be 0)		<input type="checkbox"/>										
		<p>Verify Packet Reception</p> <p style="text-align: center;"><b>Memory Dump - Absolute Addresses - SAU 8</b></p> <p>Packet Details:</p> <table style="margin-left: auto; margin-right: auto;"> <tr> <td style="padding-right: 20px;">APID:</td> <td>512</td> </tr> <tr> <td>Type:</td> <td>6</td> </tr> <tr> <td>Subtype:</td> <td>6</td> </tr> <tr> <td>PI1:</td> <td></td> </tr> <tr> <td>PI2:</td> <td></td> </tr> </table>	APID:	512	Type:	6	Subtype:	6	PI1:		PI2:		MemDmpAbsAdd	
APID:	512													
Type:	6													
Subtype:	6													
PI1:														
PI2:														
		<p>Verify Packet Telemetry</p> <table style="margin-left: auto; margin-right: auto;"> <tr> <td style="padding-right: 20px;">Memory_ID</td> <td>AE060070</td> <td>= 020E &lt;hex&gt;</td> </tr> </table>	Memory_ID	AE060070	= 020E <hex>									
Memory_ID	AE060070	= 020E <hex>												
		<p>Verify Packet Telemetry</p> <table style="margin-left: auto; margin-right: auto;"> <tr> <td style="padding-right: 20px;">Start_Address</td> <td>AE061070</td> <td>= 7A90 &lt;hex&gt;</td> </tr> </table>	Start_Address	AE061070	= 7A90 <hex>									
Start_Address	AE061070	= 7A90 <hex>												
		<p>Verify Packet Telemetry</p> <table style="margin-left: auto; margin-right: auto;"> <tr> <td style="padding-right: 20px;">Dumped_Byte</td> <td>AE063070</td> <td>= 0000xxxx &lt;hex&gt;</td> </tr> </table>	Dumped_Byte	AE063070	= 0000xxxx <hex>									
Dumped_Byte	AE063070	= 0000xxxx <hex>												
3		Disable Hardware Write Protection for SGM A		Next Step: 4										
3.1		Uplink next block of commands (tagged with release times)		<input type="checkbox"/>										

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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
		Command below disables the hardware write protection of SGMA applied by the RM. The write protection is controlled by a bit in the PIMWriteDisable register of the CROME ASIC.		
	ET=+ UT=+00.00.00	Execute Telecommand  <b>PIMwriteDisClear_RMA</b>  TC Control Flags :  Subsch. ID : 20 Det. descr. : TC(6,2) Load Mem Using Abs Address - PIM Write Disable Clear RMA  GBM IL DSE --Y -- --	ACZ82109	
	ET=+ UT=+00.00.05	Execute Telecommand  <b>DumpPIMdisClear_RMA</b>  Subsch. ID : 20 Det. descr. : TC(6,5) Dump Mem Using Abs Address - Dump PIM Disable Clear RMA	ACZ78109	
3.2		Verify write protection disable (bit 3 (with LSB as bit 1) in the dumped word (32 bits) must be 0)		<input type="checkbox"/>
		Verify Packet Reception <b>Memory Dump - Absolute Addresses - SAU 8</b> Packet Details:  APID: 512 Type: 6 Subtype: 6 PI1: PI2:	MemDmpAbsAdd	
		Verify Packet Telemetry <b>Memory_ID</b> AE060070	= 020E <hex>	
		Verify Packet Telemetry <b>Start_Address</b> AE061070	= 7A90 <hex>	
		Verify Packet Telemetry <b>Dumped_Byte</b> AE063070	= xx0x xxxx xxxx xxxx xxxx xxxx xxxx xxxx <bin>	
4		Disable BSW Write Protection for SGM B		Next Step: 5
4.1		Uplink next block of commands (commands valid for ASW V4B002 only)		<input type="checkbox"/>

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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch										
		<p>Command below sets the write protection words for both SGM's to 0 (disabled). Only the second word will retain its value, since the status of SGMA, which remains in use by the ASW, is autonomously modified during the updating of the Sun vector. Note that the instantiated command is currently valid only for ASW V4B002</p>												
	ET=+ UT=+00.00.00	<p>Execute Telecommand</p> <p style="text-align: right;"><b>WritesGM_Locked</b></p> <p>TC Control Flags :</p> <p style="text-align: right;"><b>GBM IL DSE</b> --Y -- ---</p> <p>Subsch. ID : 20            Det. descr. : TC(6,2) Load Memory Using Absolute Addresses - Write SGM Locked</p>	ACZ80109											
		<p>Use generic TC(6,5) to dump variable SgmLocked from RAM. Address of SgmLocked depends on the software version, and can be found in image.syms file of the software build (under \ACMS\ASW_3.4_b2\Code\OBSP_3_4\B002\AAE\image.syms). The length should be 8 bytes.</p> <p>The 1st 32bit word in that memory area is the write protection status of SGM A, and the 2nd 32 word is that of SGM B</p>												
	ET=+ UT=+00.00.05	<p>Execute Telecommand</p> <p style="text-align: right;"><b>Dump Memory</b></p> <p>Command Parameter(s) :</p> <table style="width: 100%; border: none;"> <tr> <td style="text-align: right;">Memory ID</td> <td>AH6M0109</td> <td style="text-align: right;">020E &lt;hex&gt;</td> </tr> <tr> <td style="text-align: right;">Start Address</td> <td>AH6M1109</td> <td style="text-align: right;">7A90 &lt;hex&gt;</td> </tr> <tr> <td style="text-align: right;">Length SAU</td> <td>AH6M3109</td> <td style="text-align: right;">8 &lt;dec&gt;</td> </tr> </table> <p>Subsch. ID : 20            Det. descr. : TC(6,5) Dump Memory Using Absolute Addresses</p>	Memory ID	AH6M0109	020E <hex>	Start Address	AH6M1109	7A90 <hex>	Length SAU	AH6M3109	8 <dec>	AC063109		
Memory ID	AH6M0109	020E <hex>												
Start Address	AH6M1109	7A90 <hex>												
Length SAU	AH6M3109	8 <dec>												
4.2		<p>Verify write protection disable            (the second 32 bit word in the dump area should be 0)</p>		□										
		<p>Verify Packet Reception</p> <p style="text-align: center;"><b>Memory Dump - Absolute Addresses - SAU 8</b></p> <p>Packet Details:</p> <table style="width: 100%; border: none;"> <tr> <td style="text-align: right;">APID:</td> <td>512</td> </tr> <tr> <td style="text-align: right;">Type:</td> <td>6</td> </tr> <tr> <td style="text-align: right;">Subtype:</td> <td>6</td> </tr> <tr> <td style="text-align: right;">PI1:</td> <td></td> </tr> <tr> <td style="text-align: right;">PI2:</td> <td></td> </tr> </table>	APID:	512	Type:	6	Subtype:	6	PI1:		PI2:		MemDmpAbsAdd	
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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
		Verify Packet Telemetry Memory_ID AE060070	= 020E <hex>	
		Verify Packet Telemetry Start_Address AE061070	= 7A90 <hex>	
		Verify Packet Telemetry Dumped_Byte AE063070	= xxxx0000 <hex>	
5		Disable Hardware Write Protection for SGM B		Next Step: 6
5.1		Uplink next block of commands (tagged with release times)		<input type="checkbox"/>
		Command below disables the hardware write protection of SGMB applied by the RM. The write protection is controlled by a bit in the PIMwriteDisable register of the CROME ASIC.		
	ET=+ UT=+00.00.00	Execute Telecommand PIMwriteDisClear_RMB  TC Control Flags : GBM IL DSE --Y -- --  Subsch. ID : 20 Det. descr. : TC(6,2) Load Mem Using Abs Address - PIM Write Disable Clear RMB	ACZ83109	
	ET=+ UT=+00.00.05	Execute Telecommand DumpPIMdisClear_RMB  Subsch. ID : 20 Det. descr. : TC(6,5) Dump Mem Using Abs Address - Dump PIM Disable Clear RMB	ACZ79109	
5.2		Verify write protection disable (bit 3 (with LSB as bit 1) in the dumped word (32 bits) must be 0)		<input type="checkbox"/>
		Verify Packet Reception Memory Dump - Absolute Addresses - SAU 8 Packet Details: APID: 512 Type: 6 Subtype: 6 PI1: PI2:	MemDmpAbsAdd	
		Verify Packet Telemetry Memory_ID AE060070	= 020E <hex>	

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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
		Verify Packet Telemetry Start_Address AE061070	= 7A90 <hex>	
		Verify Packet Telemetry Dumped_Byte AE063070	= xx0x xxxx xxxx xxxx xxxx xxxx xxxx xxxx <bin>	
6		Set Power-on-reset Registers on both RMs		Next Step: END
6.1		Uplink 1st block of commands (tagged with release times)		□
	ET=+ UT=+00.00.00	Execute Telecommand Load Memory Command Parameter(s) : Memory ID AH6M0109 Start Address AH6M1109 Length SAU AH6M2109 Memory Data 32 AH6M6109 Memory Checksum AH6M7109 TC Control Flags : GBM IL DSE --Y -- -- Subsch. ID : 20 Det. descr. : TC(6,2) Load Memory Using Absolute Addresses	AC062109  00C0 <hex> 0890 <hex> 4 <dec> (Def) 00000001 <hex> 94E1 <hex>	
	ET=+ UT=+00.00.05	Execute Telecommand Load Memory Command Parameter(s) : Memory ID AH6M0109 Start Address AH6M1109 Length SAU AH6M2109 Memory Data 32 AH6M6109 Memory Checksum AH6M7109 Subsch. ID : 20 Det. descr. : TC(6,2) Load Memory Using Absolute Addresses	AC062109  00F0 <hex> 0890 <hex> 4 <dec> (Def) 00000001 <hex> 94E1 <hex>	
	ET=+ UT=+00.00.05	Execute Telecommand Get RM-A status Subsch. ID : 20 Det. descr. : TC(8,1) - Get RM-A status	ACZZ4109	
6.2		Verify update in ASW and RM A		□
		Verify Telemetry PwrOnResetRegA AEG41050	= Warm Start	AND=ZAA07999

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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
		Verify Telemetry PwrOnResetRegB AEG42050	= Warm Start	AND=ZAA07999
		Verify Telemetry PwrOnResetRegA AEW3Y109	= 1 <dec>	AND=ZAAM0999
		Verify Telemetry PwrOnResetRegB AEW3Z109	= 1 <dec>	AND=ZAAM0999
6.3		Uplink next command		<input type="checkbox"/>
		Execute Telecommand Get RM-B status TC Control Flags : GBM IL DSE --Y -- --- Subsch. ID : 20 Det. descr. : TC(8,1) - Get RM-B status	ACZZ5109	
6.4		Verify update in RM B		<input type="checkbox"/>
		Verify Telemetry PwrOnResetRegA AEW3Y109	= 1 <dec>	AND=ZAAM0999
		Verify Telemetry PwrOnResetRegB AEW3Z109	= 1 <dec>	AND=ZAAM0999
TC Seq. Name :HRA2POR2 (ClearPowerOnResetRegs)				
TimeTag Type: N Sub Schedule ID: <input type="checkbox"/>				
7		Disable BSW Write Protection for SGM A		Next Step: 8
7.1		Uplink next block of commands (commands valid for ASW V4B002 only)		<input type="checkbox"/>
		Command below sets the write protection words for both SGM's to 0 (disabled). Only the first word will retain its value, since the status of SGMB, which remains in use by the ASW, is autonomously modified during the updating of the Sun vector. Note that the instantiated command is currently valid only for ASW V4B002		

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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
	ET=+ UT=+00.00.00	Execute Telecommand  <b>WriteSGM_Locked</b>  <i>TC Control Flags :</i>  Subsch. ID : 20 Det. descr. : TC(6,2) Load Memory Using Absolute Addresses - Write SGM Locked  GBM IL DSE --Y -- --	ACZ80109	
		<i>Use generic TC(6,5) to dump variable SgmLocked from RAM. Address of SgmLocked depends on the software version, and can be found in image.syms file of the software build (under \ACMS\ASW_3.4_b2\Code\OBSP_3_4\B002\AAE\image.syms). The length should be 8 bytes.</i>  <i>The 1st 32bit word in that memory area is the write protection status of SGM A, and the 2nd 32 word is that of SGM B</i>		
	ET=+ UT=+00.00.05	Execute Telecommand  <b>Dump Memory</b>  <i>Command Parameter(s) :</i> Memory ID                   AH6M0109 Start Address                AH6M1109 Length SAU                   AH6M3109  Subsch. ID : 20 Det. descr. : TC(6,5) Dump Memory Using Absolute Addresses	AC063109  020E <hex> 7A90 <hex> 8 <dec>	
7.2		Verify write protection disable (the first 32 bit word in the dump area should be 0)		<input type="checkbox"/>
		Verify Packet Reception <b>Memory Dump - Absolute Addresses - SAU 8</b> <i>Packet Details:</i>  APID: Type: Subtype: PI1: PI2:	MemDmpAbsAdd  512 6 6	
		Verify Packet Telemetry <b>Memory_ID</b> AE060070	= 020E <hex>	
		Verify Packet Telemetry <b>Start_Address</b> AE061070	= 7A90 <hex>	
		Verify Packet Telemetry <b>Dumped_Byte</b> AE063070	= 0000xxxx <hex>	

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Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
8		Disable Hardware Write Protection for SGM A		Next Step: 9
8.1		Uplink next block of commands (tagged with release times)		<input type="checkbox"/>
		Command below disables the hardware write protection of SGMA applied by the RM. The write protection is controlled by a bit in the PIMWriteDisable register of the CROME ASIC.		
	ET=+ UT=+00.00.00	Execute Telecommand  <b>PIMwriteDisClear_RMA</b>  TC Control Flags :  <b>GBM IL DSE</b> <b>--Y -- ---</b>  Subsch. ID : 20 Det. descr. : TC(6,2) Load Mem Using Abs Address - PIM Write Disable Clear RMA	<b>ACZ82109</b>	
	ET=+ UT=+00.00.05	Execute Telecommand  <b>DumpPIMdisClear_RMA</b>  Subsch. ID : 20 Det. descr. : TC(6,5) Dump Mem Using Abs Address - Dump PIM Disable Clear RMA	<b>ACZ78109</b>	
8.2		Verify write protection disable (bit 3 (with LSB as bit 1) in the dumped word (32 bits) must be 0)		<input type="checkbox"/>
		Verify Packet Reception <b>Memory Dump - Absolute Addresses - SAU 8</b> Packet Details:  <b>APID:</b> <b>Type:</b> <b>Subtype:</b> <b>PI1:</b> <b>PI2:</b>	<b>MemDmpAbsAdd</b>  <b>512</b> <b>6</b> <b>6</b>	
		Verify Packet Telemetry <b>Memory_ID</b> <b>AE060070</b>	<b>= 020E &lt;hex&gt;</b>	
		Verify Packet Telemetry <b>Start_Address</b> <b>AE061070</b>	<b>= 7A90 &lt;hex&gt;</b>	
		Verify Packet Telemetry <b>Dumped_Byte</b> <b>AE063070</b>	<b>= xx0x xxxx</b> <b>xxxx xxxx</b> <b>xxxx xxxx</b> <b>xxxx xxxx &lt;bin&gt;</b>	
9		Disable BSW Write Protection for SGM B		Next Step: 10

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9.1		Uplink next block of commands (commands valid for ASW V4B002 only)		<input type="checkbox"/>
		Command below sets the write protection words for both SGM's to 0 (disabled). Only the second word will retain its value, since the status of SGMA, which remains in use by the ASW, is autonomously modified during the updating of the Sun vector. Note that the instantiated command is currently valid only for ASW V4B002		
	ET=+ UT=+00.00.00	Execute Telecommand  TC Control Flags :  Subsch. ID : 20 Det. descr. : TC(6,2) Load Memory Using Absolute Addresses - Write SGM Locked	WriteSGM_Locked  ACZ80109	
		Use generic TC(6,5) to dump variable SgmLocked from RAM. Address of SgmLocked depends on the software version, and can be found in image.syms file of the software build (under \ACMS\ASW_3.4_b2\Code\OBSP_3_4\B002\AAE\image.syms). The length should be 8 bytes.  The 1st 32bit word in that memory area is the write protection status of SGM A, and the 2nd 32 word is that of SGM B		
	ET=+ UT=+00.00.05	Execute Telecommand  Command Parameter(s) : Memory ID AH6M0109 Start Address AH6M1109 Length SAU AH6M3109  Subsch. ID : 20 Det. descr. : TC(6,5) Dump Memory Using Absolute Addresses	Dump Memory  AC063109	
9.2		Verify write protection disable (the second 32 bit word in the dump area should be 0)		<input type="checkbox"/>

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		Verify Packet Reception <b>Memory Dump - Absolute Addresses - SAU 8</b> Packet Details: <div style="text-align: right; margin-right: 20px;">             APID: 512              Type: 6              Subtype: 6              PI1:              PI2:           </div>	MemDmpAbsAdd	
		Verify Packet Telemetry <b>Memory_ID</b> AE060070	= 020E <hex>	
		Verify Packet Telemetry <b>Start_Address</b> AE061070	= 7A90 <hex>	
		Verify Packet Telemetry <b>Dumped_Byte</b> AE063070	= xxxx0000 <hex>	
10		Disable Hardware Write Protection for SGM B		Next Step: 11
10.1		Uplink next block of commands (tagged with release times)		<input type="checkbox"/>
		Command below disables the hardware write protection of SGMB applied by the RM. The write protection is controlled by a bit in the PIMWriteDisable register of the CROME ASIC.		
	ET=+ UT=+00.00.00	Execute Telecommand <b>PIMwriteDisClear_RMB</b> TC Control Flags : <div style="text-align: right; margin-right: 20px;">             GBM IL DSE              --Y -- ---           </div> Subsch. ID : 20 Det. descr. : TC(6,2) Load Mem Using Abs Address - PIM Write Disable Clear RMB	ACZ83109	
	ET=+ UT=+00.00.05	Execute Telecommand <b>DumpPIMdisClear_RMB</b> Subsch. ID : 20 Det. descr. : TC(6,5) Dump Mem Using Abs Address - Dump PIM Disable Clear RMB	ACZ79109	
10.2		Verify write protection disable (bit 3 (with LSB as bit 1) in the dumped word (32 bits) must be 0)		<input type="checkbox"/>





Set/Clear Power-on-reset Register  
 File: H\_CRP\_AOC\_2POR.xls  
 Author: dsalt-hp



Step No.	Time	Activity/Remarks	TC/TLM	Display/ Branch
	ET=+ UT=+00.00.05	Execute Telecommand  Get RM-A status  Subsch. ID : 20 Det. descr. : TC(8,1) - Get RM-A status	ACZZ4109	
11.2		Verify update in ASW and RM A		☐
		Verify Telemetry PwrOnResetRegA AEG41050	= Power On	AND=ZAA07999
		Verify Telemetry PwrOnResetRegB AEG42050	= Power On	AND=ZAA07999
		Verify Telemetry PwrOnResetRegA AEW3Y109	= 0 <dec>	AND=ZAAM0999
		Verify Telemetry PwrOnResetRegB AEW3Z109	= 0 <dec>	AND=ZAAM0999
11.3		Uplink next command		☐
		Execute Telecommand  Get RM-B status  TC Control Flags : GBM IL DSE --Y -- ---  Subsch. ID : 20 Det. descr. : TC(8,1) - Get RM-B status	ACZZ5109	
11.4		Verify update in RM B		☐
		Verify Telemetry PwrOnResetRegA AEW3Y109	= 0 <dec>	AND=ZAAM0999
		Verify Telemetry PwrOnResetRegB AEW3Z109	= 0 <dec>	AND=ZAAM0999
<b>End of Procedure</b>				