Read main registers
File: H_COP_DHS_0710.xls
Author: S. Manganelli

## Procedure Summary

### Objectives

The procedure will be run during Commissioning to read all main
CROME_A, CROME_B, PM_COCOS, SSMM_COCOS_A, SSMM_COCOS_B and CPU
System registers

### Summary of Constraints

At least 150 kbit/s TM bit rate, or run procedure H_FCP_DHS_1032
to lower TC execution rate.

21 apr 2009: RUAG confirms that when an MM board is off or
uninitialized, the corresponding SpaceWire module on the PM
board is usually switched off. And accessing registers in a
switched off ASIC module would typically cause a PM reset. ESOC
requests a verification on the adequacy of PTV for read register
TCs that may result in a level 3 if the corresponding module is
OFF or uninitialized.

### Spacecraft Configuration

**Start of Procedure**

Any

**End of Procedure**

Unchanged

### Reference File(s)

**Input Command Sequences**

**Output Command Sequences**

### Referenced Displays

**ANDs**      **GRDs**      **SLDs**

### Configuration Control Information

| DATE | FOP ISSUE | VERSION | MODIFICATION DESCRIPTION | AUTHOR | SPR REF |
|------|-----------|---------|--------------------------|--------|---------|
| 24/02/09 | 2.1 | 1 | Created | cmevi-hp | |
| 22/04/09 | 2.3 | 2 | Added constraint update | S. Manganelli | |

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## Procedure Flowchart Overview

```
            ┌─────────────────────────┐
            │    START of Procedure    │
            └─────────────────────────┘
                         │
                         │
                         │
                  ┌──────────────┐
               ┌─┤  1  HCD0710   │
               │ │ Perform register│
               │ │    dumps         │
               └─┴──────────────┘
                         │
                         │
                         │
                         ▼
            ┌─────────────────────────┐
            │     END of Procedure     │
            └─────────────────────────┘
```
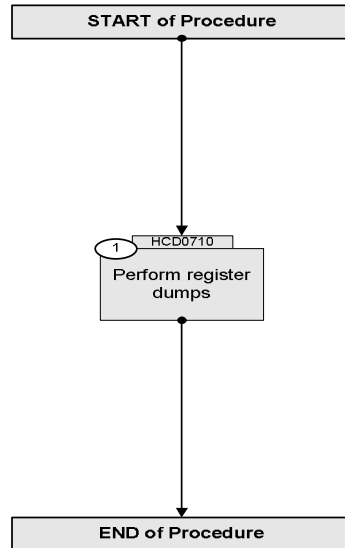
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| Step No. | Time | Activity/Remarks | TC/TLM | Display/ Branch | AIT Comment |
|---|---|---|---|---|---|
| | | **Beginning of Procedure** | | | |
| | HCD0710 | *TC Seq. Name :* HCD0710 (   )<br><br>*TimeTag Type:*<br>*Sub Schedule ID:* | | | |
| 1 | | Perform register dumps<br><br>type: [Proc] | | | |
| | | H_FCP_DHS_1031  Read main registers | | | |
| | | | | | |
| | | End of Sequence | | | |
| | | **End of Procedure** | | | |