

**PROCEDURE REFERENCE LIST IN ALPHA NUMERIC ORDER SHOWING
WHERE IN THE FOP A PROCEDURE IS LOCATED**

ID	TITLE	Volume	Chapter	Section
C CRP OBS 1100	<i>Enable/Disable write to CDMU CPU and COCOS registers or EEPROM</i>	7	2	n/a
C CRP OBS 1102	<i>Enable/Disable write to CDMU ASW/BSW code and constants</i>	7	2	n/a
C CRP OBS 2100	<i>Enable/Disable write to ACC CPU and COCOS registers or EEPROM</i>	7	2	n/a
C CRP OBS 2102	<i>Enable/Disable write to ACC ASW/BSW code and constants</i>	7	2	n/a
C GSP MCS MINI	<i>Herschel/Planck Mini System Startup</i>	8	8	n/a
C GSP MCS NCTRS	<i>Herschel/Planck NCTRS Setup</i>	8	8	n/a
C GSP MCS RTSI	<i>Enabling/Disabling of RTSI</i>	8	8	n/a
C GSP MCS SCOS	<i>Herschel/Planck SCOS Client Setup</i>	8	8	n/a
C GSP MCS SIM	<i>Connecting Ops MCS to SIM</i>	8	8	n/a
C GSP SYS POPA	<i>Post Pass Activities</i>	8	8	n/a
C GSP SYS PREP	<i>DTCP Preparation</i>	8	8	n/a
H Vol1	<i>Mission Management</i>	1	n/a	n/a
H Vol2	<i>Mission Phases</i>	2	n/a	n/a
H Vol3 Payload FCPs	<i>Payload FCP's</i>	3	n/a	n/a
H Vol3 Platform FCPs	<i>Platform FCP's</i>	3	n/a	n/a
H Vol4 Payload FCPs	<i>Payload FCP's</i>	4	n/a	n/a
H Vol4 Payload HIFI FCPs	<i>Payload FCP's</i>	4	n/a	n/a

ID	TITLE	Volume	Chapter	Section
H Vol4 Payload HIFI FCPs Part 2	<i>Payload FCP's</i>	4	n/a	n/a
H Vol4 Payload PACS FCPs	<i>Payload FCP's</i>	4	n/a	n/a
H Vol4 Payload SPIRE FCPs	<i>Payload FCP's</i>	4	n/a	n/a
H Vol4 Payload SPIRE FCPs Part 1	<i>Payload FCP's</i>	4	n/a	n/a
H Vol4 Payload SPIRE FCPs Part2	<i>Payload FCP's</i>	4	n/a	n/a
H Vol4 Payload SPIRE FCPs Part3	<i>Payload FCP's</i>	4	n/a	n/a
H Vol4 Payload SPIRE FCPs Part4	<i>Payload FCP's</i>	4	n/a	n/a
H Vol4 Platform FCPs	<i>Platform FCP's</i>	4	n/a	n/a
H Vol5 Payload FCPs	<i>Payload FCP's</i>	5	n/a	n/a
H Vol5 Platform FCPs	<i>Platform FCP's</i>	5	n/a	n/a
H Vol6 ch2 ACMS FCPs	<i>ACMS FCP's</i>	6	n/a	n/a
H Vol6 ch2 CDMU FCPs	<i>CDMU FCP's</i>	6	n/a	n/a
H Vol6 ch2 CDMU FCPs part2	<i>CDMU FCP's</i>	6	n/a	n/a
H Vol6 ch2 Power FCPs	<i>Power FCP's</i>	6	n/a	n/a
H Vol6 ch2 System FCPs	<i>System FCP's</i>	6	n/a	n/a
H Vol6 ch2 Thermal FCPs	<i>Thermal FCP's</i>	6	n/a	n/a
H Vol6 ch2 TT&C FCPs	<i>TT&C FCP's</i>	6	n/a	n/a
H Vol6 ch3 CCU FCPs	<i>CCU FCP's</i>	6	n/a	n/a

ID	TITLE	Volume	Chapter	Section
H Vol6 ch3 HIFI FCPs	<i>HIFI FCP's</i>	6	n/a	n/a
H Vol6 ch3 PACS FCPs	<i>PACS FCP's</i>	6	n/a	n/a
H Vol6 ch3 SPIRE FCPs	<i>SPIRE FCP's</i>	6	n/a	n/a
H Vol6 ch3 SREM FCPs	<i>SREM FCP's</i>	6	n/a	n/a
H Vol6 ch3 VMC FCPs	<i>VMC FCP's</i>	6	n/a	n/a
H Vol7 ch2 ACMS FCPs	<i>ACMS CRP's</i>	7	n/a	n/a
H Vol7 ch2 CDMU FCPs	<i>CDMU CRP's</i>	7	n/a	n/a
H Vol7 ch2 Power FCPs	<i>Power CRP's</i>	7	n/a	n/a
H Vol7 ch2 System FCPs	<i>System CRP's</i>	7	n/a	n/a
H Vol7 ch2 Thermal FCPs	<i>Thermal CRP's</i>	7	n/a	n/a
H Vol7 ch2 TT&C FCPs	<i>TT&C CRP's</i>	7	n/a	n/a
H Vol7 ch3 CCU FCPs	<i>CCU CRP's</i>	7	n/a	n/a
H Vol7 ch3 HIFI FCPs	<i>HIFI CRP's</i>	7	n/a	n/a
H Vol7 ch3 PACS FCPs	<i>PACS CRP's</i>	7	n/a	n/a
H Vol7 ch3 SPIRE FCPs	<i>SPIRE CRP's</i>	7	n/a	n/a
H Vol7 ch3 SREM FCPs	<i>SREM CRP's</i>	7	n/a	n/a
H Vol7 ch3 VMC FCPs	<i>VMC CRP's</i>	7	n/a	n/a
H Vol8	<i>Mission Support Procedures</i>	8	n/a	n/a

ID	TITLE	Volume	Chapter	Section
H_COP_AOC_0620	<i>ACMS Initial GYR+CRS Bias Calibration in SCM</i>	4	2	n/a
H_COP_AOC_0632	<i>ACMS Update OBDB GYR Bias</i>	4	2	n/a
H_COP_AOC_0634	<i>ACMS GYR + CRS Cal Slews in SCM (LGA)</i>	4	2	n/a
H_COP_AOC_0636	<i>ACMS Slew to MGA compatible attitude</i>	4	2	n/a
H_COP_AOC_0638	<i>ACMS Update OBDB (Gyr+CRS S.F. + ALN)</i>	4	2	n/a
H_COP_AOC_0650	<i>ACMS SCM Fine Pointing Functional Check (LGA)</i>	4	2	n/a
H_COP_AOC_0661	<i>ACMS SCM Rasters Functional Check pt1</i>	4	2	n/a
H_COP_AOC_0662	<i>ACMS SCM Rasters Functional Check pt2</i>	4	2	n/a
H_COP_AOC_0670	<i>ACMS SCM Line Scans Functional Check</i>	4	2	n/a
H_COP_AOC_0680	<i>ACMS Gyro Bias Drift Calibration #1</i>	4	2	n/a
H_COP_AOC_0682	<i>ACMS Gyro Bias Drift Calibration #2</i>	4	2	n/a
H_COP_AOC_0684	<i>ACMS Update OBDB GYR Bias Drift</i>	4	2	n/a
H_COP_AOC_7151	<i>ACMS Switch ON STR2 (STR1_OP)</i>	4	2	n/a
H_COP_AOC_7152	<i>ACMS STR2 Functional Checks</i>	4	2	n/a
H_COP_AOC_7153	<i>ACMS STR2 Memory Dump</i>	4	2	n/a
H_COP_AOC_7154	<i>ACMS Reconfigure to STR2_OP, STR1_NONOP</i>	4	2	n/a
H_COP_AOC_7155	<i>ACMS STR1 Functional Checks</i>	4	2	n/a
H_COP_AOC_7156	<i>ACMS STR1 Memory Dump</i>	4	2	n/a

ID	TITLE	Volume	Chapter	Section
H_COP_AOC_7157	<i>ACMS Reconfigure to STR1_OP, STR2_NONOP</i>	4	2	n/a
H_COP_AOC_7158	<i>ACMS Switch ON STR2 (STR1_OP)</i>	4	2	n/a
H_COP_AOC_9101	<i>ACMS ACC Memory Dump (MGA) (serv 6)</i>	4	2	n/a
H_COP_CCU_HPLM	<i>H-EPLM Commissioning Operations</i>	4	3	n/a
H_COP_DHS_0110	<i>CDMS Health checks</i>	4	2	n/a
H_COP_DHS_0210	<i>CDMS OBSM Dumps (MGA) (serv 6)</i>	4	2	n/a
H_COP_DHS_0310	<i>CDMS MOT, EAT, TCT or FCCT Thresholds Adjustment</i>	4	2	n/a
H_COP_DHS_0410	<i>Dump OBCPs code</i>	4	2	n/a
H_COP_DHS_0510	<i>CDMS OBT Stability Measurement</i>	4	2	n/a
H_COP_DHS_0610	<i>Perform dumps from SSMMB</i>	4	2	n/a
H_COP_DHS_0710	<i>Read main registers</i>	4	2	n/a
H_COP_DHS_0910	<i>Perform transition to Nominal</i>	4	2	n/a
H_COP_HIF_CPR1	<i>HIFI Open-Loop Small Steps</i>	4	3	n/a
H_COP_HIF_CPR2	<i>HIFI Open-Loop Scans</i>	4	3	n/a
H_COP_HIF_CPR3	<i>HIFI Closed-Loop Health Check</i>	4	3	n/a
H_COP_HIF_CPRR	<i>HIFI Chopper response time</i>	4	3	n/a
H_COP_HIF_FPFT	<i>HIFI FPU Functional Tests</i>	4	3	n/a
H_COP_HIF_HRFT	<i>HIFI HRS Functional Tests</i>	4	3	n/a

ID	TITLE	Volume	Chapter	Section
H_COP_HIF_IFFT	<i>HIFI IF Chain Functional Tests</i>	4	3	n/a
H_COP_HIF_LOFT	<i>HIFI LO functional tests</i>	4	3	n/a
H_COP_HIF_NFF1	<i>FPU functional test</i>	4	3	n/a
H_COP_HIF_NFF2	<i>FPU functional test</i>	4	3	n/a
H_COP_HIF_NFF3	<i>FPU functional test</i>	4	3	n/a
H_COP_HIF_NFF4	<i>FPU functional test</i>	4	3	n/a
H_COP_HIF_NFF5	<i>FPU functional test</i>	4	3	n/a
H_COP_HIF_NFF6	<i>FPU functional test</i>	4	3	n/a
H_COP_HIF_NFF7	<i>FPU functional test</i>	4	3	n/a
H_COP_HIF_NFF8	<i>FPU functional test</i>	4	3	n/a
H_COP_HIF_NFHR	<i>HRS Functional Test</i>	4	3	n/a
H_COP_HIF_NFL1	<i>LO functional test</i>	4	3	n/a
H_COP_HIF_NFL2	<i>LO functional test</i>	4	3	n/a
H_COP_HIF_NFL3	<i>LO functional test</i>	4	3	n/a
H_COP_HIF_NFL4	<i>LO functional test</i>	4	3	n/a
H_COP_HIF_NFL5	<i>LO functional test</i>	4	3	n/a
H_COP_HIF_NFL6	<i>LO functional test</i>	4	3	n/a
H_COP_HIF_NFL7	<i>LO functional test</i>	4	3	n/a

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H_COP_HIF_NFL8	<i>LO functional test</i>	4	3	n/a
H_COP_HIF_NFL9	<i>LO functional test</i>	4	3	n/a
H_COP_HIF_NFLA	<i>LO functional test</i>	4	3	n/a
H_COP_HIF_NFLB	<i>LO functional test</i>	4	3	n/a
H_COP_HIF_NFLC	<i>LO functional test</i>	4	3	n/a
H_COP_HIF_NFLD	<i>LO functional test</i>	4	3	n/a
H_COP_HIF_NFLE	<i>LO functional test</i>	4	3	n/a
H_COP_HIF_NFWB	<i>WBS Functional Test</i>	4	3	n/a
H_COP_HIF_NIF1	<i>IF Chain Functional Test</i>	4	3	n/a
H_COP_HIF_NIF2	<i>IF Chain Functional Test</i>	4	3	n/a
H_COP_HIF_NIF3	<i>IF Chain Functional Test</i>	4	3	n/a
H_COP_HIF_NIF4	<i>IF Chain Functional Test</i>	4	3	n/a
H_COP_HIF_NIF5	<i>IF Chain Functional Test</i>	4	3	n/a
H_COP_HIF_NIF6	<i>IF Chain Functional Test</i>	4	3	n/a
H_COP_HIF_NUT0	<i>LCU Safety Table and Memory Patch Upload</i>	4	3	n/a
H_COP_HIF_PS10	<i>Parametric Scan at 1897 GHz</i>	4	3	n/a
H_COP_HIF_PS20	<i>Parametric Scan at 1834 GHz</i>	4	3	n/a
H_COP_HIF_RCRT	<i>HIFI-COP-2.1-CPR-RespTime_Redundant</i>	4	3	n/a

ID	TITLE	Volume	Chapter	Section
H_COP_HIF_RFF1	<i>FPU functional test</i>	4	3	n/a
H_COP_HIF_RFF2	<i>FPU functional test</i>	4	3	n/a
H_COP_HIF_RFF3	<i>FPU functional test</i>	4	3	n/a
H_COP_HIF_RFF4	<i>FPU functional test</i>	4	3	n/a
H_COP_HIF_RFF5	<i>FPU functional test</i>	4	3	n/a
H_COP_HIF_RFF6	<i>FPU functional test</i>	4	3	n/a
H_COP_HIF_RFF7	<i>FPU functional test</i>	4	3	n/a
H_COP_HIF_RFF8	<i>FPU functional test</i>	4	3	n/a
H_COP_HIF_RFHR	<i>HRS Functional Test</i>	4	3	n/a
H_COP_HIF_RFL1	<i>LO functional test</i>	4	3	n/a
H_COP_HIF_RFL2	<i>LO functional test</i>	4	3	n/a
H_COP_HIF_RFL3	<i>LO functional test</i>	4	3	n/a
H_COP_HIF_RFL4	<i>LO functional test</i>	4	3	n/a
H_COP_HIF_RFL5	<i>LO functional test</i>	4	3	n/a
H_COP_HIF_RFL6	<i>LO functional test</i>	4	3	n/a
H_COP_HIF_RFL7	<i>LO functional test</i>	4	3	n/a
H_COP_HIF_RFL8	<i>LO functional test</i>	4	3	n/a
H_COP_HIF_RFL9	<i>LO functional test</i>	4	3	n/a

ID	TITLE	Volume	Chapter	Section
H_COP_HIF_RFLA	<i>LO functional test</i>	4	3	n/a
H_COP_HIF_RFLB	<i>LO functional test</i>	4	3	n/a
H_COP_HIF_RFLC	<i>LO functional test</i>	4	3	n/a
H_COP_HIF_RFLD	<i>LO functional test</i>	4	3	n/a
H_COP_HIF_RFLE	<i>LO functional test</i>	4	3	n/a
H_COP_HIF_RFWB	<i>WBS Functional Test</i>	4	3	n/a
H_COP_HIF_RIF1	<i>IF Chain Functional Test</i>	4	3	n/a
H_COP_HIF_RIF2	<i>IF Chain Functional Test</i>	4	3	n/a
H_COP_HIF_RIF3	<i>IF Chain Functional Test</i>	4	3	n/a
H_COP_HIF_RIF4	<i>IF Chain Functional Test</i>	4	3	n/a
H_COP_HIF_RIF5	<i>IF Chain Functional Test</i>	4	3	n/a
H_COP_HIF_RIF6	<i>IF Chain Functional Test</i>	4	3	n/a
H_COP_HIF_RIF7	<i>IF Chain Functional Test</i>	4	3	n/a
H_COP_HIF_RUT0	<i>LCU Safety Table and Memory Patch Upload</i>	4	3	n/a
H_COP_HIF_SP30	<i>Dixplexer Scan</i>	4	3	n/a
H_COP_HIF_SP31	<i>HIFI-COP-3-Spur_B3b_1</i>	4	3	n/a
H_COP_HIF_SP32	<i>HIFI-COP-3-Spur_B3b_2</i>	4	3	n/a
H_COP_HIF_SP33	<i>HIFI-COP-3-Spur_B3b_3</i>	4	3	n/a

ID	TITLE	Volume	Chapter	Section
H_COP_HIF_SPUR	<i>HIFI Dedicated Spurious Analysis (Band 3b)</i>	4	3	n/a
H_COP_HIF_SWON	<i>HIFI Switch On Redundant</i>	4	3	n/a
H_COP_HIF_WBFT	<i>HIFI WBS Functional Tests</i>	4	3	n/a
H_COP_HIF_WU3B	<i>HifiManCmd_LO_Warmup_3b</i>	4	3	n/a
H_COP_OBS_0210	<i>Dump of CDMU memories (code and constants)</i>	4	2	n/a
H_COP_OBS_9101	<i>Dump of ACC memories (code and constants)</i>	4	2	n/a
H_COP_OBS_9102	<i>Dump of ACC registers</i>	4	2	n/a
H_COP_PAC_A001	<i>PACS_Start_Autonomy_Function_14_OBS</i>	4	3	n/a
H_COP_PAC_A002	<i>PACS_Start_Autonomy_Function_17_OBS</i>	4	3	n/a
H_COP_PAC_A003	<i>PACS_Disable_HK_OBS</i>	4	3	n/a
H_COP_PAC_C101	<i>PACS_Chopper_EnDis_Test_NoConf_ast1</i>	4	3	n/a
H_COP_PAC_C102	<i>PACS_Chopper_EnDis_Test_NoConf_ast300</i>	4	3	n/a
H_COP_PAC_C103	<i>PACS_Chopper_EnDis_PlateauTest_NoConf_ast</i>	4	3	n/a
H_COP_PAC_C201	<i>PACS_Chopper_uk_move_3000_ast_OBS</i>	4	3	n/a
H_COP_PAC_C202	<i>PACS_Chopper_uk_move_12000_ast_OBS</i>	4	3	n/a
H_COP_PAC_C203	<i>PACS_Chopper_uk_move_18000_ast_OBS</i>	4	3	n/a
H_COP_PAC_C204	<i>PACS_Chopper_uk_move_21000_ast_OBS</i>	4	3	n/a
H_COP_PAC_CHP1	<i>PACS Chopper Stability Verification</i>	4	3	n/a

ID	TITLE	Volume	Chapter	Section
H_COP_PAC_CHP2	<i>PACS Chopper Dynamic Behaviour Verification</i>	4	3	n/a
H_COP_PAC_CHP3	<i>PACS Chopper Dynamic Behaviour Verification</i>	4	3	n/a
H_COP_PAC_CHP4	<i>PACS Chopper Dynamic Behaviour Verification</i>	4	3	n/a
H_COP_PAC_CX01	<i>PACS_En_chopper_ast_OBS</i>	4	3	n/a
H_COP_PAC_CX02	<i>PACS_Dis_chopper_ast_OBS</i>	4	3	n/a
H_COP_PAC_CX03	<i>PACS_SwOff_chopper_ast_OBS</i>	4	3	n/a
H_COP_PAC_CX04	<i>SPEC_Chopper_dhk_5hk_1khz_OBS</i>	4	3	n/a
H_COP_PAC_CX05	<i>SPEC_Chopper_dhk_int_OBS</i>	4	3	n/a
H_COP_PAC_D001	<i>PACS_Spec_Heat_SWON_OBS</i>	4	3	n/a
H_COP_PAC_D002	<i>PACS_Spec_Heat_SFT</i>	4	3	n/a
H_COP_PAC_D003	<i>PACS_Spec_Heat_SWOF_OBS</i>	4	3	n/a
H_COP_PAC_D101	<i>PACS_Spec_Flash_SWON_OBS</i>	4	3	n/a
H_COP_PAC_D102	<i>PACS_Spec_Flash_SFT</i>	4	3	n/a
H_COP_PAC_D103	<i>PACS_Spec_Flash_SWOF_OBS</i>	4	3	n/a
H_COP_PAC_D201	<i>PACS_Spec_CRE_1pF0bias_Setup_Warm_OBS</i>	4	3	n/a
H_COP_PAC_D202	<i>PACS_Spec_CRE_Setup_Cold_OBS</i>	4	3	n/a
H_COP_PAC_D203	<i>PACS_Spec_CRE_01pF0bias_Setup_Warm_OBS</i>	4	3	n/a
H_COP_PAC_D204	<i>SpectroscopytoNonPrime_OBS</i>	4	3	n/a

ID	TITLE	Volume	Chapter	Section
H_COP_PAC_D301	<i>PACS_Phot_DPU_DMC_Setup_OBS</i>	4	3	n/a
H_COP_PAC_D302	<i>PACS_Phot_Switchon_OBS</i>	4	3	n/a
H_COP_PAC_D303	<i>PACS_Phot_Sequencer_Setup_OBS</i>	4	3	n/a
H_COP_PAC_D304	<i>PACS_Phot_SPU_Setup_OBS</i>	4	3	n/a
H_COP_PAC_D305	<i>PACS_Phot_Cold_Startinputsignal_OBS</i>	4	3	n/a
H_COP_PAC_D306	<i>PACS_Phot_SPU_Reset_OBS</i>	4	3	n/a
H_COP_PAC_D307	<i>PACS_Phot_Switchoff_OBS</i>	4	3	n/a
H_COP_PAC_F001	<i>PACS_Spec_Fil_Diaghk_Setup_OBS</i>	4	3	n/a
H_COP_PAC_F002	<i>CONF_spec_fltw_OBS</i>	4	3	n/a
H_COP_PAC_F003	<i>PACS_Spec_Fil_Testseq_OBS</i>	4	3	n/a
H_COP_PAC_F101	<i>PACS_Phot_HK_Setup_OBS</i>	4	3	n/a
H_COP_PAC_F102	<i>PACS_Phot_Fil_Diaghk_Setup_OBS</i>	4	3	n/a
H_COP_PAC_F103	<i>CONF_phot_fltw_OBS</i>	4	3	n/a
H_COP_PAC_F104	<i>PACS_Phot_Fil_Testseq_OBS</i>	4	3	n/a
H_COP_PAC_FDIR	<i>Test of PACS FDIR functions</i>	4	3	n/a
H_COP_PAC_G001	<i>PACS_Spec_Gra_Diaghk_Setup_OBS</i>	4	3	n/a
H_COP_PAC_G002	<i>CONF_grating_SFTheII_OBS</i>	4	3	n/a
H_COP_PAC_G003	<i>PACS_Spec_Gra_Healthcheck_OBS</i>	4	3	n/a

ID	TITLE	Volume	Chapter	Section
H_COP_PAC_G101	<i>PACS_Spec_Gra_Ampl_Loop_OBS</i>	4	3	n/a
H_COP_PAC_G102	<i>PACS_Spec_Gra_Ampl_Loop_OBS</i>	4	3	n/a
H_COP_PAC_G201	<i>PACS_Spec_Gra_IST_Check_1_OBS</i>	4	3	n/a
H_COP_PAC_G202	<i>PACS_Spec_Gra_IST_Check_2_OBS</i>	4	3	n/a
H_COP_PAC_G301	<i>PACS_Spec_Gra_IST_Filt_List_OBS</i>	4	3	n/a
H_COP_PAC_G302	<i>PACS_Spec_Gra_IST_PID_List_OBS</i>	4	3	n/a
H_COP_PAC_G303	<i>PACS_Spec_Gra_IST_PID_Loop_OBS</i>	4	3	n/a
H_COP_PAC_GRA1	<i>PACS Grating Controller Parameter Tuning</i>	4	3	n/a
H_COP_PAC_GRA2	<i>PACS Grating Controller Parameter Tuning</i>	4	3	n/a
H_COP_PAC_GRA3	<i>PACS Grating Controller Parameter Tuning</i>	4	3	n/a
H_COP_PAC_GRA4	<i>PACS Grating Controller Parameter Tuning</i>	4	3	n/a
H_COP_PAC_GRA5	<i>PACS Grating Controller Parameter Tuning</i>	4	3	n/a
H_COP_PAC_GX01	<i>PACS_Spec_Gra_IST_SWON_OBS</i>	4	3	n/a
H_COP_PAC_GX02	<i>PACS_Spec_Gra_IST_SWOF_OBS</i>	4	3	n/a
H_COP_PAC_GX03	<i>PACS_Spec_Gra_IST_Enable_OBS</i>	4	3	n/a
H_COP_PAC_GX04	<i>PACS_Spec_Gra_IST_Disable_OBS</i>	4	3	n/a
H_COP_PAC_GX07	<i>PACS_Spec_Gra_IST_Move_Abs_OBS</i>	4	3	n/a
H_COP_PAC_GX08	<i>PACS_Spec_Gra_IST_Move_Rel_OBS</i>	4	3	n/a

ID	TITLE	Volume	Chapter	Section
H_COP_PAC_GX09	<i>PACS_Spec_Gra_IST_Set_Ctrl_Par</i>	4	3	n/a
H_COP_PAC_GX10	<i>CONF_grating_IST_OBS</i>	4	3	n/a
H_COP_PAC_GX11	<i>PACS_Spec_Gra_IST_Home_OBS</i>	4	3	n/a
H_COP_PAC_L001	<i>PACS_CS_SFT_Cold_OBS</i>	4	3	n/a
H_COP_PAC_LLOP	<i>PACS_Open_Launch_Lock_OBS</i>	4	3	n/a
H_COP_PAC_SFT	<i>PACS Short Functional Test - HeII</i>	4	3	n/a
H_COP_PAC_SWON	<i>PACS Initial Switch ON</i>	4	3	n/a
H_COP_PAC_SX01	<i>PACS_Phot_SFT_Cooler_OBS</i>	4	3	n/a
H_COP_PAC_X001	<i>PACS_GeGa_SFT_Init_Cold_He2_OBS</i>	4	3	n/a
H_COP_PAC_X002	<i>PACS_Chopper_SFT_Cold_OpenLoop_OBS</i>	4	3	n/a
H_COP_PAC_X003	<i>PACS_Spec_HK_Setup_OBS</i>	4	3	n/a
H_COP_PAC_X004	<i>PACS_Diaghk_Reset_OBS</i>	4	3	n/a
H_COP_PAC_X005	<i>PACS_Spec_FlashHeat_Diaghk_Setup_OBS</i>	4	3	n/a
H_COP_PAC_X006	<i>PACS_Spec_SPU_Buffer_Setup_OBS</i>	4	3	n/a
H_COP_PAC_X007	<i>PACS_Spec_SPU_Reset_OBS</i>	4	3	n/a
H_COP_PAC_X008	<i>Pacs_DMC_SET_OBSID</i>	4	3	n/a
H_COP_PAC_X009	<i>CONF_chopper_ast_OBS</i>	4	3	n/a
H_COP_PAC_X010	<i>PACS_SwOn_chopper_ast_OBS</i>	4	3	n/a

ID	TITLE	Volume	Chapter	Section
H_COP_PAC_X011	<i>PACS_Spec_Gra_IST_Check_4_OBS</i>	4	3	n/a
H_COP_PAC_X012	<i>PACS_Spec_Gra_IST_Full_Charac_OBS</i>	4	3	n/a
H_COP_SPI_3TAB	<i>SpireEngPatchTables</i>	4	3	n/a
H_COP_SPI_APDS	<i>SPIRE_IST_DNS_PHOT Apply nominal Photometer detector settings</i>	4	3	n/a
H_COP_SPI_ASDS	<i>SPIRE_IST_DNS_SPEC Apply nominal Spectrometer detector settings</i>	4	3	n/a
H_COP_SPI_BS03	<i>Mode_BSMFunc03 Open Loop Dyn Check</i>	4	3	n/a
H_COP_SPI_BS06	<i>Mode_BSMFunc06 Closed Loop Op Chop Test</i>	4	3	n/a
H_COP_SPI_BS5A	<i>Mode_BSMFunc05a Open Loop Chop Test</i>	4	3	n/a
H_COP_SPI_BS5B	<i>Mode_BSMFunc05b Closed Loop Chop Test</i>	4	3	n/a
H_COP_SPI_BSMF	<i>BSM_OFF</i>	4	3	n/a
H_COP_SPI_BSMI	<i>BSM_INIT</i>	4	3	n/a
H_COP_SPI_BSSC	<i>Mode_BSMFunc01 Chop Jiggle Sensors Check</i>	4	3	n/a
H_COP_SPI_BT2A	<i>SpireEngBsmTuneFf BSM Tuning Feb A 2010</i>	4	3	n/a
H_COP_SPI_BT3A	<i>SpireEngBsmTuneFf BSM Tuning Mar A 2010</i>	4	3	n/a
H_COP_SPI_BT3B	<i>SpireEngBsmTuneFf BSM Tuning Mar B 2010</i>	4	3	n/a
H_COP_SPI_CDIS	<i>SPIRE-CP-DISCHARGE-COOLER</i>	4	3	n/a
H_COP_SPI_CF10	<i>SMEC Functional Tests, part 2A.</i>	4	3	n/a
H_COP_SPI_CF11	<i>SMEC Functional Tests, part A.</i>	4	3	n/a

ID	TITLE	Volume	Chapter	Section
H_COP_SPI_CFT1	<i>SPIRE CFT Checks Characterisation</i>	4	3	n/a
H_COP_SPI_CFT2	<i>Beam Steering Mechanism Cold Functional Tests</i>	4	3	n/a
H_COP_SPI_CFT3	<i>Photometer Functional Tests</i>	4	3	n/a
H_COP_SPI_CFT4	<i>Spectrometer Cold Functional Tests</i>	4	3	n/a
H_COP_SPI_CFT5	<i>SMEC Functional Tests, part A.</i>	4	3	n/a
H_COP_SPI_CFT6	<i>SMEC Functional Tests, Part B.</i>	4	3	n/a
H_COP_SPI_CFT7	<i>Mode Transistion SPIRE to REDY Mode</i>	4	3	n/a
H_COP_SPI_CFT8	<i>Set Nominal HK Rate</i>	4	3	n/a
H_COP_SPI_CFT9	<i>Spectrometer Cold Functional Tests</i>	4	3	n/a
H_COP_SPI_CHK5	<i>SpireEngCheckPM Daily</i>	4	3	n/a
H_COP_SPI_CRSC	<i>SPIRE-CP-SMEC-CRYOCOVER-SCANS SMEC Cryo Scan</i>	4	3	n/a
H_COP_SPI_CT10	<i>SMEC Functional Tests, part 2 A.</i>	4	3	n/a
H_COP_SPI_CT11	<i>SMEC Functional Tests, part A.</i>	4	3	n/a
H_COP_SPI_DCK5	<i>SpireEngCheckPM Daily</i>	4	3	n/a
H_COP_SPI_DCPN	<i>Mode_DcuFunc11_Phot BDA Switch On Check</i>	4	3	n/a
H_COP_SPI_DCSC	<i>Mode_DcuFunc02 Nom Sci Contents Integrity Check</i>	4	3	n/a
H_COP_SPI_DCSN	<i>SPIRE-CP-FUNC-DCU-11-SPEC BDA Switch On Check</i>	4	3	n/a
H_COP_SPI_DPIC	<i>Mode_DcuFunc13_Phot BDA Integrity Check</i>	4	3	n/a

ID	TITLE	Volume	Chapter	Section
H_COP_SPI_DPNC	<i>Mode_ILT_PERF_DNA_P Phot BDA Noise Check</i>	4	3	n/a
H_COP_SPI_DRCN	<i>DRCU_START DRCU Start</i>	4	3	n/a
H_COP_SPI_DRCR	<i>DRCU_START (RED) DPU & DRCU Start</i>	4	3	n/a
H_COP_SPI_DSIC	<i>Mode_DcuFunc13_Spec BDAs Integrity Check</i>	4	3	n/a
H_COP_SPI_LBMP	<i>SPIRE-FUNC-SMEC-LVDT-P LVDT Backup Mode (PRIME)</i>	4	3	n/a
H_COP_SPI_LBMR	<i>SPIRE-FUNC-SMEC-LVDT-R LVDT Backup Mode (RED)</i>	4	3	n/a
H_COP_SPI_LCLS	<i>SPIRE-CP-FUNC-SMEC-LVDT-SCAN Close Loop Scan Test</i>	4	3	n/a
H_COP_SPI_LINP	<i>SPIRE-FUNC-SMEC-LVDT-INIT-P Initialise LVDT mode (PRIME)</i>	4	3	n/a
H_COP_SPI_LINR	<i>SPIRE-FUNC-SMEC-LVDT-INIT-R Initialise LVDT mode RED</i>	4	3	n/a
H_COP_SPI_LSCP	<i>SPIRE-CP-FUNC-SMEC-01-LVDT-P SMEC Encoder/LVDT Sensor Check.</i>	4	3	n/a
H_COP_SPI_LSCR	<i>SPIRE-CP-FUNC-SMEC-01-LVDT-R SMEC Encoder/LVDT Sensor Check.</i>	4	3	n/a
H_COP_SPI_MACR	<i>CREC_MANUAL Manually recycle the SPIRE Sorption Cooler</i>	4	3	n/a
H_COP_SPI_MBSF	<i>SPIRE_BSM_OFF</i>	4	3	n/a
H_COP_SPI_MBSI	<i>SPIRE_BSM_INIT</i>	4	3	n/a
H_COP_SPI_MBSN	<i>BSM_ON Mode Transistion</i>	4	3	n/a
H_COP_SPI_MCSC	<i>Mode_McuFunc03 MCU Nom Sci Contents Check</i>	4	3	n/a
H_COP_SPI_MCUF	<i>MCU_OFF</i>	4	3	n/a
H_COP_SPI_MCUN	<i>MCU_BOOT</i>	4	3	n/a

ID	TITLE	Volume	Chapter	Section
H_COP_SPI_MDRF	<i>DRCU_OFF Mode Trans DRCU OFF</i>	4	3	n/a
H_COP_SPI_MDRN	<i>SPIRE_DRCU_ON</i>	4	3	n/a
H_COP_SPI_MMCB	<i>MCU_BOOT Mode Transisiton</i>	4	3	n/a
H_COP_SPI_MMCF	<i>MCU_OFF Mode Trans</i>	4	3	n/a
H_COP_SPI_MPHE	<i>PDET_OFF Mode Trans Phot OFF</i>	4	3	n/a
H_COP_SPI_MPHN	<i>SPIRE_PDET_ON Mode Trans</i>	4	3	n/a
H_COP_SPI_MREC	<i>Manual Cooler Recycle</i>	4	3	n/a
H_COP_SPI_MSCF	<i>SCU_OFF Mode Trans SCU thermometry & cooler heat switches OFF</i>	4	3	n/a
H_COP_SPI_MSCN	<i>SPIRE_SCU_ON Mode Trans SPIRE SCU Thermometry ON</i>	4	3	n/a
H_COP_SPI_MSIP	<i>SPIRE_SMEC_INIT PRIME Mode Transition</i>	4	3	n/a
H_COP_SPI_MSIR	<i>SPIRE_SMEC_INIT RED Mode Transition</i>	4	3	n/a
H_COP_SPI_MSMF	<i>SMEC_OFF Mode Trans</i>	4	3	n/a
H_COP_SPI_MSNP	<i>SPIRE_SMEC_ON PRIME Mode Trans</i>	4	3	n/a
H_COP_SPI_MSNR	<i>SPIRE_SMEC_ON RED Mode Trans</i>	4	3	n/a
H_COP_SPI_MSPF	<i>SPIRE_SDET_OFF Mode Trans, switch off Spectrometer</i>	4	3	n/a
H_COP_SPI_MSPN	<i>SPIRE_SDET_ON Mode Trans switch Spectrometer ON</i>	4	3	n/a
H_COP_SPI_NSON	<i>SPIRE ON, DPU_START - Switch ON the nominal SPIRE units</i>	4	3	n/a
H_COP_SPI_PCAC	<i>Mode_PCALFunc01 Phot Calibration Characterisation Test</i>	4	3	n/a

ID	TITLE	Volume	Chapter	Section
H_COP_SPI_PCSH	<i>SPIRE-CP-PHOT-PCAL-FLASH PCAL FLASH</i>	4	3	n/a
H_COP_SPI_PHDG	<i>SPIRE-CP-START-PHOT-DATA</i>	4	3	n/a
H_COP_SPI_PHOF	<i>PDET_OFF Photometer BDAs Switch Off</i>	4	3	n/a
H_COP_SPI_PHVT	<i>Mode_ILT_PERF_VSS_PC Photometer BDA Vss Test</i>	4	3	n/a
H_COP_SPI_PPDS	<i>PTC PID Sub K</i>	4	3	n/a
H_COP_SPI_PPDT	<i>PTC PID T2</i>	4	3	n/a
H_COP_SPI_PRST	<i>SPIRE-CP-RESET-PHOT-OFFSETS</i>	4	3	n/a
H_COP_SPI_PTC2	<i>SPIRE_CP_FUNC_PTC_PID_T2 PTC PID Tuning with Thermistor 2</i>	4	3	n/a
H_COP_SPI_PTCT	<i>SPIRE_CP_FUNC_PTC_PID_SUBKTEMP Phot Thermal Controller Tuning</i>	4	3	n/a
H_COP_SPI_REDB	<i>Mode_Go2BasicREDY</i>	4	3	n/a
H_COP_SPI_REDX	<i>Mode_Go2REDY</i>	4	3	n/a
H_COP_SPI_REDY	<i>Mode_Go2REDY</i>	4	3	n/a
H_COP_SPI_RSON	<i>SPIRE ON, DPU_START - Switch ON the redundant SPIRE units</i>	4	3	n/a
H_COP_SPI_SC2P	<i>SPIRE_CP_SCAL2_PID Tune Control Loop PID</i>	4	3	n/a
H_COP_SPI_SC2R	<i>SPIRE-CP-FUNC-SCAL2-PID (RED) Control Loop Tuning</i>	4	3	n/a
H_COP_SPI_SC4P	<i>SPIRE-CP-FUNC-SCAL4-PID Control Loop PID Tuning</i>	4	3	n/a
H_COP_SPI_SC4R	<i>SPIRE_CP_SCAL4_PID (RED) Control Loop PID Tuning</i>	4	3	n/a
H_COP_SPI_SCAC	<i>Mode_SCALFunc01 SCAL Characterisation Test</i>	4	3	n/a

ID	TITLE	Volume	Chapter	Section
H_COP_SPI_SCAT	<i>Mode_ScuFunc06 SCU AC Thermometry Check</i>	4	3	n/a
H_COP_SPI_SCDT	<i>Mode_ScuFunc03 SCU DC Thermometry Check</i>	4	3	n/a
H_COP_SPI_SCSC	<i>Mode_ScuFunc02 SCU Nominal Sci Contents</i>	4	3	n/a
H_COP_SPI_SCUF	<i>SPIRE_SCU_OFF Switch OFF SCU Thermometry</i>	4	3	n/a
H_COP_SPI_SETB	<i>SPIRE_CP_FUNC_NHK Set Basic HK rate pre Funct Tests</i>	4	3	n/a
H_COP_SPI_SETF	<i>SPIRE_CP_FUNC_NHK Set Nominal HK rate pre Funct Tests</i>	4	3	n/a
H_COP_SPI_SLW1	<i>SPIRE-CP-FUNC-13-SLW160 Dark Curves</i>	4	3	n/a
H_COP_SPI_SLW2	<i>SPIRE-CP-FUNC-13-SLW240 Dark Curves</i>	4	3	n/a
H_COP_SPI_SLW8	<i>SPIRE-CP-FUNC-13-SLW80 Dark Load Curves</i>	4	3	n/a
H_COP_SPI_SLWC	<i>SPIRE-CP-FUNC-DCU-13-SLW SLW BDAs Integrity Check</i>	4	3	n/a
H_COP_SPI_SLWL	<i>SPIRE-CP-FUNC-DCU-14-SLW SLW BDAs noise check</i>	4	3	n/a
H_COP_SPI_SLWN	<i>Mode_DcuFunc11_SLW SLW BDA Switch ON check</i>	4	3	n/a
H_COP_SPI_SLWV	<i>SPIRE-CP-SLW-VSS SPEC SLW JFET Source Voltage Test</i>	4	3	n/a
H_COP_SPI_SMCP	<i>Mode_SMECFunc04b Open Loop Position Check</i>	4	3	n/a
H_COP_SPI_SMCR	<i>SMEC Scans Cryocover</i>	4	3	n/a
H_COP_SPI_SMCS	<i>Mode_SMECFunc07 Closed Loop Scan Check</i>	4	3	n/a
H_COP_SPI_SMEP	<i>Mode_SMECFunc03 SMEC Encoder Levels Check</i>	4	3	n/a
H_COP_SPI_SMER	<i>Mode_SMECFunc03 (RED) SMEC Encoder Levels Check</i>	4	3	n/a

ID	TITLE	Volume	Chapter	Section
H_COP_SPI_SMFP	<i>Mode_SMECFuncFFOffset Open Loop FeedForward Offset Test</i>	4	3	n/a
H_COP_SPI_SMFR	<i>Mode_SMECFuncFFOffset (RED) Open Loop FeedForward Test</i>	4	3	n/a
H_COP_SPI_SMIP	<i>SPIRE-CP-SMEC-INIT-P</i>	4	3	n/a
H_COP_SPI_SMIR	<i>SMEC_INIT</i>	4	3	n/a
H_COP_SPI_SMLN	<i>SpireEngSmecLedLvdt Switch on SMEC LED and LVDT</i>	4	3	n/a
H_COP_SPI_SMLP	<i>Mode_SMECFunc01 Encoder and LVDT Check</i>	4	3	n/a
H_COP_SPI_SMLR	<i>Mode_SMECFunc01 (RED) Encoder and LVDT Check</i>	4	3	n/a
H_COP_SPI_SMLV	<i>SMEC LVDT Back-up Mode Test.</i>	4	3	n/a
H_COP_SPI_SMOF	<i>SMEC_OFF</i>	4	3	n/a
H_COP_SPI_SMOL	<i>Mode_SMECFunc02a SMEC Open Launch Latch</i>	4	3	n/a
H_COP_SPI_SMOS	<i>Mode_SMECFunc09 SMEC Open Loop Scan Check</i>	4	3	n/a
H_COP_SPI_SMPF	<i>Mode_SMECFunc04a SMEC Open Loop Positioning Test</i>	4	3	n/a
H_COP_SPI_SMPR	<i>Mode_SMECFunc04a (RED) SMEC Open Positioning Test</i>	4	3	n/a
H_COP_SPI_SOHC	<i>Mode_ScuFunc07 Sorption Heater Cooler Check</i>	4	3	n/a
H_COP_SPI_SPDG	<i>SPIRE-CP-START-SPEC-DATA</i>	4	3	n/a
H_COP_SPI_SPDL	<i>SPEC Dark Loads</i>	4	3	n/a
H_COP_SPI_SPNO	<i>Mode_ILT_PERF_DNA_S Spec BDA Noise Check</i>	4	3	n/a
H_COP_SPI_SPOF	<i>SDET_OFF Spec BDA Switch Off</i>	4	3	n/a

ID	TITLE	Volume	Chapter	Section
H_COP_SPI_SPPD	<i>SPEC SCAL PID Tuning Tests</i>	4	3	n/a
H_COP_SPI_SPPH	<i>SPEC Phase Ups</i>	4	3	n/a
H_COP_SPI_SPSH	<i>SPIRE-CP-SPEC-PCAL-FLASH SPEC Flash</i>	4	3	n/a
H_COP_SPI_SPU1	<i>SPIRE-CP-PHASEUP-SPEC160</i>	4	3	n/a
H_COP_SPI_SPU2	<i>SPIRE_IST_PHASEUP_SPEC240</i>	4	3	n/a
H_COP_SPI_SPU8	<i>SPIRE-CP-PHASEUP-SPEC80</i>	4	3	n/a
H_COP_SPI_SPVT	<i>Mode_ILT_PERF_VSS_SC Spec BDA Vss Test</i>	4	3	n/a
H_COP_SPI_SRST	<i>SPIRE-CP-RESET-SPEC-OFFSETS</i>	4	3	n/a
H_COP_SPI_STDG	<i>SPIRE-CP-STOP-DCU-DATA</i>	4	3	n/a
H_COP_SPI_SWON	<i>SPIRE Initial Instrument Switch On</i>	4	3	n/a
H_COP_SYS_DEC0	<i>Stop Decontamination heating during Telescope cool down</i>	4	2	n/a
H_COP_SYS_DEC2	<i>Telescope decontamination during Telescope cool down</i>	4	2	n/a
H_COP_SYS_DECD	<i>Decontamination MOT and EAT entries definition</i>	4	2	n/a
H_COP_TCS_CLNR	<i>Thermal Control loops Reconfiguration from Nominal to Redundant</i>	4	2	n/a
H_COP_TCS_CLRN	<i>Thermal Control loops Reconfiguration from Redundant to Nominal</i>	4	2	n/a
H_COP_TCS_TCS0	<i>TCS Commissioning Sequence of Activities</i>	4	2	n/a
H_COP_TCS_TCS2	<i>Check redundant heaters</i>	4	2	n/a
H_COP_TCS_TCS4	<i>Class B thermal performance verification</i>	4	2	n/a

ID	TITLE	Volume	Chapter	Section
H_COP_TTC_T12	<i>Switchover from chain 1 to 2</i>	4	2	n/a
H_COP_TTC_T21	<i>Switchover from chain 2 to 1</i>	4	2	n/a
H_COP_TTC_TTC0	<i>TTC commissioning sequence of activities</i>	4	2	n/a
H_COP_TTC_TTC1	<i>Switch TTC chain 1 to MGA</i>	4	2	n/a
H_COP_TTC_TTC2	<i>RXs Thresholds Check</i>	4	2	n/a
H_COP_TTC_TTC4	<i>TC + TM + ranging check</i>	4	2	n/a
H_COP_TTC_TTC5	<i>TM Functional Check</i>	4	2	n/a
H_COP_TTC_TTC7	<i>MGA Characterisation</i>	4	2	n/a
H_CRP_AOC_03AH	<i>Recovery from Level 3a Trigger</i>	7	2	n/a
H_CRP_AOC_0DVA	<i>Herschel ACMS : Abort Delta-V</i>	7	2	n/a
H_CRP_AOC_0S34	<i>Recovery from Separation Straps 3 and 4 (Double) Failure</i>	7	2	n/a
H_CRP_AOC_0S78	<i>Recovery from Separation Straps 7 or 8 Failure</i>	7	2	n/a
H_CRP_AOC_0SUR	<i>Update SM units configuration</i>	7	2	n/a
H_CRP_AOC_2BCM	<i>Modify ACMS Data Bus Communication Configuration</i>	7	2	n/a
H_CRP_AOC_2BRT	<i>Get ACC Boot Report</i>	7	2	n/a
H_CRP_AOC_2BVL	<i>Modify ACMS Data Bus Validity Table</i>	7	2	n/a
H_CRP_AOC_2CPD	<i>Modify CPDU Configuration</i>	7	2	n/a
H_CRP_AOC_2PNR	<i>PM Nominal mode reset</i>	7	2	n/a

ID	TITLE	Volume	Chapter	Section
H_CRP_AOC_2POR	<i>Set/Clear Power-on-reset Register</i>	7	2	n/a
H_CRP_AOC_2SGM	<i>Modify SGM Configuration</i>	7	2	n/a
H_CRP_AOC_3DET	<i>Select CRS Detection Unit</i>	7	2	n/a
H_CRP_AOC_3SUR	<i>Select CRS Survival Unit</i>	7	2	n/a
H_CRP_AOC_4G01	<i>GYR Electronics reconfiguration</i>	7	2	n/a
H_CRP_AOC_4RCF	<i>Individual GYR reconfiguration</i>	7	2	n/a
H_CRP_AOC_4RPW	<i>Re-power GYRE</i>	7	2	n/a
H_CRP_AOC_4S01	<i>STR Reconfiguration</i>	7	2	n/a
H_CRP_AOC_5ACD	<i>Dump memory area</i>	7	2	n/a
H_CRP_AOC_5F2C	<i>Command RCS to Coarse pointing</i>	7	2	n/a
H_CRP_AOC_5LVC	<i>Modify RCS Nominal Configuration</i>	7	2	n/a
H_CRP_AOC_5NCF	<i>Modify RCS Nominal Configuration</i>	7	2	n/a
H_CRP_AOC_5SCF	<i>Modify RCS Survival Configuration</i>	7	2	n/a
H_CRP_AOC_6RCF	<i>Reconfigure RWLs</i>	7	2	n/a
H_CRP_AOC_7NRC	<i>SAS Nominal reconfiguration</i>	7	2	n/a
H_CRP_AOC_D2AC	<i>Set RM Attempt Counter</i>	7	2	n/a
H_CRP_AOC_D2AD	<i>Set RM Activation and Toggle Delays</i>	7	2	n/a
H_CRP_AOC_D2AE	<i>Enable/Disable RM Alarms</i>	7	2	n/a

ID	TITLE	Volume	Chapter	Section
H_CRP_AOC_D2AP	<i>Set RM Alarm Polarity</i>	7	2	n/a
H_CRP_AOC_D2PS	<i>Change RM Programming Set</i>	7	2	n/a
H_CRP_AOC_D2TD	<i>Set RM Alarm Temporisatation Delays</i>	7	2	n/a
H_CRP_AOC_D5LP	<i>Restore launch pad update OBDB values</i>	7	2	n/a
H_CRP_AOC_DBSW	<i>Update BSW Health Table</i>	7	2	n/a
H_CRP_AOC_DCTR	<i>Manual CTR Synchronisation</i>	7	2	n/a
H_CRP_AOC_DSGM	<i>Access to Write-protected Area of SGM</i>	7	2	n/a
H_CRP_AOC_XA2C	<i>Recovery from SIR</i>	7	2	n/a
H_CRP_AOC_XN2S	<i>Command ACMS from any nominal mode to SM</i>	7	2	n/a
H_CRP_AOC_XS2A	<i>Recovery from SM with return to PMA or PMB</i>	7	2	n/a
H_CRP_CCU_AB00	<i>CCU Switch OFF</i>	7	3	n/a
H_CRP_CCU_AB01	<i>CCU Switch ON</i>	7	3	n/a
H_CRP_CCU_CCUR	<i>CCU Anomaly</i>	7	3	n/a
H_CRP_CCU_CRYO	<i>Cryostat Anomaly</i>	7	3	n/a
H_CRP_CCU_DEBU	<i>Enable/Disable CCU meas. debug mode</i>	7	3	n/a
H_CRP_CCU_DLCR	<i>DLCM anomaly</i>	7	3	n/a
H_CRP_CCU_MANG	<i>Start/stop Payload managment function</i>	7	3	n/a
H_CRP_CCU_RTR	<i>Recovery from a CCU A or CCU B Non-Vital RT Invalid</i>	7	3	n/a

ID	TITLE	Volume	Chapter	Section
H_CRP_CCU_VBN0	<i>Big Nozzle Open</i>	7	3	n/a
H_CRP_CCU_VBN1	<i>Big Nozzle Close</i>	7	3	n/a
H_CRP_CCU_VLV0	<i>Reinforce V501/503 & V103/106 Opening</i>	7	3	n/a
H_CRP_DHS_1001	<i>Disabling transmission for non essential APIDs.</i>	7	2	n/a
H_CRP_DHS_1002	<i>Enable/disable NEss APID downlink</i>	7	2	n/a
H_CRP_DHS_1003	<i>Enable or disable transmission of TM(3,26) for all APIDs</i>	7	2	n/a
H_CRP_DHS_1004	<i>Switch Off Mass Memory Board and Banks</i>	7	2	n/a
H_CRP_DHS_1005	<i>CDMU PM Reboot</i>	7	2	n/a
H_CRP_DHS_1006	<i>Change Status of 1553 Bus</i>	7	2	n/a
H_CRP_DHS_1007	<i>Synchronise SSMMB pointers to SSMMMA values.</i>	7	2	n/a
H_CRP_DHS_1008	<i>AIR Event Action entry management</i>	7	2	n/a
H_CRP_DHS_1009	<i>TTR switchover for NOTM procedure</i>	7	2	n/a
H_CRP_DHS_1010	<i>Rollback from TTR sw/over as for CRP_DHS_1009</i>	7	2	n/a
H_CRP_DHS_3001	<i>Start or stop the whole ASW internal event filtering function</i>	7	2	n/a
H_CRP_DHS_3002	<i>Starting or stopping the on board monitoring</i>	7	2	n/a
H_CRP_DHS_3003	<i>Start or stop the whole event-action function</i>	7	2	n/a
H_CRP_DHS_3004	<i>Start or stop the whole FDIR function</i>	7	2	n/a
H_CRP_DHS_3005	<i>Start or stop the whole mode management function</i>	7	2	n/a

ID	TITLE	Volume	Chapter	Section
H_CRP_DHS_3006	<i>Start or stop the whole payload function</i>	7	2	n/a
H_CRP_DHS_3007	<i>Start or stop Lou Baffle management</i>	7	2	n/a
H_CRP_DHS_3011	<i>Enable or disable Reconfiguration Module</i>	7	2	n/a
H_CRP_DHS_3013	<i>Configuration of SIR and CIR relay</i>	7	2	n/a
H_CRP_DHS_3014	<i>SGM maintenance</i>	7	2	n/a
H_CRP_DHS_3015	<i>Configuration of PM relay 0 and 1</i>	7	2	n/a
H_CRP_DHS_3016	<i>Health table maintenance</i>	7	2	n/a
H_CRP_DHS_3017	<i>Unit in Use table maintenance</i>	7	2	n/a
H_CRP_DHS_3018	<i>RM alarm maintenance</i>	7	2	n/a
H_CRP_DHS_3019	<i>RM PAP table maintenance</i>	7	2	n/a
H_CRP_DHS_3020	<i>Enable or disable RM CPDU TC</i>	7	2	n/a
H_CRP_DHS_3021	<i>CROME register maintenance</i>	7	2	n/a
H_CRP_DHS_3022	<i>Dump memory area</i>	7	2	n/a
H_CRP_DHS_3023	<i>Write memory area</i>	7	2	n/a
H_CRP_DHS_3024	<i>Check memory area</i>	7	2	n/a
H_CRP_DHS_3025	<i>Write, dump and check MM addresses</i>	7	2	n/a
H_CRP_DHS_3026	<i>Copying content of a memory area to another</i>	7	2	n/a
H_CRP_DHS_3027	<i>PM switchover from A to B</i>	7	2	n/a

ID	TITLE	Volume	Chapter	Section
H_CRP_DHS_3028	<i>PM switchover from B to A</i>	7	2	n/a
H_CRP_DHS_3029	<i>MM reset</i>	7	2	n/a
H_CRP_DHS_3035	<i>Perform a standard patch</i>	7	2	n/a
H_CRP_DHS_3036	<i>Perform a severe patch</i>	7	2	n/a
H_CRP_DHS_3043	<i>Loss of TM link</i>	7	2	n/a
H_CRP_DHS_3044	<i>Check Survival mode configuration</i>	7	2	n/a
H_CRP_DHS_3045	<i>S/C Mode transition from Survival to Sun Acquisition</i>	7	2	n/a
H_CRP_DHS_3046	<i>Configuration check after level 3 or 4</i>	7	2	n/a
H_CRP_DHS_3049	<i>Configuration check after TTR board failure</i>	7	2	n/a
H_CRP_DHS_3055	<i>Roll back to TTR B after TTR switchover</i>	7	2	n/a
H_CRP_DHS_3056	<i>Roll back to TTR A after TTR switchover</i>	7	2	n/a
H_CRP_DHS_3057	<i>Clear SW Alarm in ERC 32</i>	7	2	n/a
H_CRP_DHS_3058	<i>S/C Mode transition from Launch to Sun Acquisition</i>	7	2	n/a
H_CRP_DHS_3059	<i>S/C Mode transition from Nominal to Sun Acquisition</i>	7	2	n/a
H_CRP_DHS_3060	<i>S/C Mode transition from Nominal to Survival</i>	7	2	n/a
H_CRP_DHS_3061	<i>S/C Mode transition from Earth Acquisition to Sun Acquisition</i>	7	2	n/a
H_CRP_DHS_3062	<i>S/C Mode transition from Earth Acquisition to Survival</i>	7	2	n/a
H_CRP_DHS_3063	<i>S/C Mode transition from Sun Acquisition to Survival</i>	7	2	n/a

ID	TITLE	Volume	Chapter	Section
H_CRP_DHS_3064	<i>S/C self transition to Launch</i>	7	2	n/a
H_CRP_DHS_3065	<i>S/C self transition to Sun Acquisition</i>	7	2	n/a
H_CRP_DHS_3066	<i>S/C self transition to Nominal</i>	7	2	n/a
H_CRP_DHS_3067	<i>S/C self transition to Earth Acquisition</i>	7	2	n/a
H_CRP_DHS_3068	<i>S/C self transition to Survival</i>	7	2	n/a
H_CRP_DHS_3069	<i>RM log clearing</i>	7	2	n/a
H_CRP_DHS_3070	<i>Configuration check after MM board failure</i>	7	2	n/a
H_CRP_DHS_3071	<i>Recovery after MM A failure</i>	7	2	n/a
H_CRP_DHS_3072	<i>Recovery after MM B failure</i>	7	2	n/a
H_CRP_DHS_ACDIS	<i>Disable ACC APID in MTL</i>	7	2	n/a
H_CRP_DHS_ATT_A	<i>Reset PAP attempt counters on RM A</i>	7	2	n/a
H_CRP_DHS_ATT_B	<i>Reset PAP attempt counters on RM B</i>	7	2	n/a
H_CRP_DHS_DODSR	<i>Survival register modif after DoD</i>	7	2	n/a
H_CRP_DHS_DODUU	<i>Unit in Use modif after DoD</i>	7	2	n/a
H_CRP_DHS_FCCT	<i>FCCT parameter update</i>	7	2	n/a
H_CRP_DHS_HKCY	<i>Cycle through HK / Periodic / Diag CDMU packets</i>	7	2	n/a
H_CRP_DHS_MTLRJ	<i>MTL rejoin</i>	7	2	n/a
H_CRP_DHS_OBCP	<i>Load Instrument FDIR OBCPs</i>	7	2	n/a

ID	TITLE	Volume	Chapter	Section
H_CRP_DHS_PAP_A	<i>Choose new PAP set for RM A</i>	7	2	n/a
H_CRP_DHS_PAP_B	<i>Choose new PAP set for RM B</i>	7	2	n/a
H_CRP_DHS_SW40	<i>MOT EAT HLT cleanup after restart with OBSW 4_0</i>	7	2	n/a
H_CRP_DHS_WDDL	<i>Patch of Watchdog Temporisation Delay</i>	7	2	n/a
H_CRP_DHS_XEAT	<i>Loading of the EAT entries related to OBCEPs</i>	7	2	n/a
H_CRP_EPS_BCR	<i>Enable or disable BCRs</i>	7	2	n/a
H_CRP_EPS_BDR	<i>Switch BDRs APS and Input Switch ONOFF</i>	7	2	n/a
H_CRP_EPS_DNELR	<i>DNEL Reset</i>	7	2	n/a
H_CRP_EPS_DOD	<i>Set DoD voltage threshold</i>	7	2	n/a
H_CRP_EPS_EOC	<i>Set EoC level</i>	7	2	n/a
H_CRP_EPS_FDIPR	<i>Trigger PCS FDIR Level 1 Recovery</i>	7	2	n/a
H_CRP_EPS_IF5N	<i>Configure TMTC N (RT 5) Nominal after PCDU 1553 bus failure</i>	7	2	n/a
H_CRP_EPS_IF5R	<i>Switch to TMTC N after PCDU 1553 bus failure</i>	7	2	n/a
H_CRP_EPS_IF6N	<i>Configure TMTC R (RT 6) Nominal after PCDU 1553 bus failure</i>	7	2	n/a
H_CRP_EPS_IF6R	<i>Switch to TMTC R after PCDU 1553 bus failure</i>	7	2	n/a
H_CRP_EPS_IFNR	<i>Switchover from N to R TMTC</i>	7	2	n/a
H_CRP_EPS_IFR	<i>Configuration check after PCDU 1553 bus failure</i>	7	2	n/a
H_CRP_EPS_IFRN	<i>Switchover from R to N TMTC</i>	7	2	n/a

ID	TITLE	Volume	Chapter	Section
H_CRP_EPS_LCLR	<i>LCL Recovery after trip-off</i>	7	2	n/a
H_CRP_EPS_MANG	<i>Start/Stop PCDU Management function</i>	7	2	n/a
H_CRP_EPS_RTERR	<i>Recovery after PCDU RT Error Flags on 1553 S/C bus</i>	7	2	n/a
H_CRP_HIF_CALL	<i>HIFI Recovery - ON-CALL SOE</i>	7	3	n/a
H_CRP_HIF_FAST	<i>Fast HK Monitoring</i>	7	3	n/a
H_CRP_HIF_ICU	<i>HIFI ICU SEU recovery</i>	7	3	n/a
H_CRP_HIF_ISEU	<i>Recovery of ICU SEU</i>	7	3	n/a
H_CRP_HIF_PHIG	<i>HifiManCmdCopyToPMHigh</i>	7	3	n/a
H_CRP_HIF_PLOW	<i>HifiManCmdCopyToPMLow</i>	7	3	n/a
H_CRP_HIF_RLBM	<i>Load a new OBSM image in Rescue mode</i>	7	3	n/a
H_CRP_HIF_RROF	<i>Switch OFF from Rescue mode (Nominal)</i>	7	3	n/a
H_CRP_HIF_SPCN	<i>HIFI Recovery - SPACON</i>	7	3	n/a
H_CRP_HIF_XPRT	<i>HIFI Recovery - EXPERT</i>	7	3	n/a
H_CRP_OBS_LCUD	<i>Execute HIFI LCU DUMP in case of SEU</i>	7	3	n/a
H_CRP_OBS_LCUP	<i>Execute HIFI LCU patch in case of SEU</i>	7	3	n/a
H_CRP_PAC_BOLO	<i>PacsEng_BOLO_cooler</i>	7	3	n/a
H_CRP_PAC_CSSF	<i>Transition to SAFE mode</i>	7	3	n/a
H_CRP_PAC_NSIF	<i>Immediate switch OFF (Nominal)</i>	7	3	n/a

ID	TITLE	Volume	Chapter	Section
H_CRP_PAC_OBCP	<i>PACS OBCP Recovery</i>	7	3	n/a
H_CRP_PAC_PEP1	<i>SpirePacsEng_Parallel_PacsEpilogue</i>	7	3	n/a
H_CRP_PAC_PHOT	<i>PacsEng_PHOT_orbit_prologue</i>	7	3	n/a
H_CRP_PAC_PPRO	<i>SpirePacsEng_Parallel_PacsPrologue</i>	7	3	n/a
H_CRP_PAC_RSIF	<i>Immediate switch OFF (Redundant)</i>	7	3	n/a
H_CRP_PAC_SPEC	<i>PacsEng_Spec_setup_Flex</i>	7	3	n/a
H_CRP_SPI_CREC	<i>CRP SpireEngCoolerRecycle</i>	7	3	n/a
H_CRP_SPI_DUMP	<i>SpireEngSlowPMDump Dump and Check Memory (CRP)</i>	7	3	n/a
H_CRP_SPI_ENEA	<i>Enable SPIRE Event Actions</i>	7	3	n/a
H_CRP_SPI_LTAB	<i>Lifeboat SpireEngLoadAllTables</i>	7	3	n/a
H_CRP_SPI_MON	<i>Start Monitoring</i>	7	3	n/a
H_CRP_SPI_ON	<i>Switch ON SPIRE</i>	7	3	n/a
H_CRP_SPI_PH2R	<i>Recovery SpireEngPHOT_STBYtoREDY</i>	7	3	n/a
H_CRP_SPI_PTAB	<i>SPIRE table bitflip recovery</i>	7	3	n/a
H_CRP_SPI_R2PH	<i>Lifeboat SpireEngREDYtoPHOT_STBY</i>	7	3	n/a
H_CRP_SPI_R2SP	<i>Lifeboat EngREDYtoSPEC_STBY</i>	7	3	n/a
H_CRP_SPI_SCAN	<i>SpireEngSmecScan</i>	7	3	n/a
H_CRP_SPI_SEU	<i>SpireEngPatchPM</i>	7	3	n/a

ID	TITLE	Volume	Chapter	Section
H_CRP_SPI_SMLN	<i>Lifeb SpireEngSmecLedLvdt Switch on SMEC LED and LVDT</i>	7	3	n/a
H_CRP_SYS_ANOM	<i>System Anomalies</i>	7	2	n/a
H_CRP_SYS_BLIND	<i>Both STRs unhealthy</i>	7	2	n/a
H_CRP_SYS_CHECK	<i>FDIR 3&4 Anomaly Checkout</i>	7	2	n/a
H_CRP_SYS_DEC2	<i>Restart telescope decontamination</i>	7	2	n/a
H_CRP_SYS_DECC	<i>Restart telescope decontamination</i>	7	2	n/a
H_CRP_SYS_DECH	<i>Recovery from a decontamination heater stuck ON failure</i>	7	2	n/a
H_CRP_SYS_DECM	<i>Start/Stop Decontamination heating function</i>	7	2	n/a
H_CRP_SYS_DECP	<i>Decontamination Heating parameters Update</i>	7	2	n/a
H_CRP_SYS_DECR	<i>Decontamination Heating failure recovery</i>	7	2	n/a
H_CRP_SYS_DOD	<i>Herschel DoD Recovery</i>	7	2	n/a
H_CRP_SYS_LOUB	<i>Roll back to Nominal Lou heating configuration</i>	7	2	n/a
H_CRP_SYS_LOUM	<i>Start/Stop Lou Baffle Management function</i>	7	2	n/a
H_CRP_SYS_LOUP	<i>LOU Baffle Parameters Update</i>	7	2	n/a
H_CRP_SYS_LOUR	<i>Recovery from a LOU heater failure</i>	7	2	n/a
H_CRP_SYS_NOTC	<i>No TC Recovery</i>	7	2	n/a
H_CRP_SYS_NOTM	<i>No TM Recovery</i>	7	2	n/a
H_CRP_SYS_PATA	<i>ACC Memory Patch</i>	7	2	n/a

ID	TITLE	Volume	Chapter	Section
H_CRP_SYS_PATC	<i>CDMU Memory Patch</i>	7	2	n/a
H_CRP_SYS_PATCS	<i>Severe CDMU Memory Patch</i>	7	2	n/a
H_CRP_SYS_PMSA	<i>ACC PM Switchover</i>	7	2	n/a
H_CRP_SYS_PMSC	<i>CDMU PM Switchover</i>	7	2	n/a
H_CRP_SYS_RIOP	<i>Rejoin Instrument Operations</i>	7	2	n/a
H_CRP_SYS_TMTC	<i>Configure TM/TC after Mode Drop</i>	7	2	n/a
H_CRP_TCS_01H0	<i>Switch OFF HCS of HPS1</i>	7	2	n/a
H_CRP_TCS_01H1	<i>Switch ON HCS of HPS1</i>	7	2	n/a
H_CRP_TCS_02H0	<i>Switch OFF HCS of HPS2</i>	7	2	n/a
H_CRP_TCS_02H1	<i>Switch ON HCS of HPS2</i>	7	2	n/a
H_CRP_TCS_03H0	<i>Switch OFF HCS of HPS3</i>	7	2	n/a
H_CRP_TCS_03H1	<i>Switch ON HCS of HPS3</i>	7	2	n/a
H_CRP_TCS_04H0	<i>Switch OFF HCS of HPS4</i>	7	2	n/a
H_CRP_TCS_04H1	<i>Switch ON HCS of HPS4</i>	7	2	n/a
H_CRP_TCS_05H0	<i>Switch OFF HCS of HPS5</i>	7	2	n/a
H_CRP_TCS_05H1	<i>Switch ON HCS of HPS5</i>	7	2	n/a
H_CRP_TCS_06H0	<i>Switch OFF HCS of HPS6</i>	7	2	n/a
H_CRP_TCS_06H1	<i>Switch ON HCS of HPS6</i>	7	2	n/a

ID	TITLE	Volume	Chapter	Section
H_CRP_TCS_07H0	<i>Switch OFF HCS of HPS7</i>	7	2	n/a
H_CRP_TCS_07H1	<i>Switch ON HCS of HPS7</i>	7	2	n/a
H_CRP_TCS_08H0	<i>Switch OFF HCS of HPS8</i>	7	2	n/a
H_CRP_TCS_08H1	<i>Switch ON HCS of HPS8</i>	7	2	n/a
H_CRP_TCS_09H0	<i>Switch OFF HCS of HPS9</i>	7	2	n/a
H_CRP_TCS_09H1	<i>Switch ON HCS of HPS9</i>	7	2	n/a
H_CRP_TCS_10H0	<i>Switch OFF HCS of HPS10</i>	7	2	n/a
H_CRP_TCS_10H1	<i>Switch ON HCS of HPS10</i>	7	2	n/a
H_CRP_TCS_11H0	<i>Switch OFF HCS of HPS11</i>	7	2	n/a
H_CRP_TCS_11H1	<i>Switch ON HCS of HPS11</i>	7	2	n/a
H_CRP_TCS_12H0	<i>Switch OFF HCS of HPS12</i>	7	2	n/a
H_CRP_TCS_12H1	<i>Switch ON HCS of HPS12</i>	7	2	n/a
H_CRP_TCS_13H0	<i>Switch OFF HCS of HPS13</i>	7	2	n/a
H_CRP_TCS_13H1	<i>Switch ON HCS of HPS13</i>	7	2	n/a
H_CRP_TCS_14H0	<i>Switch OFF HCS of HPS14</i>	7	2	n/a
H_CRP_TCS_14H1	<i>Switch ON HCS of HPS14</i>	7	2	n/a
H_CRP_TCS_15H0	<i>Switch OFF HCS of HPS15</i>	7	2	n/a
H_CRP_TCS_15H1	<i>Switch ON HCS of HPS15</i>	7	2	n/a

ID	TITLE	Volume	Chapter	Section
H_CRP_TCS_16H0	<i>Switch OFF HCS of HPS16</i>	7	2	n/a
H_CRP_TCS_16H1	<i>Switch ON HCS of HPS16</i>	7	2	n/a
H_CRP_TCS_17H0	<i>Switch OFF HCS of HPS17</i>	7	2	n/a
H_CRP_TCS_17H1	<i>Switch ON HCS of HPS17</i>	7	2	n/a
H_CRP_TCS_18H0	<i>Switch OFF HCS of HPS18</i>	7	2	n/a
H_CRP_TCS_18H1	<i>Switch ON HCS of HPS18</i>	7	2	n/a
H_CRP_TCS_CLMX	<i>Thermal Control Loop MOT Check_mask update</i>	7	2	n/a
H_CRP_TCS_CLN	<i>Configure Redundant Heater Group [10 to 18] Nominal after CL failure</i>	7	2	n/a
H_CRP_TCS_CLN2	<i>Configure Redundant Heater Group [1 to 9] Nominal after CL failure</i>	7	2	n/a
H_CRP_TCS_CLR	<i>Configuration check after thermal control loop failure</i>	7	2	n/a
H_CRP_TCS_CLRB	<i>Roll back after thermal control loop on board reconfiguration</i>	7	2	n/a
H_CRP_TCS_FCCG	<i>Restore Ground FCCT parameters</i>	7	2	n/a
H_CRP_TCS_FDIR	<i>Trigger TCS FDIR Level 2 Recovery</i>	7	2	n/a
H_CRP_TCS_HCNR	<i>Heater Group reconfiguration after failure of the Nominal one</i>	7	2	n/a
H_CRP_TCS_HCPR	<i>Reset Heater Group protection</i>	7	2	n/a
H_CRP_TCS_HCR	<i>Configuration check after HCS high dissipation detection</i>	7	2	n/a
H_CRP_TCS_HCRN	<i>Heater Group reconfiguration from HPS 10-18 to HPS 1-9</i>	7	2	n/a
H_CRP_TCS_HPNO	<i>Nominal HPS switch OFF</i>	7	2	n/a

ID	TITLE	Volume	Chapter	Section
H_CRP_TCS_HPN1	<i>Nominal HPS switch ON</i>	7	2	n/a
H_CRP_TCS_HPR0	<i>Redundant HPS switch OFF</i>	7	2	n/a
H_CRP_TCS_HPR1	<i>Redundant HPS switch ON</i>	7	2	n/a
H_CRP_TCS_HPSR	<i>HPS current OOLs</i>	7	2	n/a
H_CRP_TCS_MANG	<i>Start/Stop Thermal Control function</i>	7	2	n/a
H_CRP_TCS_RLRB	<i>Roll back after thermal control loop on board reconfiguration TCS B</i>	7	2	n/a
H_CRP_TCS_TCT	<i>Thermal Control Table maintenance</i>	7	2	n/a
H_CRP_TCS_TCTF	<i>In-flight Thermal Control Table updates</i>	7	2	n/a
H_CRP_TCS_THMR	<i>Configuration check after thermistor failure</i>	7	2	n/a
H_CRP_TTC_60R	<i>Configuration check after Ground timeout procedure</i>	7	2	n/a
H_CRP_TTC_60RB	<i>Alignment of TTC configuration</i>	7	2	n/a
H_CRP_TTC_ENCR	<i>Reset TM Encoder</i>	7	2	n/a
H_CRP_TTC_FDIR	<i>Trigger TTC FDIR Level 1 Recovery</i>	7	2	n/a
H_CRP_TTC_MANG	<i>Start/Stop TTC Management function</i>	7	2	n/a
H_CRP_TTC_RTERR	<i>Recovery after XPND RT Error Flags on 1553 S/C bus</i>	7	2	n/a
H_CRP_TTC_SWR	<i>Configuration check after RFDN SWs failure</i>	7	2	n/a
H_CRP_TTC_SWRB	<i>Roll back after RFDN SWs failure</i>	7	2	n/a
H_CRP_TTC_SWXF	<i>Antenna switching via Mission Specific commands</i>	7	2	n/a

ID	TITLE	Volume	Chapter	Section
H_CRP_TTC_T100	<i>Switch OFF Tx1 and TWTA1</i>	7	2	n/a
H_CRP_TTC_T101	<i>Switch ON Tx1 and TWTA1</i>	7	2	n/a
H_CRP_TTC_T10R	<i>Switch to chain 1 after XPND1 or TWTA1 failure</i>	7	2	n/a
H_CRP_TTC_T10X	<i>Configure Tx1</i>	7	2	n/a
H_CRP_TTC_T1CM	<i>Transponder 1 Coherent Mode Activation/Deactivation</i>	7	2	n/a
H_CRP_TTC_T1HC	<i>TTC chain 1 health check</i>	7	2	n/a
H_CRP_TTC_T1HR	<i>Tx1 and TM encoder in use configuration for HR</i>	7	2	n/a
H_CRP_TTC_T1L1	<i>Tx1 and TM encoder in use configuration for LR1</i>	7	2	n/a
H_CRP_TTC_T1L2	<i>Tx1 and TM encoder in use configuration for LR2</i>	7	2	n/a
H_CRP_TTC_T1LR	<i>Switch to chain 1 after a TTC-S out of limit</i>	7	2	n/a
H_CRP_TTC_T1MR	<i>Tx1 and TM encoder in use configuration for MR</i>	7	2	n/a
H_CRP_TTC_T1N	<i>Configure TTC chain 1 Nominal after XPN2 or TWTA2 failure</i>	7	2	n/a
H_CRP_TTC_T1RM	<i>Transponder 1 Ranging Activation/Deactivation</i>	7	2	n/a
H_CRP_TTC_T200	<i>Switch OFF TX2 and TWTA2</i>	7	2	n/a
H_CRP_TTC_T201	<i>Switch ON TX2 and TWTA2</i>	7	2	n/a
H_CRP_TTC_T20R	<i>Switch to chain 2 after XPND2 or TWTA2 failure</i>	7	2	n/a
H_CRP_TTC_T20X	<i>Configure TX2</i>	7	2	n/a
H_CRP_TTC_T2CM	<i>Transponder 2 Coherent Mode Activation/Deactivation</i>	7	2	n/a

ID	TITLE	Volume	Chapter	Section
H_CRP_TTC_T2HR	<i>Tx2 and TM encoder in use configuration for HR</i>	7	2	n/a
H_CRP_TTC_T2L1	<i>Tx2 and TM encoder in use configuration for LR1</i>	7	2	n/a
H_CRP_TTC_T2L2	<i>Tx2 and TM encoder in use configuration for LR2</i>	7	2	n/a
H_CRP_TTC_T2LR	<i>Switch to chain 2 after a TTC-S out of limit</i>	7	2	n/a
H_CRP_TTC_T2MR	<i>Tx2 and TM encoder in use configuration for MR</i>	7	2	n/a
H_CRP_TTC_T2MX	<i>Configure TX2 for MR</i>	7	2	n/a
H_CRP_TTC_T2N	<i>Configure TTC chain 2 Nominal after XPND1 or TWTA1 failure</i>	7	2	n/a
H_CRP_TTC_T2RM	<i>Transponder 2 Ranging Activation/Deactivation</i>	7	2	n/a
H_CRP_TTC_TLR	<i>Recovery after a TTC-S out of limit</i>	7	2	n/a
H_CRP_TTC_TMES	<i>Switch TM Enc Subsampling ON/OFF</i>	7	2	n/a
H_CRP_TTC_TTCR	<i>Configuration check after XPNDs or TWTAs failure</i>	7	2	n/a
H_CRP_TTC_TU01	<i>Switch ON Tx and TWTA in use contingency</i>	7	2	n/a
H_CRP_TTC_TU0X	<i>Configure Tx in use</i>	7	2	n/a
H_CRP_TTC_TUL1	<i>Tx and TM encoder in use configuration for LR1</i>	7	2	n/a
H_CRP_TTC_TUL2	<i>Tx and TM encoder in use configuration for LR2</i>	7	2	n/a
H_FCP_AOC_0OCM	<i>Procedure for pointing in OCM</i>	6	2	n/a
H_FCP_AOC_0SCM	<i>Procedure for SCM Fine Pointing</i>	6	2	n/a
H_FCP_AOC_0SFX	<i>Procedure for planning transition to SCM</i>	6	2	n/a

ID	TITLE	Volume	Chapter	Section
H_FCP_AOC_1GDR	<i>Update GRY biases</i>	6	2	n/a
H_FCP_AOC_1GSM	<i>Update GYR scale factors and misalignments</i>	6	2	n/a
H_FCP_AOC_1STM	<i>Update STR misalignments</i>	6	2	n/a
H_FCP_AOC_1STO	<i>Update STR Alignment Quaternion</i>	6	2	n/a
H_FCP_AOC_3000	<i>ACMS health check</i>	6	2	n/a
H_FCP_AOC_3001	<i>Verify SCM Configuration</i>	6	2	n/a
H_FCP_AOC_3011	<i>ERD buffer dump</i>	6	2	n/a
H_FCP_AOC_3A01	<i>Transition from OCM or SCM to SAM</i>	6	2	n/a
H_FCP_AOC_3M02	<i>Update Control Parameters - inertia related</i>	6	2	n/a
H_FCP_AOC_3M03	<i>Update Sun Earth Ephemerides</i>	6	2	n/a
H_FCP_AOC_3O01	<i>Perform Delta-V</i>	6	2	n/a
H_FCP_AOC_3O02	<i>Entry in OCM for first Delta-V</i>	6	2	n/a
H_FCP_AOC_3O04	<i>Change ACMS mode from SCM to OCM</i>	6	2	n/a
H_FCP_AOC_3O05	<i>Change ACMS mode from SAM to OCM</i>	6	2	n/a
H_FCP_AOC_3S01	<i>Perform SCM Fine Pointing</i>	6	2	n/a
H_FCP_AOC_3S02	<i>Perform SCM Raster Pointing</i>	6	2	n/a
H_FCP_AOC_3S03	<i>Perform SCM scan</i>	6	2	n/a
H_FCP_AOC_3S04	<i>Command Peak-up</i>	6	2	n/a

ID	TITLE	Volume	Chapter	Section
H_FCP_AOC_3S05	<i>Command SSO Tracking</i>	6	2	n/a
H_FCP_AOC_3S06	<i>Science Observations</i>	6	2	n/a
H_FCP_AOC_3S07	<i>Entry into SCM for the first time after PM start/reset</i>	6	2	n/a
H_FCP_AOC_4001	<i>ACC RM Enable/Disable</i>	6	2	n/a
H_FCP_AOC_4C01	<i>Update CRS ARAD thresholds</i>	6	2	n/a
H_FCP_AOC_4R14	<i>Declare RWL assembly operational</i>	6	2	n/a
H_FCP_AOC_4R20	<i>Perform RWL biasing in SCM</i>	6	2	n/a
H_FCP_AOC_4R34	<i>Perform RWL bias in OCM</i>	6	2	n/a
H_FCP_AOC_4R44	<i>Update RWL misalignment</i>	6	2	n/a
H_FCP_AOC_4S01	<i>Declare STR Operational</i>	6	2	n/a
H_FCP_AOC_4S11	<i>Make STR1 operational as main</i>	6	2	n/a
H_FCP_AOC_4S21	<i>Make STR2 operational as main</i>	6	2	n/a
H_FCP_AOC_4S41	<i>Update S/C orbital velocity in STR</i>	6	2	n/a
H_FCP_AOC_4S4X	<i>Update S/C orbital velocity in STR</i>	6	2	n/a
H_FCP_AOC_4S51	<i>Memory dump for STR main</i>	6	2	n/a
H_FCP_AOC_4S52	<i>Memory dump for STR redundant</i>	6	2	n/a
H_FCP_AOC_4S61	<i>Patch memory of the main STR</i>	6	2	n/a
H_FCP_AOC_4S62	<i>Patch memory of the redundant STR</i>	6	2	n/a

ID	TITLE	Volume	Chapter	Section
H_FCP_AOC_4S71	<i>Dump defective pixel table for STR main</i>	6	2	n/a
H_FCP_AOC_4S81	<i>Update parameters for STR main</i>	6	2	n/a
H_FCP_AOC_4S91	<i>Dump STR CCD Image</i>	6	2	n/a
H_FCP_AOC_5004	<i>Maintenance phase of 1 day period</i>	6	2	n/a
H_FCP_AOC_5007	<i>STR health check</i>	6	2	n/a
H_FCP_AOC_5008	<i>CRS health check</i>	6	2	n/a
H_FCP_AOC_5009	<i>SAS health check</i>	6	2	n/a
H_FCP_AOC_5010	<i>AAD health check</i>	6	2	n/a
H_FCP_AOC_5011	<i>RCS Health Check</i>	6	2	n/a
H_FCP_AOC_5012	<i>ACC Health Check</i>	6	2	n/a
H_FCP_AOC_5013	<i>GYR health check</i>	6	2	n/a
H_FCP_AOC_5014	<i>RWL health check</i>	6	2	n/a
H_FCP_AOC_5CBH	<i>Switch ON thruster catbed heaters</i>	6	2	n/a
H_FCP_AOC_8CCM	<i>Dump STRM CCD Image</i>	6	2	n/a
H_FCP_AOC_8CCZ	<i>Dump STRM CCD Image</i>	6	2	n/a
H_FCP_AOC_8DPT	<i>Patch STR Defective Pixel Table</i>	6	2	n/a
H_FCP_AOC_8ON1	<i>Make STR1 operational and configure mode</i>	6	2	n/a
H_FCP_AOC_8ON2	<i>Make STR2 operational and configure mode</i>	6	2	n/a

ID	TITLE	Volume	Chapter	Section
H_FCP_AOC_8PM1	<i>Patch STR1 Memory</i>	6	2	n/a
H_FCP_AOC_8PM2	<i>Patch STR2 memory</i>	6	2	n/a
H_FCP_AOC_8PMM	<i>Patch STRmain Memory</i>	6	2	n/a
H_FCP_AOC_D3FD	<i>Configure DTM for FD</i>	6	2	n/a
H_FCP_AOC_D3LW	<i>Enable/Disable Low-Rate Mode Telemetry</i>	6	2	n/a
H_FCP_AOC_D5DF	<i>Restore default OBDB for first dV</i>	6	2	n/a
H_FCP_AOC_D5SM	<i>Update RCS Control Parameters - thrust level related</i>	6	2	n/a
H_FCP_AOC_DAFO	<i>Herschel ACMS : Set ACMS FDIR Mode</i>	6	2	n/a
H_FCP_AOC_DCIR	<i>Modify CIR flag in OBDB</i>	6	2	n/a
H_FCP_AOC_DCOV	<i>Update STR Covariance Check related parameters</i>	6	2	n/a
H_FCP_AOC_DDEF	<i>Update Default OBDB Values</i>	6	2	n/a
H_FCP_AOC_DGYR	<i>Disable GYR checks prior to CRYO cover release</i>	6	2	n/a
H_FCP_AOC_DODB	<i>Herschel ACMS : Generic OBDB Modification Procedure</i>	6	2	n/a
H_FCP_AOC_DODD	<i>Generic OBDB Dump Procedure</i>	6	2	n/a
H_FCP_AOC_DRMR	<i>Read RM Register Content</i>	6	2	n/a
H_FCP_AOC_DRWL	<i>Enable/Disable RWS Low Speed Zone avoidance</i>	6	2	n/a
H_FCP_AOC_DSTF	<i>STR FDIR Disable/Enable</i>	6	2	n/a
H_FCP_CCU_ACQP	<i>CCU acquisition period update</i>	6	3	n/a

ID	TITLE	Volume	Chapter	Section
H_FCP_CCU_CHECK	<i>CCU subsystem checkout</i>	6	3	n/a
H_FCP_CCU_DLCM	<i>CCU DLCM</i>	6	3	n/a
H_FCP_CCU_MONS	<i>CCU Sensors monitoring</i>	6	3	n/a
H_FCP_CCU_MONV	<i>CCU Valves Status monitoring</i>	6	3	n/a
H_FCP_CCU_REPO	<i>Payload management status Report</i>	6	3	n/a
H_FCP_CCU_TCHK	<i>CCU TM TOPE check</i>	6	3	n/a
H_FCP_DHS_1001	<i>Upload default housekeeping and diagnostic packets.</i>	6	2	n/a
H_FCP_DHS_1002	<i>Upload default MOT table.</i>	6	2	n/a
H_FCP_DHS_1003	<i>Nominal TRANSMIT/STORAGE settings</i>	6	2	n/a
H_FCP_DHS_1004	<i>Nominal TM Pkt to Store Routing</i>	6	2	n/a
H_FCP_DHS_1005	<i>Define the standard packet stores on SSMM A and B (4 banks)</i>	6	2	n/a
H_FCP_DHS_1006	<i>Default packet store catalogues.</i>	6	2	n/a
H_FCP_DHS_1007	<i>Dump of PSITs from default stores.</i>	6	2	n/a
H_FCP_DHS_1008	<i>AD protocol initialisation</i>	6	2	n/a
H_FCP_DHS_1009	<i>Enable default HK packets</i>	6	2	n/a
H_FCP_DHS_1010	<i>MM Banks 0, 1, 2 and 3 power ON.</i>	6	2	n/a
H_FCP_DHS_1011	<i>MTL Uplink</i>	6	2	n/a
H_FCP_DHS_1012	<i>Write survival register after separation</i>	6	2	n/a

ID	TITLE	Volume	Chapter	Section
H_FCP_DHS_1013	<i>Enable/disable VCI RT</i>	6	2	n/a
H_FCP_DHS_1014	<i>Dump of CEL and SSMM packet stores</i>	6	2	n/a
H_FCP_DHS_1015	<i>Routine CDMU Checks for DTCP</i>	6	2	n/a
H_FCP_DHS_1016	<i>Rejoin HIFI</i>	6	2	n/a
H_FCP_DHS_1017	<i>Rejoin PACS</i>	6	2	n/a
H_FCP_DHS_1018	<i>Rejoin SPIRE</i>	6	2	n/a
H_FCP_DHS_1019	<i>Set SCBP 2 (HIFI prime)</i>	6	2	n/a
H_FCP_DHS_1020	<i>Set SCBP 4 (PACS Prime)</i>	6	2	n/a
H_FCP_DHS_1021	<i>Set SCBP 3 (SPIRE prime)</i>	6	2	n/a
H_FCP_DHS_1022	<i>Select SCBP 8 (parallel mode)</i>	6	2	n/a
H_FCP_DHS_1023	<i>Set SCBP 7 (burst mode)</i>	6	2	n/a
H_FCP_DHS_1025	<i>SSMM dumps after DTCP loss.</i>	6	2	n/a
H_FCP_DHS_1026	<i>Start MTL function during LEOP</i>	6	2	n/a
H_FCP_DHS_1027	<i>Dumping BSW TM routing Info array</i>	6	2	n/a
H_FCP_DHS_1028	<i>List the HKID defined on board (using SGM dump)</i>	6	2	n/a
H_FCP_DHS_1029	<i>CDMU and ACC TC test (Serv 17 or BD/BSW counter only)</i>	6	2	n/a
H_FCP_DHS_1030	<i>CDMU OBSW data acquisition</i>	6	2	n/a
H_FCP_DHS_1031	<i>Read main registers</i>	6	2	n/a

ID	TITLE	Volume	Chapter	Section
H_FCP_DHS_1032	<i>Set the [TM generating TC] exe rate</i>	6	2	n/a
H_FCP_DHS_1033	<i>Abort or modify rate of MTL reports</i>	6	2	n/a
H_FCP_DHS_1034	<i>Increase VC0 rate</i>	6	2	n/a
H_FCP_DHS_1035	<i>Dump of main On-board tables from SGM</i>	6	2	n/a
H_FCP_DHS_1036	<i>HIFI LCL Supercommutation</i>	6	2	n/a
H_FCP_DHS_1037	<i>Setup OBCP MOT EAT for SPIRE FDIR</i>	6	2	n/a
H_FCP_DHS_1038	<i>Set SCBP 14 (HIFI prime with SupComm)</i>	6	2	n/a
H_FCP_DHS_1039	<i>Re-Enabling of Payload Subschedules</i>	6	2	n/a
H_FCP_DHS_3001	<i>Define housekeeping or diagnostic packet</i>	6	2	n/a
H_FCP_DHS_3002	<i>Clear housekeeping or diagnostic packet</i>	6	2	n/a
H_FCP_DHS_3004	<i>Set timeout for ASW internal event filtering</i>	6	2	n/a
H_FCP_DHS_3005	<i>Report ASW event filter function and settings</i>	6	2	n/a
H_FCP_DHS_3006	<i>Switch to TM encoder A and OBT A</i>	6	2	n/a
H_FCP_DHS_3007	<i>Switch to TM encoder B and OBT B</i>	6	2	n/a
H_FCP_DHS_3008	<i>Switch PM A ON or OFF</i>	6	2	n/a
H_FCP_DHS_3009	<i>Switch PM B ON or OFF</i>	6	2	n/a
H_FCP_DHS_3010	<i>Read TTR CROME register</i>	6	2	n/a
H_FCP_DHS_3011	<i>Abort current memory dump</i>	6	2	n/a

ID	TITLE	Volume	Chapter	Section
H_FCP_DHS_3012	<i>Dump and interpretation of Reconfiguration Log</i>	6	2	n/a
H_FCP_DHS_3013	<i>Dump and interpretation of Unit in Use table</i>	6	2	n/a
H_FCP_DHS_3014	<i>Dump and interpretation of CEL</i>	6	2	n/a
H_FCP_DHS_3015	<i>Dump and interpretation of SEL</i>	6	2	n/a
H_FCP_DHS_3016	<i>Map a MM ID to a MM bank</i>	6	2	n/a
H_FCP_DHS_3017	<i>Switching a MM bank ON or OFF</i>	6	2	n/a
H_FCP_DHS_3018	<i>MM ID reinitialization</i>	6	2	n/a
H_FCP_DHS_3019	<i>Remote terminal synchronization with bus controller</i>	6	2	n/a
H_FCP_DHS_3020	<i>Time verification</i>	6	2	n/a
H_FCP_DHS_3021	<i>Set central time reference synchronization</i>	6	2	n/a
H_FCP_DHS_3022	<i>Enable or disable release of TC from MTL</i>	6	2	n/a
H_FCP_DHS_3023	<i>Clearing the complete MTL</i>	6	2	n/a
H_FCP_DHS_3024	<i>Normal MTL maintenance</i>	6	2	n/a
H_FCP_DHS_3025	<i>Starting or stopping the MTL function</i>	6	2	n/a
H_FCP_DHS_3026	<i>Report MTL function</i>	6	2	n/a
H_FCP_DHS_3027	<i>Dump MTL summary or detailed report</i>	6	2	n/a
H_FCP_DHS_3028	<i>Enable or disable the on board monitoring for a parameter</i>	6	2	n/a
H_FCP_DHS_3029	<i>Clearing the complete monitoring table</i>	6	2	n/a

ID	TITLE	Volume	Chapter	Section
H_FCP_DHS_3030	<i>Monitoring table maintenance</i>	6	2	n/a
H_FCP_DHS_3032	<i>Report monitoring function</i>	6	2	n/a
H_FCP_DHS_3033	<i>Enable or disable the generation of an housekeeping or diagnostic packet</i>	6	2	n/a
H_FCP_DHS_3034	<i>TM packet routing to downlink or MM or both</i>	6	2	n/a
H_FCP_DHS_3035	<i>Enable or disable storage in a MM packet store</i>	6	2	n/a
H_FCP_DHS_3036	<i>Allocate or deallocate TM type and subtype in a MM packet store</i>	6	2	n/a
H_FCP_DHS_3037	<i>TM packet store downlink and maintenance</i>	6	2	n/a
H_FCP_DHS_3038	<i>Packet store creation and VC 2, 3 allocation</i>	6	2	n/a
H_FCP_DHS_3039	<i>Packet store deletion</i>	6	2	n/a
H_FCP_DHS_3040	<i>Perform connection test</i>	6	2	n/a
H_FCP_DHS_3041	<i>Load or delete an OBCP</i>	6	2	n/a
H_FCP_DHS_3042	<i>Start or stop an OBCP</i>	6	2	n/a
H_FCP_DHS_3043	<i>Suspend or resume an OBCP</i>	6	2	n/a
H_FCP_DHS_3044	<i>Set OBCP parameters</i>	6	2	n/a
H_FCP_DHS_3045	<i>Dump OBCP code</i>	6	2	n/a
H_FCP_DHS_3046	<i>Start or stop the whole OBCP function</i>	6	2	n/a
H_FCP_DHS_3047	<i>Report OBCP management function</i>	6	2	n/a
H_FCP_DHS_3048	<i>Clear the complete event-action table</i>	6	2	n/a

ID	TITLE	Volume	Chapter	Section
H_FCP_DHS_3049	<i>Event-action table maintenance</i>	6	2	n/a
H_FCP_DHS_3051	<i>Report event-action management status or event detection list</i>	6	2	n/a
H_FCP_DHS_3052	<i>Bus profile maintenance</i>	6	2	n/a
H_FCP_DHS_3053	<i>Select a bus profile to be active</i>	6	2	n/a
H_FCP_DHS_3054	<i>Enable or disable burst mode for a remote terminal</i>	6	2	n/a
H_FCP_DHS_3055	<i>Configure 1553 bus FDIR for a remote terminal</i>	6	2	n/a
H_FCP_DHS_3056	<i>Enable or disable TC routing to an APID</i>	6	2	n/a
H_FCP_DHS_3058	<i>Set AFO or AFS status of FDIR</i>	6	2	n/a
H_FCP_DHS_3059	<i>Maintenance of FDIR cross correlated checks</i>	6	2	n/a
H_FCP_DHS_3060	<i>Verify configuration matrices for FDIR</i>	6	2	n/a
H_FCP_DHS_3061	<i>Report FDIR management function and FDIR cross correlated table</i>	6	2	n/a
H_FCP_DHS_3067	<i>Report mode management function and current spacecraft mode</i>	6	2	n/a
H_FCP_DHS_3074	<i>Start or stop the whole reconfiguration module (TTR) function</i>	6	2	n/a
H_FCP_DHS_3075	<i>Report reconfiguration module (TTR) status</i>	6	2	n/a
H_FCP_DHS_3076	<i>Saving of HK packets in data pool buffers</i>	6	2	n/a
H_FCP_DHS_3080	<i>Survival Register maintenance</i>	6	2	n/a
H_FCP_DHS_3081	<i>CDMS Subsystem Checkout</i>	6	2	n/a
H_FCP_DHS_4001	<i>S/C Mode transition from Sun Acquisition to Nominal</i>	6	2	n/a

ID	TITLE	Volume	Chapter	Section
H_FCP_DHS_4002	<i>Check Sun Acquisition mode configuration</i>	6	2	n/a
H_FCP_DHS_4003	<i>Check Nominal mode configuration</i>	6	2	n/a
H_FCP_DHS_4004	<i>Check Earth Acquisition mode configuration</i>	6	2	n/a
H_FCP_DHS_4005	<i>S/C Mode transition from Nominal to Earth Acquisition</i>	6	2	n/a
H_FCP_DHS_4007	<i>Check Launch mode configuration</i>	6	2	n/a
H_FCP_DHS_4008	<i>S/C Mode transition from Earth Acquisition to Nominal</i>	6	2	n/a
H_FCP_DHS_4009	<i>Disable strap 2 / 3 alarms (PAP6) and check of strap 1 alarm.</i>	6	2	n/a
H_FCP_DHS_AIR	<i>CDMU checks in sun acquisition mode after AIR</i>	6	2	n/a
H_FCP_DHS_DEFFC	<i>Default FCCT uplink (editable)</i>	6	2	n/a
H_FCP_DHS_DEFMO	<i>Default MOT uplink (editable)</i>	6	2	n/a
H_FCP_DHS_DEFTC	<i>Default TCT uplink (editable)</i>	6	2	n/a
H_FCP_DHS_EACK	<i>CDMU checks in earth acquisition mode</i>	6	2	n/a
H_FCP_DHS_LMCK	<i>CDMU checks in launch mode</i>	6	2	n/a
H_FCP_DHS_NMCK	<i>CDMU checks in Nominal Mode after Sun Acquisition Mode</i>	6	2	n/a
H_FCP_DHS_PKST	<i>OBCP for Packet Store Cat reports generation</i>	6	2	n/a
H_FCP_DHS_SACK	<i>CDMU checks in sun acquisition mode after launch</i>	6	2	n/a
H_FCP_DHS_SRCK	<i>CDMU checks in Survival Mode</i>	6	2	n/a
H_FCP_DHS_STRT	<i>Acquisition of TM data (launch pad listen-in)</i>	6	2	n/a

ID	TITLE	Volume	Chapter	Section
H_FCP_EPS_CHECK	<i>PCS Subsystem Checkout</i>	6	2	n/a
H_FCP_EPS_REPO	<i>PCDU Management Status Report</i>	6	2	n/a
H_FCP_EPS_TCHK	<i>EPS Tope TM checks</i>	6	2	n/a
H_FCP_HIF_2D1A	<i>HifiManCmdSetIntoDissipative_II - 1a</i>	6	3	n/a
H_FCP_HIF_2D1B	<i>HifiManCmdSetIntoDissipative_II - 1b</i>	6	3	n/a
H_FCP_HIF_2D2A	<i>HifiManCmdSetIntoDissipative_II - 2a</i>	6	3	n/a
H_FCP_HIF_2D2B	<i>HifiManCmdSetIntoDissipative_II - 2b</i>	6	3	n/a
H_FCP_HIF_2D3A	<i>HifiManCmdSetIntoDissipative_II - 3a</i>	6	3	n/a
H_FCP_HIF_2D3B	<i>HifiManCmdSetIntoDissipative_II - 3b</i>	6	3	n/a
H_FCP_HIF_2D4A	<i>HifiManCmdSetIntoDissipative_II - 4a</i>	6	3	n/a
H_FCP_HIF_2D4B	<i>HifiManCmdSetIntoDissipative_II - 4b</i>	6	3	n/a
H_FCP_HIF_2D5A	<i>HifiManCmdSetIntoDissipative_II - 5a</i>	6	3	n/a
H_FCP_HIF_2D5B	<i>HifiManCmdSetIntoDissipative_II - 5b</i>	6	3	n/a
H_FCP_HIF_2D6A	<i>HifiManCmdSetIntoDissipative_II - 6a</i>	6	3	n/a
H_FCP_HIF_2D6B	<i>HifiManCmdSetIntoDissipative_II - 6b</i>	6	3	n/a
H_FCP_HIF_2D7A	<i>HifiManCmdSetIntoDissipative_II - 7a</i>	6	3	n/a
H_FCP_HIF_2D7B	<i>HifiManCmdSetIntoDissipative_II - 7b</i>	6	3	n/a
H_FCP_HIF_BOOT	<i>HifiManCmdRebootICU</i>	6	3	n/a

ID	TITLE	Volume	Chapter	Section
H_FCP_HIF_C2S1	<i>HifiManCmdSetIntoStandby_I</i>	6	3	n/a
H_FCP_HIF_CCDD	<i>HifiManCmd_CRC_FDIR_disable</i>	6	3	n/a
H_FCP_HIF_CCEN	<i>HifiManCmd_CRC_FDIR_enable</i>	6	3	n/a
H_FCP_HIF_CCLR	<i>Clear LOU Error Flag</i>	6	3	n/a
H_FCP_HIF_CCRC	<i>LCU checksum recalculation</i>	6	3	n/a
H_FCP_HIF_CLEM	<i>Load OBSW to EEPROM</i>	6	3	n/a
H_FCP_HIF_CLMD	<i>HifiManCmd_LCU_Full_Memory_Dump</i>	6	3	n/a
H_FCP_HIF_CLOM	<i>Load OBSW image</i>	6	3	n/a
H_FCP_HIF_CLRE	<i>HifiManCmd_LCU_RESET</i>	6	3	n/a
H_FCP_HIF_COBS	<i>OBSW functional test</i>	6	3	n/a
H_FCP_HIF_CORE	<i>HifiManCmd_LCU_lock_clear</i>	6	3	n/a
H_FCP_HIF_CPOM	<i>Patch the OBSW</i>	6	3	n/a
H_FCP_HIF_CRTM	<i>HifiManCmd_readback_LCU_table_flight</i>	6	3	n/a
H_FCP_HIF_CS2P	<i>Standby II mode to Primary mode</i>	6	3	n/a
H_FCP_HIF_CSEU	<i>HifiManCmd_OBS_SEU_check</i>	6	3	n/a
H_FCP_HIF_CSPK	<i>Simulate Peakup</i>	6	3	n/a
H_FCP_HIF_CUPM	<i>HifiManCmd_upload_LCU_patches_flight_ops</i>	6	3	n/a
H_FCP_HIF_HEAT	<i>Modify LOU heater setting</i>	6	3	n/a

ID	TITLE	Volume	Chapter	Section
H_FCP_HIF_IMEM	<i>ICU Memory Load</i>	6	3	n/a
H_FCP_HIF_LCDI	<i>HifiManCmd_LCU_comm_FDIR_disable</i>	6	3	n/a
H_FCP_HIF_LCEN	<i>HifiManCmd_LOU_FDIR_enable</i>	6	3	n/a
H_FCP_HIF_LCU	<i>LCU memory patch</i>	6	3	n/a
H_FCP_HIF_LDIS	<i>Disable the Local Oscillator Unit</i>	6	3	n/a
H_FCP_HIF_LENA	<i>Enable the Local Oscillator Unit</i>	6	3	n/a
H_FCP_HIF_LTDI	<i>HifiManCmd_LOU_FDIR_disable</i>	6	3	n/a
H_FCP_HIF_LTEN	<i>HifiManCmd_LOU_FDIR_enable</i>	6	3	n/a
H_FCP_HIF_NUT5	<i>Upload LCU safety table: 2251-2700</i>	6	3	n/a
H_FCP_HIF_NUT7	<i>Upload LCU safety table: 3151-3600</i>	6	3	n/a
H_FCP_HIF_NUTB	<i>Upload LCU safety table: 4951-5400</i>	6	3	n/a
H_FCP_HIF_R2D1	<i>HifiManCmdSetIntoDissipative_I</i>	6	3	n/a
H_FCP_HIF_RCXT	<i>Change context (Open/Closed Loop)</i>	6	3	n/a
H_FCP_HIF_RHC1	<i>Chopper Health Check Test #1</i>	6	3	n/a
H_FCP_HIF_RHC2	<i>Chopper Health Check Test #2</i>	6	3	n/a
H_FCP_HIF_RHC3	<i>Chopper Health Check Test #3</i>	6	3	n/a
H_FCP_HIF_RION	<i>HIFI_SwitchedOff_to_Intermediate_Redundant</i>	6	3	n/a
H_FCP_HIF_RLUT	<i>Upload LCU safety table</i>	6	3	n/a

ID	TITLE	Volume	Chapter	Section
H_FCP_HIF_RSOF	<i>Switch OFF HIFI units (Redundant)</i>	6	3	n/a
H_FCP_HIF_RSON	<i>HIFI_Intermediate_to_StandbyI_Redundant</i>	6	3	n/a
H_FCP_HIF_RUT0	<i>Upload LCU safety table: 1-450</i>	6	3	n/a
H_FCP_HIF_RUT1	<i>Upload LCU safety table: 451-900</i>	6	3	n/a
H_FCP_HIF_RUT2	<i>Upload LCU safety table: 901-1350</i>	6	3	n/a
H_FCP_HIF_RUT3	<i>Upload LCU safety table: 1351-1800</i>	6	3	n/a
H_FCP_HIF_RUT4	<i>Upload LCU safety table: 1801-2250</i>	6	3	n/a
H_FCP_HIF_RUT5	<i>Upload LCU safety table: 2251-2700</i>	6	3	n/a
H_FCP_HIF_RUT6	<i>Upload LCU safety table: 2701-3150</i>	6	3	n/a
H_FCP_HIF_RUT7	<i>Upload LCU safety table: 3151-3600</i>	6	3	n/a
H_FCP_HIF_RUT8	<i>Upload LCU safety table: 3601-4050</i>	6	3	n/a
H_FCP_HIF_RUT9	<i>Upload LCU safety table: 4051-4500</i>	6	3	n/a
H_FCP_HIF_RUTA	<i>Upload LCU safety table: 4501-4950</i>	6	3	n/a
H_FCP_HIF_RUTB	<i>Upload LCU safety table: 4951-5400</i>	6	3	n/a
H_FCP_HIF_RUTC	<i>Upload LCU safety table: 5401-5850</i>	6	3	n/a
H_FCP_HIF_RUTD	<i>Upload LCU safety table: 5851-6300</i>	6	3	n/a
H_FCP_HIF_RUTE	<i>Upload LCU safety table: 6301-6750</i>	6	3	n/a
H_FCP_HIF_RUTF	<i>Upload LCU safety table: 6751-7200</i>	6	3	n/a

ID	TITLE	Volume	Chapter	Section
H_FCP_HIF_RUTG	<i>Upload LCU safety table: 7201-7650</i>	6	3	n/a
H_FCP_HIF_RUTH	<i>Upload LCU safety table: 7651-7905</i>	6	3	n/a
H_FCP_HIF_S0DI	<i>HifiManCmd_Standby0_FDIR_disable</i>	6	3	n/a
H_FCP_HIF_S0EN	<i>HifiManCmd_Standby0_FDIR_enable</i>	6	3	n/a
H_FCP_OBS_1210	<i>Load CDMU OBS in EEPROM</i>	6	2	n/a
H_FCP_OBS_1241	<i>Update CDMU PM PROM ground image from memory dump</i>	6	2	n/a
H_FCP_OBS_1242	<i>Monitor dump of CDMU PM EEPROM memory area</i>	6	2	n/a
H_FCP_OBS_1243	<i>Update CDMU PM EEPROM ground image from memory dump</i>	6	2	n/a
H_FCP_OBS_1245	<i>Update CDMU CPU RAM ground image from memory dump</i>	6	2	n/a
H_FCP_OBS_1249	<i>Update CDMU CPU System Registers ground image from memory dump</i>	6	2	n/a
H_FCP_OBS_1251	<i>Update CDMU PM COCOS Registers ground image from memory dump</i>	6	2	n/a
H_FCP_OBS_1263	<i>Check CDMU OBS image in EEPROM (checksum calculation)</i>	6	2	n/a
H_FCP_OBS_1423	<i>Clean up the CDMU RM Logs</i>	6	2	n/a
H_FCP_OBS_1449	<i>Update CDMU SGM ground image from memory dump</i>	6	2	n/a
H_FCP_OBS_1451	<i>Update CDMU CROME Registers ground image from memory dump</i>	6	2	n/a
H_FCP_OBS_1651	<i>Update CDMU SSMM COCOS Registers ground image from memory dump</i>	6	2	n/a
H_FCP_OBS_2210	<i>Load ACC OBS in EEPROM</i>	6	2	n/a
H_FCP_OBS_2241	<i>Update ACC PM PROM ground image from memory dump</i>	6	2	n/a

ID	TITLE	Volume	Chapter	Section
H_FCP_OBS_2242	<i>Monitor dump of ACC PM EEPROM memory area</i>	6	2	n/a
H_FCP_OBS_2243	<i>Update ACC PM EEPROM ground image from memory dump</i>	6	2	n/a
H_FCP_OBS_2245	<i>Update ACC CPU RAM ground image from memory dump</i>	6	2	n/a
H_FCP_OBS_2249	<i>Update ACC CPU System Registers ground image from memory dump</i>	6	2	n/a
H_FCP_OBS_2251	<i>Update ACC PM COCOS Registers ground image from memory dump</i>	6	2	n/a
H_FCP_OBS_2263	<i>Check ACC OBS image in EEPROM (checksum calculation)</i>	6	2	n/a
H_FCP_OBS_2449	<i>Update ACC SGM ground image from memory dump</i>	6	2	n/a
H_FCP_OBS_2451	<i>Update ACC CROME Registers ground image from memory dump</i>	6	2	n/a
H_FCP_OBS_2810	<i>Load STR OBS in EEPROM</i>	6	2	n/a
H_FCP_OBS_2812	<i>Load STR Star Catalogue in EEPROM</i>	6	2	n/a
H_FCP_OBS_2842	<i>Update STR EEPROM ground image from memory dump</i>	6	2	n/a
H_FCP_OBS_2843	<i>Update STR EEPROM ground image from memory dump</i>	6	2	n/a
H_FCP_OBS_2844	<i>Update STR RAM ground image from memory dump</i>	6	2	n/a
H_FCP_OBS_2845	<i>Update STR RAM ground image from memory dump</i>	6	2	n/a
H_FCP_OBS_3110	<i>Load HIFI DPU OBS in instrument Intermediate mode</i>	6	3	n/a
H_FCP_OBS_3111	<i>Patch HIFI DPU OBS in instrument Intermediate mode</i>	6	3	n/a
H_FCP_OBS_3112	<i>Load HIFI DPU OBS in instrument Rescue mode</i>	6	3	n/a
H_FCP_OBS_3122	<i>Patch and dump HIFI DPU PRAM memory</i>	6	3	n/a

ID	TITLE	Volume	Chapter	Section
H_FCP_OBS_3140	<i>Monitor dump of HIFI DPU EEPROM memory area</i>	6	3	n/a
H_FCP_OBS_3141	<i>Update HIFI DPU EEPROM ground image from memory dump</i>	6	3	n/a
H_FCP_OBS_3142	<i>Monitor dump of HIFI DPU PRAM memory area</i>	6	3	n/a
H_FCP_OBS_3143	<i>Update HIFI DPU PRAM ground image from memory dump</i>	6	3	n/a
H_FCP_OBS_3144	<i>Monitor dump of HIFI DPU DRAM memory area</i>	6	3	n/a
H_FCP_OBS_3145	<i>Update HIFI DPU DRAM ground image from memory dump</i>	6	3	n/a
H_FCP_OBS_3150	<i>Monitor dump of HIFI DPU 1553 DRAM memory area</i>	6	3	n/a
H_FCP_OBS_3151	<i>Update HIFI DPU 1553 DRAM ground image from memory dump</i>	6	3	n/a
H_FCP_OBS_3160	<i>Check HIFI DPU EEPROM memory area (checksum calculation)</i>	6	3	n/a
H_FCP_OBS_3162	<i>Check HIFI DPU PRAM memory area (checksum calculation)</i>	6	3	n/a
H_FCP_OBS_3164	<i>Check HIFI DPU DRAM memory area (checksum calculation)</i>	6	3	n/a
H_FCP_OBS_3220	<i>Patch and dump HIFI LCU memory</i>	6	3	n/a
H_FCP_OBS_3241	<i>Update HIFI LCU ground image from memory dump</i>	6	3	n/a
H_FCP_OBS_4110	<i>Load PACS DPU OBS in instrument INIT mode</i>	6	3	n/a
H_FCP_OBS_4112	<i>Load PACS DPU OBS in instrument RESCUE mode</i>	6	3	n/a
H_FCP_OBS_4140	<i>Monitor dump of PACS DPU EEPROM memory area</i>	6	3	n/a
H_FCP_OBS_4141	<i>Update PACS DPU EEPROM ground image via memory dump</i>	6	3	n/a
H_FCP_OBS_4142	<i>Monitor dump of PACS DPU PRAM memory area</i>	6	3	n/a

ID	TITLE	Volume	Chapter	Section
H_FCP_OBS_4143	<i>Update PACS DPU PRAM ground image via memory dump</i>	6	3	n/a
H_FCP_OBS_4144	<i>Monitor dump of PACS DPU DRAM memory area</i>	6	3	n/a
H_FCP_OBS_4145	<i>Update PACS DPU DRAM ground image via memory dump</i>	6	3	n/a
H_FCP_OBS_4148	<i>Monitor dump of PACS DPU RAM Data 1553 memory area</i>	6	3	n/a
H_FCP_OBS_4149	<i>Update PACS DPU RAM Data 1553 ground image via memory dump</i>	6	3	n/a
H_FCP_OBS_4150	<i>Monitor dump of PACS DPU RAM Data SMCS chip memory area</i>	6	3	n/a
H_FCP_OBS_4151	<i>Update PACS DPU RAM Data SMCS chip ground image via memory dump</i>	6	3	n/a
H_FCP_OBS_4152	<i>Monitor dump of PACS DPU RAM Data SMCS memory area</i>	6	3	n/a
H_FCP_OBS_4153	<i>Update PACS DPU RAM Data SMCS ground image via memory dump</i>	6	3	n/a
H_FCP_OBS_4154	<i>Monitor dump of PACS DPU RAM Data PRAM memory area</i>	6	3	n/a
H_FCP_OBS_4155	<i>Update PACS DPU RAM Data PRAM ground image via memory dump</i>	6	3	n/a
H_FCP_OBS_4160	<i>Check PACS DPU EEPROM memory area (checksum calculation)</i>	6	3	n/a
H_FCP_OBS_4162	<i>Check PACS DPU PRAM memory area (checksum calculation)</i>	6	3	n/a
H_FCP_OBS_4164	<i>Check PACS DPU DRAM memory area (checksum calculation)</i>	6	3	n/a
H_FCP_OBS_4210	<i>Load PACS SPU OBS in instrument INIT mode</i>	6	3	n/a
H_FCP_OBS_4240	<i>Monitor dump of SPU EEPROM memory area</i>	6	3	n/a
H_FCP_OBS_4241	<i>Update PACS SPU EEPROM ground image via memory dump</i>	6	3	n/a
H_FCP_OBS_4242	<i>Monitor dump of PACS SPU PRAM memory area</i>	6	3	n/a

ID	TITLE	Volume	Chapter	Section
H_FCP_OBS_4243	<i>Update PACS SPU PRAM ground image via memory dump</i>	6	3	n/a
H_FCP_OBS_4244	<i>Monitor dump of PACS SPU DRAM memory area</i>	6	3	n/a
H_FCP_OBS_4245	<i>Update PACS SPU DRAM ground image via memory dump</i>	6	3	n/a
H_FCP_OBS_4246	<i>Monitor dump of PACS SPU RAM Ext memory area</i>	6	3	n/a
H_FCP_OBS_4247	<i>Update PACS SPU RAM Ext ground image via memory dump</i>	6	3	n/a
H_FCP_OBS_4248	<i>Monitor dump of PACS SPU RAM Data DRAM memory area</i>	6	3	n/a
H_FCP_OBS_4249	<i>Update PACS SPU RAM Data DRAM ground image via memory dump</i>	6	3	n/a
H_FCP_OBS_4260	<i>Check PACS SPU EEPROM memory area (checksum calculation)</i>	6	3	n/a
H_FCP_OBS_4262	<i>Check PACS SPU PRAM memory area (checksum calculation)</i>	6	3	n/a
H_FCP_OBS_4264	<i>Check PACS SPU DRAM memory area (checksum calculation)</i>	6	3	n/a
H_FCP_OBS_4310	<i>Load PACS DMC OBS in instrument INIT mode</i>	6	3	n/a
H_FCP_OBS_4342	<i>Monitor dump of DMC PRAM memory area</i>	6	3	n/a
H_FCP_OBS_4362	<i>Check PACS DMC PRAM memory area (checksum calculation)</i>	6	3	n/a
H_FCP_OBS_5110	<i>Load SPIRE OBS from ASW</i>	6	3	n/a
H_FCP_OBS_5111	<i>Patch SPIRE OBS in PM</i>	6	3	n/a
H_FCP_OBS_5112	<i>Load SPIRE OBS from BSW</i>	6	3	n/a
H_FCP_OBS_5122	<i>Patch and dump SPIRE DPU PRAM memory</i>	6	3	n/a
H_FCP_OBS_5142	<i>Monitor dump of SPIRE DPU PRAM memory area</i>	6	3	n/a

ID	TITLE	Volume	Chapter	Section
H_FCP_OBS_5143	<i>Update SPIRE DPU PRAM ground image from memory dump</i>	6	3	n/a
H_FCP_OBS_5144	<i>Monitor dump of SPIRE DPU DRAM memory area</i>	6	3	n/a
H_FCP_OBS_5162	<i>Check SPIRE DPU PRAM memory area (checksum calculation)</i>	6	3	n/a
H_FCP_OBS_5164	<i>Check SPIRE DPU DRAM memory area (checksum calculation)</i>	6	3	n/a
H_FCP_OBS_7101	<i>Load OBCP using OBCP Development Environment generated TC File</i>	6	2	n/a
H_FCP_OBS_7102	<i>Delete OBCP</i>	6	2	n/a
H_FCP_OBS_7108	<i>Request list of OBCPs</i>	6	2	n/a
H_FCP_OBS_7110	<i>Request list of active OBCPs</i>	6	2	n/a
H_FCP_OBS_7112	<i>Request OBCP status report</i>	6	2	n/a
H_FCP_OBS_7114	<i>Dump and verify OBCP code</i>	6	2	n/a
H_FCP_OBS_LCHP	<i>Dump of CDMU and ACC memories (code and constants)</i>	6	2	n/a
H_FCP_OBS_LPAT	<i>Dump of CDMU and ACC memories (code and constants) (for AIT)</i>	6	2	n/a
H_FCP_OBS_LPRA	<i>Dump of CDMU and ACC registers (for AIT)</i>	6	2	n/a
H_FCP_OBS_LPRG	<i>Dump of CDMU and ACC registers</i>	6	2	n/a
H_FCP_PAC_CCDM	<i>Pacs_Dpu_MemCheck</i>	6	3	n/a
H_FCP_PAC_CSBF	<i>PACS_Burstmode_Reset</i>	6	3	n/a
H_FCP_PAC_CSNB	<i>PACS_Burstmode_Setup</i>	6	3	n/a
H_FCP_PAC_DAILY	<i>PACS daily health check</i>	6	3	n/a

ID	TITLE	Volume	Chapter	Section
H_FCP_PAC_NCMM	<i>Check DMC software (Nominal)</i>	6	3	n/a
H_FCP_PAC_NCSM	<i>Check SPU software (Nominal)</i>	6	3	n/a
H_FCP_PAC_NLDM	<i>Pacs_LoadDpuObsw_Nominal</i>	6	3	n/a
H_FCP_PAC_NLMM	<i>Load DMC software (Nominal)</i>	6	3	n/a
H_FCP_PAC_NLSM	<i>Load SPU software (Nominal)</i>	6	3	n/a
H_FCP_PAC_NRDM	<i>Pacs_LoadDpuObsw_Rescue_Nominal</i>	6	3	n/a
H_FCP_PAC_NSOF	<i>PACS_Switch_Off_Nominal</i>	6	3	n/a
H_FCP_PAC_NSON	<i>PACS_Switch_On_Nominal</i>	6	3	n/a
H_FCP_PAC_RCMM	<i>Check DMC software (Redundant)</i>	6	3	n/a
H_FCP_PAC_RCSM	<i>Check SPU software (Redundant)</i>	6	3	n/a
H_FCP_PAC_RLDM	<i>Pacs_LoadDpuObsw_Redundant</i>	6	3	n/a
H_FCP_PAC_RLMM	<i>Load DMC software (Redundant)</i>	6	3	n/a
H_FCP_PAC_RLSM	<i>Load SPU software (Redundant)</i>	6	3	n/a
H_FCP_PAC_RRDM	<i>Pacs_LoadDpuObsw_Rescue_Redundant</i>	6	3	n/a
H_FCP_PAC_RSOF	<i>PACS_Switch_Off_Redundant</i>	6	3	n/a
H_FCP_PAC_RSON	<i>PACS_Switch_On_Redundant</i>	6	3	n/a
H_FCP_RM_ACC	<i>Herschel SREM Accumulation</i>	6	3	n/a
H_FCP_RM_OFF	<i>Herschel SREM Switch OFF</i>	6	3	n/a

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H_FCP_RM_ON	<i>Herschel SREM Switch ON</i>	6	3	n/a
H_FCP_SPI_APAT	<i>SpireEngPatchPM Patch Two Bit Flips April 2010</i>	6	3	n/a
H_FCP_SPI_BSMF	<i>BSM_OFF - Switch OFF the BSM</i>	6	3	n/a
H_FCP_SPI_BSMI	<i>BSM_INIT - Initialise the BSM</i>	6	3	n/a
H_FCP_SPI_BSMN	<i>BSM_ON - Switch ON the BSM</i>	6	3	n/a
H_FCP_SPI_CLEM	<i>Write the ASW to EEPROM</i>	6	3	n/a
H_FCP_SPI_CLOM	<i>Load a new OBSM image from the ASW</i>	6	3	n/a
H_FCP_SPI_CPOM	<i>Patch a new OBSM image from the ASW</i>	6	3	n/a
H_FCP_SPI_CREC	<i>CREC_VM - Recycle the Dilution Cooler</i>	6	3	n/a
H_FCP_SPI_DUMP	<i>SpireEngPMDump Dummpp and Check Memory</i>	6	3	n/a
H_FCP_SPI_MCUF	<i>MCU_OFF - Switch OFF the MCU</i>	6	3	n/a
H_FCP_SPI_MGUN	<i>MCU_BOOT - Switch ON the MCU</i>	6	3	n/a
H_FCP_SPI_NDRF	<i>DRCU_OFF - Switch OFF the nominal DRCU</i>	6	3	n/a
H_FCP_SPI_NDRN	<i>DRCU_START - Switch ON the nominal DRCU</i>	6	3	n/a
H_FCP_SPI_NLBM	<i>Load a new OBSM image from the BSW</i>	6	3	n/a
H_FCP_SPI_NSOF	<i>Switch OFF the nominal SPIRE units</i>	6	3	n/a
H_FCP_SPI_NSON	<i>DPU_START - Switch ON the nominal SPIRE units</i>	6	3	n/a
H_FCP_SPI_PHOF	<i>PDET_OFF - Switch OFF the Photometer</i>	6	3	n/a

ID	TITLE	Volume	Chapter	Section
H_FCP_SPI_PHON	<i>PDET_ON - Switch ON the Photometer</i>	6	3	n/a
H_FCP_SPI_RDRF	<i>DRCU_OFF - Switch OFF the redundant DRCU</i>	6	3	n/a
H_FCP_SPI_RDRN	<i>DRCU_START - Switch ON the redundant DRCU</i>	6	3	n/a
H_FCP_SPI_RLBM	<i>Load a new OBSM image from the BSW</i>	6	3	n/a
H_FCP_SPI_RSOF	<i>Switch OFF the redundant SPIRE units</i>	6	3	n/a
H_FCP_SPI_RSON	<i>DPU_START - Switch ON the redundant SPIRE units</i>	6	3	n/a
H_FCP_SPI_SCUF	<i>Switch OFF the SCU Thermometry</i>	6	3	n/a
H_FCP_SPI_SCUN	<i>SCU_ON - Switch ON the SCU Thermometry</i>	6	3	n/a
H_FCP_SPI_SDEF	<i>SDET_OFF - Switch OFF the Spectrometer</i>	6	3	n/a
H_FCP_SPI_SDEN	<i>SDET_ON - Switch ON the SPIRE Spectrometer</i>	6	3	n/a
H_FCP_SPI_SMECF	<i>SMEC_OFF - Switch OFF the SMEC</i>	6	3	n/a
H_FCP_SPI_SMCI	<i>SMEC_INIT - Initialise the SMEC</i>	6	3	n/a
H_FCP_SPI_SMCN	<i>SMEC_ON - Switch ON the SMEC</i>	6	3	n/a
H_FCP_SYS_LOU0	<i>Stop LOU Baffle Decontamination</i>	6	2	n/a
H_FCP_SYS_LOU1	<i>Start LOU Baffle Decontamination</i>	6	2	n/a
H_FCP_SYS_LOUS	<i>Lou Baffle Management Status Report</i>	6	2	n/a
H_FCP_TCS_CHECK	<i>TCS Subsystem Checkout</i>	6	2	n/a
H_FCP_TCS_FCCG	<i>TCS FCCT (FDIR Cross Correlated Table) Ground TOPE Check</i>	6	2	n/a

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H_FCP_TCS_FCCT	<i>TCS FCCT (FDIR Cross Correlated Table) TOPE Check</i>	6	2	n/a
H_FCP_TCS_LCU1	<i>HIFI LCU warm-up</i>	6	2	n/a
H_FCP_TCS_REPO	<i>Thermal Control Status Report</i>	6	2	n/a
H_FCP_TCS_TCTC	<i>TCS TCT (Thermal Control Table) TOPE Check</i>	6	2	n/a
H_FCP_TCS_WOVF	<i>Thermal loop 20 (WOV) THM2 fixed value</i>	6	2	n/a
H_FCP_TTC_60GT	<i>Change Ground Loss MOT entry parameters</i>	6	2	n/a
H_FCP_TTC_CHECK	<i>TTC Subsystem Checkout</i>	6	2	n/a
H_FCP_TTC_QCHK	<i>TTC S/S Routine Quick Check-out</i>	6	2	n/a
H_FCP_TTC_R1BR	<i>Select RX1 TC bit rate</i>	6	2	n/a
H_FCP_TTC_R2BR	<i>Select RX2 TC bit rate</i>	6	2	n/a
H_FCP_TTC_REPO	<i>TTC Management Status Report</i>	6	2	n/a
H_FCP_TTC_RUBR	<i>Select RX in use TC bit rate</i>	6	2	n/a
H_FCP_TTC_SWX	<i>Configure RFDN switches</i>	6	2	n/a
H_FCP_TTC_T2HC	<i>TTC chain 2 health check</i>	6	2	n/a
H_FCP_TTC_TCHK	<i>TTC Tope TM checks</i>	6	2	n/a
H_FCP_TTC_TU00	<i>Switch OFF TX and TWTA in use</i>	6	2	n/a
H_FCP_TTC_TU01	<i>Switch ON TX and TWTA in use</i>	6	2	n/a
H_FCP_TTC_TUCM	<i>Transponder in use Coherent Mode Activation/Deactivation</i>	6	2	n/a

ID	TITLE	Volume	Chapter	Section
H_FCP_TTC_TUHR	<i>Tx and TM encoder in use configuration for HR</i>	6	2	n/a
H_FCP_TTC_TUMR	<i>Tx and TM encoder in use configuration for MR</i>	6	2	n/a
H_FCP_TTC_TURM	<i>Transponder in use Ranging Activation/Deactivation</i>	6	2	n/a
H_GSP_AOC_2PON	<i>n/a</i>	8	7	n/a
H_GSP_AOC_2RMS	<i>Herschel ACMS : Reconfiguration Module Setup (to be part of ACC power on procedure of ESOC simulator)</i>	8	7	n/a
H_GSP_AOC_OBDB	<i>Herschel ACMS : OBDB Setup (to be part of ACC power on procedure of ESOC simulator)</i>	8	7	n/a
H_GSP_MCS_MSTK	<i>Transfer of Manual Stack printouts to HSC</i>	8	8	n/a
H_GSP_SYS_DTCP	<i>DTCP Activities</i>	8	8	n/a
H_LEO_CCU_MON1	<i>Disable CCUA/B monit #1 (512s) packets</i>	3	3	n/a
H_LEO_DHS_DUMP	<i>SSMM dumps during LEOP</i>	3	2	n/a
H_LEO_DHS_HK15	<i>Disable and remove AIV HKID 15 definition</i>	3	2	n/a
H_LEO_EPS_BATE	<i>EPS initial status at first AOS and Battery charging</i>	3	2	n/a
H_LEO_EPS_NCA	<i>NCA activation</i>	3	2	n/a
H_LEO_EPS_RW1	<i>Reaction Weels LCLs Close</i>	3	2	n/a
H_LEO_SYS_CSEP	<i>Check Separation</i>	3	2	n/a
H_LEO_SYS_DEC0	<i>Stop Decontamination heating</i>	3	2	n/a
H_LEO_SYS_DEC1	<i>Start Decontamination heating</i>	3	2	n/a
H_LEO_SYS_DECS	<i>Decontamination Heating Status Report</i>	3	2	n/a

ID	TITLE	Volume	Chapter	Section
H_LEO_SYS_LLO	<i>Manual LPU switch OFF</i>	3	2	n/a
H_LEO_TCS_FCCE	<i>Restore FCCT parameters in flight</i>	3	2	n/a
H_LEO_TTC_LCHK	<i>TTC S/S LEOP Quick Check-out</i>	3	2	n/a
H_LEO_TTC_MGAR	<i>Switch RX2 to MGA</i>	3	2	n/a
H_LEO_TTC_TWT1	<i>Switch TWT Amp in use ON</i>	3	2	n/a
H_LEO_VMC_OP	<i>Herschel VMC Operation</i>	3	3	n/a
H_MPP_TTC_TU00	<i>Switch OFF TX and TWTA in use</i>	8	4	n/a
H_MPP_TTC_TU01	<i>Switch ON TX and TWTA in use</i>	8	4	n/a
H_MPP_TTC_TUMR	<i>Tx and TM encoder in use configuration for MR</i>	8	4	n/a
Herschel On-Call SOE Instructions	<i>Herschel On-Call SOE Instructions</i>	8	n/a	n/a