



## **SPIRE Procedure**

**SPIRE On Board Software Upload  
Procedure for IST  
Sunil D. Sidher**

**Ref:** SPIRE-RAL-PRC-002866  
**Issue:** Issue 1.3  
**Date:** 4<sup>th</sup> November 2008  
**Page:** 1 of 22

# **SPIRE Onboard Software Upload Procedure for IST**

**Sunil Sidher & Tim Grundy**

**SPIRE-RAL-PRC-002866**

**Issue 1.3**

**4<sup>th</sup> November 2008**

**Approved by:**



## SPIRE Procedure

### SPIRE On Board Software Upload Procedure for IST Sunil D. Sidher

**Ref:** SPIRE-RAL-PRC-002866  
**Issue:** Issue 1.3  
**Date:** 4<sup>th</sup> November 2008  
**Page:** 2 of 22

## 1. Introduction

This document describes the procedure to upload On Board Software (OBS) version 3.0.B for the SPIRE instrument using the Alenia OBSM tool as implemented on the Herschel CCS. This procedure requires communication with SPIRE personnel. The SPIRE I-EGSE will be used to verify that the upload procedure is successful.

### 1.1 Scope

The Alenia OBSM tool is intended to be used for all such uploads and patching of the OBS for the FM IST. It addresses the following issues:

- Upload of an entire OBS image from the Boot Software (BSW)
- Upload of an entire OBS image into Program Memory (PM) from the OBS Application Software (ASW)
- Manual patch of the OBS image in PM
- Verification of the upload and patch in all cases

For SPIRE OBS 2.2.H, only the first option of upload from the BSW will be used. This is because previous attempts to patch and upload from the ASW using the OBSM tool have led to verification problems.

### 1.2 Applicable Documents

AD#	Title	Reference	Issue#	Date
AD01	SPIRE On-Board Software User Manual	SPIRE-IFS-PRJ-001391	3.0.0	29 <sup>th</sup> Oct 2008
AD02	SPIRE Data ICD	SPIRE-RAL-PRJ-001078	2.1	12/07/2007
AD03	SCOS-2000 OBSM External Interfaces Control Document	S2K-MCS-ICD-0014-TOS-GCI	1.3	11/07/2001

### 1.3 Reference Documents

RD#	Title	Reference	Issue#	Date
RD01	SPIRE Warm Units Integration Test Procedures	SPIRE-RAL-PRC-002680	1.3	15/02/2007
RD02	SPIRE OBS 3.0.B Acceptance Test Report	SPIRE-RAL-REP-003167	Issue 1	4 <sup>th</sup> Nov 2008

### 1.4 Change Record

Doc	Issue#	Changes	Date of Change
Draft	1	First Draft Version – untested	18/02/2007
Issue	1	Issue 1 <ul style="list-style-type: none"><li>▪ writing latest OBS into both partitions of EEPROM.</li><li>▪ checking the PM contents in SPIRE-OBS-</li></ul>	20/03/2007



## SPIRE Procedure

### SPIRE On Board Software Upload Procedure for IST Sunil D. Sidher

**Ref:** SPIRE-RAL-PRC-002866  
**Issue:** Issue 1.3  
**Date:** 4<sup>th</sup> November 2008  
**Page:** 3 of 22

		UL-04	
Issue	1.1	<ul style="list-style-type: none"><li>Updated after the following tests:<ul style="list-style-type: none"><li>OBS upload tests at Astrium/FN on the FM DPU from the Boot Software.</li><li>Manual patching tests at RAL on the AVM-1 DPU.</li></ul></li><li>All the test procedures have been renamed and redefined.</li></ul>	27/07/2007
Issue	1.2	Removed the procedures for patching and full image upload because of verification limitations in the Alenia OBSM tool. Only the full OBS image upload from the Boot Software (BSW) will be performed for version 2.2.H.	06/02/2008
Issue	1.3	Version for upload of OBS 3.0.B <ul style="list-style-type: none"><li>Included procedure for uploading the complete PM image from the ASW</li><li>The DM BSW upload procedures are not applicable for this release as CCS prefer to upload using the PM image.</li></ul>	4 <sup>th</sup> Nov 2008

## 1.5 Duration

The estimated duration for uploading a SPIRE OBS image from the BSW is approximately **40 minutes**.  
**Total duration will be ~ 2 hours for upload to both prime and redundant DPU.**

## 1.6 Open Issues – **Not applicable for this release**

SPIRE-OBS-22H-BSW-UL-IMAGE-FORCCS.tcl is a standalone TCL script which can be executed from the CCS to upload the SPIRE OBS 2.2.H image from the BSW. This script has been tested on the SPIRE AVM and is provided by SPIRE as an alternative to using the Alenia OBSM tool. It has the advantage that the upload can be completed in ~20 minutes, while still respecting the DM page boundaries.

This script assumes that the Load Memory TC ZCZ04999 is defined in the CCS database and that it accepts four parameters, viz. ZH013999, ZH014999, ZH015999 and ZH010999.

## 1.7 NCR Status

PRISMA NCRs fixed in this release: 3324, 3327, 4128, 4197, 4198, 4200, 4457, 4495, 4507, 4516

## 1.8 List of Acronyms

<b>ASW</b>	Application Software
<b>AVM</b>	Avionics Model
<b>BSW</b>	Boot Software
<b>CCS</b>	Central Checkout System
<b>DM</b>	Data Memory (DSP)



## SPIRE Procedure

### SPIRE On Board Software Upload Procedure for IST Sunil D. Sidher

**Ref:** SPIRE-RAL-PRC-002866  
**Issue:** Issue 1.3  
**Date:** 4<sup>th</sup> November 2008  
**Page:** 4 of 22

<b>DPU</b>	Digital Processing Unit
<b>DSP</b>	Digital Signal Processor
<b>EEPROM</b>	Electrically Erasable Programmable Read Only Memory
<b>EGSE</b>	Electrical Ground Support Equipment
<b>I-EGSE</b>	Instrument EGSE
<b>FM</b>	Flight Model
<b>FN</b>	Friedrichshafen
<b>FPU</b>	Focal Plane Unit
<b>IST</b>	Integrated System Test
<b>NCR</b>	Non Conformance Report
<b>OBS</b>	On Board Software
<b>OBSM</b>	OBS Management Tool
<b>PM</b>	Program Memory (DSP)
<b>PROM</b>	Programmable Read Only Memory
<b>SAU</b>	Single Addressable Unit



## SPIRE Procedure

**SPIRE On Board Software Upload  
Procedure for IST  
Sunil D. Sidher**

**Ref:** SPIRE-RAL-PRC-002866  
**Issue:** Issue 1.3  
**Date:** 4<sup>th</sup> November 2008  
**Page:** 5 of 22

### Table of contents

1.	Introduction .....	2
1.1	Scope .....	2
1.2	Applicable Documents .....	2
1.3	Reference Documents .....	2
1.4	Change Record .....	2
1.5	Duration .....	3
1.6	Open Issues – Not applicable for this release .....	3
1.7	NCR Status .....	3
1.8	List of Acronyms .....	3
2.	Test Configuration .....	6
3.	OBS Upload Procedure Overview .....	7
3.1	General instructions for executing the test procedures .....	7
3.2	General Pass/Fail Criteria .....	7
3.3	Constraints .....	7
3.4	Procedures for Upload of OBS .....	7
3.4.1	Procedure SPIRE-OBS-UL-BSW-01: Not applicable for OBS 3.0.B .....	7
3.4.2	Procedure SPIRE-OBS-UL-ASW-01: .....	11
3.4.3	Procedure SPIRE-OBS-UL-EEPROM-01: .....	16
3.4.4	Procedure SPIRE-OBS-UL-BSW-02: Not applicable for OBS 3.0.B .....	17
3.4.5	Procedure SPIRE-OBS-UL-EEPROM-02: .....	20

•



## **SPIRE Procedure**

**SPIRE On Board Software Upload  
Procedure for IST  
Sunil D. Sidher**

**Ref:** SPIRE-RAL-PRC-002866  
**Issue:** Issue 1.3  
**Date:** 4<sup>th</sup> November 2008  
**Page:** 6 of 22

## **2. Test Configuration**

### **HCDMU:**

- The Bus list selected on the HCDMU should be for SPIRE PRIME Instrument, (i.e., 27 TM slots allocated for SPIRE telemetry). For the PRIME side tests the BUS Configuration should be SPIRE Prime (i.e, RT=21) and for the REDUNDANT side test the BUS Configuration should be SPIRE Redundant (i.e, RT=22)
- The HCDMU and CCS should be interconnected.

### **CCS & I-EGSE:**

- The CCS and the I-EGSE should be interconnected via the Pipe GW.
- The SPIRE MIB should be imported on the CCS.
- The CCSHandler application software should be running on the I-EGSE.
- I-EGSE system is up and running.(Database, SCOS , QLA, EGSE Router and Gateway, TM ingestion)



## SPIRE Procedure

### SPIRE On Board Software Upload Procedure for IST Sunil D. Sidher

**Ref:** SPIRE-RAL-PRC-002866  
**Issue:** Issue 1.3  
**Date:** 4<sup>th</sup> November 2008  
**Page:** 7 of 22

## 3. OBS Upload Procedure Overview

### 3.1 General instructions for executing the test procedures

- Before carrying out the next procedure within the test sequence always ask for the go ahead by the SPIRE staff.
- Section 3.4 of this document specifies the sequence and detailed steps to be executed.
- The procedure tables in section 3.4 include blank boxes where the actual values of parameters can be noted. Based on the comparison with the expected values the success or failure of a step should be recorded in the final column of the table.
- The last row in a procedure table should be used to record the overall Pass/Fail result of each test.
- Any text in boldface in the procedural steps generally indicates an action which may have to be performed manually by the CCS staff.

### 3.2 General Pass/Fail Criteria

Consecutive failure of 2 executions of the same procedure is enough to declare the overall test result as failed. If the repetition of a failed test execution is successful this one should be repeated once again as a 'health' check.

### 3.3 Constraints

A general constraint (inferred from the test configuration described above) is that the SPIRE DPU power interface to the Herschel satellite must be connected before carrying out this procedure.

### 3.4 Procedures for Upload of OBS

#### 3.4.1 Procedure SPIRE-OBS-UL-BSW-01: **Not applicable for OBS 3.0.B**

<b>Version</b>	1.1
<b>Date</b>	6 <sup>th</sup> February 2008
<b>Purpose</b>	To upload complete SPIRE OBS DM image from the BSW for DPU PRIME
<b>Initial configuration</b>	SPIRE DPU PRIME and DRCU are switched off
<b>Final configuration</b>	SPIRE DPU PRIME is ON and SPIRE HK is being produced , SPIRE DRCU is OFF
<b>Preconditions</b>	<ul style="list-style-type: none"><li>• SPIRE FM DPU is electrically integrated with the Herschel Satellite</li><li>• SPIRE MIB PRIME is imported in the CCS database.</li><li>• CCS is up and running</li><li>• I-EGSE is up and running</li><li>• DPU AND OBS PARAMETERS display is selected on the CCS</li></ul>
<b>Duration</b>	~40 minutes
<b>Pass/Fail criteria</b>	Verification of successful upload is that nominal and critical HK reports start being generated at their nominal rates of 1Hz and 0.5Hz respectively.

#### Procedure Steps:



## SPIRE Procedure

**SPIRE On Board Software Upload  
Procedure for IST  
Sunil D. Sidher**

**Ref:** SPIRE-RAL-PRC-002866  
**Issue:** Issue 1.3  
**Date:** 4<sup>th</sup> November 2008  
**Page:** 8 of 22

Step	Description	Parameter	Expected Values Before/After	Actual Values Before/After	Pass/Fail
1	Select DPU AND OBS PARAMETERS display is on the CCS	—	—	—	
2	Power ON the SPIRE DPU PRIME unit using the dedicated spacecraft LCL line and configure 1553 Spacecraft bus for SPIRE DPU PRIME (RT = 21)	—	—	—	
3	Wait for the boot software to produce at least 2 event packets (5,1) with Event ID 0x8008 and SID 0x0003.  The last three parameters in the report (before the packet checksum) should be 0xABAB, 0xCDCD and 0xAAAA. Chec	—	—	—	
4	Prepare to load the SPIRE OBS from file SPDPRMDA_000022H_REF_2008_023T200600.IMG  The OBS image is in ICD 14 format (AD03).  NOTE: This image specifies the addresses in DM.	—	—	—	
5	Verify with the I-EGSE staff the actual sequence of the actual sequence of Load Memory commands which will be uplinked to the DPU. This is to verify that the upload is to the correct Memory Area:  In the Load Memory TCs generated by the OBSM tool, is the Memory ID set to 0x11?	Memory ID  Number of	0x11  TBD		





## SPIRE Procedure

**SPIRE On Board Software Upload  
Procedure for IST  
Sunil D. Sidher**

**Ref:** SPIRE-RAL-PRC-002866  
**Issue:** Issue 1.3  
**Date:** 4<sup>th</sup> November 2008  
**Page:** 9 of 22

Step	Description	Parameter	Expected Values Before/After	Actual Values Before/After	Pass/Fail
	<p><b>Record the number of Load Memory TCs that have been generated by the OBSM tool.</b></p> <p><b>Ensure that the TCs generated by the OBSM tool respect DM page boundaries. A DM page is 1024 SAUs, where a DM SAU is 32-bits.</b></p> <p><b>Note: NSAU parameter should be 32, based on previous DM image uploads using the Alenia OBSM tool.</b></p>	(6,2) Load Memory TCs			
6	<p><b>Once instructed by the I-EGSE staff, proceed with the upload of OBS.</b></p> <p><i>For N Load Memory (6,2) TCs the upload will take N/2 seconds</i></p> <p><b>For each Load Memory TC, the BSW generates a TM(5,1) event report with Event ID 0x8111 and SID 0x0003. The four subsequent 16-bit words in the packet should be zero. These signify a successful acceptance of the TC by the BSW.</b></p>	Time taken for upload	—		
7	<p>Execute the SPIRE TC (8,4) SCD11505: LOAD_TC_AND_BOOT</p> <p>It copies the OBS from DM to PM and starts running it.</p>	—	—	—	
8	<p><b>Check that Nominal and Critical HK packets are arriving at the CCS:</b></p> <p><b>SPIRE Nominal HK:</b></p> <ul style="list-style-type: none"> <li>(type ,subtype) : (3,25)</li> <li>APID : 0x502</li> </ul>				



## SPIRE Procedure

**SPIRE On Board Software Upload  
Procedure for IST  
Sunil D. Sidher**

**Ref:** SPIRE-RAL-PRC-002866  
**Issue:** Issue 1.3  
**Date:** 4<sup>th</sup> November 2008  
**Page:** 10 of 22

Step	Description	Parameter	Expected Values Before/After	Actual Values Before/After	Pass/Fail
	<b>SPIRE Critical HK:</b> <ul style="list-style-type: none"> <li>(type ,subtype) : (3,25)</li> <li>APID: 0x500</li> </ul>				
9	<b>Check that THSK parameter is refreshing every second</b>	THSK	Refreshing @ 1 Hz		
10	<b>Check that TM2N parameter is incrementing by 1 every second</b>	TM2N	Incrementing @ 1Hz		
11	<b>Check that TM1N parameter is incrementing by 1 every 2 second</b>	TM1N	Incrementing @ 0.5Hz		
12	<b>Check the version of the OBS which has been uploaded</b>	OBSVER1 OBSVER2 OBSVER2	2 2 H		



## SPIRE Procedure

**SPIRE On Board Software Upload  
Procedure for IST  
Sunil D. Sidher**

**Ref:** SPIRE-RAL-PRC-002866  
**Issue:** Issue 1.3  
**Date:** 4<sup>th</sup> November 2008  
**Page:** 11 of 22

### 3.4.2 Procedure SPIRE-OBS-UL-ASW-01:

<b>Version</b>	1.2
<b>Date</b>	4 <sup>th</sup> Nov 2008
<b>Purpose</b>	To upload the complete SPIRE OBS PM image from the ASW and reboot
<b>Initial configuration</b>	SPIRE DPU PRIME or REDUNDANT is ON and generating nominal and critical HK, SPIRE DRCU is OFF
<b>Final configuration</b>	SPIRE DPU PRIME or REDUNDANT is ON and SPIRE HK is being produced , SPIRE DRCU is OFF
<b>Preconditions</b>	<ul style="list-style-type: none"><li>• SPIRE FM DPU is electrically integrated with the Herschel Satellite</li><li>• SPIRE MIB PRIME is imported in the CCS database.</li><li>• CCS is up and running</li><li>• I-EGSE is up and running</li><li>• DPU AND OBS PARAMETERS display is selected on the CCS</li></ul>
<b>Duration</b>	~40 minutes for PRIME and ~40 minutes for REDUNDANT
<b>Pass/Fail criteria</b>	<ul style="list-style-type: none"><li>• The new OBS starts up after upload and produces nominal and critical HK reports at 1Hz and 0.5Hz respectively</li><li>• Memory dumps and checks produce expected results</li></ul>

### Procedure Steps:

Step	Description	Parameter	Expected Values Before/ After	Actual Values Before/ After	Pass/ Fail
1	<b>Select DPU AND OBS PARAMETERS display on the CCS</b>	—	—	—	
2	Execute SPIRE TC(8,4) SCR01500 to stop Nominal HK Report:  CLEAR_HK_REPORT (PCKTID=0x301)  The Nominal HK report generation should stop.	TM2N	Not incrementing anymore	—	
3	Execute SPIRE TC(8,4) SCM06500 to clone the OBS into high PM:  <b>Check with I-EGSE staff for the value of TC parameters</b>	—	—	—	



## SPIRE Procedure

**SPIRE On Board Software Upload  
Procedure for IST  
Sunil D. Sidher**

**Ref:** SPIRE-RAL-PRC-002866  
**Issue:** Issue 1.3  
**Date:** 4<sup>th</sup> November 2008  
**Page:** 12 of 22

Step	Description	Parameter	Expected Values Before/ After	Actual Values Before/ After	Pass/ Fail
	CLONE_OBS (DEST_ADDR, N_PM_WORDS)	DEST_ADDR N_PM_WORDS	0x40000 0x16E13		
4	<p><b>Prepare to load the SPIRE OBS PM image from file:</b></p> <p><b>SPDPRMPG_000030B_REF_2008_309T113100.IMG</b></p> <p><b>Check with the I-EGSE staff the actual PM offset address where the image is to be loaded. Apply this offset when using the OBSM tool to generate the (Type=6, Subtype2) TCs</b></p>	—	—	—	
5	<p><b>Verify with the I-EGSE staff the actual sequence of the actual sequence of Load Memory commands which will be uploaded to the DPU. This is to verify that the upload is to the correct Memory Area and Address:</b></p> <p><b>In the Load Memory TCs generated by the OBSM tool, is the Memory ID set to 0x0?</b></p> <p><b>Record the PM offset and the number of Load Memory TCs that have been generated by the OBSM tool.</b></p>	<p>Memory ID</p> <p>PM Offset</p> <p>Number of (6,2) Load Memory TCs</p>	<p>0x0</p> <p>0x40000</p> <p>TBD</p>		
6	<p><b>Once instructed by the I-EGSE staff, proceed with the upload of OBS.</b></p> <p><i>For N Load Memory (6,2) TCs the upload will take N/2 seconds</i></p>	Time taken for upload	—	—	
7	<b>Once all the Load Memory TCs have been executed successfully, send the</b>				



## SPIRE Procedure

**SPIRE On Board Software Upload  
Procedure for IST  
Sunil D. Sidher**

**Ref:** SPIRE-RAL-PRC-002866  
**Issue:** Issue 1.3  
**Date:** 4<sup>th</sup> November 2008  
**Page:** 13 of 22

Step	Description	Parameter	Expected Values Before/ After	Actual Values Before/ After	Pass/ Fail
	<p><b>following commands to check the contents of uploaded image in high PM using SPIRE TC(6,9) SCM02500:</b></p> <p>CHECK_MEMORY( MEMORYID=0 x0, STARTADDR, NSAU)</p> <p><b>Checksum in TM(6,10) packet</b></p> <p>CHECK_MEMORY( MEMORYID=0 x0, STARTADDR, NSAU)</p> <p><b>Checksum in TM(6,10) packet</b></p> <p>CHECK_MEMORY( MEMORYID=0 x0, STARTADDR, NSAU)</p> <p><b>Checksum in TM(6,10) packet</b></p>	<p>STARTADDR NSAU</p> <p>Checksum</p> <p>STARTADDR NSAU</p> <p>Checksum</p> <p>STARTADDR NSAU</p> <p>Checksum</p>	<p>0x44000 0x1795</p> <p>0x4C5F</p> <p>0x46000 0xFFFF</p> <p>0x078F</p> <p>0x55FFF 0xE14</p> <p>0xD362</p>		
8	<p><b>If previous step is successful then send the command to commit the new OBS and reboot: SPIRE TC(8,4) SCM07500:</b></p> <p>COMMIT_OBS_AND_ REBOOT (FROM_ADDR=, N_PM_WORDS)</p> <p><b>Confirm the parameter values with the I-EGSE staff</b></p>	<p>FROM_ADDR N_PM_WORDS</p>	<p>0x40000 0x16E13</p>		



## SPIRE Procedure

**SPIRE On Board Software Upload  
Procedure for IST  
Sunil D. Sidher**

**Ref:** SPIRE-RAL-PRC-002866  
**Issue:** Issue 1.3  
**Date:** 4<sup>th</sup> November 2008  
**Page:** 14 of 22

Step	Description	Parameter	Expected Values Before/ After	Actual Values Before/ After	Pass/ Fail
9	<p><b>Check that Nominal HK packets have started arriving again at the CCS:</b> SPIRE Nominal HK:</p> <ul style="list-style-type: none"> <li>(type ,subtype) : (3,25)</li> <li>APID : 0x502</li> </ul> <p><b>Check that THSK parameter is refreshing every second</b></p> <p><b>Check that TM2N parameter is incrementing by 1 every second</b></p>	<p>THSK</p> <p>TM2N</p>	<p>Refreshing @ 1 Hz</p> <p>Incrementing @ 1Hz</p>		
10	<b>Check that TM1N parameter is still incrementing by 1 every 2 seconds</b>	TM1N	Incrementing @ 0.5Hz		
11	<b>Check the version of the OBS which has been uploaded</b>	OBSVER1 OBSVER2 OBSVER2	3 0 B		
12	<p><b>Now send commands to verify the checksum of the new OBS in low PM using SPIRE TC(6,9) SCM02500:</b></p> <p>CHECK_MEMORY( MEMORYID=0 x0, STARTADDR, NSAU)</p> <p><b>Checksum in TM(6,10) packet</b></p> <p>CHECK_MEMORY( MEMORYID=0 x0, STARTADDR, NSAU)</p> <p><b>Checksum in TM(6,10) packet</b></p> <p>CHECK_MEMORY( MEMORYID=0 x0, STARTADDR, NSAU)</p>	<p>STARTADDR NSAU</p> <p>Checksum</p> <p>STARTADDR NSAU</p> <p>Checksum</p>	<p>0x4000 0x1795</p> <p>0x4C5F</p> <p>0x6000 0xFFFF</p> <p>0x078F</p>		



## SPIRE Procedure

**SPIRE On Board Software Upload  
Procedure for IST  
Sunil D. Sidher**

**Ref:** SPIRE-RAL-PRC-002866  
**Issue:** Issue 1.3  
**Date:** 4<sup>th</sup> November 2008  
**Page:** 15 of 22

Step	Description	Parameter	Expected Values Before/ After	Actual Values Before/ After	Pass/ Fail
	Checksum in TM(6,10) packet	STARTADDR NSAU	0x15FFF 0xE14		
		Checksum	0xD362		



## SPIRE Procedure

**SPIRE On Board Software Upload  
Procedure for IST  
Sunil D. Sidher**

**Ref:** SPIRE-RAL-PRC-002866  
**Issue:** Issue 1.3  
**Date:** 4<sup>th</sup> November 2008  
**Page:** 16 of 22

### 3.4.3 Procedure SPIRE-OBS-UL-EEPROM-01:

<b>Version</b>	1.2
<b>Date</b>	4 <sup>th</sup> Nov 2008
<b>Purpose</b>	To write the uploaded SPIRE OBS into the DPU
<b>Initial configuration</b>	SPIRE DPU PRIME is on and the OBS is running
<b>Final configuration</b>	SPIRE DPU PRIME is on and the OBS is running. The OBS is written into the EEPROM
<b>Preconditions</b>	<ul style="list-style-type: none"> <li>• CCS is up and running</li> <li>• I-EGSE is up and running</li> <li>• DPU AND OBS PARAMETERS display is selected on the CCS</li> <li>• Procedure SPIRE-OBS-UL-ASW-01 has been successfully run.</li> </ul>
<b>Duration</b>	~2 minutes
<b>Pass/Fail criteria</b>	TC to write the OBS into the EEPROM is successful

#### Procedure Steps:

Step	Description	Parameter	Expected Values Before/After	Actual Values Before/After	Pass/Fail
1	Select DPU AND OBS PARAMETERS display is on the CCS	—	—	—	
2	<p>Send TC(8,4) SCD08505 to write the OBS into secondary partition of the EEPROM:</p> <pre>WRITE2EEPROM(     STARTADDR,     ENDADDR,     PARTITION_FLAG,     JUMP_NPAGES – Group Repeater, )</pre> <p><b>Note: OBS 3.0.B will be written to just the secondary partition. Primary partition is not to be used until further notice.</b></p>	0x4000 0x16E13 1 0	—	—	
5	<p>Wait for ~30 seconds to receive TM(1,7) report.</p> <p>Reception of this report indicates a successful completion of procedure.</p>	—	—	—	





## SPIRE Procedure

**SPIRE On Board Software Upload  
Procedure for IST  
Sunil D. Sidher**

**Ref:** SPIRE-RAL-PRC-002866  
**Issue:** Issue 1.3  
**Date:** 4<sup>th</sup> November 2008  
**Page:** 17 of 22

### 3.4.4 Procedure SPIRE-OBS-UL-BSW-02: **Not applicable for OBS 3.0.B**

<b>Version</b>	1.1
<b>Date</b>	6 <sup>th</sup> February 2008
<b>Purpose</b>	To upload complete SPIRE OBS DM image from the BSW for DPU REDUNDANT
<b>Initial configuration</b>	SPIRE DPU and DRCU REDUNDANT are switched off
<b>Final configuration</b>	SPIRE DPU REDUNDANT is ON and SPIRE HK is being produced , SPIRE DRCU is OFF
<b>Preconditions</b>	<ul style="list-style-type: none"><li>• SPIRE FM DPU is electrically integrated with the Herschel Satellite</li><li>• SPIRE MIB REDUNDANT is imported in the CCS database.</li><li>• CCS is up and running</li><li>• I-EGSE is up and running</li><li>• DPU AND OBS PARAMETERS display is selected on the CCS</li></ul>
<b>Duration</b>	~40 minutes
<b>Pass/Fail criteria</b>	Verification of successful upload is that nominal and critical HK reports start being generated at their nominal rates of 1Hz and 0.5Hz respectively.

#### Procedure Steps:

Step	Description	Parameter	Expected Values Before/After	Actual Values Before/After	Pass/Fail
1	Select DPU AND OBS PARAMETERS display is on the CCS	—	—	—	
2	Power ON the SPIRE DPU REDUNDANT unit using the dedicated spacecraft LCL line and configure 1553 Spacecraft bus for SPIRE DPU PRIME (RT = 22)	—	—	—	
3	Wait for the boot software to produce at least 2 event packets (5,1) with Event ID 0x8008 and SID 0x0003. These events get generated at 10 second intervals.  The last three parameters in the report (before the packet checksum) should be 0xABAB, 0xCDCD and 0xAAAA.	—	—	—	
4	Prepare to load the SPIRE	—	—	—	



## SPIRE Procedure

**SPIRE On Board Software Upload  
Procedure for IST  
Sunil D. Sidher**

**Ref:** SPIRE-RAL-PRC-002866  
**Issue:** Issue 1.3  
**Date:** 4<sup>th</sup> November 2008  
**Page:** 18 of 22

Step	Description	Parameter	Expected Values Before/After	Actual Values Before/After	Pass/Fail
	<p><b>OBS from file</b> SPDPRMDA_000022H_REF_2008_023T200600.IMG</p> <p>The OBS image is in ICD 14 format (AD03).</p> <p><b>NOTE:</b> This image specifies the addresses in DM.</p>				
5	<p>Verify with the I-EGSE staff the actual sequence of the actual sequence of Load Memory commands which will be uplinked to the DPU. This is to verify that the upload is to the correct Memory Area:</p> <p>In the Load Memory TCs generated by the OBSM tool, is the Memory ID set to 0x11?</p> <p>Record the number of Load Memory TCs that have been generated by the OBSM tool.</p> <p>Ensure that the TCs generated by the OBSM tool respect DM page boundaries. A DM page is 1024 SAUs, where a DM SAU is 32-bits.</p> <p><b>Note:</b> NSAU parameter should be 32, based on previous DM image uploads using the Alenia OBSM tool.</p>	<p>Memory ID</p> <p>Number of (6,2) Load Memory TCs</p>	<p>0x11</p> <p>TBD</p>		
6	<p>Once instructed by the I-EGSE staff, proceed with the upload of OBS.</p> <p><i>For N Load Memory (6,2) TCs the upload will take N/2</i></p>	Time taken	—		



## SPIRE Procedure

**SPIRE On Board Software Upload  
Procedure for IST  
Sunil D. Sidher**

**Ref:** SPIRE-RAL-PRC-002866  
**Issue:** Issue 1.3  
**Date:** 4<sup>th</sup> November 2008  
**Page:** 19 of 22

Step	Description	Parameter	Expected Values Before/After	Actual Values Before/After	Pass/Fail
	<i>seconds</i>  <b>For each Load Memory TC, the BSW generates a TM(5,1) event report with Event ID 0x8111 and SID 0x0003. The four subsequent 16-bit words in the packet should be zero. These signify a successful acceptance of the TC by the BSW.</b>	for upload			
7	Execute the SPIRE TC (8,4) SCD11505: LOAD_TC_AND_BOOT  It copies the OBS FROM DM to PM and starts running it.	—	—	—	
8	<b>Check that Nominal and Critical HK packets are arriving at the CCS:</b> <b>SPIRE Nominal HK:</b> <ul style="list-style-type: none"> <li>(type ,subtype) : (3,25)</li> <li>APID : 0x503</li> </ul> <b>SPIRE Critical HK:</b> <ul style="list-style-type: none"> <li>(type ,subtype) : (3,25)</li> <li>APID: 0x501</li> </ul>				
9	<b>Check that THSK parameter is refreshing every second</b>	THSK	Refreshing @ 1 Hz		
10	<b>Check that TM2N parameter is incrementing by 1 every second</b>	TM2N	Incrementing @ 1Hz		
11	<b>Check that TM1N parameter is incrementing by 1 every 2 second</b>	TM1N	Incrementing @ 0.5Hz		
12	<b>Check the version of the OBS which has been uploaded</b>	OBSVER1 OBSVER2 OBSVER2	2 2 H		



## SPIRE Procedure

**SPIRE On Board Software Upload  
Procedure for IST  
Sunil D. Sidher**

**Ref:** SPIRE-RAL-PRC-002866  
**Issue:** Issue 1.3  
**Date:** 4<sup>th</sup> November 2008  
**Page:** 20 of 22

### 3.4.5 Procedure SPIRE-OBS-UL-EEPROM-02:

<b>Version</b>	1.2
<b>Date</b>	4 <sup>th</sup> Nov 2008
<b>Purpose</b>	To write the uploaded SPIRE OBS into the DPU
<b>Initial configuration</b>	SPIRE DPU REDUNDANT is on and the OBS is running
<b>Final configuration</b>	SPIRE DPU REDUNDANT is on and the OBS is running. The OBS is written into the EEPROM
<b>Preconditions</b>	<ul style="list-style-type: none"> <li>• CCS is up and running</li> <li>• I-EGSE is up and running</li> <li>• DPU AND OBS PARAMETERS display is selected on the CCS</li> <li>• Procedure SPIRE-OBS-UL-ASW-01 has been successfully run.</li> </ul>
<b>Duration</b>	~5 minutes
<b>Pass/Fail criteria</b>	TC to write the OBS into the EEPROM is successful

#### Procedure Steps:

Step	Description	Parameter	Expected Values Before/After	Actual Values Before/After	Pass/Fail
1	Select <b>DPU AND OBS PARAMETERS</b> display is on the CCS	—	—	—	
2	Send TC(8,4) SCD08505 to write the OBS into primary partition of the EEPROM:  WRITE2EEPROM( STARTADDR, ENDADDR, PARTITION_FLAG, JUMP_NPAGES – Group Repeater, ) )	—  0x4000 0x16E13 0 0	—	—	
3	Wait for ~30 seconds to receive TM(1,7) report. Reception of this report indicates a successful completion of procedure.	—	—	—	



## SPIRE Procedure

**SPIRE On Board Software Upload  
Procedure for IST  
Sunil D. Sidher**

**Ref:** SPIRE-RAL-PRC-002866  
**Issue:** Issue 1.3  
**Date:** 4<sup>th</sup> November 2008  
**Page:** 21 of 22

Step	Description	Parameter	Expected Values Before/After	Actual Values Before/After	Pass/Fail
4	<b>Send TC(8,4) SCD08505 to write the OBS into secondary partition of the EEPROM:</b>  WRITE2EEPROM( STARTADDR, ENDADDR, PARTITION_FLAG, JUMP_NPAGES – Group Repeater, ) 	—  0x4000 0x16E13 1 0	—	—	
5	Wait for ~30 seconds to receive TM(1,7) report. Reception of this report indicates a successful completion of procedure.	—	—	—	



## **SPIRE Procedure**

**SPIRE On Board Software Upload  
Procedure for IST  
Sunil D. Sidher**

**Ref:** SPIRE-RAL-PRC-002866  
**Issue:** Issue 1.3  
**Date:** 4<sup>th</sup> November 2008  
**Page:** 22 of 22

---

---