EADS Astrium **Test Report** **Herschel**

Title: HERSCHEL S/C Level Conducted EMC Test Report

CI-No: 100000

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Distribution: See Distribution List (last page)

Issue	Date	Sheet	Description of Change	Release
1	28.01.08	All	Initial Issue	

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1 Scope

This test report summarises the EMC CE tests performed on the Herschel S/C. The test was performed in accordance with the TAS-F test specification, [AD1], applying the ASED Test Procedure, [AD2]. The applicable test configuration was established with [AD3].

The ASED AS-RUN Test Procedure, plots in frequency and time domain and photos of the test set up and the Functional AIT Test Report are annexed to this document.

2 Documents

2.1 Applicable documents

The following documents of the latest issue in effect or as defined herein form a part of this document to the extent specified herein.

AD1	H-P-2-ASP-TS-0819	HERSCHEL FM EMC Test Requirements Specification
AD2	H-P-DW-AI-0004	HERSCHEL S/C Level Conducted EMC Test Procedure
AD3	HP-2-ASED-PR-0100	Herschel EGSE & Satellite & Instrument Procedure for the SAT EMC CE Test in Warm Conditions
AD4	HP-2-ASP-MN-9877	Test Readiness Review for Herschel EMC CE Test
AD5	HP-2-ASP-MN-9908	Post Test Review: Herschel FM EMC CE Test

Test Readiness Review for Herschel EMC CE Test

2.2 Reference documents

RD1	H-P-PR-AI-0091	HERSCHEL PFM SVM – EMC Confidence Test Procedure
RD2	H-P-RP-AI-0176	HERSCHEL PFM SVM – EMC Confidence Test Report
RD3	H-P-RP-AI-0166	Conducted Susceptibility CM on RWL's TOCO and TD lines Test Report
RD4	H-P-2-ASP-TN-1406	Herschel FM EMC CE Data Collection

3 Non-Conformances and Procedure Variations

3.1 Summary of NCR's

No NCR's were raised during the test:

3.2 Summary of Procedure Variations

The tests of sect 8.9 'Conducted Susceptibility Tests on Signal Lines' were not performed. According the EMC test requirement spec HP-2-ASP-TS-0819, sect. 7.6.1, these tests have to be performed only if not all CE test were successful. All CE tests were successful.

A complete list of all Procedure Variations see Annex 1 in the 'AS RUN' TP. All Procedure Variations were accepted by TAS-F and ESA. See AD5.

4 Test execution and results

4.1 Test execution summary

The following Table 4-1 provides an overview of the test execution.

Test series	Carried out	Remarks	Results	Foot note
CE Tests on power lines	Yes	No explicit limit defined for go/no-go decision	Conducted emissions in FD and TD show no significant exceeding	The results are in the same order as expected from previous tests
CE tests on signal lines	Yes	No explicit limit defined for go/no-go decision	Conducted emissions in FD and TD show no significant exceeding	The results are in the same order as expected from previous tests
CS Tests	No	Test execution only as option if CE test results will show significant high emissions		Restricted CS Test planned for SPIRE on power line in the frame of the SPIRE SPT

Table 4-1: **Test execution summary**

Figure 4-1 shows the grounding of Herschel during EMC CE test.

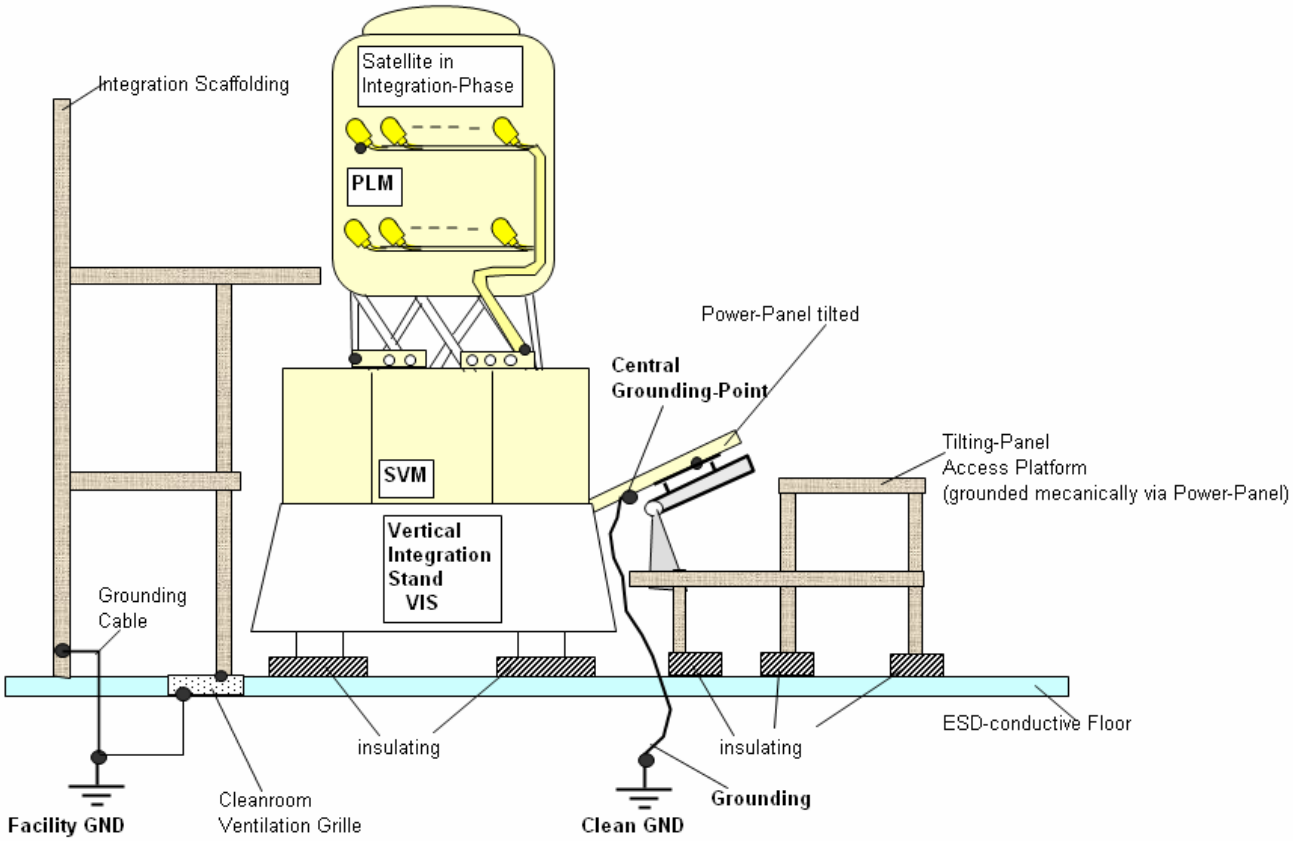

Figure 4-1: **Grounding Concept for EMC Test**

4.2 Test result summary

Table 4-2 provides an overview of the test results.
High levels were measured at 100 kHz in the time domain at the TWTA/EPC. These levels were already seen and accepted during the SVM confidence test.
There was a plot error during the common mode, time domain test of the PACS BOLC. Since tests in frequency domain and time domain, differential mode showed low levels this failure was accepted. This is also covered by a procedure variation.
Both issues were also discussed at the TRR and accepted by all parties.

Lines tested	FD/DM/I	FD/CM/I	TD/DM/V	TD/CM/V	TD/I	First assessment
HIFI/ICU	46 dBµA/43 kHz 43 dBµA/65 kHz 45 dBµA/130 kHz	45 dBµA/65 kHz 25 dBµA/130 kHz 55 dBµA/520 kHz	180 mVpp	240 mVpp	175 mApp	OK
HIFI/LCU	22 dBµA/5 MHz	23 dBµA/68 kHz 32 dBµA/4 MHz	102 mVpp	128 mVpp	272 mApp	OK very low levels between 10kHz and 4 MHz in DM?
CCU	26 dBµA/300 kHz	47 dBµA/810 kHz 28 dBµA/14 MHz	100 mVpp	158 mVpp	85 mApp	OK
STR	<45dBµA 1 to 5kHz 48 dBµA/129 kHz	35 dBµA/129 kHz	250 mVpp	680 mVpp	1.3 App/ 8 Hz	OK
TWTA/EPC	62 dBµA/100 kHz	65 dBµA/500 kHz	2.7 Vpp/ 100kHz	1.7 Vpp/ 100kHz	90 mApp	OK despite the high levels and in line with the results already seen and accepted during SVM confidence tests
RWL/Signals	N/A	58 dBµA/340 kHz	N/A	190 mVpp	N/A	OK the limit of 77 dBuA not exceeded
SPIRE/FCU	35 dBµA/210 kHz 48 dBµA/2.3 MHz	40 dBµA/210 kHz	100 mVpp	94 mVpp	100 mApp	OK
PACS/DPU	56 dBµA/68 kHz	<43dBµA/50-800k	140 mVpp	157 mVpp	180 mApp	OK
PACS/SPU	62 dBµA/1MHz	32 dBµA/20 MHz <20dBµA/0.01-1M	450 mVpp	280 mVpp	200 mApp	OK
PACS/BOLC	53 dBµA/125kHz 42 dBµA/5.5MHz	58 dBµA/125kHz 38 dBµA/20MHz	105 mVpp	plot error	45 mApp	OK
PACS/MEC	35 dBµA/60kHz	43 dBµA/60kHZ 33 dBµA/20MHz	95 mVpp	200 mVpp	91 mApp	OK

Table 4-2: **Test result summary**

4.3 Statement of Instrument Suppliers

Preliminary instrument comments are included in the instruments systems meeting from December. No further comments were received:

PACS: 6.12.2007 - SCI-PT-49884 - PACS System meeting n°4

- "EMC CE: Test has been completed. No anomalies found on PACS. TAS-F will distribute initial results from CE test."

SPIRE: 5.12.2007 - SCI-PT-49883 - SPIRE System meeting n°4

- EMC CE: feedback from test (ASED)
- CE test has been completed. Data will be send to SPIRE
- No anomalies have been detected. SPIRE will provide short statement after reception of test data.

The only concern was the low level on HIFI LCU"

HIFI: 4.12.2007 - SCI-PT-49880 - HIFI AIT System meeting n°2

- EMC CE: feedback from test
- Measured values on HIFI LCU power lines show different signature w.r.t HIFI LCU unit level CE tests.
- To be further explained in the CE test report."

5 Conclusions

The test was performed successfully, i.e.

- the specified test requirements, conditions and input values were met
- all measurement results were well within the limits as judged by the customer TAS-F.
- all required data were measured and stored. No CS tests were regarded necessary, not on the power lines as well as not on the signal lines due to the good results obtained in conducted emissions. This was confirmed by TAS-F.
- the measured data have adequate quality and are suitable for further analysis if this would become necessary.
- no non-conformance affecting the test results was raised
- all required results have been provided

Annex 1

'AS-RUN' Test Procedure

Content:

Filled-in 'HERSCHEL S/C Level Conducted EMC Test Procedure'; HP-2-ASED-TP-0155, Issue 1.1

118 pages

AS RUN

TITLE: HERSCHEL S/C Level
Conducted EMC Test Procedure

CI-NO: 100 000

PREPARED BY: Klaus Tigges / Clemens Kalde C. Kalde **DATE:** 29.11.07

CHECKED BY: Michael Hopfgarten M. Hopfgarten **DATE:** 29.11.07

PRODUCT ASSURANCE: for René Stritter B. Balage **DATE:** 29.11.07

CONFIGURATION CONTROL: for Walter Wietbrock **DATE:** 29.11.07.

PROJECT MANAGEMENT: for Wolfgang Fricke J. Kroeker **DATE:** 29.11.07

Test Director B. GOBILLOT (TAS-F) 23/4/2007

TEST LOCATION: EADS Astrium GmbH, Immenstaad

DATE OF TEST:

DISTRIBUTION: See Distribution List (Last Page)

Change Record:

Ausg./Rev.: Issue/Rev.:	Datum: Date:	Blatt: Sheet:	Beschreibung der Änderung: Description of Change:	Freigabe: Release:
1	16.11.07		Initial Issue	
1.1	28.11.07	9,15-17, 47,48	RD 4 transfered to AD10	
		16, 47, 48, 51-54	PACS always in Safe Mode	
		23	Name of Test Director changed	
		47, 48	References to AD 10 adapted	

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1 SCOPE

1.1 Objective

This procedure describes the activities to be carried out for the Conducted EMC Test of the satellite.
The objective of this activity is to confirm the system level compatibility margins concerning the conducted EMC requirements.

1.2 Test Flow

A general test flow is shown in the **Table 1-1** below.

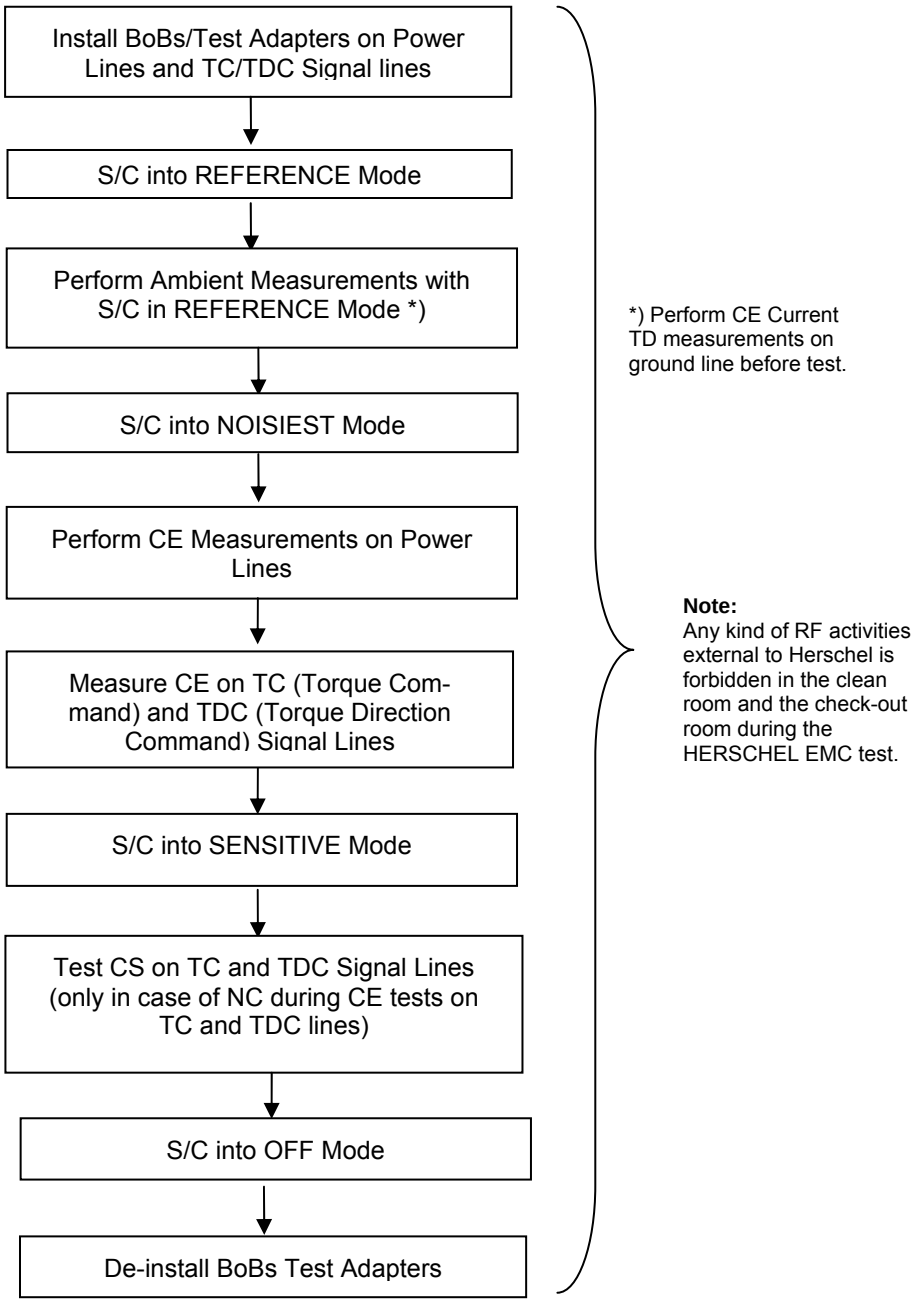


Table 1-1: Satellite Conducted EMC Test Flow

1.3 Test Specimen

The test specimen is the satellite. The model is the PFM.

2 DOCUMENTS/DRAWINGS

2.1 Applicable Documents

The following documents in their latest issue are applicable to this procedure:

AD1	HP-2-ASED-PL-00007	Herschel PA Plan	Issue 2.1
AD2	HP-2-ASED-PL-0023	Herschel Contamination Control Plan	Issue 2
AD3	H-P-2-ASP-ID-0621	HERSCHEL PLM Electrical Interface Control Document, EICD	Issue 3
AD4	H-P-IC-AI-0003	HERSCHEL/PLANCK SVM Electrical ICD	Issue 6
AD5	H-P-1-ASPI-SP-0027	General Design and Interface Requirements	Issue 5
AD6	H-P-1-ASPI-SP-0037	HERSCHEL/PLANCK EMC Specification	Issue 4.0
AD7	H-P-1-ASPI-PL-0038	HERSCHEL/PLANCK EMC/ESD Control Plan	Issue 3
AD8	H-P-2-ASP-TS-0819	HERSCHEL FM EMC Test Requirements Specification	Issue 4
AD9	H-P-DW-AI-0004	HERSCHEL FM EGSE to SVM connection diagrams	Issue 1
AD10	HP-2-ASED-PR-0100	Herschel EGSE & Satellite & Instrument Procedure for the SAT EMC CE Test in Warm Conditions	1.0

2.2 Reference Documents

In this section all documents are given which either

- could serve as reference for the conducted tests, or
- may be referred in the test report for clarification/justification of an outcome (result) of the test.

RD1	H-P-PR-AI-0091	HERSCHEL PFM SVM – EMC Confidence Test Procedure	01
RD2	H-P-RP-AI-0176	HERSCHEL PFM SVM – EMC Confidence Test Report	01
RD3	H-P-RP-AI-0166	Conducted Susceptibility CM on RWL's TOCO and TD lines Test Report	01
RD4		Now AD 10	
RD5	H-P-2-ASP-TN-1406	Herschel FM EMC CE Data Collection	1

3 CONFIGURATION

3.1 EGSE Configuration

The configuration is the PFM of the satellite in combination with the EGSE and SCOE in accordance to the AD9.

The configuration of the used EGSE as used for the REFERENCE, NOISIEST and SENSITIVE Mode are shown in the next tables. In OFF Mode, all EGSE is switched off.

Subsystem	Configuration for REFERENCE Mode
TM/TC DFE	• Online • TM Chain A • TC Chain A • Archiving ON
CDMU SCOE	• Not used
Power SCOE	Battery Simulator Nominal Set: • Offline but the SCOE is switched on • Vbat = 24 V • Icharge = 10 A • Idisch = 16 A • OVP = 27 V • OCP = ON Solar Array Simulator Nominal Set: • Offline but the SCOE is switched on • Voc = 43 V • Isc = 2.0 A • Rs = 0.1 • N = 100 • Vprot = 45 V • Iprot (FPCS) less than 3.3 A • AIT BDR 1 and BDR 2 = ON • Separation Straps 1 to 8 = not separated • SA Temp simulation set to 110 °C
ACMS SCOE	• Closed loop configuration to support OCM. RWLs not running.
TT&C SCOE	All instruments ON and RF cables connected to TT&C subsystem in order to provide • RF downlink signal spectrum monitoring

Table 3-1: EGSE Configuration for the REFERENCE Mode

Subsystem	Configuration for NOISIEST Mode
TM/TC DFE	• Online • TM Chain A • TC Chain A • Archiving ON
CDMU SCOE	• Not used
Power SCOE	Battery Simulator Nominal Set: • Online • Vbat = 24 V • Icharge = 10 A • Idisch = 16 A • OVP = 27 V • OCP = ON Solar Array Simulator Nominal Set: • Online • Voc = 43 V • Isc = 2.0 A • Rs = 0.1 • N = 100 • Vprot = 45 V • Iprot (FPCS) less than 3.3 A • AIT BDR 1 and BDR 2 = ON • Separation Straps 1 to 8 = separated • SA Temp simulation set to 110 °C
ACMS SCOE	• Closed loop configuration to support OCM • RWL's spin up at 1000 rpm
TT&C SCOE	All instruments ON and RF cables connected to TT&C subsystem in order to provide • RF downlink signal spectrum monitoring

Table 3-2: EGSE Configuration for the NOISIEST Mode

Subsystem	Configuration for SENSITIVE Mode
TM/TC DFE	• Online • TM Chain A • TC Chain A • Archiving ON
CDMU SCOE	• Not used
Power SCOE	Battery Simulator Nominal Set: • Online • Vbat = 24 V • Icharge = 10 A • Idisch = 16 A • OVP = 27 V • OCP = ON Solar Array Simulator Nominal Set: • Online • Voc = 43 V • Isc = 2.0 A • Rs = 0.1 • N = 100 • Vprot = 45 V • Iprot (FPCS) less than 3.0 A • AIT BDR 1 and BDR 2 = ON • Separation Straps 1 to 8 = separated • SA Temp simulation set to 110 °C
ACMS SCOE	• Closed loop configuration to support OCM (RWLs not running)
TT&C SCOE	All instruments ON and RF cables connected to TT&C subsystem in order to provide • RF downlink signal spectrum monitoring

Table 3-3: EGSE Configuration for the SENSITIVE Mode

3.2 Satellite Configuration

The configuration of the used satellite as used for the REFERENCE, NOISIEST and SENSITIVE Mode is shown in the next tables. In OFF Mode the satellite is switched off.

Subsystem/ Unit	Configuration for REFERENCE Mode
Power Panel	
CDMU	OFF
ACC	OFF
PCDU	OFF
Battery	OFF
TTC Panel	
EPC1	OFF
TWT1	OFF
EPC2	OFF
TWT2	OFF
XPND1	OFF
XPND2	OFF
RFDN	OFF
AOCS sensors	
STR1	OFF
STR2	OFF
CRS1	OFF
CRS2	OFF
RWL-1	OFF
RWL-2	OFF
RWL-3	OFF
RWL-4	OFF
GYRO A	OFF
GYRO B	OFF
SAS	OFF
AAD	OFF
Propulsion	
PT	OFF
20N thrusters cat bed	OFF
Latch valve	OFF
Miscellaneous	
SREM	OFF
VMC	OFF
SPIRE Panel	
HSDPU N	OFF
HSDPU R	OFF
HSFCU N	OFF
HSFCU R	OFF
HSDCU N	OFF
HSDCU R	OFF

Subsystem/ Unit	Configuration for REFERENCE Mode
CCU A	OFF
CCU B	OFF
PACS Panel	
FPSPU N	OFF
FPSPU R	OFF
FPDPU N	OFF
FPDPU R	OFF
FPDEC/MEC1	OFF
FPDEC/MEC2	OFF
FPBOLC N	OFF
FPBOLC R	OFF
HIFI Panels	
FHWEH	OFF
FHWEV	OFF
FHLCU N	OFF
FHLCU R	OFF
FHHRH	OFF
FHHRV	OFF
FHICU N	OFF
FHICU R	OFF

Table 3-4: Satellite Configuration for the REFERENCE Mode

Subsystem/ Unit	Configuration for NOISIEST Mode
Power Panel	See AD10 for Mode definition
CDMU	ON, NOM
ACC	ON, in STANDBY
PCDU	ON
Battery	Not connected
TTC Panel	
EPC1	ON
TWT1	ON
EPC2	OFF
TWT2	OFF
XPND1	ON, RX + TX
XPND2	ON, but only RX
RFDN	/
AOCS sensors	See AD10 for Mode definition
STR1	ON, Dumping Mode
STR2	STB
CRS1	ON
CRS2	ON
RWL-1	ON
RWL-2	ON
RWL-3	ON
RWL-4	ON
GYRO A	ON
GYRO B	OFF
SAS	/
AAD	/
Propulsion	See AD10 for Mode definition
PT	ON
20N thrusters cat bed	OFF
Latch valve	OFF
Miscellaneous	
SREM	ON
VMC	ON
SPIRE Panel	Set the mode according to AD10 **IMPORTANT: SPIRE in Science Mode for tests on SPIRE power lines. Else SPIRE in Standby Mode**
HSDPU N	ON
HSDPU R	OFF
HSFCU N	ON
HSFCU R	OFF
HSDCU N	ON
HSDCU R	OFF
CCU A	ON set mode according to AD10
CCU B	ON set mode according to AD10

Subsystem/ Unit	Configuration for NOISIEST Mode
PACS Panel	Set the mode according to AD10 **IMPORTANT: PACS in Safe Mode for all tests.**
FPSPU N	ON
FPSPU R	OFF
FPDPU N	ON
FPDPU R	OFF
FP/MEC1	ON
FPDEC/MEC2	OFF
FPBOLC N	ON
FPBOLC R	OFF
HIFI Panels	Set the mode according to AD10 **IMPORTANT: HIFI in Science Mode for tests on HIFI power lines and for tests on AOCS signal lines. Else HIFI in Standby Mode**
FHWEH	ON
FHWEV	ON
FHLCU N	ON
FHLCU R	OFF
FHHRH	ON
FHHRV	ON
FHICU N	ON
FHICU R	OFF

Table 3-5: Satellite Configuration for the NOISIEST Mode

Subsystem/ Unit	Configuration for SENSITIVE Mode
Power Panel	See AD10 for Mode definition
CDMU	ON, NOM
ACC	ON, in STANDBY
PCDU	ON
Battery	Not connected
TTC Panel	See AD10 for Mode definition
EPC1	ON
TWT1	ON
EPC2	OFF
TWT2	OFF
XPND1	ON, RX + TX
XPND2	ON, but only RX
RFDN	/
AOCS sensors	See AD10 for Mode definition
STR1	ON, Dumping Mode
STR2	STB
CRS1	ON
CRS2	ON
RWL-1	ON
RWL-2	ON
RWL-3	ON
RWL-4	ON
GYRO A	ON
GYRO B	OFF
SAS	/
AAD	/
Propulsion	See AD10 for Mode definition
PT	ON
20N thrusters cat bed	OFF
Latch valve	OFF
Miscellaneous	See AD10 for Mode definition
SREM	ON
VMC	ON
SPIRE Panel	
HSDPU N	OFF
HSDPU R	OFF
HSFCU N	OFF
HSFCU R	OFF
HSDCU N	OFF
HSDCU R	OFF
CCU A	ON set mode according to AD10
CCU B	ON set mode according to AD10
PACS Panel	
FPSPU N	OFF

Subsystem/ Unit	Configuration for SENSITIVE Mode
FPSPU R	OFF
FPDPU N	OFF
FPDPU R	OFF
FPDEC/MEC1	OFF
FPDEC/MEC2	OFF
FPBOLC N	OFF
FPBOLC R	OFF
HIFI Panels	
FHWEH	OFF
FHWEV	OFF
FHLCU N	OFF
FHLCU R	OFF
FHHRH	OFF
FHHRV	OFF
FHICU N	OFF
FHICU R	OFF

Table 3-6: Satellite Configuration for the SENSITIVE Mode

3.3 Instrumentation Selection and Calibration

Instrumentation used during the test shall be within its certification period and selected for use in tests such that it meets the specified accuracy requirements. Each instrument Model/Type number and Invent No. shall be recorded with its certification status into the equipment list for every test. The functionality of the test article shall be systematically verified during hardware acceptance testing. The test equipment list shall be included in the test report.

Equipment:	Manufacturer:	Type:	Inventar No:	Next Cal
IDAS 5 current probe	Tektronix	A6303XL		10 2008
Amplifier for current probe	Tektronix	AM5030		10.2008
Oscilloscope	LeCroy	424	10004647	30.11.08
Probe, Current	LeCroy	AP015	DSS 10001460	30.11.08
Diff. Voltage Probe	LeCroy	AP033	DSS 10002381	30.11.08
Spectrum Analyser	R & S	FSG8	ser 100009	24.09.08
Current Probe	Solar	6741	DS 28179	8.11.08

Table 3-7: Test Equipment List

3.4 Facility

The activities as detailed in this procedure shall be carried out in the EADS Astrium clean room class 100 in Friedrichshafen. See **Figure 3-1** below.

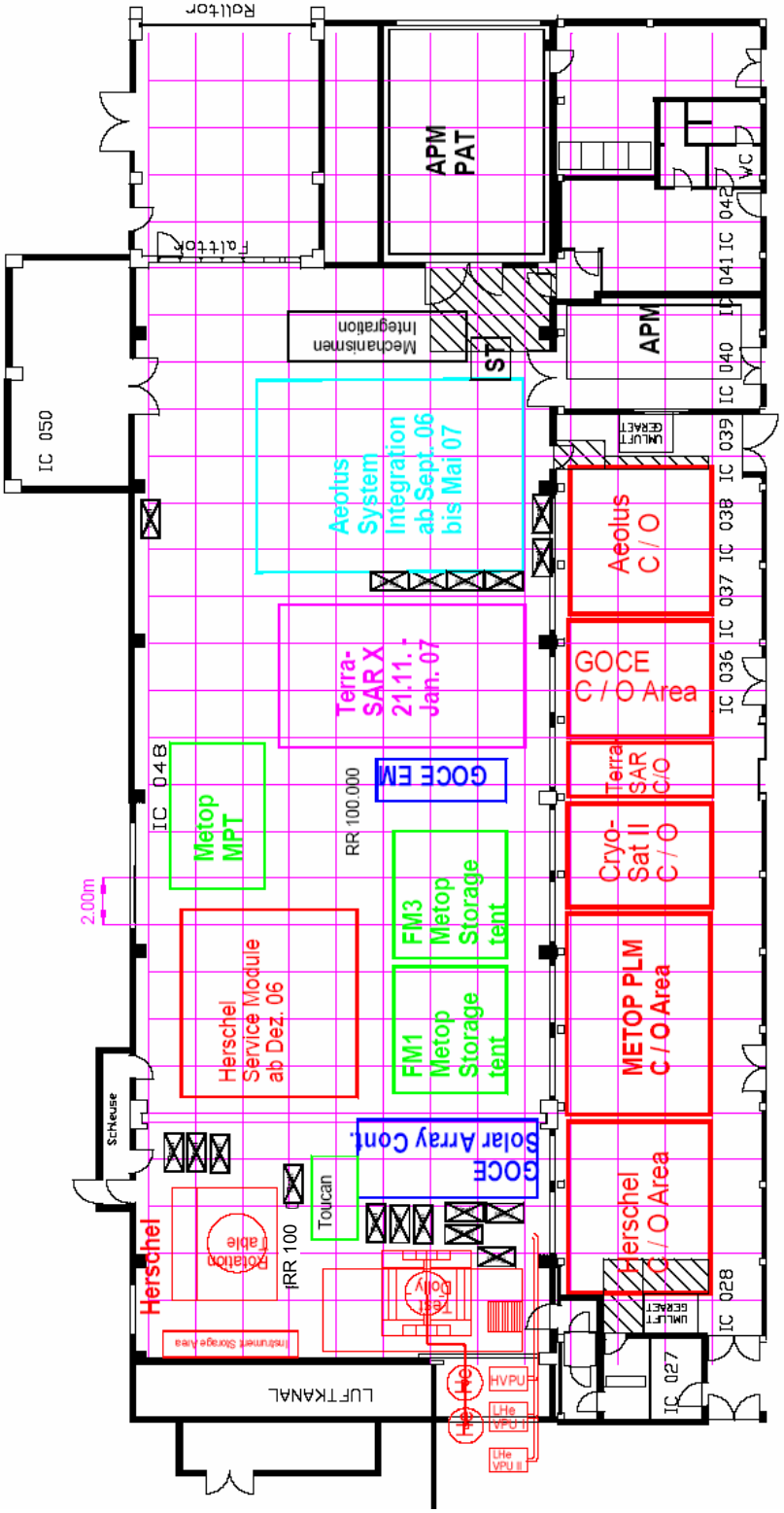


Figure 3-1: Cleanroom of Astrium Friedrichshafen

4 CONDITIONS

4.1 General Requirements

- The handling of the test set-up shall be in accordance with controlled procedure only
- Handling, mechanical and electrical, has to be done only by qualified personnel
- Test item has to be switched-off when changing the Test Configuration

4.2 Environmental Conditions

All activities specified in the procedure have to be performed in a clean room class 100.

Temperature: 22°C +/- 3°C
Relative Humidity: 40% to 60%

The cleanliness requirements will be observed throughout the activities, and the overall contamination control requirements identified in the Herschel Contamination Control Plan, AD2, will be observed.

4.3 Receiver/Analyser Settings

Using the MIL-STD-461E, the following table shall serve as guideline for receiver bandwidth and data presentation. It is strongly recommended to show only about 2 decades within one plot.

Conducted Emissions, NB:

	MIL-STD-461E		Analyser Settings (Peak detection Mode/Max. Hold)			
Plot	**f- range**	**Min. Meas. Time**	**ResBW (3 dB)**	**Video BW**	**Sweep Points/ virtual Stepp size**	**Sweep-time**
Plot 1	30 Hz – 1 kHz	0,015 s/Hz (ca. 15 s)	10 Hz	30 Hz	251/3,9 Hz < ResBW	20 s
Plot 1	1 kHz – 10 kHz	0,15 s/kHz (ca. 1,5 s)	100 Hz	300 Hz	251/36 Hz < ResBW	10 s
Plot 2	10 kHz – 150 kHz	0,015 s/kHz (ca. 2,2 s)	1 kHz	3 kHz	251/560 Hz < ResBW	10 s
Plot 2	150 kHz – 1 MHz	1,5 s/MHz (ca. 1,5 s)	10 kHz	30 kHz	251/3,4 kHz < ResBW	10 s
Plot 3	1 MHz – 30 MHz	1,5 s/MHz (ca. 45 s)	10 kHz	30 kHz	4001/7,25 kHz < ResBW	50 s
Plot 4	30 MHz – 50 MHz	0,15 s/MHz (ca. 3 s)	100 kHz	300 kHz	501/40 kHz < ResBW	10 s

Table 4-1: Receiver/Analyser Settings

4.4 General Precautions and Safety

4.4.1 General Safety Requirements

No special hazards are expected. The application of the standard technical rules for mechanical and electrical integration and test activities is sufficient.
Lower level procedures called up by this procedure may define their own safety requirement in the relevant chapters which must be respected accordingly.
Any open waveguides of HIFI shall be closed by copper tape or a dummy load, details on the configuration to be defined during the TRR.
The RF antennas shall be covered by the antenna test caps or removed and the antenna ports loaded by suitable RF load in order to avoid RF transmission in command failure case.

Special precautions concerning EMC test:

During the EMC test, special measurement adapters on power- and signal lines shall be installed to get access to the lines under test.

It is absolutely mandatory to fix the EMC adapter connectors mechanically against disconnecting by accident or stress!

Pay special attention when clamping and unclamping the measurement transducers (e.g. voltage- and current probes) to and from the EMC adapter wires. Wires may breake under stress.

4.4.2 ESD constraints

In order to prevent ESD sensitive H/W from any possible damages by accidental electrostatic discharges an ESD protected area must be defined and setup during ESD sensitive activities:

- Floor and test bench of the ESD protected area has to be covered with anti-static mats
- During all handling activities (as transport, mounting, mating/de-mating of connectors, measurements with individual measurement devices, etc.) the operator has to work on anti static mats with correct clothing and personal grounding-straps
- Adequate ESD clothing is required:
  - Anti static coat
  - Anti static gloves
  - Anti static boots
- Transportation of ESD sensitive H/W will be made only in ESD protective bag or box.

4.5 Management Activities

4.5.1 Pre-Test Activities

At least the following tasks have to be successfully completed before start of integration and test activities according this procedure:

- This procedure released and accepted
- Formal release to start given by the board following review of relevant test procedures and test configurations.

4.5.2 Procedure Variation

Major activities deviating from the approved test procedure require the agreement of Project, AIV and PA responsible, and shall be documented via Activity Control Sheets (ACS). All ACS's generated in the frame of the execution of this procedure shall be listed in the ACS Summary Sheet.

4.5.3 Criteria for Failure

If the results of any test performed using this procedure or a lower level procedure which this procedure refers to yields a value which lies outside the specified limits, it shall be considered as a non-conformance. Initial analysis of the result will be applied to establish whether the result is due to measurement error or incorrect specification limits. A NCR will then be raised to report the non-conformance. Depending on the magnitude of the non-conformance, and its impact, either a minor or a major NCR will be raised. In case of major NCR the test shall be continued only upon written or verbal authorisation of Customer (Alcatel and ESA). All NCR's raised in the frame of the execution of this procedure shall be listed in the NCR Summary Sheet.

The NCR process is described in the Herschel PA Plan, AD1.

4.5.4 Test Completion and Post-Test Activities

All data that has been recorded during the test activities specified in this procedure shall be collected and retained in a centralised reference volume, and will include:

- PFM logbook
- Relevant CCS logs
- Photographs and plots
- Filled out test procedure
- Activity Control Sheets (ACS), if any
- Copies of NCR's, if any

All these test data shall be available for presentation at the Test Review Board (TRB) which will finally conclude on the test.
A test report shall be produced whose contents shall be as follows:

- Brief summary of the test results
- "As-run" test procedure as an annex (this includes housekeeping data, temperature curves, etc.).
- List of NCR's raised
- List of ACS's generated.
- Relevant meeting minutes (e. g. TRR, TRB)
- Filled out Sign-off Sheet (see section 9.3 of this procedure).

4.6 PA Requirements

Quality Assurance shall monitor all operations (handling, transportation, disassembly, installation and test) as necessary to assure compliance with this procedure and the applicable requirements of the Herschel PA Plan, AD1.
In the course of this procedure PA shall pay particular attention to:

- the application of adequate protections to critical surfaces
- the records in the log-sheet
- the recording of the serial number of the test equipment used
- ensure that the test equipment used is within actual calibration cycle

PA has to make sure that NCR's are raised when applicable and treated by NRB procedure as defined in the Herschel PA Plan, AD1.

4.7 Personnel

Title	Function	Name
Test Director	Overall Responsible	Benoit Gobillot
Test Conductor	EMC Test Responsibility	Michael Hopfgarten
Test Engineer	EMC Test Definition	Clemens Kalde
Test Engineer	EMC Test Equipment Responsible	Hubert Stiehle
EGSE Operator	Operate EGSE	See TRR
Mech. Operator(s)	All mech. Integration activities, handles the Satellite during testing	See TRR
Satellite Operators	Operate the satellite during testing	See TRR
PA Representative	To ensure PA requirements	Dave Hendry

Table 4-2: Personnel

5 TEST REQUIREMENTS

5.1 CE Current on Satellite Ground Line

No (significant) current shall flow through the satellites ground line. Object of this test is to check the current on the satellites ground line and compare the measured value with the expected value of about 50 mApp. DC currents shall also be measured with this measurement (min. DC current detectable will be about 10 … 20 mApp).

The measurement shall be done with the S/C in REFERENCE Mode prior to the reference tests on power lines.

Measure the current ripple in time domain (TD). The measurement shall be taken in the frequency range from DC to 50 MHz.

5.1.1 CE Current on Satellite Ground Line Test Set-Up

The grounding wire shall be connected to the power panel grounding starpoint location (insert at the right side below the panel when panel is opened). **For the change of the grounding starpoint from the trolly to the grounding starpoint connection, it must be ensured by parallel connection that the satellite will never be without grounding (floating).**

5.2 CE Current on Primary Power Lines

Objective of this test is to provide confirmation that the conducted **current** emissions, measured on the primary power lines towards PCDU are compliant with specification.

This test is performed to fulfil EMC requirement EMCPLM-000 and EMCSYS-092 of HERSCHEL EMC Specification [AD6].

The following lines shall be tested:

- HIFI-LCU
- HIFI-ICU
- SPIRE-FCU
- PACS-DPU
- PACS-SPU
- CCU
- PACS-BOLC
- PACS-MEC

Furthermore the tests in accordance to EMCSYS-096 shall be performed:

- TWTA
- STR1

In **frequency domain** the test shall be performed in common mode and differential Mode. In **time domain** only differential measurements are applicable.

A **reference measurement** with Spacecraft OFF / EGSE ON before the test shall identify any emissions generated by the support or external power equipment.

5.2.1 Requirements for CE Current on PP Lines

CE Current Time Domain:

The minimum bandwidth used for time domain measurement shall be 50MHz. The actual DC currents shall be measured prior to test and documented in the test procedure/-report.

The results shall be compared with the results as got on subsystem and unit level (refer to RD-7) and analysed by ESA/ TAS.

CE Current Frequency domain:

In the frequency range 30 Hz – 50 MHz, the conducted emission on power lines shall be recorded and provided to the EMC team of ASPI/ESA for analysis. The CE shall be deemed by comparison with subsystem and unit test level results (refer to RD-7), and assessed by TAS and ESA.

5.2.2 CE Current Test Set-Up

The power line connection on the PCDU side shall be interrupted by specifically built test aids in order to get access to the power lines and to enable the measurements in time and frequency domain.
The frequency domain measurements shall be taken with a current probe and a spectrum analyser whereas an oscilloscope will be used for the time domain measurements.
A photograph shall be taken from the set-up, showing break-out boxes, the PCDU power connectors, the test cables and the current probe.

The principle to be applied for the common mode and differential mode measurements is shown in the figure below.

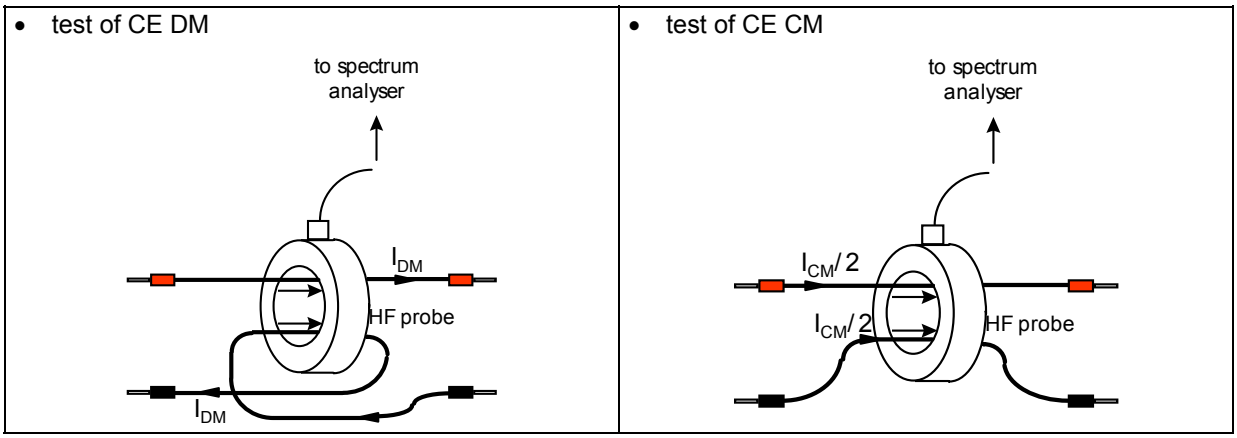


Figure 5-1: Schematic for CM and DM Measurements

5.3 CE Voltage on Primary Power Lines

Objective of this test is the measurement of conducted **voltage** emissions on the primary power lines towards PCDU.

This test is performed to fulfil EMC requirement EMCPLM-000 and EMCSYS-092 of HERSCHEL EMC Specification [AD6].
The following lines shall be tested:

- HIFI-LCU
- HIFI-ICU
- SPIRE-FCU
- PACS-DPU
- PACS-SPU
- CCU
- PACS-BOLC
- PACS-MEC

Furhtermore the tests in accordance to EMCSYS-096 shall be added:

- TWTA
- STR1

The test shall be done in **time domain**, differential mode and common mode (between the return line and the satellite structure.
A **background measurement** with Spacecraft OFF / EGSE ON before the test shall identify any emissions generated by the support or external power equipment.

5.3.1 Requirements for CE Voltage on PP Lines

The conducted emission voltage on power lines (both, between Plus/Returns and Return/Structure) shall be deemed by comparison with unit/satellite test level results and assessed by the EMC team. The minimum bandwidth used for measurement shall be 50 MHz. Only periodic components are considered to contribute to ''ripple''.

5.3.2 CE Voltage Test Set-Up on PP Lines

The power line connection on the PCDU side shall be interrupted by test adapters in order to get access to the power lines and to enable the time domain voltage measurements.
The measurement shall be taken with an oscilloscope.
A photograph shall be taken from the set-up, showing break-out boxes/test adapter, the test cables and the voltage probe.

5.4 CE Current on Signal Lines

Objective of this test is to check that the conducted current emissions on ACC-RWL signal lines TC and TDC are compliant with specification. This test is performed to fulfil EMC requirement EMCSYS-096 of HERSCHEL EMC Specification [AD6].
The following signal lines shall be tested:

- Torque Command (TC)
- Torque Direction Command (TDC)

The test shall be done in **frequency domain, common mode** (CM).
A **background measurement** with Spacecraft OFF / EGSE ON before the test shall identify any emissions generated by the support or external power equipment.

5.4.1 Requirements for CE Current on Signal Lines

In the frequency range 30 Hz – 50 MHz, the conducted current emission on signal lines shall not exceed 77 dBµA rms.

5.4.2 CE Current Signal Lines Test Set-Up

The TC and TDC signal lines shall be interrupted by a test adapter placed at DB02 level in order to get access to the lines for common mode measurement in accordance to Figure 5-2 and Figure 5-3 below and paragraph 6.3.1.

As an option only the TC and TDC signal lines may be interrupted by a test adapter placed at DB71 level, see Figure 5-4 and paragraph 6.3.2.

The measurement shall be taken with a current probe connected to a spectrum analyser.
A photograph shall be taken from the set-up, showing break-out boxes, the PCDU power connectors and the test cables and the voltage probe.

CE on TC lines

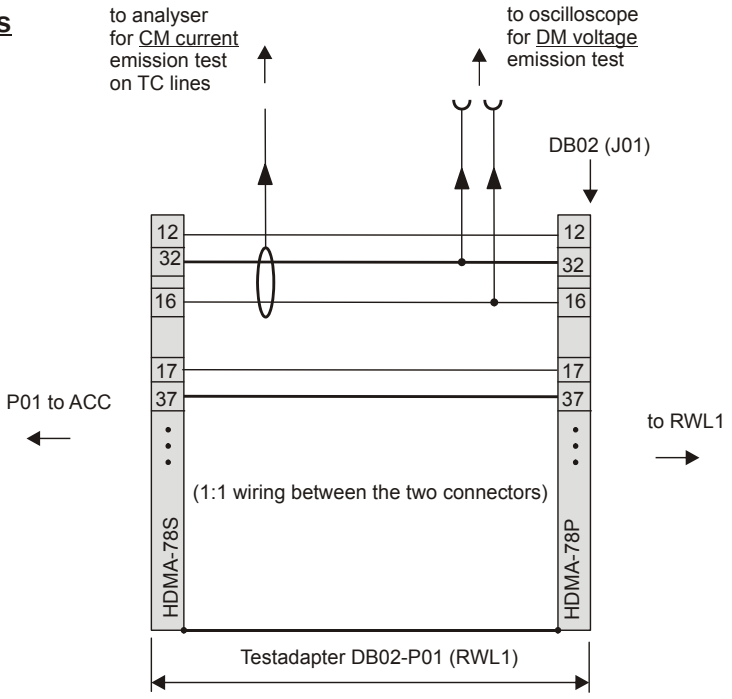


Figure 5-2: TC Test Adapter arrangement for CE, DB02 level

CE on TDC lines

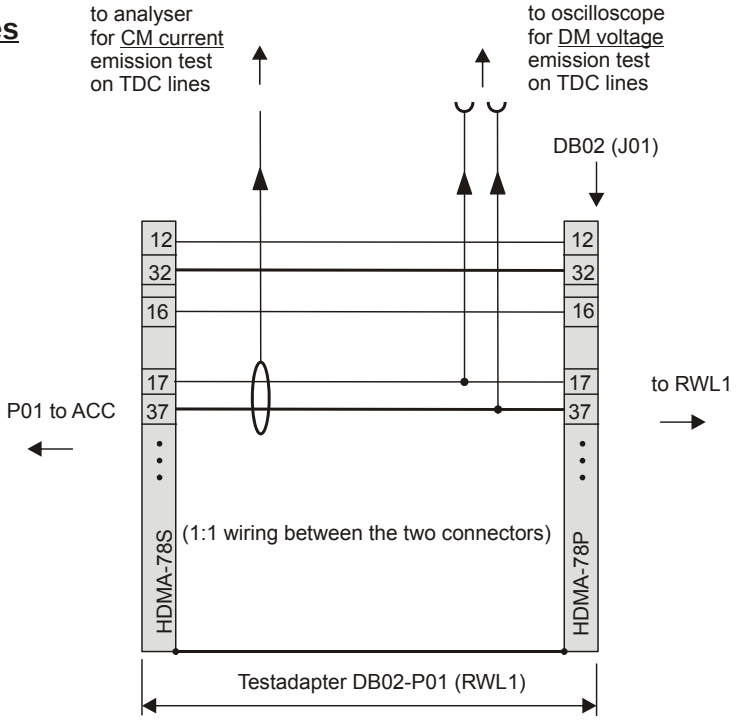


Figure 5-3: TDC Test Adapter arrangement for CE, DB02 level

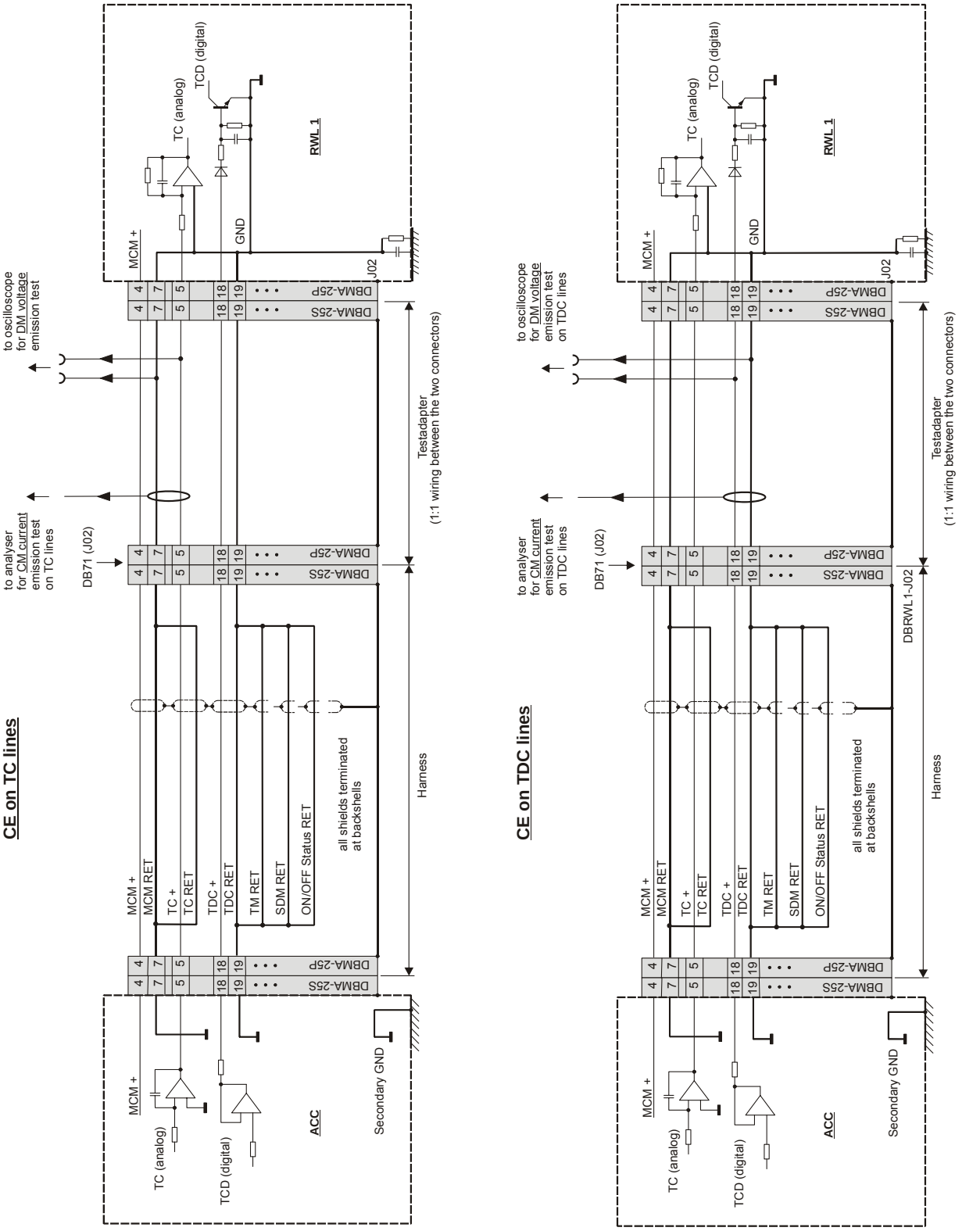


Figure 5-4: Optional TC/TDC Test Adapter arrangement for CE, DB 71 level

5.5 CE Voltage on Signal Lines

ACC-RWL signal lines TC and TDC:
Objective of this test is to check that the conducted voltage emissions on ACC-RWL signal lines TC and TDC are within the values measured at sub system level. This test is performed to fulfil EMC requirement EMCSYS-096 of HERSCHEL EMC Specification [AD6].
The following signal lines shall be tested:

- Torque Command (TC)
- Torque Direction (TDC)

During the emission test on RWL signal lines:

- the TC command shall be set to ‘‘500’’ leading to a read out TLM (MCM) close to 0.5105V,
- the TDC command shall be set to CLOCKWISE direction

MILBUS:
In addition the conducted voltage emissions shall be measured on

- ACC/1553 (MILBUS)

During the emission measurements the MILBUS shall be nominal operating.

The tests shall be done in **time domain**, **differential mode** (DM).
A **background measurement** with Spacecraft OFF / EGSE ON before the test shall identify any emissions generated by the support or external power equipment.

5.5.1 Requirements for CE Voltage Requirements on Signal Lines

TC/TDC: The voltage ripple shall be compared to 300 mVpp. The minimum bandwidth used for measurement shall be 50MHz.

MILBUS: No requirement exists. The measurement is for information only.

5.5.2 CE Voltage Signal Lines Test Set-Up

TC and TDC:
The TC and TDC signal lines shall be interrupted by a test adapter placed at DB02 level in order to get access to the lines for common mode measurement in accordance to Figure 5-2 and Figure 5-3 and paragraph 6.3.1.

As an option only the TC and TDC signal lines may be interrupted by a test adapter placed at DB71 level, see Figure 5-4 and paragraph 6.3.2.

MILBUS:
The MILBUS signal lines shall be interrupted by a test adapter (see paragraph 6.3.3) in order to get access to the lines for differential mode voltage measurements.

All the measurement shall be taken with an oscilloscope.

A photograph shall be taken from the set-up, showing break-out boxes, the PCDU power connectors and the test cables and the voltage probe.

5.6 CS Voltage on Signal Lines (optional)

TC and TDC:
Objective of this test is to check that the ACC-RWL signal lines TC and TDC present no risk of susceptibility when submitted to the levels specified in [AD01] EMCEQ-650. This test is performed to fulfil EMC requirement EMCSYS-094 of HERSCHEL EMC Specification [AD6]
The following lines shall be tested:

- Torque Command (TC)
- Torque Direction Command (TDC)

The test shall be done in **time domain, differential mode**.
As **reference measurement** before the test the TLM values shall be monitored at RWL side without injecting any noise. During the emission test

- the TC command shall be set to ‘‘500’’ leading to a read out TLM (MCM) close to 0.5105V.
- the TDC command shall be set to: CLOCKWISE direction

The frequencies to be tested are listed in the step by step procedure.

5.6.1 Requirements for CS Voltage on Signal Lines

In the frequency range 50 kHz – 50 MHz, the injected voltage should not exceed 2 Vpp either on the signal or the AGND line. In any case, even if the level of 2 Vpp is not reached the imposed current has to be monitored and it **should not exceed 100 mApp** at injection point.
Monitoring of the frequency and current amplitude shall be done by use of a spectrum analyser. The 100 mApp limit applies **only** for the injected spectral frequency; amplitudes of other signal parts of the emission spectrum are not relevant.

All along the tests sequence the TLM (MCM) read out monitoring shall be recorded.

Success Criteria:

1. The RWL shall not exhibit any failures malfunctions or unintended responses when submitted to the injected signals.
2. The motor current TLM (MCM) shall keep inside the range +/-12.5 mV around the value without noise injection when submitted to the injected signals.

5.6.2 CS Signal Voltage Test Set-Up

The TC and TDC signal lines shall be interrupted by a test adapter

The torque command and -direction lines shall be interrupted by a test adapter placed at DB02 level in order to get access to the signal lines and to enable the injection and the time domain voltage measurements in accordance to Figure 5-5 and Figure 5-6 below and paragraph 6.3.1.

As an option only the TC and TDC signal lines may be interrupted by a test adapter placed at DB71 level near the RWL, see Figure 5-7 and paragraph 6.3.2.

A photograph shall be taken from the set-up, showing the position of the break-out box, the voltage probe and the concerned signal lines.

CS on TC lines

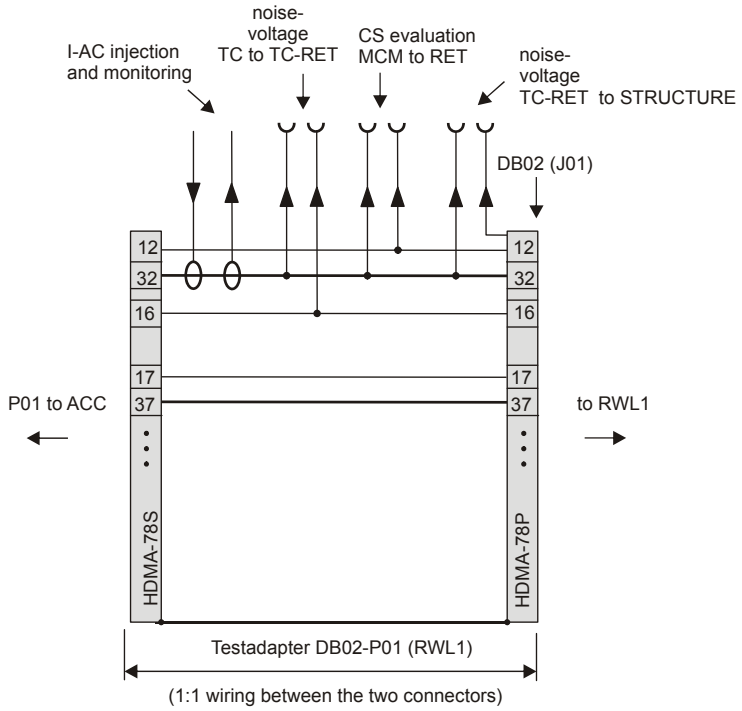


Figure 5-5: TC Test Adapter arrangement for CS, DB02 level

CS on TDC lines

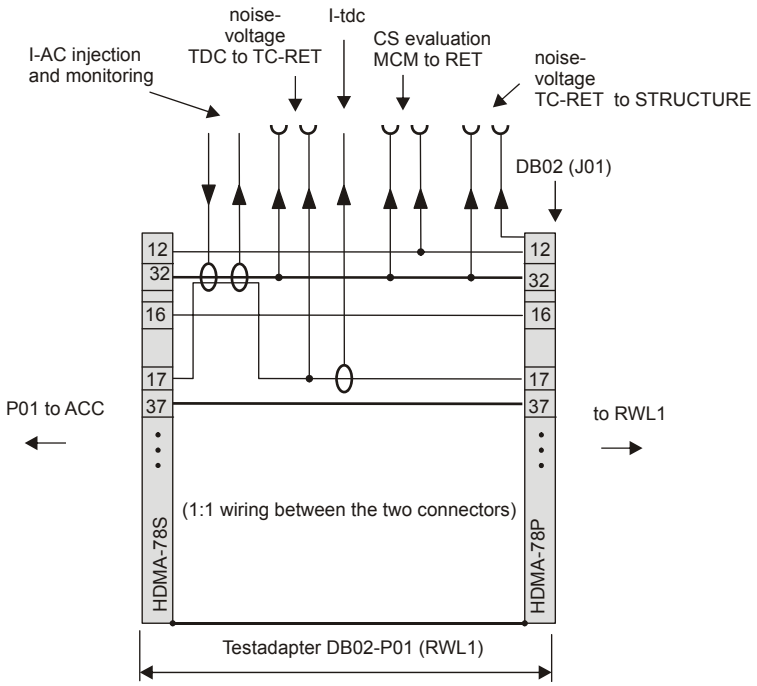


Figure 5-6: TDC Test Adapter arrangement for CS, DB02 level

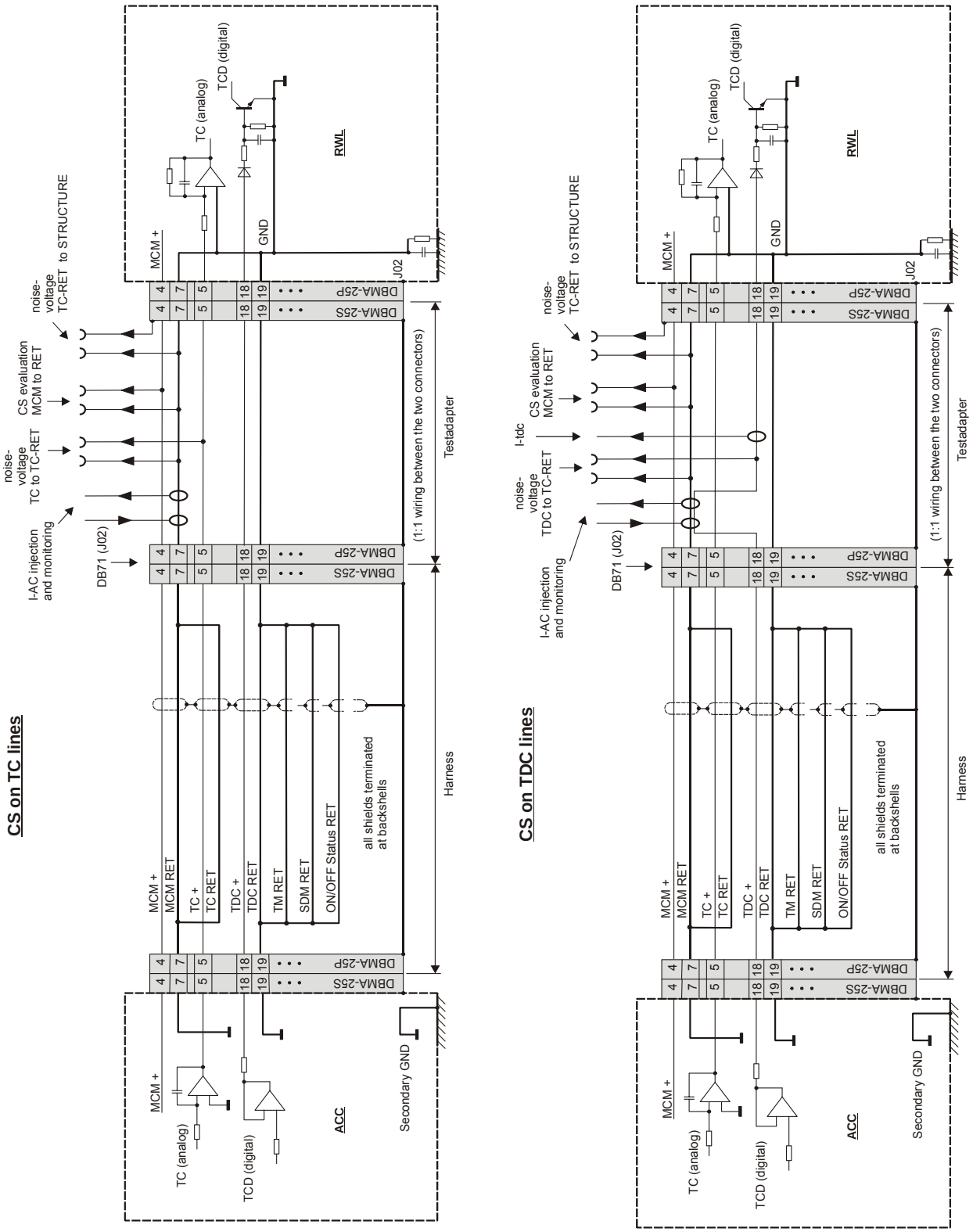


Figure 5-7: TC/TDC Test Adapter arrangement for CS (optional, for measurements on the RWL panel)

6 TEST- AID/ADAPTER ARRANGEMENTS

6.1 General Test- Aid/Adapter Types

General configuration for power- and signal line adapters:

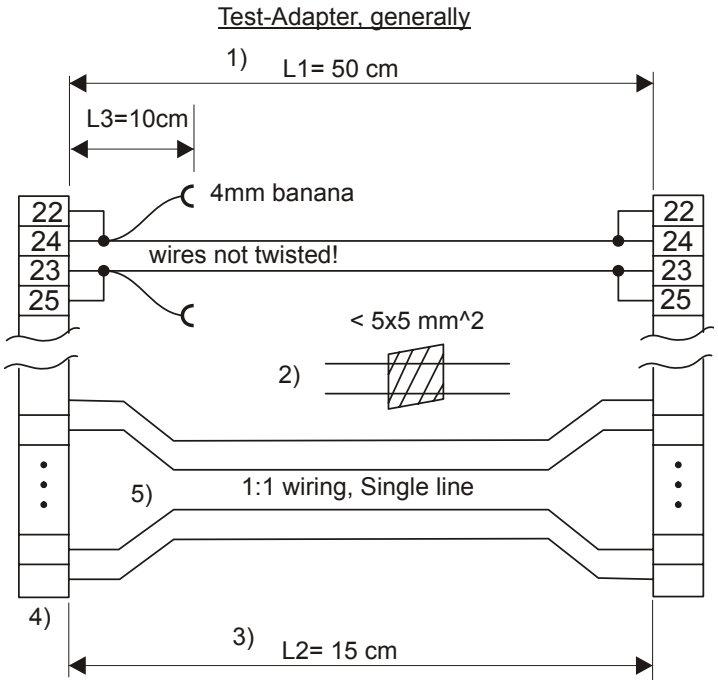


Figure 6-1: Test Adapter, General Configuration

1) L1 > L2 to avoid breaking the wire under test during differential measurements with (large) current probes (see extra sketch below). If all wires have the same length, the stress is only on the tested wire!

2) Maximum alowed cross-section of two wires to match for the feed through current clamp.

3) L3 as short as possible/meaningfull to save place, see 1) also.

4) Material: standard wires and connectors. However connector savers shall be used in addition! Manufactoring via external supplier.

5) 1:1 single line wiring of all other pins. Twisting respecting the flight harness not neccessary due to the shortness of the wires.

6) All wires of every adapter shall be AWG 20.

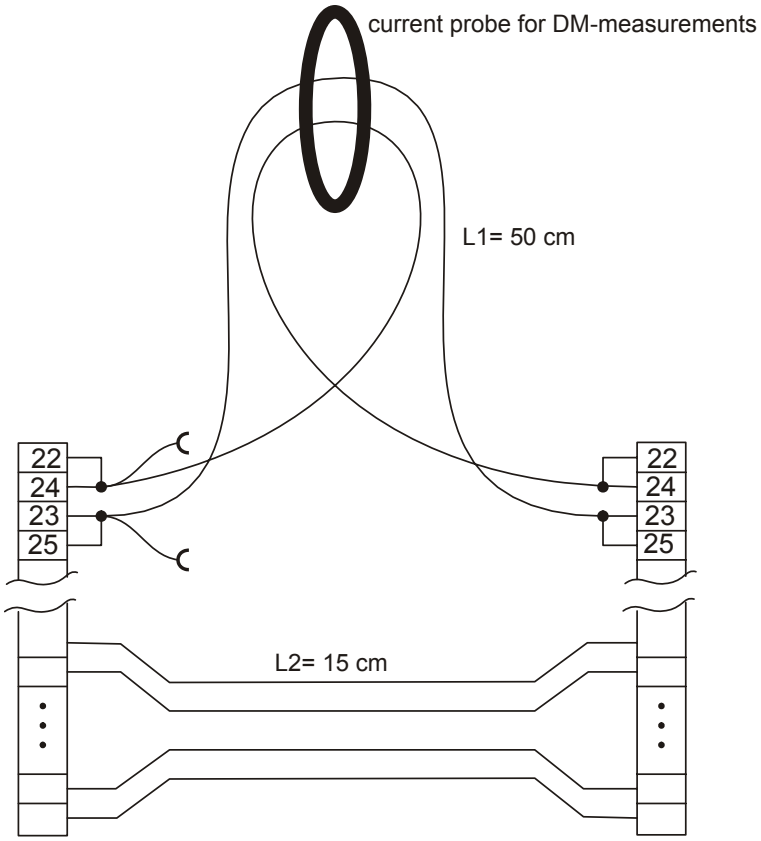


Figure 6-2: Test Adapter, Current Probe Arrangement

6.1.1 Power line Test Adapters

The following adapter types shall be used for testing on power lines:

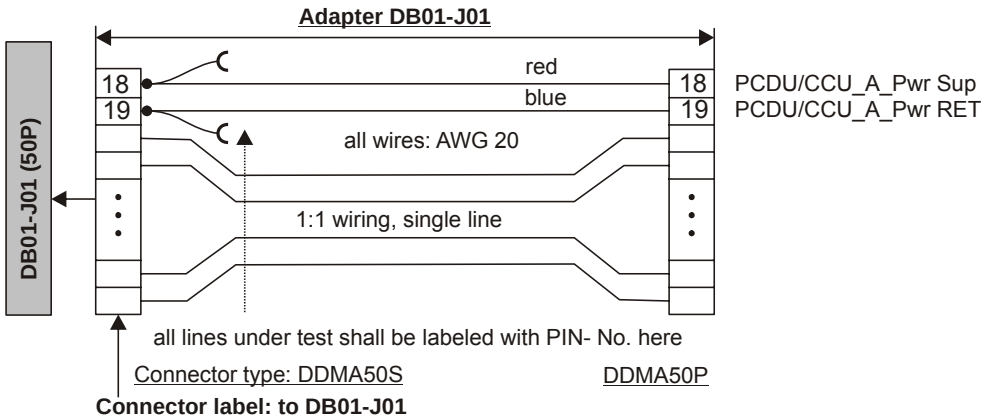


Figure 6-3: Test Adapter DB01-J01

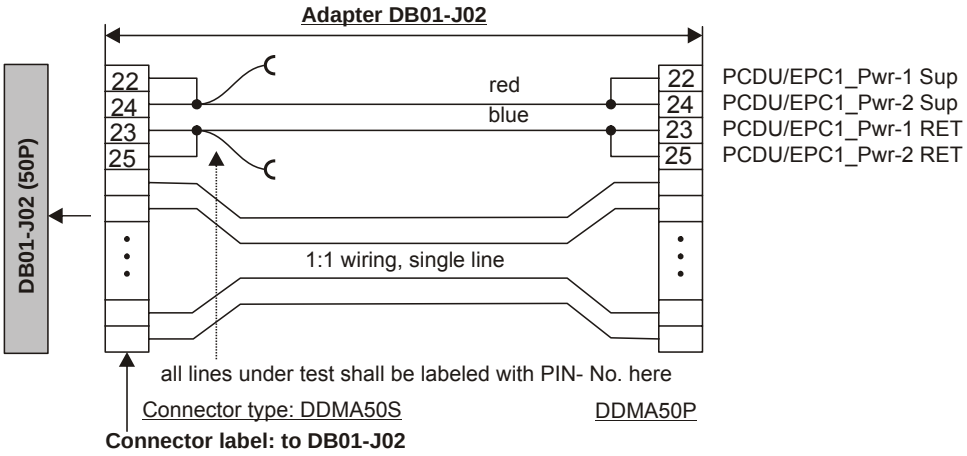


Figure 6-4: Test Adapter DB01-J02

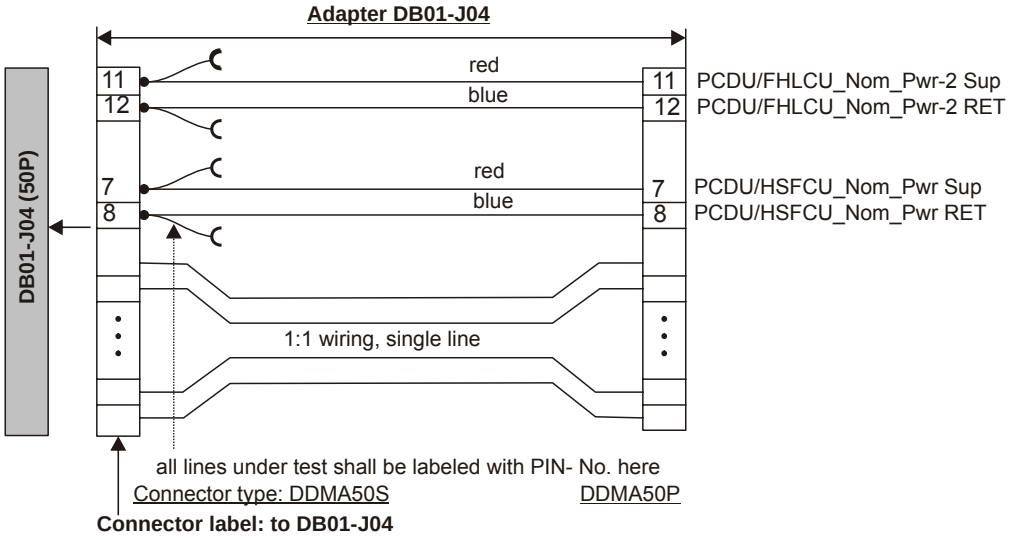


Figure 6-5: Test Adapter DB01-J04

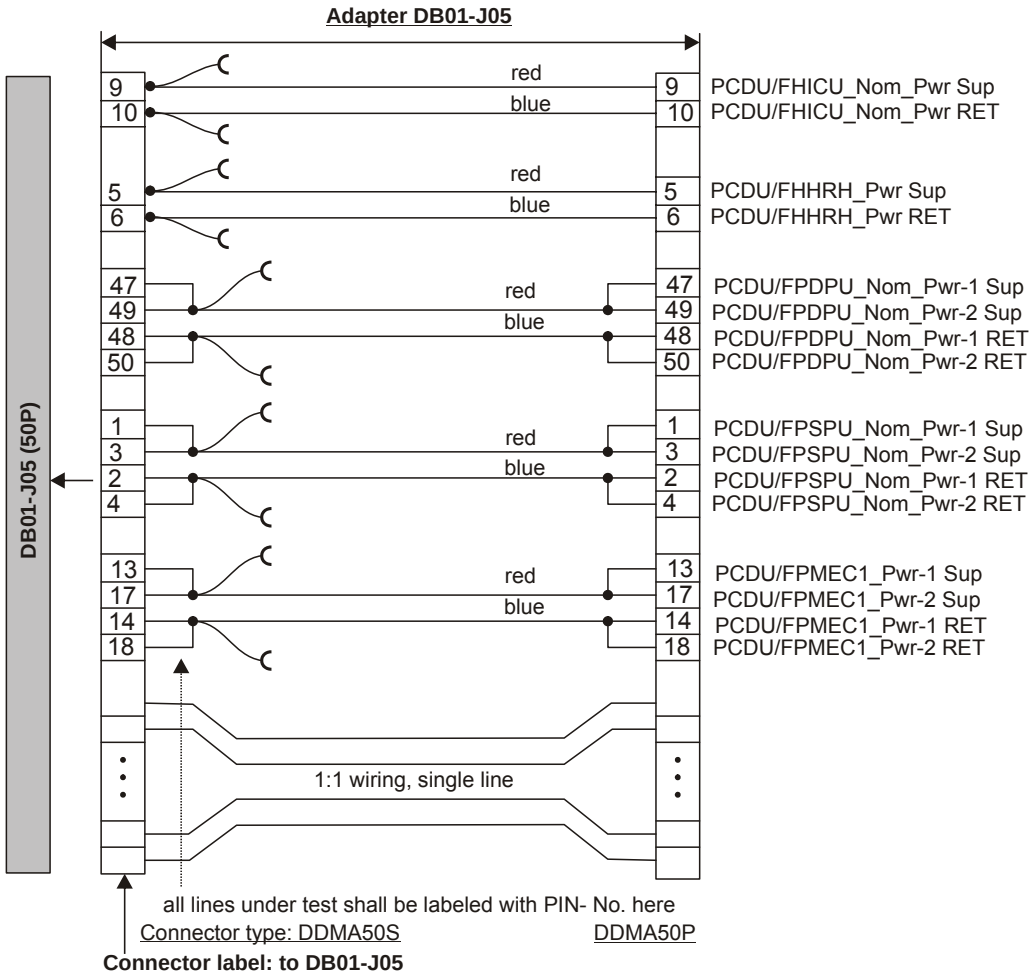


Figure 6-6: Test Adapter DB01-J05

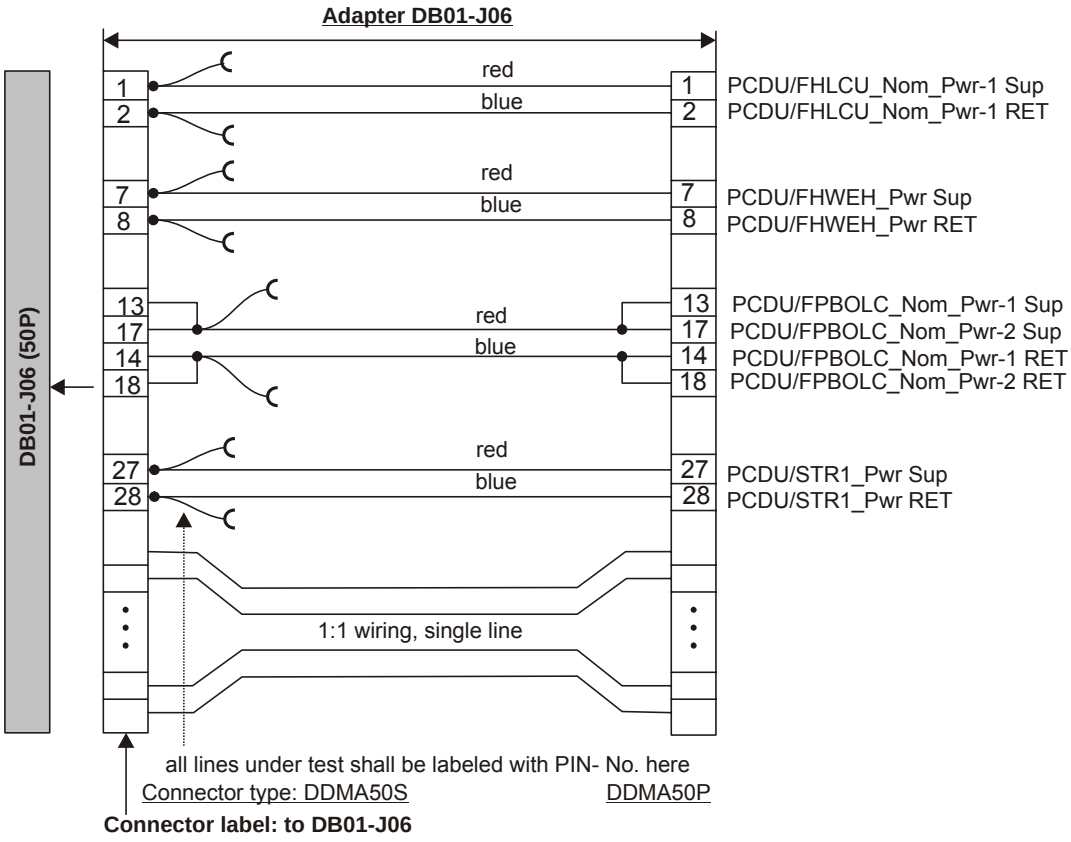


Figure 6-7: Test Adapter for DB01-J06

CE Voltage measurement in Time Domain, Pwr-Return to Structure:

For this measurement the voltage probe "-" shall be connected to the satellite structure.
The accsess pont is …tbd during test…

6.1.2 Signal line Test Adapter

Note that the signal lines are shielded and the shield is rooted via the connector shells.

The following adapter type shall be used for CE/CS on TC/TDC signal lines:

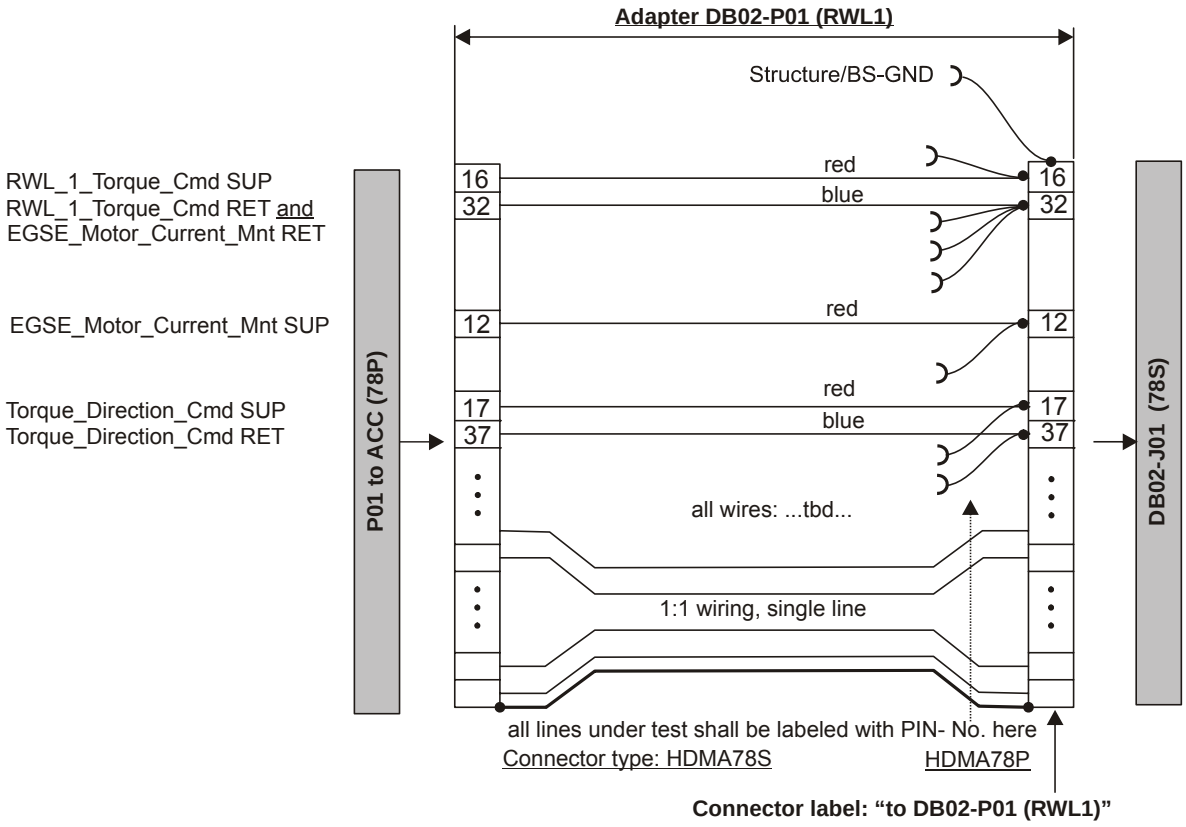


Figure 6-8: Test adapter DB02-P01

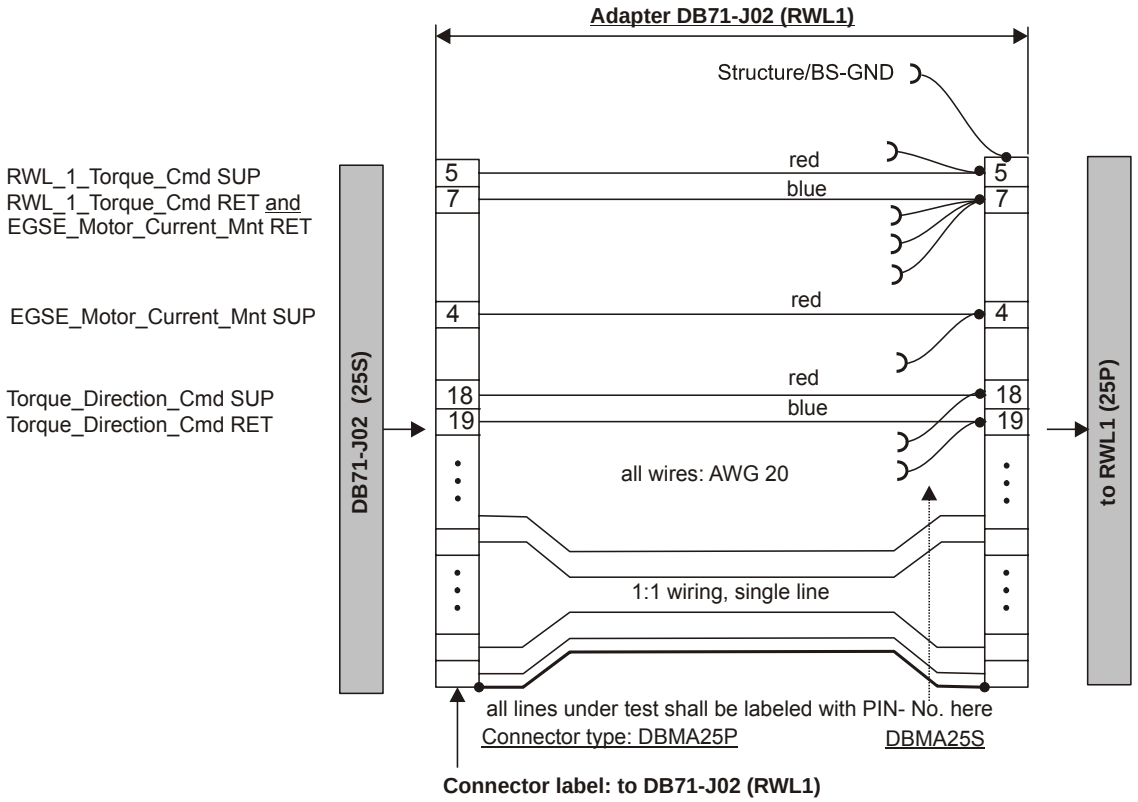


Figure 6-9: Optional test adapter DB71-J02 (for RWL panel only!)

The following adapter type shall be used for CE on MIL-BUS signal lines:

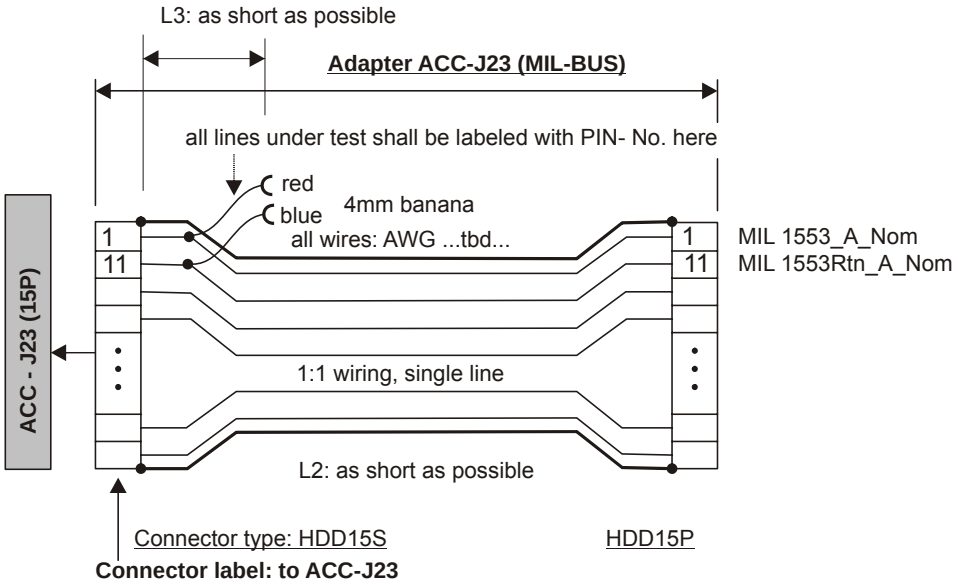


Figure 6-10: Test adapter ACC-J23 (L3 is < 10 cm, L2 is < 5 cm)

6.2 Arrangements for Power Lines

The following table shows the relation between test equipment, test adapter type and accessability of the lines under test.

Equipment		Test Adapter	Accessible per:			Comment/ Current Cap. (A)
			Bracket – Connector (Adapter Name)	Type	Pin	
HIFI	LCU main	Figure 6-7	DB01 - J06	DDMA-50P	PLS: 1 (*) RTN: 2	AWG 18/4
- " -	- " -	Figure 6-5	DB01 – J04	DDMA-50P	PLS: 11 (*) RTN: 12	AWG 18/4
- " -	ICU main	Figure 6-6	DB01 - J05	DDMA-50P	PLS: 9 RTN: 10	AWG 20/2
SPIRE	FCU main	Figure 6-5	DB01 - J04	DDMA-50P	PLS: 7 RTN: 8	AWG 20/4
PACS	DPU main	Figure 6-6	DB01 - J05	DDMA-50P	PLS: 47/49 RTN: 48/50	AWG 20/2
- " -	SPU main	Figure 6-6	DB01 - J05	DDMA-50P	PLS: 01/03 RTN: 02/04	AWG 20/1,5
CCU	CCU main	Figure 6-3	DB01 - J01	DDMA-50P	PLS: 18 RTN: 19	AWG 20/1,5
TWTA	EPC1	Figure 6-4	DB01 - J02	DDMA-50P	PLS: 22/24 RTN: 23/25	AWG 20/3
STR	STR1	Figure 6-7	DB01 - J06	DDMA-50P	PLS: 27 RTN: 28	AWG 20/1
PACS	MEC main	Figure 6-6	DB01 - J05	DDMA-50P	PLS: 13/17 RTN: 14/18	AWG 20/2
- " -	BOLC main	Figure 6-7	DB01 - J06	DDMA-50P	PLS: 13/17 RTN: 14/18	AWG 20/1,2

In detail:

HIFI-LCU-main Power Lines (*):

- The measurement shall be done on the **PCDU/FHLCU_Pwrs SUP/RTN** lines.
- The test aids Figure 6-7 and Figure 6-5 shall be placed at DB01 level on harness side. The connectors to be disconnected for installing the adapter are J06 and J04.
- **Attention!** The power lines to be tested are rooted in parallel via two connectors (J06 and J04). Take care to install the current probe correctly.

HIFI-ICU main Power Lines:

- The measurement shall be done on the **PCDU/FHICU_Nom_Pwr SUP/RTN** lines.
- The test aid shall be placed at DB01 level on harness side. The connector to be disconnected for installing the adapter is J05.

SPIRE-FCU main Power Lines:

- The measurement shall be done on the **PCDU/HSFCU_Nom_Pwr SUP/RTN** lines.
- The test aid Figure 6-5 shall be placed at DB01 level on harness side. The connector to be disconnected for installing the adapter is J04.

PACS-DPU main Power Lines:

- The measurement shall be done on the **PCDU/FPDPU_Nom_Pwrs SUP/RTN** lines.
- The test aid Figure 6-6 shall be placed at DB01 level on harness side. The connector to be disconnected for installing the adapter is J05.

PACS-SPU main Power Lines:

- The measurement shall be done on the **PCDU/FPSPU1_ Pwrs SUP/RTN** lines.
- The test aid Figure 6-6 shall be placed at DB01 level on harness side. The connector to be disconnected for installing the adapter is J05.

PACS-MEC main Power Lines:

- The measurement shall be done on the **PCDU/FPMEC1_ Pwrs SUP/RTN** lines.
- The test aid Figure 6-6 shall be placed at DB01 level on harness side. The connector to be disconnected for installing the adapter is J05.

PACS-BOLC main Power Lines:

- The measurement shall be done on the **PCDU/FPBOLC_ Pwrs SUP/RTN** lines.
- The test aid Figure 6-7 shall be placed at DB01 level on harness side. The connector to be disconnected for installing the adapter is J06.

CCU main Power Lines:

- The measurement shall be done on the **PCDU/CCU_A_ Pwrs SUP/RTN** lines.
- The test aid Figure 6-3 shall be placed at DB01 level on harness side. The connector to be disconnected for installing the adapter is J01.

TWTA-EPC1 Power Lines:

- The measurement shall be done on the **PCDU/EPC1_Pwrs SUP/RTN** lines.
- The test aid Figure 6-4 shall be placed at DB01 level on harness side. The connector to be disconnected for installing the adapter is J02.

STR1 Power Lines:

- The measurement shall be done on the **PCDU/STR1_Pwr SUP/RTN** lines.
- The test aid Figure 6-7 shall be placed at DB01 level on harness side. The connector to be disconnected for installing the adapter is J06.

6.3 Arrangements for Signal Lines

The following shows the test adapter arrangement, test adapter type and accessability of the lines under test.

6.3.1 RWL adapter arrangement for measuring on DB02 Bracket

- The test aid Figure 6-8 shall be placed at DB02 level. The connector to be disconnected for installing the adapter is DB02-J01.
- The conducted emission measurement (CE testing) shall be done on the **Torque_Cmd SUP/RET (16+32)** and **Torque_Direction_Cmd SUP/RET (17+32)** lines (see also Figure 5-2 and Figure 5-3).
- The conducted susceptibility test (CS testing) shall be done on the **MCM/TC_RET (32)** line for the TC-interface (see also Figure 5-5).
- The conducted susceptibility test (CS testing) shall be done on the **MCM/TC_RET (32) and TDC + (17)** lines for the TDC- interface (see also Figure 5-6).

6.3.2 *Optional RWL adapter arrangement for measuring on DB71 Bracket (RWL panel)*

- The test aid Figure 6-9 (see also Figure 5-4) shall be placed at DB71 level. The connector to be disconnected for installing the adapter is DB71-J02.
- The conducted emission measurement (CE testing) shall be done on the **Torque_Cmd SUP/RET (5+7)** and **Torque_Direction_Cmd SUP/RET (18+19)** lines.
- The conducted susceptibility test (CS testing) shall be done on the **MCM/TC_RET (7)** line for the TC-interface.
- The conducted susceptibility test (CS testing) shall be done on the **MCM/TC_RET (7) and TDC + (18)** lines for the TDC- interface.

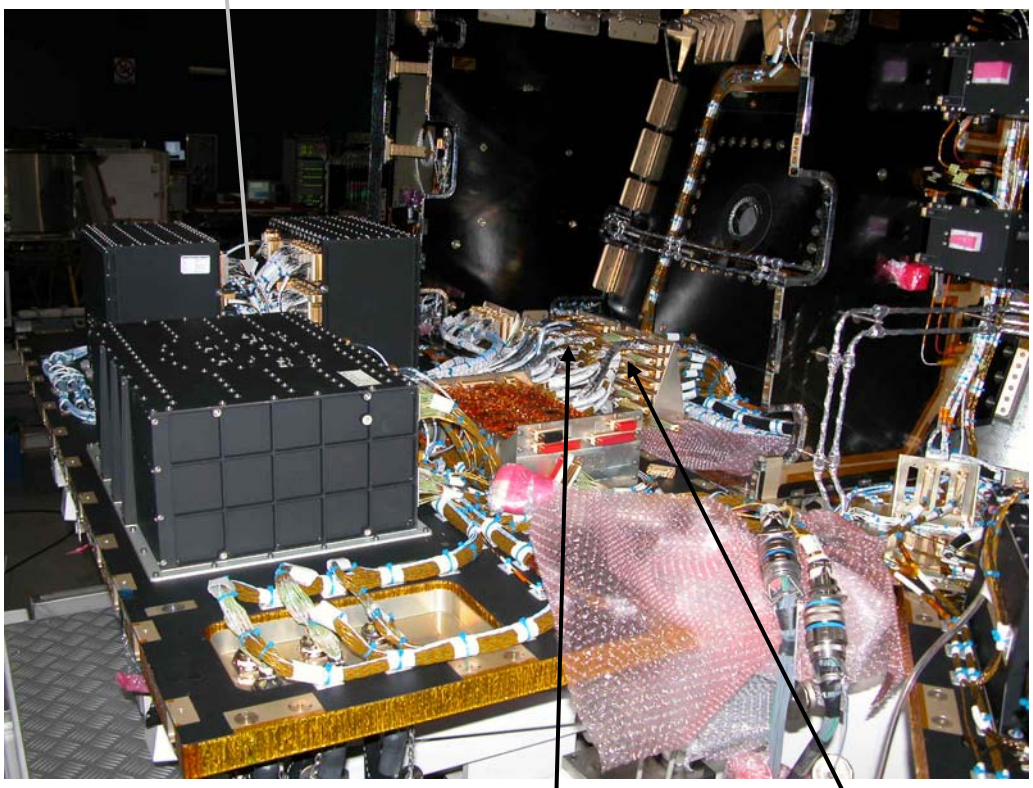
6.3.3 *MILBUS adapter arrangement for measuring on ACC level*

- The test aid Figure 6-10 shall be placed at ACC level. The connector to be disconnected for installing the adapter is ACC-J23.
- The conducted emission measurement (CE testing) shall be done differentially between the **MIL 1553_A_Nom (1)** and **MIL 1553Rtn_A_Nom (11)** lines.

6.4 Test- Aid/Adapter locations/accessability on HERSCHEL

6.4.1 Adapter arrangement for CE tests on power- and signal lines

ACC:
Access to connector:
J23 for CE measurements on MILBUS signal lines. Insert Test Adapter on the ACC.
Connector savers shall be used!



Bracket DB02:
Access to connector:
J01 for CE measurements on RWL1 signal lines. Insert Test Adapter on this side of the bracket.
Connector savers shall be used!

Bracket DB01:
Access to connectors:
J01, J02, J03, J04, J05, J06 for CE measurements on power lines.
Insert Test Adapter on this side of the bracket.
Connector type at DB01 on this side is “DyMA-xxP”. So the test adapter connector type must be” DyMA-xxS”.
Connector savers shall be used!

Pecautions:

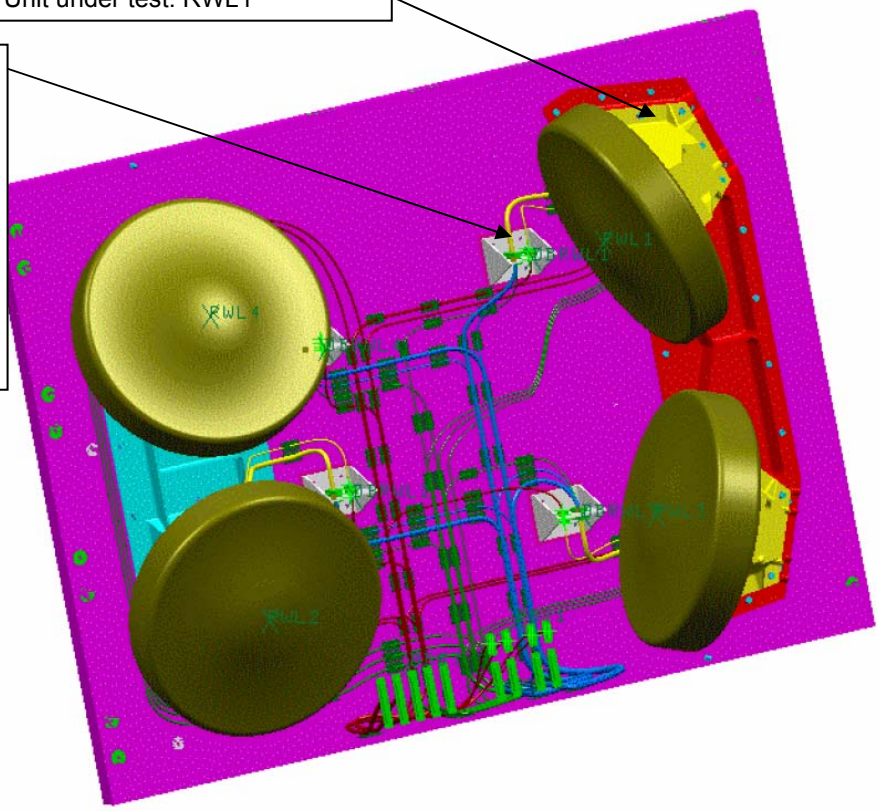
It is absolutely mandatory to fix the EMC adapter connectors (power and signal) mechanically against disconnecting by accident or stress!

Note that the signal lines are shielded and the shield is rooted via the connector shells. So, when installing the EMC adapters and later performing the measurements, take care and measures that the shielding path is conductively not interrupted!

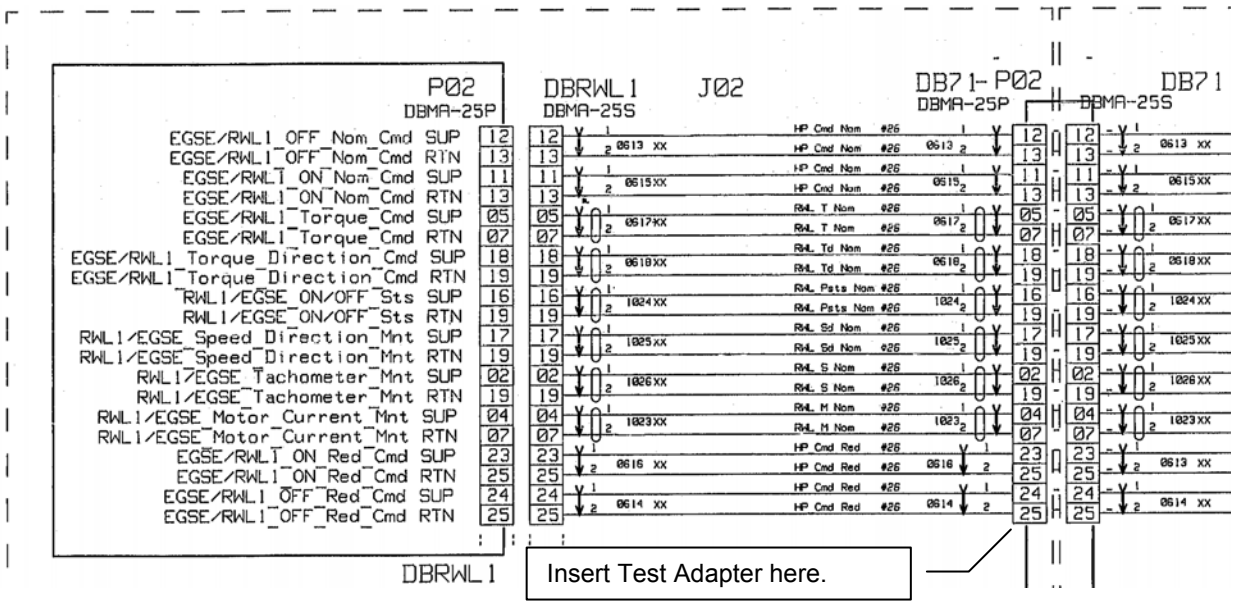
6.4.2 *Optional adapter arrangement for CE/CS tests on signal lines*

Unit under test: RWL1

Bracket DB71:
Insert Test Adapter on this side of the bracket.
Connector type at DB71 on this side to be disconnected is “DBMA-25P”. So the test adapter connector type must also be” DBMA-25P” at DB71 side.
Connector savers shall be used!



Test adapter placement at DB71 level:



Pecautions: See previouse chapter.

7 ACTIVITIES FLOW

The following tables depict the flow of the activities described in this procedure.

7.1 General Operational Procedures

The following table describes the general procedures to be followed follwed by the SCOE operators in order to switch the satellite into the required configuration for the conducted tests.

Test Step No/ Info		Description/Comments
A		**REFERENCE MODE**
A.1	**OP**	Switch satellite EGSE into the REFERENCE mode Configuration according to the configuration table in chapter 3.1: perform Section 8.1.1 of AD10
A.2	**OP**	Confirm that all satellite equipment is OFF (unpowered): perform Section 8.1.2 of AD10
A.3	**OP**	Confirm that HIFI, PACS and SPIRE are OFF (unpowered): perform Section 8.1.3 of AD10

Test Step No/ Info		Description/Comments
B		**NOISIEST MODE**
B.1	**OP**	Switch satellite EGSE into the NOISIEST mode Configuration according to the configuration table in chapter 3.1. No actions required here as performed in B.2 below
B.2	**OP**	SVM NOISIEST mode
B.2.1	**OP**	Switch on into noisiest mode: perform sect. 8.1.1, 8.1.2, 8.1.3, 8.2.1 and 8.2.7 of AD10
B.2.2	**OP**	Switch PACS to Safe Mode perform Section 8.2.3.1 of AD10
B.2.3	**OP**	Switch SPIRE to STANDBY: perform Section 8.2.4.1 of AD10
B.2.4	**OP**	Switch HIFI to STANDBY: perform Section 8.2.5.1 of AD10
B.2.5	**OP**	Confirm that SVM & CCU equipment is operating correctly in NOISIEST mode and Instruments powered in STANDBY: perform Section 8.2.2 of AD10
B.3	**OP**	PACS NOISIEST mode
B.3.1		If SVM OFF: perform B.2
B.3.2	**OP**	If SPIRE is in NOISIEST mode then return to STANDBY: perform section 8.2.4.3 of AD10
B.3.3	**OP**	If HIFI is in NOISIEST mode then return to STANDBY: perform section 8.2.5.3 of AD10
B.3.4	**OP**	PACS is in NOISIEST mode and confirm: perform section 8.2.3.2 of AD10
B.4	**OP**	HIFI NOISIEST mode
B.4.1		If SVM OFF: perform B.2
B.4.2	**OP**	NA
B.4.3	**OP**	If SPIRE is in NOISIEST mode then return to STANDBY: perform section 8.2.4.3 of AD10
B.4.4	**OP**	HIFI to NOISY mode and confirm: perform section 8.2.5.2 of AD10

Test Step No/ Info		Description/Comments
B.5	**OP**	SPIRE NOISIEST mode
B.5.1	**OP**	If SVM OFF: perform B.2
B.5.2	**OP**	If HIFI is in NOISIEST mode then return to STANDBY: perform section 8.2.5.3 of AD10
B.5.3	**OP**	NA
B.5.4	**OP**	SPIRE to NOISY mode and confirm: perform section 8.2.4.2 of AD10
B.6	**OP**	RWL-1-4 noisiest mode @ 15 min : perform section 8.2.6 of AD10

Test Step No/ Info		Description/Comments
C		**SENSITIVE MODE (**according to the configuration table in chapter 3.1)
C.1	**OP**	If SATELLITE initially OFF
C.1.1	**OP**	Switch on into sensitive mode: perform sect. 8.1.1, 8.1.2, 8.1.3, 8.2.1 and 8.2.7 of AD10
C.2	**OP**	If SATELLITE ON in NOISIEST mode
C.2.1	**OP**	If SPIRE is in NOISIEST mode then return to STANDBY: perform section 8.2.4.3 of AD10
C.2.2	**OP**	Switch OFF SPIRE and confirm: perform section 8.2.4.4 of AD10
C.2.3	**OP**	If HIFI is in NOISIEST mode then return to STANDBY: perform section 8.2.5.3 of AD10
C.2.4	**OP**	Switch OFF HIFI and confirm: perform section 8.2.5.4 of AD10
C.2.5	**OP**	NA
C.2.6	**OP**	Switch OFF PACS and confirm: perform section 8.2.3.4 of AD10
C.2.7	**OP**	Spin down reaction wheels: perfom section 8.3.1 of AD10
C.3	**OP**	Confirm sensitive mode: perform section 8.3.2

Test Step No/ Info		Description/Comments
D		**OFF MODE**
D.1	**OP**	SPIRE OFF
D.1.1	**OP**	If SPIRE is in NOISIEST mode then return to STANDBY: perform section 8.2.4.3 of AD10
D.1.2	**OP**	Switch OFF SPIRE and confirm: perform section 8.2.4.4 of AD10
D.2	**OP**	HIFI OFF
D.2.1	**OP**	If HIFI is in NOISIEST mode then return to STANDBY: perform section 8.2.5.3 of AD10
D.2.2	**OP**	Switch OFF HIFI and confirm: perform section 8.2.5.4 of AD10
D.3	**OP**	PACS OFF
D.3.1	**OP**	NA
D.3.2	**OP**	Switch OFF PACS and confirm: perform section 8.2.3.4 of AD10
D.4	**OP**	SVM & CCU OFF
D.4.1	**OP**	Switch OFF SVM: perform section 8.4.1.1 of AD10
D.4.2	**OP**	Confirm that all satellite equipment is OFF (unpowered) : perform section 8.4.1.2 of AD10

Test Step No/ Info		Description/Comments
D.5	**OP**	Switch all EGSE OFF

7.2 Tests Activities Flow

The following table depicts the flow of the activities described in this procedure.

Test Step No	Activity	SATELLITE Operational Mode
0	**Install BoBs/Test Adapter to Power- and Signal Lines**	OFF Mode
0.1	Test adapter “DB01-J01” for CE on CCU-MAIN	
0.2	Test adapter “DB01-J02” for CE on TWTA-EPC1	
0.3	Test adapter “DB01-J04” for CE on HIFI-LCU-MAIN and SPIRE-FCU-main	
0.4	Test adapter “DB01-J05” for CE on HIFI-ICU-MAIN, PACS-DPU-MAIN, PACS-SPU-MAIN and PACS-MEC1-MAIN	
0.5	Test adapter “DB01-J06” for CE on HIFI-LCU-MAIN, PACS-BOLC-MAIN and STR1	
0.6	Test adapter “DB02-P01” for CE/CS on Signal Lines: RWL1 TC/TDC	
0.7	Test adapter “ACC-J23” for CE on Signal Lines: MILBUS	
1	**Reference Test on Satellite Ground Line**	REFERENCE Mode
1.1	CE Current Ripple in Time Domain	
2	**Reference Test on HIFI-LCU-main Power Lines**	REFERENCE Mode
2.1	If not already done switch into REFERENCE Mode according to chapter 7.1 ‘’A’’ and confirm the mode.	
2.2	CE Current in Frequency Domain Common Mode	
2.3	CE Current in Frequency Domain Differential Mode	
2.4	CE Current in Time Domain Differential Mode	
2.5	CE Voltage in Time Domain Differential Mode	
2.6	CE Voltage in Time Domain, Pwr-Return to Structure	
3	**Reference Test on HIFI-ICU-main Power Lines**	REFERENCE Mode
3.1	If not already done switch into REFERENCE Mode according to chapter 7.1 ‘’A’’ and confirm the mode.	
3.2	CE Current in Frequency Domain Common Mode	
3.3	CE Current in Frequency Domain Differential Mode	
3.4	CE Current in Time Domain Differential Mode	
3.5	CE Voltage in Time Domain Differential Mode	
3.6	CE Voltage in Time Domain, Pwr-Return to Structure	
4	**Reference Test on SPIRE-FCU-main Power Lines**	REFERENCE Mode
4.1	If not already done switch into REFERENCE Mode according to chapter 7.1 ‘’A’’ and confirm the mode.	
4.2	CE Current in Frequency Domain Common Mode	
4.3	CE Current in Frequency Domain Differential Mode	
4.4	CE Current in Time Domain Differential Mode	

Test Step No	Activity	SATELLITE Operational Mode
4.5	CE Voltage in Time Domain Differential Mode	
4.6	CE Voltage in Time Domain, Pwr-Return to Structure	
5	**Reference Test on PACS-DPU-main Power Lines**	REFERENCE Mode
5.1	If not already done switch into REFERENCE Mode according to chapter 7.1 ''A'' and confirm the mode.	
5.2	CE Current in Frequency Domain Common Mode	
5.3	CE Current in Frequency Domain Differential Mode	
5.4	CE Current in Time Domain Differential Mode	
5.5	CE Voltage in Time Domain Differential Mode	
5.6	CE Voltage in Time Domain, Pwr-Return to Structure	
6	**Reference Test on PACS-SPU-main Power Lines**	REFERENCE Mode
6.1	If not already done switch into REFERENCE Mode according to chapter 7.1 ''A'' and confirm the mode.	
6.2	CE Current in Frequency Domain Common Mode	
6.3	CE Current in Frequency Domain Differential Mode	
6.4	CE Current in Time Domain Differential Mode	
6.5	CE Voltage in Time Domain Differential Mode	
6.6	CE Voltage in Time Domain, Pwr-Return to Structure	
7	**Reference Test on PACS-BOLC-main Power Lines**	REFERENCE Mode
7.1	If not already done switch into REFERENCE Mode according to chapter 7.1 ''A'' and confirm the mode.	
7.2	CE Current in Frequency Domain Common Mode	
7.3	CE Current in Frequency Domain Differential Mode	
7.4	CE Current in Time Domain Differential Mode	
7.5	CE Voltage in Time Domain Differential Mode	
7.6	CE Voltage in Time Domain, Pwr-Return to Structure	
8	**Reference Test on PACS-MEC-main Power Lines**	REFERENCE Mode
8.1	If not already done switch into REFERENCE Mode according to chapter 7.1 ''A'' and confirm the mode.	
8.2	CE Current in Frequency Domain Common Mode	
8.3	CE Current in Frequency Domain Differential Mode	
8.4	CE Current in Time Domain Differential Mode	
8.5	CE Voltage in Time Domain Differential Mode	
8.6	CE Voltage in Time Domain, Pwr-Return to Structure	
9	**Reference Test on CCU-main Power Lines**	REFERENCE Mode
9.1	If not already done switch into REFERENCE Mode according to chapter 7.1 ''A'' and confirm the mode.	
9.2	CE Current in Frequency Domain Common Mode	
9.3	CE Current in Frequency Domain Differential Mode	
9.4	CE Current in Time Domain Differential Mode	
9.5	CE Voltage in Time Domain Differential Mode	
9.6	CE Voltage in Time Domain, Pwr-Return to Structure	

Test Step No	Activity	SATELLITE Operational Mode
10	**Reference Test on TWTA-EPC1 Power Lines**	
10.1	If not already done switch into REFERENCE Mode according to chapter 7.1 ''A'' and confirm the mode.	
10.2	CE Current in Frequency Domain Common Mode	
10.3	CE Current in Frequency Domain Differential Mode	
10.4	CE Current in Time Domain Differential Mode	
10.5	CE Voltage in Time Domain Differential Mode	
10.6	CE Voltage in Time Domain, Pwr-Return to Structure	
11	**Reference Test on STR1 Power Lines**	REFERENCE mode
11.1	If not already done switch into REFERENCE Mode according to chapter 7.1 ''A'' and confirm the mode.	
11.2	CE Current in Frequency Domain Common Mode	
11.3	CE Current in Frequency Domain Differential Mode	
11.4	CE Current in Time Domain Differential Mode	
11.5	CE Voltage in Time Domain Differential Mode	
11.6	CE Voltage in Time Domain, Pwr-Return to Structure	
12	**Reference Test on TC Signal Lines (RWL1)**	REFERENCE Mode
12.1	If not already done switch into REFERENCE Mode according to chapter 7.1 ''A'' and confirm the mode.	
12.2	CE Current in Frequency Domain Common Mode	
12.3	CE Voltage in Time Domain Common Mode	
13	**Reference Test on TDC Signal Lines (RWL1)**	REFERENCE Mode
13.1	If not already done switch into REFERENCE Mode according to chapter 7.1 ''A'' and confirm the mode.	
13.2	CE Current in Frequency Domain Common Mode	
13.3	CE Voltage in Time Domain Common Mode	
14	**Reference Test on ACC/1553 (MILBUS) Signal Lines**	REFERENCE Mode
14.1	If not already done switch into REFERENCE Mode according to chapter 7.1 ''A'' and confirm the mode.	
14.2	CE Voltage in Time Domain Differential Mode	
15	**Reference Test on Satellite Ground Line DELETED**	
15.1	CE Current Ripple in Time Domain	
16	**Test on HIFI-LCU-main Power Lines**	NOISIEST Mode with **HIFI in SCIENCE**, PACS in Safe Mode and SPIRE in STANDBY: **''B4''**
16.1	If not already done switch into NOISIEST mode according to chapter 7.1 ''B4'' and confirm the mode	
16.2	CE Current in Frequency Domain Common Mode	
16.3	CE Current in Frequency Domain Differential Mode	
16.4	CE Current in Time Domain Differential Mode	
16.5	CE Voltage in Time Domain Differential Mode	
16.6	CE Voltage in Time Domain, Pwr-Return to Structure	

Test Step No	Activity	SATELLITE Operational Mode
17	**Test on HIFI-ICU-main Power Lines**	NOISIEST Mode with **HIFI in SCIENCE** PACS in Safe Mode and SPIRE in STANDBY: **''B4''**
17.1	If not already done switch into NOISIEST mode according to chapter 7.1 ''B4'' and confirm the mode	
17.2	CE Current in Frequency Domain Common Mode	
17.3	CE Current in Frequency Domain Differential Mode	
17.4	CE Current in Time Domain Differential Mode	
17.5	CE Voltage in Time Domain Differential Mode	
17.6	CE Voltage in Time Domain, Pwr-Return to Structure	
18	**Test on SPIRE-FCU-main Power Lines**	NOISIEST Mode with **SPIRE in SCIENCE** PACS in Safe Mode and HIFI in STANDBY: **''B5''**
18.1	If not already done switch into NOISIEST mode according to chapter 7.1 ''B5'' and confirm the mode	
18.2	CE Current in Frequency Domain Common Mode	
18.3	CE Current in Frequency Domain Differential Mode	
18.4	CE Current in Time Domain Differential Mode	
18.5	CE Voltage in Time Domain Differential Mode	
18,6	CE Voltage in Time Domain, Pwr-Return to Structure	
19	**Test on PACS-DPU-main Power Lines**	NOISIEST Mode with **PACS in Safe Mode** and HIFI and SPIRE in STANDBY: **''B3''**
19.1	If not already done switch into NOISIEST mode according to chapter 7.1 ''B3'' and confirm the mode	
19.2	CE Current in Frequency Domain Common Mode	
19.3	CE Current in Frequency Domain Differential Mode	
19.4	CE Current in Time Domain Differential Mode	
19.5	CE Voltage in Time Domain Differential Mode	
19.5	CE Voltage in Time Domain, Pwr-Return to Structure	
20	**Test on PACS-SPU-main Power Lines**	NOISIEST Mode with **PACS in Safe Mode** and HIFI and SPIRE in STANDBY: **''B3''**
20.1	If not already done switch into NOISIEST mode according to chapter 7.1 ''B3'' and confirm the mode	
20.2	CE Current in Frequency Domain Common Mode	
20.3	CE Current in Frequency Domain Differential Mode	
20.4	CE Current in Time Domain Differential Mode	
20.5	CE Voltage in Time Domain Differential Mode	
20.6	CE Voltage in Time Domain, Pwr-Return to Structure	
21	**Test on PACS-BOLC-main Power Lines**	NOISIEST Mode with **PACS in Safe Mode** and HIFI and SPIRE in STANDBY: **''B3''**
21.1	If not already done switch into NOISIEST mode according to chapter 7.1 ''B3'' and confirm the mode	

Test Step No	Activity	SATELLITE Operational Mode
21.2	CE Current in Frequency Domain Common Mode	
21.3	CE Current in Frequency Domain Differential Mode	
21.4	CE Current in Time Domain Differential Mode	
21.5	CE Voltage in Time Domain Differential Mode	
21.6	CE Voltage in Time Domain, Pwr-Return to Structure	
22	**Test on PACS-MEC-main Power Lines**	NOISIEST Mode with **PACS in Safe Mode** and HIFI and SPIRE in STANDBY: **''B3''**
22.1	If not already done switch into NOISIEST mode according to chapter 7.1 ''B3'' and confirm the mode	
22.2	CE Current in Frequency Domain Common Mode	
22.3	CE Current in Frequency Domain Differential Mode	
22.4	CE Current in Time Domain Differential Mode	
22.5	CE Voltage in Time Domain Differential Mode	
22.6	CE Voltage in Time Domain, Pwr-Return to Structure	
23	**Test on CCU-main Power Lines**	NOISIEST Mode with **HIFI in SCIENCE, PACS in Safe Mode** and SPIRE in STANDBY: **''B4''**
23.1	If not already done switch into NOISIEST mode according to chapter 7.1 ''B4'' and confirm the mode	
23.2	CE Current in Frequency Domain Common Mode	
23.3	CE Current in Frequency Domain Differential Mode	
23.4	CE Current in Time Domain Differential Mode	
23.5	CE Voltage in Time Domain Differential Mode	
23.6	CE Voltage in Time Domain, Pwr-Return to Structure	
24	**Test on TWTA-EPC1 Power Lines**	NOISIEST Mode with **HIFI in SCIENCE, PACS in Safe Mode** and SPIRE in STANDBY: **''B4''**
24.1	If not already done switch into NOISIEST mode according to chapter 7.1 ''B4'' and confirm the mode	
24.2	CE Current in Frequency Domain Common Mode	
24.3	CE Current in Frequency Domain Differential Mode	
24.4	CE Current in Time Domain Differential Mode	
24.5	CE Voltage in Time Domain Differential Mode	
24.6	CE Voltage in Time Domain, Pwr-Return to Structure	
25	**Test on STR1 Power Lines**	NOISIEST Mode with **HIFI in SCIENCE, PACS in Safe Mode** and SPIRE in STANDBY: **''B4''**
25.1	If not already done switch into NOISIEST mode according to chapter 7.1 ''B4'' and confirm the mode	
25.2	CE Current in Frequency Domain Common Mode	
25.3	CE Current in Frequency Domain Differential Mode	
25.4	CE Current in Time Domain Differential Mode	
25.5	CE Voltage in Time Domain Differential Mode	
25.6	CE Voltage in Time Domain, Pwr-Return to Structure	

Test Step No	Activity	SATELLITE Operational Mode
26	**Reference Test on Satellite Ground Line DELETED**	
26.1	CE Current Ripple in Time Domain	
27	**CE Test on TC Signal Lines (RWL1)**	NOISIEST Mode with **HIFI in SCIENCE, PACS in Safe Mode** and SPIRE in STANDBY: **''B4''**
27.1	If not already done switch into NOISIEST mode according to chapter 7.1 ''B4'' and confirm the mode	
27.2	CE Current in Frequency Domain Common Mode	
27.3	CE Voltage in Time Domain Common Mode	
28	**CE Test on TDC Signal Lines (RWL1)**	NOISIEST Mode with **HIFI in SCIENCE, PACS in Safe Mode** and SPIRE in STANDBY: **''B4''**
28.1	If not already done switch into NOISIEST mode according to chapter 7.1 ''B4'' and confirm the mode	
28.2	CE Current in Frequency Domain Common Mode	
28.3	CE Voltage in Time Domain Common Mode	
29	**CE Test on ACC/1553 (MILBUS) Signal Lines**	NOISIEST Mode with **HIFI in SCIENCE, PACS in Safe Mode** and SPIRE in STANDBY: **''B4''**
29.1	If not already done switch into NOISIEST Mode according to chapter 7.1 ''B4'' and confirm the mode.	
29.2	CE Voltage in Time Domain Differential Mode	
30	**CS Test on TC Signal Lines (RWL1)**	SENSITIVE Mode (to be performed only in case of NC on CE test): **''C''**
30.1	If not already done switch into SENSITIVE mode according to chapter 7.1 ''C'' and confirm the mode	
30.2	Arrangement of the test equipment	
30.3	Reference Measurements	
30.4	CS testing	
30.5	Susceptibility evaluation	
31	**CS Test on TDC Signal Lines (RWL1)**	SENSITIVE Mode (to be performed only in case of NC on CE test): **''C''**
31.1	If not already done switch into SENSITIVE mode according to chapter 7.1 ''C'' and confirm the mode	
31.2	Arrangement of the test equipment	
31.3	Reference Measurements	
31.4	CS testing	
31.5	Susceptibility evaluation	
32	**De-install BoBs/Test adapters**	OFF Mode
	See also step No. 1	

Table 7-1: Test Activities Flow

8 STEP BY STEP PROCEDURE/REPORT

The step by step procedure table shall be filled in during the test to be the basis for the test report. Unless otherwise noted, the test activities shall be performed by the EMC team.
If helpful the following editorial aids may be inserted into the procedure table by "copy and paste":

☑ "Done" sign
Pass "Pass" status for meeting a requirement
Fail "Fail" status for not meeting a requirement
OP Executant: Satellite /EGSE and operational responsibles
EMC Executant: EMC responsible

	Table row for **comments,** if any

	Table row for **photos,** when taken

	Table row for **plots,** when taken

8.1 Installation of Test Adapters to Power- and Signal Lines

Test Step No/ Info		Description/Comments
0	☑	**INSTALL TEST ADAPTER ON POWER- AND SIGNAL LINES LINES** In additional see paragraph 6.4: Test- Aid/Adapter locations/accessability on HERSCHEL" **<u>Pecautions:</u>** **Any open waveguides of HIFI shall be closed by copper tape or a dummy load, details on the configuration to be defined during the TRR.** **The RF antennas shall be covered by the antenna test caps or removed and the antenna ports loaded by suitable RF load in order to avoid RF transmission in command failure case.** **It is absolutely mandatory to fix the EMC adapter connectors (power and signal) mechanically against disconnecting by accident or stress!** **Note that the signal lines are shielded and the shield is rooted via the connector shells. So, when installing the EMC adapters, take care and measures that the shielding path is conductively not interrupted!**
Date/Time		29.11.
0.1	☑	**Test adapter "DB01-J01" for CE on <u>CCU-MAIN</u>**
		The test aid Figure 6-3 shall be placed at DB01 level on harness side. The connector to be

Test Step No/ Info		Description/Comments
		disconnected for installing the adapter is J01. The adaper supports measurements on the *PCDU/CCU_A_ Pwrs SUP/RTN* lines.
		Photos: 5826 - 5829
Date/Time		29.11.
0.2	☑	**Test adapter "DB01-J02" for CE on TWTA-EPC1**
		The test aid Figure 6-4 shall be placed at DB01 level on harness side. The connector to be disconnected for installing the adapter is J02. The adaper supports measurements on the *PCDU/EPC1_Pwrs SUP/RTN* lines.
		Photos: 5826 - 5829
Date/Time		29.11.
0.3	☑	**Test adapter "DB01-J04" for CE on HIFI-LCU-MAIN and SPIRE-FCU-main**
		The test aids Figure 6-5 shall be placed at DB01 level on harness side. The connector to be disconnected for installing the adapter is J04. The adaper supports measurements on the *PCDU/FHLCU_Pwrs SUP/RTN and* *PCDU/HSFCU_Nom_Pwr SUP/RTN* lines.
		Photos: 5826 - 5829
Date/Time		29.11.
0.4	☑	**Test adapter "DB01-J05" for CE on HIFI-ICU-MAIN, PACS-DPU-MAIN, PACS-SPU-MAIN and PACS-MEC1-MAIN**
		The test aid shall be placed at DB01 level on harness side. The connector to be disconnected for installing the adapter is J05. The adaper supports measurements on the *PCDU/FHICU_Nom_Pwr SUP/RTN,* *PCDU/FPDPU_Nom_Pwrs SUP/RTN and* *PCDU/FPSPU1_ Pwrs SUP/RTN lines* *PCDU/FPMEC1_ Pwrs SUP/RTN lines*

Test Step No/ Info		Description/Comments
		Photos: 5826 - 5829
Date/Time		29.11.
0.5	☑	**Test adapter "DB01-J06" for CE on HIFI-LCU-MAIN, PACS-BOLC-MAIN and STR1**
		The test aid Figure 6-7 shall be placed at DB01 level on harness side. The connector to be disconnected for installing the adapter is J06. The adaper supports measurements on the *PCDU/FHLCU_Pwrs SUP/RTN,* *PCDU/FPBOLC_Pwrs SUP/RTN and* *PCDU/STR1_Pwr SUP/RTN* lines.
		Photos: 5826 - 5829
Date/Time		29.11.
0.6	☑	**Test adapter "DB02-P01" for CE/CS on Signal Lines: RWL1 TC/TDC**
		The test aid Figure 6-8 shall be placed at DB02 level. The connector to be disconnected for installing the adapter is DB02-P01. The adaper supports measurements on the *Torque_Cmd SUP/RET and* *Torque_Direction_Cmd SUP/RET* lines.
		Photos: 5826 - 5829
Date/Time		29.11.
0.7	☑	**Test adapter "ACC-J23" for CE on Signal Lines: MILBUS**
		The test aid Figure 6-10 shall be placed at ACC level. The connector to be disconnected for installing the adapter is ACC-J23. The adaper supports measurements on the *MIL1553/1553Rtn* lines.
		Photos: 5826 - 5829

8.2 Reference/Ambient Tests on Satellite Ground Line

This test shall be performed prior to the ambient measurements on power- and signal lines.

Test Step No., Executant		Description/Comments
1		**Reference/Ambient Test on Satellite Ground Line**
1.1	☑	REFERENCE Mode
	OP	If not already done switch into REFERENCE mode according to chapter 7.1 ''A'' and confirm the mode
		REFERENCE mode confirmed
Date/Time		30.11./11h00
		The measurement shall be done on the **Satellite Ground** line. The ground line is accessible from underneath the power panel. Take care to install the probes correctly.
Date/Time		30.11./11:00
1.2☑		CE Current in Time Domain, single line measurement
		Set the current clamp on the satellite ground line for single line measurements. Measure the ambient DC current and the current ripple with an oscilloscope (BW ≥ 50 MHz).
		Plot sinus2: Current probe alone. Measured Ambient DC current: I-DCamb = 0 mA Measured current ripple : I-amb = 10 mApp Plot sinus1 Measurement:: Measured Ambient DC current: I-DCamb = 0 mA Measured current ripple : I-amb = 11 mApp
		Photos: 5832
Date/Time		Remove the current probe. 30.11./11:00

8.3 Reference/Ambient Tests on Power Lines

Test Step No., Executant		Description/Comments
		REFERENCE/AMBIENT TEST ON POWER LINES LINES In additional see paragraph 6.2 “Arrangements for Power Lines”. **Before starting an EMC test:** **Any open waveguides of HIFI shall be closed by copper tape or a dummy load, details on the configuration to be defined during the TRR.** **The RF antennas shall be covered by the antenna test caps or removed and the antenna ports loaded by suitable RF load in order to avoid RF transmission in command failure case.** **Ckeck that all the EMC adapter connectors are mechanically secured against disconnecting by accident or stress!** **Pay special attention and take precautions when clamping and unclamping the measurement transducers (e.g. voltage- and current probes) to and from the EMC adapter wires. Wires may breake under stress.**
2		**Reference/Ambient Test on HIFI-LCU-main Power Lines**
2.1	☑	REFERENCE Mode
	OP	If not already done switch into REFERENCE mode according to chapter 7.1 ‘‘A’’ and confirm the mode.
		On Power SCOE verify the spacecraft OFF condition: Reference mode has been confirmed. Measured: U = _____ **Procedure Variation**. No busvoltage as spacecraft switched OFF
Date/Time		30.11./12:10
		The measurement shall be done on the **PCDU/FHLCU_Pwrs SUP/RTN** lines. The power lines are accessible via the test adapter DB01-J06 (Figure 6-7) and DB01–J04 (Figure 6-5). **Attention!** The power lines under test were rooted in parallel via the two connectors (J06 and J04). “plus”: DB01-J06 pin 1, parallel to DB01–J04 pin 11 “return”: DB01-J06 pin 2, parallel to DB01–J04 pin 12 Take care to install the probes correctly.
Date/Time		
2.2	☑	CE Current in Frequency Domain, Common Mode
		Set the current clamp on the HIFI-LCU-main power lines for CM measurements in accor-

Test Step No., Executant	Description/Comments
	dance to the CM set-up Figure 5-1: Schematic for CM and DM Measurements. Measure the ambient current from 30 Hz to 50 MHz.
	Plot 004: 30 Hz – 10 kHz, CM-ambient, HIFI-LCU-main -PWR Plot 005: 10 kHz – 1 MHz, CM-ambient, HIFI-LCU-main -PWR Plot 006: 1 MHz – 30 MHz, CM-ambient, HIFI-LCU-main -PWR Plot 006: 30 MHz – 50 MHz, CM-ambient, HIFI-LCU-main -PWR
	Photos: 5833,
	Remove the current probe.
Date/Time	30.11./12:20
2.3 ☑	CE Current in Frequency Domain, Differential Mode
30.11./ 14:00	Set the current clamp on the HIFI-LCU-main power lines for DM measurements in accordance to the DM set-up Figure 5-1: Schematic for CM and DM Measurements. Measure the ambient current from 30 Hz to 50 MHz.
	Plot 007: 30 Hz – 10 kHz, DM-ambient, HIFI-LCU-main -PWR Plot 008: 10 kHz – 1 MHz, DM-ambient, HIFI-LCU-main -PWR Plot 009: 1 MHz – 30 MHz, DM-ambient, HIFI-LCU-main -PWR Plot 009: 30 MHz – 50 MHz, DM-ambient, HIFI-LCU-main -PWR
	Photos: 5834
	Remove the current probe.
Date/Time	30.11./14:12
2.4 ☑	CE Current in Time Domain, Differential Mode
	Set the current clamp on the HIFI-LCU-main power lines for DM measurements as before. Measure the ambient DC current and the current ripple with an oscilloscope (BW ≥ 50 MHz).
	Plot sinus3: Measured Ambient DC current: I-DCamb = 8 mA Measured current ripple : I-amb = 7.7 mApp
	Photos: 5836
	Remove the current probe.
Date/Time	30.11./14:25
2.5 ☑	CE Voltage in Time Domain Differential Mode
	Set the voltage probe on the HIFI-LCU-main power lines for DM voltage measurements and measure the differential ambient voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. “plus”: DB01-J06 pin 1 “return”: DB01-J06 pin 2

Test Step No., Executant		Description/Comments
		and/or “plus”: DB01–J04 pin 11 “return”: DB01–J04 pin 12 We used: “plus”: DB01-J06 pin 1 “return”: DB01-J06 pin 2
		Plot 4_12_23 (repeated measurement): Measured Ambient voltage ripple: U-amb = 5 mVpp (>/< 2,5 V)
		Photos: 5837
		Remove the oscilloscope connection from the test adapter.
Date/Time		04.12./13:00 (measurement repeated that day/time)
2.6	☑	CE Voltage in Time Domain, Pwr-Return to Structure
		Connect the differential voltage probe "+" input to the HIFI-LCU-main RETURN power line and the differential voltage probe "-" input to the satellite structure. Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. “return”: DB01-J06 pin 2 "satellite structure": …connector bracket DB01during test… and/or “return”: DB01–J04 pin 12 "satellite structure": …connector bracket DB01 during test
		Measured Ambient voltage ripple: U-amb = 34.6 mVpp. J06 pin 2 to structure: Plot 4_12_26 (repeated measurement) Measured Ambient voltage ripple: U-amb = 20 mVpp. J04 pin 12 to structure: Plot: Plot: 4_12_25 (repeated measurement)
		Photos: 5838, 5839
Date/Time		Remove the oscilloscope connection from the test adapter. 04.12./13:00 (measurement repeated that day/time)

3		**Reference/Ambient Test on HIFI-ICU-main Power Lines**
3.1	☑	REFERENCE mode

	OP	If not already done switch into REFERENCE mode according to chapter 7.1 ‘‘A’’ and confirm the mode
		On Power SCOE verify the spacecraft OFF condition.: Measured: U = _____ **Procedure Variation**. No busvoltage as spacecraft switched OFF
Date/Time		30.11./15:25
		The measurement shall be done on the **PCDU/FHICU_Nom_Pwr SUP/RTN** lines. The power lines are accessible via the test adapter DB01-J05 (Figure 6-6). “plus”: DB01-J05 pin 9 “return”: DB01-J05 pin 10
Date/Time		30.11./15:30
3.2	☑	CE Current in Frequency Domain, Common Mode
		Set the current clamp on the HIFI-ICU-main power lines for CM measurements in accordance to the CM set-up Figure 5-1: Schematic for CM and DM Measurements. Measure the ambient current from 30 Hz to 50 MHz.
		Plot 010: 30 Hz – 10 kHz, CM-ambient, HIFI-ICU-main -PWR Plot 011: 10 kHz – 1 MHz, CM-ambient, HIFI- ICU -main -PWR Plot 012: 1 MHz – 30 MHz, CM-ambient, HIFI- ICU -main -PWR Plot 012: 30 MHz – 50 MHz, CM-ambient, HIFI- ICU -main -PWR
		Photos: 5840
		Remove the current probe.
Date/Time		30.11./15:37
3.3	☑	CE Current in Frequency Domain, Differential Mode
		Set the current clamp on the HIFI-ICU-main power lines for DM measurements in accordance to the DM set-up Figure 5-1: Schematic for CM and DM Measurements. Measure the ambient current from 30 Hz to 50 MHz.
		Plot 013: 30 Hz – 10 kHz, DM-ambient, HIFI- ICU -main -PWR Plot 014: 10 kHz – 1 MHz, DM-ambient, HIFI- ICU -main -PWR Plot 015: 1 MHz – 30 MHz, DM-ambient, HIFI- ICU -main -PWR Plot 015: 30 MHz – 50 MHz, DM-ambient, HIFI- ICU -main -PWR
		Photos: 5841
		Remove the current probe.
Date/Time		30.11./15:58
3.4	☑	CE Current in Time Domain, Differential Mode
		Set the current clamp on the HIFI-ICU-main power lines for DM measurements as before. Measure the ambient DC current and the current ripple with an oscilloscope (BW ≥ 50 MHz).

		Plot sinus12: Measured Ambient DC current: I-DCamb < 1 mA Measured current ripple : I-amb = 8.5 mApp
		Photos: 5843
		Remove the current probe.
Date/Time		30.11./16:09
3.5	☑	CE Voltage in Time Domain Differential Mode
		Set the voltage probe on the HIFI-ICU-main power lines for DM voltage measurements and measure the differential ambient voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. “plus”: DB01-J05 pin 9 “return”: DB01-J05 pin 10
		Plot 4_12_22 (repeated measurement): Measured Ambient voltage ripple: U-amb = 7.5 mVpp (>/< 2,5 V)
		Photos: 5844
		Remove the oscilloscope connection from the test adapter.
Date/Time		04.12./13:17 (measurement repeated that day/time)
3.6	☑	CE Voltage in Time Domain, Pwr-Return to Structure
		Connect the differential voltage probe "+" input to the HIFI-ICU-main RETURN power line and the differential voltage probe "-" input to the satellite structure. Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. “return”: DB01-J05 pin 10 "satellite structure": DB01 Connector Bracket
		Plot 4_14_24 (repeated measurement): Measured ambient voltage ripple: U-amb = 55 mVpp
		Photos: 5845
Date/Time:		Remove the oscilloscope connection from the test adapter. 04.12./13:17 (measurement repeated that day/time)

4		**Reference/Ambient Test on SPIRE-FCU-main Power Lines**
4.1	☑	REFERENCE Mode
	OP	If not already done switch into REFERENCE mode according to chapter 7.1 ‘‘A’’ and confirm the mode
		On Power SCOE verify the spacecraft OFF condition: Measured: U = _____ **Procedure Variation**. No busvoltage as spacecraft switched

		OFF
Date/Time		30.11./16:30
		The measurement shall be done on the **PCDU/HSFCU_Nom_Pwr SUP/RTN** lines. The power lines are accessible via the test adapter DB01-J04 (Figure 6-5). “plus”: DB01-J04 pin 7 “return”: DB01-J04 pin 8
Date/Time		
4.2	☑	CE Current in Frequency Domain, Common Mode
		Set the current clamp on the SPIRE-FCU-main power lines for CM measurements in accordance to the CM set-up Figure 5-1: Schematic for CM and DM Measurements. Measure the ambient current from 30 Hz to 50 MHz.
		Plot 015: 30 Hz – 10 kHz, CM-ambient, SPIRE-FCU -main -PWR Plot 016: 10 kHz – 1 MHz, CM-ambient, SPIRE-FCU -main -PWR Plot 017: 1 MHz – 30 MHz, CM-ambient, SPIRE-FCU -main -PWR Plot 017: 30 MHz – 50 MHz, CM-ambient, SPIRE-FCU -main -PWR
		Photos: 5846
		Remove the current probe.
Date/Time		30.11./16:45
4.3	☑	CE Current in Frequency Domain, Differential Mode
		Set the current clamp on the SPIRE-FCU -main power lines for DM measurements in accordance to the DM set-up Figure 5-1: Schematic for CM and DM Measurements. Measure the ambient current from 30 Hz to 50 MHz.
		Plot 018: 30 Hz – 10 kHz, DM-ambient, SPIRE-FCU -main -PWR Plot 019: 10 kHz – 1 MHz, DM-ambient, SPIRE-FCU -main -PWR Plot 020: 1 MHz – 30 MHz, DM-ambient, SPIRE-FCU -main -PWR Plot 020: 30 MHz – 50 MHz, DM-ambient, SPIRE-FCU -main -PWR
		Photos: 5847
		Remove the current probe.
Date/Time		30.11./16:30
4.4	☑	CE Current in Time Domain, Differential Mode
		Set the current clamp on the SPIRE-FCU -main power lines for DM measurements as before. Measure the ambient DC current and the current ripple with an oscilloscope (BW ≥ 50 MHz).
		Plot sinus18: Measured Ambient DC current: I-DCamb <1 mA Measured current ripple : I-amb = 10 mApp

		Photos: 5850
		Remove the current probe.
Date/Time		30.11./17:10
4.5	☑	CE Voltage in Time Domain Differential Mode
		Set the voltage probe on the SPIRE-FCU-main power lines for DM voltage measurements and measure the differential ambient voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. “plus”: DB01-J04 pin 7 “return”: DB01-J04 pin 8
		Plot 4_12_27 (repeated measurement): Measured Ambient voltage ripple: U-amb = 3.5 mVpp (>/< 2,5 V)
		Photos: 5848
		Remove the oscilloscope connection from the test adapter.
Date/Time		04.12./13:25 (measurement repeated that day/time)
4.6	☑	CE Voltage in Time Domain, Pwr-Return to Structure
		Connect the differential voltage probe "+" input to the SPIRE-FCU-main RETURN power line and the differential voltage probe "-" input to the satellite structure. Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. “return”: DB01-J04 pin 8 "satellite structure": Connector Bracket DB01
		Plot 4_12_28 (repeated measurement): Measured Ambient voltage ripple: U-amb = 55 mVpp
		Photos: 5849
Date/Time		Remove the oscilloscope connection from the test adapter. 04.12./13:25 (measurement repeated that day/time)

5		**Reference/Ambient Test on PACS-DPU-main Power Lines**
5.1	☑	REFERENCE Mode
	OP	If not already done switch into REFERENCE mode according to chapter 7.1 ‘‘A’’ and confirm the mode
		On Power SCOE verify the spacecraft OFF condition by SCOE parameters: Main Bus Voltage= 0 V, Measured: U = _____ **Procedure Variation**. No busvoltage as spacecraft switched OFF Main Bus Current Output = = A, Measured: I = _____ **Procedure Variation**. No buscurrent as spacecraft switched OFF

Date/Time		30.11./17:20
		The measurement shall be done on the **PCDU/FPDPU_Nom_Pwrs SUP/RTN** lines. The power lines are accessible via the test adapter DB01-J05 (Figure 6-6). “plus”: DB01-J05 pin 47//49 “return”: DB01-J05 pin 48//50
Date/Time		
5.2	☑	CE Current in Frequency Domain, Common Mode
		Set the current clamp on the PACS-DPU-main power lines for CM measurements in accordance to the CM set-up Figure 5-1: Schematic for CM and DM Measurements. Measure the ambient current from 30 Hz to 50 MHz.
		Plot 021: 30 Hz – 10 kHz, CM-ambient, PACS-DPU -main -PWR Plot 022: 10 kHz – 1 MHz, CM-ambient, PACS-DPU -main -PWR Plot 023: 1 MHz – 30 MHz, CM-ambient, PACS-DPU -main -PWR Plot 023: 30 MHz – 50 MHz, CM-ambient, PACS-DPU -main -PWR
		Photos: 5851
		Remove the current probe.
Date/Time		30.11./17:25
5.3	☑	CE Current in Frequency Domain, Differential Mode
		Set the current clamp on the PACS-DPU -main power lines for DM measurements in accordance to the DM set-up Figure 5-1: Schematic for CM and DM Measurements. Measure the ambient current from 30 Hz to 50 MHz.
		Plot 024: 30 Hz – 10 kHz, DM-ambient, PACS-DPU -main -PWR Plot 025: 10 kHz – 1 MHz, DM-ambient, PACS-DPU -main -PWR Plot 026: 1 MHz – 30 MHz, DM-ambient, PACS-DPU -main -PWR Plot 026: 30 MHz – 50 MHz, DM-ambient, PACS-DPU -main -PWR
		Photos: 5852
		Remove the current probe.
Date/Time		30.11./17:30
5.4	☑	CE Current in Time Domain, Differential Mode
		Set the current clamp on the PACS-DPU -main power lines for DM measurements as before. Measure the ambient DC current and the current ripple with an oscilloscope (BW ≥ 50 MHz).
		Plot sinus19: Measured Ambient DC current: I-DCamb = <1 mA Measured current ripple : I-amb = 10 mApp

		Photos: 5853
		Remove the current probe.
Date/Time		30.11./17:35
5.5	☑	CE Voltage in Time Domain Differential Mode
		Set the voltage probe on the PACS-DPU -main power lines for DM voltage measurements and measure the differential ambient voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. “plus”: DB01-J05 pin 47//49 “return”: DB01-J05 pin 48//50
		Plot 4_12_29 (repeated measurement): Measured Ambient voltage ripple: U-amb = 5.7 mVpp (>/< 2,5 V)
		Photos: 5854
		Remove the oscilloscope connection from the test adapter.
Date/Time		04.12./13:17 (measurement repeated that day/time)
5.6	☑	CE Voltage in Time Domain, Pwr-Return to Structure
		Connect the differential voltage probe "+" input to the PACS-DPU -main RETURN power line and the differential voltage probe "-" input to the satellite structure. Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. “return”: DB01-J05 pin 48//50 "satellite structure": Connector Bracket DB01
		Plot 4_12_30 Measured Ambient voltage ripple: U-amb = 34.5 mVpp
		Photos: 5855
Date/Time		Remove the oscilloscope connection from the test adapter. 04.12./13:17 (measurement repeated that day/time)

6		**Reference/Ambient Test on PACS-SPU-main Power Lines**
6.1	☑	REFERENCE Mode
	OP	If not already done, switch into REFERENCE mode according to chapter 7.1 ‘’A’’ and confirm the mode
	OP	On Power SCOE verify the spacecraft OFF condition by SCOE parameters: Main Bus Voltage= 0 V, Measured: U = _____ **Procedure Variation**. No busvoltage as spacecraft switched OFF Main Bus Current Output = = A, Measured: I = _____ **Procedure Variation**. No busvoltage as spacecraft switched OFF

Date/Time		04.12./8h30
		The measurement shall be done on the **PCDU/FPSPU1_ Pwrs SUP/RTN** lines. The power lines are accessible via the test adapter DB01-J05 (Figure 6-6). “plus”: DB01-J05 pin 1//3 “return”: DB01-J05 pin 2//4
Date/Time		
6.2	☑	CE Current in Frequency Domain, Common Mode
		Set the current clamp on the PACS-SPU-main power lines for CM measurements in accordance to the CM set-up Figure 5-1: Schematic for CM and DM Measurements. Measure the ambient current from 30 Hz to 50 MHz.
		Plot 088: 30 Hz – 10 kHz, CM-ambient, PACS-SPU -main -PWR Plot 089: 10 kHz – 1 MHz, CM-ambient, PACS-SPU -main -PWR Plot 090: 1 MHz – 30 MHz, CM-ambient, PACS-SPU -main -PWR Plot 090: 30 MHz – 50 MHz, CM-ambient, PACS-SPU -main -PWR
		Photos: 5909
		Remove the current probe.
Date/Time		04.12./8h32
6.3	☑	CE Current in Frequency Domain, Differential Mode
		Set the current clamp on the PACS-SPU -main power lines for DM measurements in accordance to the DM set-up Figure 5-1: Schematic for CM and DM Measurements. Measure the ambient current from 30 Hz to 50 MHz.
		Plot 091: 30 Hz – 10 kHz, DM-ambient, PACS-SPU -main -PWR Plot 092: 10 kHz – 1 MHz, DM-ambient, PACS-SPU -main -PWR Plot 093: 1 MHz – 30 MHz, DM-ambient, PACS-SPU -main -PWR Plot 093: 30 MHz – 50 MHz, DM-ambient, PACS-SPU -main -PWR
		Photos: 5910
		Remove the current probe.
Date/Time		04.12./8h45
6.4	☑	CE Current in Time Domain, Differential Mode
		Set the current clamp on the PACS-SPU -main power lines for DM measurements as before. Measure the ambient DC current and the current ripple with an oscilloscope (BW ≥ 50 MHz).
		Plot 4_12_1 Measured Ambient DC current: I-DCamb = 0 mA Measured current ripple : I-amb = 8.4 mApp

		Photos: 5910
		Remove the current probe.
Date/Time		04.12./8h45
6.5	☑	CE Voltage in Time Domain Differential Mode
		Set the voltage probe on the PACS-SPU -main power lines for DM voltage measurements and measure the differential ambient voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. “plus”: DB01-J05 pin 1//3 “return”: DB01-J05 pin 2//4
		Plot 4_12_2: Measured Ambient voltage ripple: U-amb = 2,4 mVpp (>/< 2,5 V)
		Photos: 5909
		Remove the oscilloscope connection from the test adapter.
Date/Time		04.12/8h43
6.6	☑	CE Voltage in Time Domain, Pwr-Return to Structure
		Connect the differential voltage probe "+" input to the PACS-SPU -main RETURN power line and the differential voltage probe "-" input to the satellite structure. Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. “return”: DB01-J05 pin 2//4 "satellite structure": Connector Bracket DB01
		Plot: none Measured Ambient voltage ripple: U-amb = 64 mVpp
		Photos: 5911
Date/Time		Remove the oscilloscope connection from the test adapter. 04.12./8h50

7		**Reference/Ambient Test on PACS-BOLC-main Power Lines**
7.1	☑	REFERENCE Mode
	OP	If not already done, switch into REFERENCE mode according to chapter 7.1 ‘’A’’ and confirm the mode
		On Power SCOE verify the spacecraft OFF condition: Measured: U = _____ **Procedure Variation**. No busvoltage as spacecraft switched OFF
Date/Time		04.12./8h55

		The measurement shall be done on the **PCDU/FPBOLC_ Pwrs SUP/RTN** lines. The power lines are accessible via the test adapter DB01-J06 (Figure 6-7). "plus": DB01-J06 pin 13//17 "return": DB01-J06 pin 14//18
Date/Time		04.12./9h00
7.2	☑	CE Current in Frequency Domain, Common Mode
		Set the current clamp on the PACS-BOLC-main power lines for CM measurements in accordance to the CM set-up Figure 5-1: Schematic for CM and DM Measurements. Measure the ambient current from 30 Hz to 50 MHz.
		Plot 094: 30 Hz – 10 kHz, CM-ambient, PACS-BOLC -main -PWR Plot 095: 10 kHz – 1 MHz, CM-ambient, PACS- BOLC -main -PWR Plot 096: 1 MHz – 30 MHz, CM-ambient, PACS- BOLC -main -PWR Plot 096: 30 MHz – 50 MHz, CM-ambient, PACS- BOLC -main -PWR
		Photos: 5912
		Remove the current probe.
Date/Time		5.12./9h02
7.3	☑	CE Current in Frequency Domain, Differential Mode
		Set the current clamp on the PACS-BOLC-main power lines for DM measurements in accordance to the DM set-up Figure 5-1: Schematic for CM and DM Measurements. Measure the ambient current from 30 Hz to 50 MHz.
		Plot 097: 30 Hz – 10 kHz, DM-ambient, PACS- BOLC -main -PWR Plot 098: 10 kHz – 1 MHz, DM-ambient, PACS- BOLC -main -PWR Plot 099: 1 MHz – 30 MHz, DM-ambient, PACS- BOLC -main -PWR Plot 099: 30 MHz – 50 MHz, DM-ambient, PACS- BOLC -main -PWR
		Photos: 5913
		Remove the current probe.
Date/Time		4.12./9h10
7.4	☑	CE Current in Time Domain, Differential Mode
		Set the current clamp on the PACS-BOLC-main power lines for DM measurements as before. Measure the ambient DC current and the current ripple with an oscilloscope (BW ≥ 50 MHz).
		Plot 4_12_5: Measured Ambient DC current: I-DCamb = 0 mA Measured current ripple : I-amb = 10 mApp
		Photos: 5913

		Remove the current probe.
Date/Time		4.12./9h11
7.5	☑	CE Voltage in Time Domain Differential Mode
		Set the voltage probe on the PACS-BOLC-main power lines for DM voltage measurements and measure the differential ambient voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. “plus”: DB01-J06 pin 13//17 “return”: DB01-J06 pin 14//18
		Plot 4_12_3: Measured Ambient voltage ripple: U-amb = 1.2 mVpp (>/< 2,5 V)
		Photos: 5912
		Remove the oscilloscope connection from the test adapter.
Date/Time		4.12./9h03
7.6	☑	CE Voltage in Time Domain, Pwr-Return to Structure
		Connect the differential voltage probe "+" input to the PACS-BOLC-main RETURN power line and the differential voltage probe "-" input to the satellite structure. Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. “return”: DB01-J06 pin 14//18 "satellite structure": DB01 Connector Bracket
		Plot 4_12_4: Measured Ambient voltage ripple: U-amb = 47 mVpp
		Photos: 5914
Date/Time		Remove the oscilloscope connection from the test adapter. 04.12./9h12

8		**Reference/Ambient Test on PACS-MEC-main Power Lines**
8.1	☑	REFERENCE Mode
	OP	If not already done, switch into REFERENCE mode according to chapter 7.1 ‘’A’’ and confirm the mode
		On Power SCOE verify the spacecraft OFF condition: Measured: U = _____ **Procedure Variation**. No busvoltage as spacecraft switched OFF
Date/Time		4.12./9h14

		The measurement shall be done on the **PCDU/FPMEC1_ Pwrs SUP/RTN** lines. The power lines are accessible via the test adapter DB01-J05 (Figure 6-6). "plus": DB01-J06 pin 13//17 "return": DB01-J06 pin 14//18
Date/Time		4.12./9h14
8.2	☑	CE Current in Frequency Domain, Common Mode
		Set the current clamp on the PACS-MEC-main power lines for CM measurements in accordance to the CM set-up Figure 5-1: Schematic for CM and DM Measurements. Measure the ambient current from 30 Hz to 50 MHz.
		Plot 200: 30 Hz – 10 kHz, CM-ambient, PACS-MEC -main -PWR Plot 201: 10 kHz – 1 MHz, CM-ambient, PACS- MEC -main -PWR Plot 202: 1 MHz – 30 MHz, CM-ambient, PACS- MEC -main -PWR Plot 202: 30 MHz – 50 MHz, CM-ambient, PACS- MEC -main -PWR
		Photos: 5915
		Remove the current probe.
Date/Time		4.12./9h21
8.3	☑	CE Current in Frequency Domain, Differential Mode
		Set the current clamp on the PACS-MEC-main power lines for DM measurements in accordance to the DM set-up Figure 5-1: Schematic for CM and DM Measurements. Measure the ambient current from 30 Hz to 50 MHz.
		Plot 203: 30 Hz – 10 kHz, DM-ambient, PACS- MEC -main -PWR Plot 204: 10 kHz – 1 MHz, DM-ambient, PACS- MEC -main -PWR Plot 205: 1 MHz – 30 MHz, DM-ambient, PACS- MEC -main -PWR Plot 205: 30 MHz – 50 MHz, DM-ambient, PACS- MEC -main -PWR
		Photos: 5917
		Remove the current probe.
Date/Time		04.12./9h28
8.4	☑	CE Current in Time Domain, Differential Mode
		Set the current clamp on the PACS-MEC-main power lines for DM measurements as before. Measure the ambient DC current and the current ripple with an oscilloscope (BW ≥ 50 MHz).
		Plot 4_12_8: Measured Ambient DC current: I-DCamb = 0 mA Measured current ripple : I-amb = 12 mApp
		Photos: 5917
		Remove the current probe.

Date/Time	04.12./9h25
8.5	CE Voltage in Time Domain Differential Mode
	Set the voltage probe on the PACS-MEC-main power lines for DM voltage measurements and measure the differential ambient voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. “plus”: DB01-J05 pin 13//17 “return”: DB01-J05 pin 14//18
	Plot 4_12_6: Measured Ambient voltage ripple: U-amb = 14 mVpp (>/< 2,5 V)
	Photos: 5916
	Remove the oscilloscope connection from the test adapter.
Date/Time	04.12./9h25
8.6	CE Voltage in Time Domain, Pwr-Return to Structure
	Connect the differential voltage probe "+" input to the PACS-MEC-main RETURN power line and the differential voltage probe "-" input to the satellite structure. Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. “return”: DB01-J06 pin 14//18 "satellite structure": Connector Bracket DB01
	Plot 4_12_7: Measured Ambient voltage ripple: u-amb = 78 mVpp
	Photos: 5917
Date/time	Remove the oscilloscope connection from the test adapter. 04.12./9h25

9		**Reference/Ambient Test on CCU-main Power Lines**
9.1	☑	REFERENCE Mode
	OP	If not already done switch into REFERENCE mode according to chapter 7.1 ‘‘A’’ and confirm the mode.
		On Power SCOE verify the spacecraft OFF condition: Measured: U = _____ **Procedure Variation**. No busvoltage as spacecraft switched OFF
Date/Time		04.12/9h29
		The measurement shall be done on the **PCDU/CCU_A_ Pwrs SUP/RTN** lines.

		The power lines are accessible via the test adapter DB01-J01 (Figure 6-3). “plus”: DB01-J01 pin 18 “return”: DB01-J01 pin 19
Date/Time		04.12./9h30
9.2	☑	CE Current in Frequency Domain, Common Mode
		Set the current clamp on the CCU-main power lines for CM measurements in accordance to the CM set-up Figure 5-1: Schematic for CM and DM Measurements. Measure the ambient current from 30 Hz to 50 MHz.
		Plot 206: 30 Hz – 10 kHz, CM-ambient, CCU -main -PWR Plot 207: 10 kHz – 1 MHz, CM-ambient, CCU -main -PWR Plot 208: 1 MHz – 30 MHz, CM-ambient, CCU -main -PWR Plot 208: 30 MHz – 50 MHz, CM-ambient, CCU -main -PWR
		Photos: 5918
		Remove the current probe.
Date/Time		04.12./9h36
9.3	☑	CE Current in Frequency Domain, Differential Mode
		Set the current clamp on the CCU -main power lines for DM measurements in accordance to the DM set-up Figure 5-1: Schematic for CM and DM Measurements. Measure the ambient current from 30 Hz to 50 MHz.
		Plot 209: 30 Hz – 10 kHz, DM-ambient, CCU -main -PWR Plot 210: 10 kHz – 1 MHz, DM-ambient, CCU -main -PWR Plot 211: 1 MHz – 30 MHz, DM-ambient, CCU -main -PWR Plot 211: 30 MHz – 50 MHz, DM-ambient, CCU -main -PWR
		Photos: 5918
		Remove the current probe.
Date/Time		04.12./9h42
9.4	☑	CE Current in Time Domain, Differential Mode
		Set the current clamp on the CCU -main power lines for DM measurements as before. Measure the ambient DC current and the current ripple with an oscilloscope (BW ≥ 50 MHz).
		Plot 4_12_11: Measured Ambient DC current: I-DCamb = 0 mA Measured current ripple : I-amb = 12 mApp
		Photos: 5918
		Remove the current probe.
Date/Time		04.12./9h40

9.5	☑	CE Voltage in Time Domain Differential Mode
		Set the voltage probe on the CCU -main power lines for DM voltage measurements and measure the differential ambient voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. “plus”: DB01-J01 pin 18 “return”: DB01-J01 pin 19
		Plot 4_12_9: Measured Ambient voltage ripple: u-amb = 2.9 mVpp (>/< 2,5 V)
		Photos: 5918
		Remove the oscilloscope connection from the test adapter.
Date/Time		04.12./
9.6	☑	CE Voltage in Time Domain, Pwr-Return to Structure
		Connect the differential voltage probe "+" input to the CCU-main RETURN power line and the differential voltage probe "-" input to the satellite structure. Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. “return”: DB01-J01 pin 19 "satellite structure": Connector Bracket DB01
		Plot 4_12_10: Measured Ambient voltage ripple: u-amb = 18.9 mVpp
		Photos:
Date/Time		Remove the oscilloscope connection from the test adapter. 04.12./9h39

10		**Reference/Ambient Test on TWTA-EPC1 Power Lines**
10.1	☑	REFERENCE Mode
	OP	If not already done switch into REFERENCE mode according to chapter 7.1 ‘’A’’ and confirm the mode.
		On Power SCOE verify the spacecraft OFF condition by SCOE parameters: Measured: U = _____ **Procedure Variation**. No busvoltage as spacecraft switched OFF
Date/Time		04.12./9h43
		The measurement shall be done on the **PCDU/EPC1_Pwrs SUP/RTN** lines. The power lines are accessible via the test adapter DB01-J02 (Figure 6-4). “plus”: DB01-J02 pin 22//24 “return”: DB01-J02 pin 23/25

Date/Time		04.12./9h43
10.2	☑	CE Current in Frequency Domain, Common Mode
		Set the current clamp on the TWTA-EPC1-main power lines for CM measurements in accordance to the CM set-up Figure 5-1: Schematic for CM and DM Measurements. Measure the ambient current from 30 Hz to 50 MHz.
		Plot 212: 30 Hz – 10 kHz, CM-ambient, TWTA-EPC1-main -PWR Plot 213: 10 kHz – 1 MHz, CM-ambient, TWTA-EPC1-main -PWR Plot 214: 1 MHz – 30 MHz, CM-ambient, TWTA-EPC1-main -PWR Plot 214: 30 MHz – 50 MHz, CM-ambient, TWTA-EPC1-main -PWR
		Photos: see at step 24
		Remove the current probe.
Date/Time		04.12./9h50
10.3	☑	CE Current in Frequency Domain, Differential Mode
		Set the current clamp on the TWTA-EPC1-main power lines for DM measurements in accordance to the DM set-up Figure 5-1: Schematic for CM and DM Measurements. Measure the ambient current from 30 Hz to 50 MHz.
		Plot 215: 30 Hz – 10 kHz, DM-ambient, TWTA-EPC1-main -PWR Plot 216: 10 kHz – 1 MHz, DM-ambient, TWTA-EPC1-main -PWR Plot 217: 1 MHz – 30 MHz, DM-ambient, TWTA-EPC1-main -PWR Plot 217: 30 MHz – 50 MHz, DM-ambient, TWTA-EPC1-main -PWR
		Photos: See step 24
		Remove the current probe.
Date/Time		04.12./10h00
10.4	☑	CE Current in Time Domain, Differential Mode
		Set the current clamp on the TWTA-EPC1-main power lines for DM measurements as before. Measure the ambient DC current and the current ripple with an oscilloscope (BW ≥ 50 MHz).
		Plot 4_12_15: Measured Ambient DC current: I-DCamb = 0mA Measured current ripple : I-amb = 13 mApp
		Photos: See step 24
		Remove the current probe.
Date/Time		04.12./9h56
10.5	☑	CE Voltage in Time Domain Differential Mode
		Set the voltage probe on the TWTA-EPC1-main power lines for DM voltage measurements and measure the differential ambient voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling.

		"plus": DB01-J02 pin 22//24 "return": DB01-J02 pin 23/25
		Plot 4_12_12 Measured Ambient voltage ripple: U-amb = 7.6 mVpp (>/< 2,5 V)
		Photos: See step 24
		Remove the oscilloscope connection from the test adapter.
Date/Time		04.12./9h50
10.6	☑	CE Voltage in Time Domain, Pwr-Return to Structure
		Connect the differential voltage probe "+" input to the TWTA-EPC1-main RETURN power line and the differential voltage probe "-" input to the satellite structure. Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. "return": DB01-J02 pin 23/25 "satellite structure": Connector Bracket DB01
		Plot 4_12_13: Measured Ambient voltage ripple: u-amb = 47 mVpp
		Photos: See step 24
Date/Time		Remove the oscilloscope connection from the test adapter. 04.12./9h55

11		**Reference/Ambient Test on STR1 Power Lines**
11.1	☑	REFERENCE Mode
	OP	If not already done switch into REFERENCE mode according to chapter 7.1 ''A'' and confirm the mode
		On Power SCOE verify the spacecraft OFF condition by SCOE parameters: Measured: U = _____ **Procedure Variation**. No busvoltage as spacecraft switched OFF
Date/Time		4./12./10h00
		The measurement shall be done on the **PCDU/STR1_Pwr SUP/RTN** lines. The power lines are accessible via the test adapter DB01-J06 (Figure 6-7). "plus": DB01-J06 pin 27 "return": DB01-J06 pin 28
Date/Time		4.12./10h00
11.2	☑	CE Current in Frequency Domain, Common Mode

		Set the current clamp on the STR1-main power lines for CM measurements in accordance to the CM set-up Figure 5-1: Schematic for CM and DM Measurements. Measure the ambient current from 30 Hz to 50 MHz.
		Plot 218: 30 Hz – 10 kHz, CM-ambient, STR1-main -PWR Plot 219: 10 kHz – 1 MHz, CM-ambient, STR1-main -PWR Plot 220: 1 MHz – 30 MHz, CM-ambient, STR1-main -PWR Plot 220: 30 MHz – 50 MHz, CM-ambient, STR1-main -PWR
		Photos: See step 25
		Remove the current probe.
Date/Time		4.12./10h06
11.3	☑	CE Current in Frequency Domain, Differential Mode
		Set the current clamp on the STR1-main power lines for DM measurements in accordance to the DM set-up Figure 5-1: Schematic for CM and DM Measurements. Measure the ambient current from 30 Hz to 50 MHz.
		Plot 221: 30 Hz – 10 kHz, DM-ambient, STR1-main -PWR Plot 222: 10 kHz – 1 MHz, DM-ambient, STR1-main -PWR Plot 223: 1 MHz – 30 MHz, DM-ambient, STR1-main -PWR Plot 223: 30 MHz – 50 MHz, DM-ambient, STR1-main -PWR
		Photos: See step 25
		Remove the current probe.
Date/Time		04.12./10h30
11.4	☑	CE Current in Time Domain, Differential Mode
		Set the current clamp on the STR1-main power lines for DM measurements as before. Measure the ambient DC current and the current ripple with an oscilloscope (BW ≥ 50 MHz).
		Plot 4_12_17: Measured Ambient DC current: I-DCamb = 0 mA Measured current ripple : I-amb = 11 mApp
		Photos: See step 25
		Remove the current probe.
Date/Time		04.12./10h49
11.5	☑	CE Voltage in Time Domain Differential Mode
		Set the voltage probe on the STR1-main power lines for DM voltage measurements and measure the differential ambient voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. “plus”: DB01-J06 pin 27 “return”: DB01-J06 pin 28

	4_12_14: Measured Ambient voltage ripple: u-amb = 5.3 mVpp (>/< 2,5 V)
	Photos: See step 25
	Remove the oscilloscope connection from the test adapter.
Date/Time	4.12./10h
11.6 ☑	CE Voltage in Time Domain, Pwr-Return to Structure
	Connect the differential voltage probe "+" input to the STR1-main RETURN power line and the differential voltage probe "-" input to the satellite structure. Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. “return”: DB01-J06 pin 28 "satellite structure": DB01 Conectorbracket
	Plot 4_12_16: Measured Ambient voltage ripple: U-amb = 276 mVpp
	Photos: See step 25
Date/Time	Remove the oscilloscope connection from the test adapter. 4.12./10h50

8.4 Reference/Ambient Tests on Signal Lines

Test Step No., Executant		Description/Comments
		REFERENCE/AMBIENT TEST ON SIGNAL LINES In additional see paragraph 6.3 “Arrangements for Signal Lines”, Figure 5-2 and Figure 5-3. **Before starting an EMC test:** **Ckeck that all the EMC adapter connectors are mechanically secured against disconnecting by accident or stress!** **Pay special attention and take precautions when clamping and unclamping the measurement transducers (e.g. voltage- and current probes) to and from the EMC adapter wires. Wires may breake under stress.** **Note that the signal lines are shielded and the shield is rooted via the connector shells. So, when installing the EMC adapters and later performing the measurements, take care and measures that the shielding path is conductively not interrupted!**
12		**Reference/Ambient Test on TC Signal Lines (RWL1)**
		The measurement shall be done on the **Torque_Cmd SUP/RET** lines. The signal lines are accessible via the test adapter DB02-P01 (Figure 6-8, Figure 5-2). “plus”: DB02- J01 pin 16 “return”: DB02- J01 pin 32
Date/Time		4.12./11h00
12.1	☑	REFERENCE Mode
	OP	If not already done switch into REFERENCE mode according to chapter 7.1 ‘’A’’ and confirm the mode
Date/Time		4.12./11h00
12.2	☑	CE Current in Frequency Domain, Common Mode
		Set the current clamp on the TC signal lines for CM measurements in accordance to the CM set-up Figure 5-1: Schematic for CM and DM Measurements. Measure the ambient current from 30 Hz to 50 MHz and compare the results with the limit of 77 dBµA. The ambient emissions shall be at least 6 dB below the requirement limit.
	Ambient	Plot 106: 30 Hz – 10 kHz, CM-ambient, TC-signal Plot 107: 10 kHz – 1 MHz, CM-ambient, TC-signal Plot 108: 1 MHz – 30 MHz, CM-ambient, TC-signal Plot 108: 30 MHz – 50 MHz, CM-ambient, TC-signal
		Photos: See step 27
		Remove the current probe.

Test Step No., Executant	Description/Comments
Date/Time	
12.3 ☑	CE Voltage in Time Domain Common Mode
	Set the voltage probe on the TC signal lines in accordance to the DM voltage set-up and measure the ambient voltage ripple with an oscilloscope (BW ≥ 50 MHz). The ambient emissions shall be at least 6 dB below the requirement limit of 300 mVpp. **Procedure Variation:** The voltage between the Torque Cmd Return line (Pin 32) and the structure (DB02 Connector bracket) shall be measured.
Ambient	Plot 4_12_18: Measured Ambient voltage ripple:u-amb = 60 mVpp (>/< 300 mVpp)
	Photos: See step 27
	Remove the oscilloscope connection from the test adapter.
Date/Time	4.12./11h05
13	**Reference/Ambient Test on TDC Signal Lines (RWL1)**
	The measurement shall be done on the **Torque_Direction_Cmd SUP/RET** lines. The signal lines are accessible via the test adapter DB02- P01 (Figure 6-8, Figure 5-3). “plus”: DB02- J02 pin 17 “return”: DB02- J02 pin 37
Date/Time	4.12./11h15
13.1 ☑	REFERENCE Mode
OP	If not already done switch into REFERENCE mode according to chapter 7.1 ‘’A’’ and confirm the mode.
Date/Time	4.12./11h15
13.2 ☑	CE Current in Frequency Domain, Common Mode
	Set the current clamp on the TDC signal lines for CM measurements in accordance to the CM set-up Figure 5-1: Schematic for CM and DM Measurements. Measure the ambient current from 30 Hz to 50 MHz and compare the results with the limit of 77 dBµA. The ambient emissions shall be at least 6 dB below the requirement limit.
Ambient	Plot 109: 30 Hz – 10 kHz, CM-ambient, TDC-signal Plot 110: 10 kHz – 1 MHz, CM-ambient, TDC -signal Plot 111: 1 MHz – 30 MHz, CM-ambient, TDC -signal Plot 111: 30 MHz – 50 MHz, CM-ambient, TDC -signal
	Photos: See setp 28
	Remove the current probe.
Date/Time	4.12./11h20
13.3 ☑	CE Voltage in Time Domain CommonMode
	Set the voltage probe on the TDC signal lines in accordance to the DM voltage set-up and measure the ambient voltage ripple with an oscilloscope (BW ≥ 50 MHz).

Test Step No., Executant	Description/Comments
	The ambient emissions shall be at least 6 dB below the requirement limit of 300 mVpp. **Procedure Variation:** The voltage between the Torque Direction Cmd Return line (Pin 37) and the structure (DB02 Connector bracket) shall be measured.
Ambient	Plot 4_12_19: Measured Ambient voltage ripple:u-amb = 45 mVpp (>/< 150 mVpp)
	Photos: See step 28
Date/Time	Remove the oscilloscope connection from the test adapter. 4.12./11h15

14		**Reference/Ambient Test on ACC/1553 Signal Lines (MILBUS)**
		The measurement shall be done on the **MILBUS** lines. The signal lines are accessible via the test adapter ACC-J23 (Figure 6-10). “MIL1553_A_Nom”: ACC-J23 pin 1 “MIL1553Rtn_A_Nom”: ACC-J23 pin 11
Date/Time		4.12./11h20
14.1	☑	REFERENCE Mode
OP		If not already done switch into REFERENCE mode according to chapter 7.1 ‘’A’’ and confirm the mode.
Date/Time		4.12./11h20
14.2	☑	CE Voltage in Time Domain Differential Mode
		Set the voltage probe on the MILBUS signal lines in accordance to the DM voltage set-up and measure the ambient voltage ripple with an oscilloscope (BW ≥ 50 MHz).
Ambient		Plot 4_12_20 and 4_12_21: Measured Ambient voltage ripple:U-amb = 1.3 mVpp
		Photos: See step 29
Date/Time		Remove the oscilloscope connection from the test adapter. 4.12./11h30

8.5 Tests on Satellite Ground Line

Test Step No., Executant		Description/Comments
15		**Test on <u>Satellite Ground</u> Line**: This Test is deleted!

Upon completion of this step no further step shall be performed that requires switch on of the S/C without:

Formal release of functional test procedure PR-0100

and

Formal closure of TRR

QA shall validate that this statement is included in the above as-run procedure and that the points above shall be validated by QA prior to any continuation of the EMC test past this step.

8.6 Conducted Emission Tests on Power Lines

Test Step No., Executant		Description/Comments
		CONDUCTED EMISSION TEST ON POWER LINES In additional see paragraph 5 “Test Requirements”. The power lines under test accessability and the current-/voltage probe arrangements are described in the previouse chapter for “Reference/Ambient- measurements” and will not be repeated here! **Before starting an EMC test:** **Ckeck that all the EMC adapter connectors are mechanically secured against dis-connecting by accident or stress!** **Pay special attention and take precautions when clamping and unclamping the measurement transducers (e.g. voltage- and current probes) to and from the EMC adapter wires. Wires may breake under stress.**
16		**CE Test on HIFI-LCU-main Power Lines**
		The measurement shall be done on the **PCDU/FHLCU_Pwrs SUP/RTN** lines.
16.1	☑	NOISIEST Mode
	OP	If not already done, switch into NOISIEST mode according to chapter 7.1 ‘’B4’’ and confirm the mode
	OP	Give nominal DC current to EMC. The value shall be recorded into the test report. WM709565 HiFiLCU_N_L53_I Nominal DC current: I-DCnom = 2.15A
Date/Time		01.12/15h20
16.2	☑	CE Current in Frequency Domain, Common Mode
		Measure the CM current from 30 Hz to 50 MHz.
	Passed	Plot 027: 30 Hz – 10 kHz, CM-NomMode, HIFI-LCU-main -PWR Plot 028: 10 kHz – 1 MHz, CM- NomMode, HIFI-LCU-main -PWR Plot 029: 1 MHz – 30 MHz, CM- NomMode, HIFI-LCU-main -PWR Plot 029: 30 MHz – 50 MHz, CM- NomMode, HIFI-LCU-main -PWR
		The measurement did not identify the LCU converter frequencies which where measured previously before the EMC test (at LCU integration). However, the EMC measurement was verified to be correct by additional current measurement verifying correct operation of the LCU DC/DC converter. See also the comment to step 16.3.
		Photos: 5856
		Remove the current probe.

Test Step No., Executant	Description/Comments
Date/Time	01.12/17h12
16.3 ☑	CE Current in Frequency Domain, Differential Mode
	Measure the DM current from 30 Hz to 50 MHz.
Passed	Plot 030: 30 Hz – 10 kHz, DM- NomMode, HIFI-LCU-main -PWR Plot 031: 10 kHz – 1 MHz, DM- NomMode, HIFI-LCU-main -PWR Plot 032: 1 MHz – 30 MHz, DM- NomMode, HIFI-LCU-main -PWR Plot 032: 30 MHz – 50 MHz, DM- NomMode, HIFI-LCU-main -PWR Tests have been repeated once more because it was expacted to to see the DC/DC converter signal (about 100 kHz). But it was not visible in TD and not in FD. Repeated plots are: Plot 030A: 30 Hz – 10 kHz, DM- NomMode, HIFI-LCU-main -PWR Plot 031A: 10 kHz – 1 MHz, DM- NomMode, HIFI-LCU-main -PWR Plot 032A: 1 MHz – 30 MHz, DM- NomMode, HIFI-LCU-main -PWR Plot 032A: 30 MHz – 50 MHz, DM- NomMode, HIFI-LCU-main -PWR
	See also the comment to step 16.2.
	Photos: 5857
	Remove the current probe.
Date/Time	01.12/17h and 01.12./19h20 (for the repeated measurement)
16.4 ☑	CE Current in Time Domain, Differential Mode
	Measure the DC current and the current ripple with an oscilloscope (BW ≥ 50 MHz).
Passed	Measured Nominal DC current:: see below (I-DCnon to be divided by 2) I-DCnom ≤ 1 A → Max ripple: I-nom ≤ 60 mApp I-DCnom > 1 A → Max ripple: I-nom ≤ [60 mApp x sqr (I-DCnom)] = xxx Plot sinus22: Current ripple Measured current ripple: I-ACnom = 272 mApp I-DCnom = 4 A (Measurement has been repeated because first measurement was done with wrong cable routing in the current probe.)
	Photos: 5858
	Remove the current probe.
Date/Time	01.12/17h00
16.5 ☑	CE Voltage in Time Domain Differential Mode
	Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling.
	The expected voltage ripple shall be compared to 2,5 Vpp. This test for information only. No limit exists.

Test Step No., Executant		Description/Comments
	 ~~For info only~~	Plot sinus23: Measured voltage ripple: U-amb = 102 mVpp (>/< 2,5 V)
		Photos: 5860
		Remove the oscilloscope connection from the test adapter.
Date/Time		01.12/16h05
16.6	☑	CE Voltage in Time Domain, Pwr-Return to Structure
		Connect the differential voltage probe "+" input to the HIFI-LCU-main RETURN power line and the differential voltage probe "-" input to the satellite structure. Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. “return”: DB01-J06 pin 2 "satellite structure": Connector Bracket DB01 and/or “return”: DB01–J04 pin 12 "satellite structure": Connector Bracket DB01
		“return”: DB01-J06 pin 2 is used Plot sinus24: Measured voltage ripple: U-amb = 128 mVpp
		Photos: 5861
		Remove the oscilloscope connection from the test adapter.

17		**CE Test on HIFI-ICU-main Power Lines**
		The measurement shall be done on the the **PCDU/FHICU_Nom_Pwr SUP/RTN** lines.
17.1	☑	NOISIEST Mode
	OP	If not already done, switch into NOISIEST mode according to chapter 7.1 ‘’B4’’ and confirm the mode
	OP	Give nominal DC current to EMC. The value shall be recorded into the test report. WM509565 HiFiICU_N_L64_I Nominal DC current: I-DCnom = 1.10 A
Date/Time		01.12/16h14
17.2	☑	CE Current in Frequency Domain, Common Mode
		Measure the CM current from 30 Hz to 50 MHz.

Passed	Plot 033: 30 Hz – 10 kHz, CM-NomMode, HIFI-ICU-main -PWR Plot 034: 10 kHz – 1 MHz, CM- NomMode, HIFI-ICU-main -PWR Plot 035: 1 MHz – 30 MHz, CM- NomMode, HIFI-ICU-main -PWR Plot 035: 30 MHz – 50 MHz, CM- NomMode, HIFI-ICU-main -PWR
	Photos: 5862
	Remove the current probe.
Date/Time	01.12./16h20
17.3	CE Current in Frequency Domain, Differential Mode
	Measure the CM current from 30 Hz to 50 MHz.
Passed	Plot 036: 30 Hz – 10 kHz, DM- NomMode, HIFI-ICU-main -PWR Plot 037: 10 kHz – 1 MHz, DM- NomMode, HIFI-ICU-main -PWR Plot 038: 1 MHz – 30 MHz, DM- NomMode, HIFI-ICU-main -PWR Plot 038: 30 MHz – 50 MHz, DM- NomMode, HIFI-ICU-main -PWR
	Photos: 5863
	Remove the current probe.
Date/Time	01.12./16h30
17.4 ☑	CE Current in Time Domain, Differential Mode
	Measure the DC current and the current ripple with an oscilloscope (BW ≥ 50 MHz).
Passed/	Measured Nominal DC current: I-DCnom = see below (I-nomDC to be devided by 2) I-DCnom ≤ 1 A → Max ripple: I-nom ≤ 60 mApp I-DCnom > 1 A → Max ripple: I-nom ≤ [60 mApp x sqr (I-DCnom)] = xxx Plot Sinus25 <nxd Sinus26: Current ripple Measured current ripple: I-nomAC = 175 mApp, ≤/> I-max ? I-nomDC = 2.05 A
	Photos: 5864
	Remove the current probe.
Date/Time	01.12/16h45
17.5 ☑	CE Voltage in Time Domain Differential Mode
	Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling.
	The expected voltage ripple shall be compared to 2,5 Vpp. This test for information only. No limit exists.
	Plot Sinus27:

	~~For info only~~	Measured voltage ripple: U-amb = 180 mVpp (>/< 2,5 V)
		Photos: 5865
		Remove the oscilloscope connection from the test adapter.
Date/Time		01.12/16h50
17.6	☑	CE Voltage in Time Domain, Pwr-Return to Structure
		Connect the differential voltage probe "+" input to the HIFI-ICU-main RETURN power line and the differential voltage probe "-" input to the satellite structure. Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. “return”: DB01-J05 pin 10 "satellite structure": Connector bracket …
		Plot Sinus27: Measured voltage ripple: U-amb = 240 mVpp
		Photos: 5866
Date/Time:		Remove the oscilloscope connection from the test adapter. 01.12/17h20

18		**CE Test on SPIRE-FCU-main Power Lines**
		The measurement shall be done on the the **PCDU/HSFCU_Nom_Pwr SUP/RTN** lines.
18.1	☑	NOISIEST Mode
	OP	If not already done, switch into NOISIEST mode according to chapter 7.1 ‘’B5’’ and confirm the mode
	OP	Give nominal DC current to EMC. The value shall be recorded into the test report. WM408565 SpireHsfN_L51_I Nominal DC current: I-DCnom = 0.94 A
Date/Time		03.12./15h21
18.2	☑	CE Current in Frequency Domain, Common Mode
		Measure the CM current from 30 Hz to 50 MHz.
	Passed	Plot 057: 30 Hz – 10 kHz, CM-NomMode, SPIRE-FCU-main -PWR Plot 058: 10 kHz – 1 MHz, CM- NomMode, SPIRE-FCU -main -PWR Plot 059: 1 MHz – 30 MHz, CM- NomMode, SPIRE-FCU -main -PWR Plot 059: 30 MHz – 50 MHz, CM- NomMode, SPIRE-FCU -main -PWR
		Comments/Limit exceedings if any
		Photos: 5889
		Remove the current probe.

Date/Time		03.12./15h27
18.3	☑	CE Current in Frequency Domain, Differential Mode
		Measure the DM current from 30 Hz to 50 MHz.
	Passed	Plot 060: 30 Hz – 10 kHz, DM- NomMode, SPIRE-FCU -main -PWR Plot 061: 10 kHz – 1 MHz, DM- NomMode, SPIRE-FCU -main -PWR Plot 062: 1 MHz – 30 MHz, DM- NomMode, SPIRE-FCU -main -PWR Plot 062: 30 MHz – 50 MHz, DM- NomMode, SPIRE-FCU -main -PWR
		Photos: 5890
		Remove the current probe.
Date/Time		03.12./15h35
18.4	☑	CE Current in Time Domain, Differential Mode
		Measure the DC current and the current ripple with an oscilloscope (BW ≥ 50 MHz).
	Passed	Measured Nominal DC current: See below (I-nomDC to be devided by factor of 2) I-DCnom ≤ 1 A → Max ripple: I-nom ≤ 60 mApp I-DCnom > 1 A → Max ripple: I-nom ≤ [60 mApp x sqr (I-DCnom)] = xxx Plot 3_12_8: Current ripple Measured current ripple: I-nomAC = 100 mApp, ≤/> I-max ? I-nomDC = 1,74 A
		Photos: 5891
		Remove the current probe.
Date/Time		03.12./15h40
18.5	☑	CE Voltage in Time Domain Differential Mode
		Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling.
		The expected voltage ripple shall be compared to 2,5 Vpp. This test for information only. No limit exists.
	~~**For info only**~~	Plot 3_12_8 and 3_12_9: Measured voltage ripple: U-amb = 86 mVpp (>/< 2,5 V)
		Photos: 5893
		Remove the oscilloscope connection from the test adapter.
Date/Time		03.12./15h45

18.6	☑	CE Voltage in Time Domain, Pwr-Return to Structure
		Connect the differential voltage probe "+" input to the SPIRE-FCU-main RETURN power line and the differential voltage probe "-" input to the satellite structure. Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. “return”: DB01-J04 pin 8 "satellite structure": …DB01 Connector Bracket
		Plot 3_12_10 and 3_12_11 Measured voltage ripple: U-amb = 36 mVpp
		Photos: 5893
Date/Time		Remove the oscilloscope connection from the test adapter. 03.12./15h55

19		**CE Test on PACS-DPU-main Power Lines**
		The measurement shall be done on the the **PCDU/FPDPU_Nom_Pwrs SUP/RTN** lines.
19.1	☑	NOISIEST Mode
	OP	If not already done, switch into NOISIEST mode according to chapter 7.1 ‘’B3’’ and confirm the mode
	OP	Give nominal DC current to EMC. The value shall be recorded into the test report. WM707565 PACSD_N_L41_I Nominal DC current: I-DCnom = 0.48 A
Date/Time		03.12./16h05
19.2	☑	CE Current in Frequency Domain, Common Mode
		Measure the CM current from 30 Hz to 50 MHz.
	 **Passed**	Plot 063: 30 Hz – 10 kHz, CM-NomMode, PACS-DPU-main -PWR Plot 064: 10 kHz – 1 MHz, CM- NomMode, PACS-DPU -main -PWR Plot 065: 1 MHz – 30 MHz, CM- NomMode, PACS-DPU -main -PWR Plot 065: 30 MHz – 50 MHz, CM- NomMode, PACS-DPU -main -PWR
		
		Photos: 5894
		Remove the current probe.
Date/Time		03.12./16h47
19.3	☑	CE Current in Frequency Domain, Differential Mode
		Measure the DM current from 30 Hz to 50 MHz.
	 **Passed**	Plot 066: 30 Hz – 10 kHz, DM- NomMode, PACS-DPU -main -PWR Plot 067: 10 kHz – 1 MHz, DM- NomMode, PACS-DPU -main -PWR Plot 068: 1 MHz – 30 MHz, DM- NomMode, PACS-DPU -main -PWR

		Plot 068: 30 MHz – 50 MHz, DM- NomMode, PACS-DPU -main -PWR
		Comments/Limit exceedings if any
		Photos: 5895
		Remove the current probe.
Date/Time		03.12./17h07
19.4	☑	CE Current in Time Domain, Differential Mode
		Measure the DC current and the current ripple with an oscilloscope (BW ≥ 50 MHz).
	 **Passed**	Measured Nominal DC current: See below (I-nomDC to be divided by a factor of 2) I-DCnom ≤ 1 A → Max ripple: I-nom ≤ 60 mApp I-DCnom > 1 A → Max ripple: I-nom ≤ [60 mApp x sqr (I-DCnom)] = xxx Plot 3_12_13 and 3_12_14: Current ripple Measured current ripple: I-nomAC = 62 mApp, ≤/> I-max ? I-nomDC = 1.3 A
		Photos: 5895
		Remove the current probe.
Date/Time		03.12./17h24
19.5	☑	CE Voltage in Time Domain Differential Mode
		Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling.
		The expected voltage ripple shall be compared to 2,5 Vpp. This test for information only. No limit exists.
	 ~~**For info only**~~	Plot 3_12_12: Measured voltage ripple: U-amb = 140 mVpp (>/< 2,5 V)
		Photos: 5895
		Remove the oscilloscope connection from the test adapter.
Date/Time		03.12./17h00
19.6	☑	CE Voltage in Time Domain, Pwr-Return to Structure
		Connect the differential voltage probe "+" input to the PACS-DPU -main RETURN power line and the differential voltage probe "-" input to the satellite structure. Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. “return”: DB01-J05 pin 48//50

	"satellite structure": DB01 Connector Bracket
	Plot 3_12_16 and 3_12_17: Measured voltage ripple: U-amb = 157 mVpp
	Photos: 5896
Date/Time:	Remove the oscilloscope connection from the test adapter. 03.12./17h32

20		**CE Test on <u>PACS-SPU-main</u> Power Lines**
		The measurement shall be done on the the the **PCDU/FPSPU1_ Pwrs SUP/RTN** lines.
20.1	☑	<u>NOISIEST Mode</u>
	OP	If not already done, switch into NOISIEST mode according to chapter 7.1 ''B3'' and confirm the mode
	OP	Give nominal DC current to EMC. The value shall be recorded into the test report. WM506565 PacsS_N_L35_I Nominal DC current: I-DCnom = 0.62A
Date/Time		03.12./17h40
20.2	☑	<u>CE Current in Frequency Domain, Common Mode</u>
		Measure the CM current from 30 Hz to 50 MHz.
	 **Passed**	Plot 070: 30 Hz – 10 kHz, CM-NomMode, PACS-SPU-main -PWR Plot 071: 10 kHz – 1 MHz, CM- NomMode, PACS-SPU -main -PWR Plot 072: 1 MHz – 30 MHz, CM- NomMode, PACS-SPU -main -PWR Plot 072: 30 MHz – 50 MHz, CM- NomMode, PACS-SPU -main -PWR
		
		Photos: 5897
		Remove the current probe.
Date/Time		03.12./17h40
20.3	☑	<u>CE Current in Frequency Domain, Differential Mode</u>
		Measure the CM current from 30 Hz to 50 MHz.
	 **Passed**	Plot 073: 30 Hz – 10 kHz, DM- NomMode, PACS-SPU -main -PWR Plot 074: 10 kHz – 1 MHz, DM- NomMode, PACS-SPU -main -PWR Plot 075: 1 MHz – 30 MHz, DM- NomMode, PACS-SPU -main -PWR Plot 075: 30 MHz – 50 MHz, DM- NomMode, PACS-SPU -main -PWR
		
		Photos: 5899
		Remove the current probe.

Date/Time	03.12./17h50
20.4 ☑	CE Current in Time Domain, Differential Mode
	Measure the DC current and the current ripple with an oscilloscope (BW ≥ 50 MHz).
 **Passed**	Measured Nominal DC current: See below I-nomDC (to be divided by a factor of 2) I-DCnom ≤ 1 A → Max ripple: I-nom ≤ 60 mApp I-DCnom > 1 A → Max ripple: I-nom ≤ [60 mApp x sqr (I-DCnom)] = xxx Plot 3_12_15 and 3_12_19: Current ripple Measured current ripple: I-nomAC = 63.5 mApp, ≤/> I-max ? I-nomDC = 854 mA
	Photos: 5899
	Remove the current probe.
Date/Time	
20.5 ☑	CE Voltage in Time Domain Differential Mode
	Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling.
	The expected voltage ripple shall be compared to 2,5 Vpp. This test for information only. No limit exists.
 ~~**For info only**~~	Plot 3_12_18: Measured voltage ripple: U-amb = 438 mVpp (>/< 2,5 V)
	Photos: 5898
	Remove the oscilloscope connection from the test adapter.
Date/Time	03.12./17h43
20.6 ☑	CE Voltage in Time Domain, Pwr-Return to Structure
	Connect the differential voltage probe "+" input to the PACS-SPU -main RETURN power line and the differential voltage probe "-" input to the satellite structure. Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. “return”: DB01-J05 pin 2//4 "satellite structure": Connector Bracket DB01
	Plot 3_12_20: Measured voltage ripple: U-amb = 280 mVpp
	Photos: 5900
	Remove the oscilloscope connection from the test adapter.

Date/Time:	03.12./17h55

21		**CE Test on PACS-BOLC-main Power Lines**
		The measurement shall be done on the the the **PCDU/FPBOLC_ Pwrs SUP/RTN** lines.
21.1	☑	NOISIEST Mode
	OP	If not already done, switch into NOISIEST mode according to chapter 7.1 ''B3'' and confirm the mode
	OP	Give nominal DC current to EMC. The value shall be recorded into the test report. WM809565 PacsB_N_L27_I Nominal DC current: I-DCnom = 0.20 A
	Date/Time	03.12./18h08
21.2	☑	CE Current in Frequency Domain, Common Mode
		Measure the CM current from 30 Hz to 50 MHz.
	Passed	Plot 076: 30 Hz – 10 kHz, CM-NomMode, PACS-BOLC -main -PWR Plot 077: 10 kHz – 1 MHz, CM- NomMode, PACS-BOLC -main -PWR Plot 078: 1 MHz – 30 MHz, CM- NomMode, PACS-BOLC -main -PWR Plot 078: 30 MHz – 50 MHz, CM- NomMode, PACS-BOLC -main -PWR
		Photos: 5901
		Remove the current probe.
	Date/Time	03.12./18h17
21.3		CE Current in Frequency Domain, Differential Mode
		Measure the DM current from 30 Hz to 50 MHz.
	Passed	Plot 079: 30 Hz – 10 kHz, DM- NomMode, PACS-BOLC -main -PWR Plot 080: 10 kHz – 1 MHz, DM- NomMode, PACS-BOLC -main -PWR Plot 081: 1 MHz – 30 MHz, DM- NomMode, PACS-BOLC -main -PWR Plot 081: 30 MHz – 50 MHz, DM- NomMode, PACS-BOLC -main -PWR
		Photos: 5902
		Remove the current probe.
	Date/Time	03.12./18h30
21.4		CE Current in Time Domain, Differential Mode
		Measure the DC current and the current ripple with an oscilloscope (BW ≥ 50 MHz).
	Passed	Measured Nominal DC current: I-nomDC (see below, to be divided by a factor of 2) I-DCnom ≤ 1 A → Max ripple: I-nom ≤ 60 mApp I-DCnom > 1 A → Max ripple: I-nom ≤ [60 mApp x sqr (I-DCnom)] = xxx

		Plot 3_12_24 and 3_12_25: Current ripple Measured current ripple: I-nomAC = 43 mApp, ≤/> I-max ? I-nomDC = 300 mA
		Photos: 5903
		Remove the current probe.
Date/Time		03.12./19h00
21.5		CE Voltage in Time Domain Differential Mode
		Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling.
		The expected voltage ripple shall be compared to 2,5 Vpp. This test for information only. No limit exists.
	 ~~For info only~~	Plot 3-12-21: Measured voltage ripple: u-amb = 105 mVpp (>/< 2,5 V)
		Photos: 5902
		Remove the oscilloscope connection from the test adapter.
Date/Time		03.12./18:15
21.6	☑	CE Voltage in Time Domain, Pwr-Return to Structure
		Connect the differential voltage probe "+" input to the PACS-BOLC-main RETURN power line and the differential voltage probe "-" input to the satellite structure. Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. “return”: DB01-J06 pin 14//18 "satellite structure": Connector Bracket DB01
		Plot 3_12_26 and 3_12_27: Measured voltage ripple: U-amb = 41 mVpp
		Photos: 2904
Date/Time		Remove the oscilloscope connection from the test adapter. 03.12./19h35

22	☑	**CE Test on PACS-MEC-main Power Lines**
		The measurement shall be done on the the the **PCDU/FPMEC1_ Pwrs SUP/RTN** lines.
22.1	☑	NOISIEST Mode
OP		If not already done, switch into NOISIEST mode according to chapter 7.1 ‘’B3’’ and confirm

		the mode
	OP	Give nominal DC current to EMC. The value shall be recorded into the test report. WM510565 PacsMec1_L65_I Nominal DC current: I-DCnom = 0.70 A
	Date/Time	03.12./19h43
22.2	☑	CE Current in Frequency Domain, Common Mode
		Measure the CM current from 30 Hz to 50 MHz.
	Passed	Plot 082: 30 Hz – 10 kHz, CM-NomMode, PACS- MEC -main -PWR Plot 083: 10 kHz – 1 MHz, CM- NomMode, PACS- MEC -main -PWR Plot 084: 1 MHz – 30 MHz, CM- NomMode, PACS- MEC -main -PWR Plot 084: 30 MHz – 50 MHz, CM- NomMode, PACS- MEC -main -PWR
		Photos: 5905
		Remove the current probe.
	Date/Time	03.12./19h50
22.3	☑	CE Current in Frequency Domain, Differential Mode
		Measure the DM current from 30 Hz to 50 MHz.
	Passed	Plot 085: 30 Hz – 10 kHz, DM- NomMode, PACS- MEC -main -PWR Plot 086: 10 kHz – 1 MHz, DM- NomMode, PACS- MEC -main -PWR Plot 087: 1 MHz – 30 MHz, DM- NomMode, PACS- MEC -main -PWR Plot 087: 30 MHz – 50 MHz, DM- NomMode, PACS- MEC -main -PWR
		Photos: 5907
		Remove the current probe.
	Date/Time	
22.4	☑	CE Current in Time Domain, Differential Mode
		Measure the DC current and the current ripple with an oscilloscope (BW ≥ 50 MHz).
	Passsed	Measured Nominal DC current: I-nomDC (see below to be divided by factor of 2. I-DCnom ≤ 1 A → Max ripple: I-nom ≤ 60 mApp I-DCnom > 1 A → Max ripple: I-nom ≤ [60 mApp x sqr (I-DCnom)] = xxx Plot 3_12_30 and 3_12_31: Current ripple Measured current ripple: I-nomAC = 91 mApp, ≤/> I-max ? I-nomDC = 1.33 A
		Photos: 5907
		Remove the current probe.
	Date/Time	03.12./1955

22.5	☑	CE Voltage in Time Domain Differential Mode
		Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling.
		The expected voltage ripple shall be compared to 2,5 Vpp. This test for information only. No limit exists.
~~For info only~~		Plot 3_12_28 and 3_12_29: Measured voltage ripple: U-amb = 19 mVpp (>/< 2,5 V)
		Photos: 5906
		Remove the oscilloscope connection from the test adapter.
Date/Time		03.12./19h45
22.6	☑	CE Voltage in Time Domain, Pwr-Return to Structure
		Connect the differential voltage probe "+" input to the PACS-MEC-main RETURN power line and the differential voltage probe "-" input to the satellite structure. Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. “return”: DB01-J06 pin 14//18 "satellite structure": DB01 Connector Bracket
		Plot 3_12_32 and 3_12_33: Measured voltage ripple: U-amb = 163 mVpp
		Photos: 5908
Date/Time		Remove the oscilloscope connection from the test adapter. 03.12./20h00

23		**CE Test on CCU-main Power Lines**
		The measurement shall be done on the the the **PCDU/CCU_A_ Pwrs SUP/RTN** lines.
23.1	☑	NOISIEST Mode
	OP	If not already done, switch into NOISIEST mode according to chapter 7.1 ‘’B3’’ and confirm the mode
	OP	Give nominal DC current to EMC. The value shall be recorded into the test report. WM106565 Ccu_A_L37_I Nominal DC current: I-DCnom = 130 mA
Date/Time		01.12/17h28
23.2	☑	CE Current in Frequency Domain, Common Mode
		Measure the CM current from 30 Hz to 50 MHz.

Passed	Plot 039: 30 Hz – 10 kHz, CM-NomMode, CCU-main -PWR Plot 040: 10 kHz – 1 MHz, CM- NomMode, CCU -main -PWR Plot 041: 1 MHz – 30 MHz, CM- NomMode, CCU -main -PWR Plot 041: 30 MHz – 50 MHz, CM- NomMode, CCU -main -PWR
	Photos: 5867
	Remove the current probe.
Date/Time	01.12/17h35
23.3 ☑	CE Current in Frequency Domain, Differential Mode
	Measure the CM current from 30 Hz to 50 MHz.
Passed	Plot 042: 30 Hz – 10 kHz, DM- NomMode, CCU -main -PWR Plot 043: 10 kHz – 1 MHz, DM- NomMode, CCU -main -PWR Plot 044: 1 MHz – 30 MHz, DM- NomMode, CCU -main -PWR Plot 044: 30 MHz – 50 MHz, DM- NomMode, CCU -main -PWR
	Photos: 5867
	Remove the current probe.
Date/Time	01.12/18:00
23.4 ☑	CE Current in Time Domain, Differential Mode
	Measure the DC current and the current ripple with an oscilloscope (BW ≥ 50 MHz).
Passed	Measured Nominal DC current: I-DCnom = xxx mA I-DCnom ≤ 1 A → Max ripple: I-nom ≤ 60 mApp I-DCnom > 1 A → Max ripple: I-nom ≤ [60 mApp x sqr (I-DCnom)] = xxx Plot Sinus29 Current ripple Measured current ripple: I-nomAC = 85 mApp, I-nomDC = 240 mA
	Photos: none
	Remove the current probe.
Date/Time	01.12./18h40
23.5 ☑	CE Voltage in Time Domain Differential Mode
	Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling.
	The expected voltage ripple shall be compared to 2,5 Vpp. This test for information only. No limit exists.

 ~~For info only~~	Plot Sinus30 Measured voltage ripple: u-amb = 100 mVpp (>/< 2,5 V)
	Photos: 5869
	Remove the oscilloscope connection from the test adapter.
Date/Time	01.12./18h45
23.6 ☑	CE Voltage in Time Domain, Pwr-Return to Structure
	Connect the differential voltage probe "+" input to the CCU-main RETURN power line and the differential voltage probe "-" input to the satellite structure. Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. “return”: DB01-J01 pin 19 "satellite structure": Connector bracket DB01
	Plot Sinus31: Measured voltage ripple: U-amb = 158 mVpp
	Photos: 5870
Date/Time:	Remove the oscilloscope connection from the test adapter. 01.12./18h58

24		**CE Test on TWTA-EPC1 Power Lines**
		The measurement shall be done on the the the **PCDU/EPC1_Pwrs SUP/RTN** lines.
24.1	☑	NOISIEST Mode
	OP	If not already done, switch into NOISIEST mode according to chapter 7.1 ‘’B4’’ and confirm the mode
	OP	Give nominal DC current to EMC. The value shall be recorded into the test report. WM210565 Twta_1_L49_I Nominal DC current: I-DCnom = 2.49 A
Date/Time		01.12./19h20
24.2	☑	CE Current in Frequency Domain, Common Mode
		Measure the CM current from 30 Hz to 50 MHz.
 **Passed**		Plot 045: 30 Hz – 10 kHz, CM-NomMode, TWTA-EPC1-main -PWR Plot 046: 10 kHz – 1 MHz, CM- NomMode, TWTA-EPC1-main -PWR Plot 047: 1 MHz – 30 MHz, CM- NomMode, TWTA-EPC1-main -PWR Plot 047: 30 MHz – 50 MHz, CM- NomMode, TWTA-EPC1-main -PWR
		

		Photos: 5871
		Remove the current probe.
Date/Time		01.12./19h30
24.3	☑	CE Current in Frequency Domain, Differential Mode
		Measure the DM current from 30 Hz to 50 MHz.
	Passed	Plot 048: 30 Hz – 10 kHz, DM- NomMode, TWTA-EPC1-main -PWR Plot 049: 10 kHz – 1 MHz, DM- NomMode, TWTA-EPC1-main -PWR Plot 050: 1 MHz – 30 MHz, DM- NomMode, TWTA-EPC1-main -PWR Plot 050: 30 MHz – 50 MHz, DM- NomMode, TWTA-EPC1-main -PWR
		Photos: 5872
		Remove the current probe.
Date/Time		19h40
24.4	☑	CE Current in Time Domain, Differential Mode
		Measure the DC current and the current ripple with an oscilloscope (BW ≥ 50 MHz).
	Passed	Measured Nominal DC current: See measurement below (I-nonDC to be devided by 2) I-DCnom ≤ 1 A → Max ripple: I-nom ≤ 60 mApp I-DCnom > 1 A → Max ripple: I-nom ≤ [60 mApp x sqr (I-DCnom)] = xxx Current ripple Measured current ripple: I-nomAC = 90 mApp, ≤/> I-max ? I-nomDC = 4,83 A
		Photos: 5873
		Remove the current probe.
Date/Time		01.12./19h48
24.5	☑	CE Voltage in Time Domain Differential Mode
		Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling.
		The expected voltage ripple shall be compared to 2,5 Vpp. This test for information only. No limit exists.
	For info only	Plot Sinus 33 and Sinus 34 Measured voltage ripple: U-amb = 2.75 V (>/< 2,5 V)
		Photos: 5874
		Remove the oscilloscope connection from the test adapter.

Date/Time		01.12./19h50
24.6	☑	CE Voltage in Time Domain, Pwr-Return to Structure
		Connect the differential voltage probe "+" input to the TWTA-EPC1-main RETURN power line and the differential voltage probe "-" input to the satellite structure. Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling. “return”: DB01-J02 pin 23/25 "satellite structure": Bracket DB01 …
		Plot Sinus35 and Sinus36: Measured voltage ripple: U-amb = 780 mVpp
		Photos: 5675
Date/Time		Remove the oscilloscope connection from the test adapter. 01.12./20h00

25		**CE Test on STR1 Power Lines**
		The measurement shall be done on the the the **PCDU/STR1_Pwr SUP/RTN** lines.
25.1	☑	NOISIEST Mode
	OP	If not already done, switch into NOISIEST mode according to chapter 7.1 ‘’B4’’ and confirm the mode
	OP	Give nominal DC current to EMC. The value shall be recorded into the test report. WMA08565 STR_1_L21_I Nominal DC current: I-DCnom = 370 mA
Date/Time		01.12/20h05
25.2	☑	CE Current in Frequency Domain, Common Mode
		Measure the CM current from 30 Hz to 50 MHz.
	 **Passed**	Plot 051: 30 Hz – 10 kHz, CM-NomMode, STR1-main -PWR Plot 052: 10 kHz – 1 MHz, CM- NomMode, STR1-main -PWR Plot 053: 1 MHz – 30 MHz, CM- NomMode, STR1-main -PWR Plot 053: 30 MHz – 50 MHz, CM- NomMode, STR1-main -PWR
		
		Photos: 5876
		Remove the current probe.
Date/Time		01.12/20h14
25.3	☑	CE Current in Frequency Domain, Differential Mode
		Measure the DM current from 30 Hz to 50 MHz.

Passed	Plot 054: 30 Hz – 10 kHz, DM- NomMode, STR1-main -PWR Plot 055: 10 kHz – 1 MHz, DM- NomMode, STR1-main -PWR Plot 056: 1 MHz – 30 MHz, DM- NomMode, STR1-main -PWR Plot 056: 30 MHz – 50 MHz, DM- NomMode, STR1-main -PWR
	Photos: 5877
	Remove the current probe.
Date/Time	01.12./20h20
25.4 ☑	CE Current in Time Domain, Differential Mode
	Measure the DC current and the current ripple with an oscilloscope (BW ≥ 50 MHz).
Passed	Measured Nominal DC current: See below (I-nomDC to be divided by factor of 2) I-DCnom ≤ 1 A → Max ripple: I-nom ≤ 60 mApp I-DCnom > 1 A → Max ripple: I-nom ≤ [60 mApp x sqr (I-DCnom)] = xxx Plot Sinus37 and Sinus38: Current ripple Measured current ripple: I-nomAC = 10 mApp, ≤/> I-max ? I-nomDC = 730 mA
	Photos: 5878
	Remove the current probe.
Date/Time	01.12/20h35
25.5 ☑	CE Voltage in Time Domain Differential Mode
	Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate time-/amplitude scaling.
	The expected voltage ripple shall be compared to 2,5 Vpp. This test for information only. No limit exists.
~~For info only~~	Plot Sinus39 and Sinus40 Measured voltage ripple: U-amb = 233 mVpp (>/< 2,5 V)
	Photos: 5879
	Remove the oscilloscope connection from the test adapter.
Date/Time	01.12/20h37
25.6 ☑	CE Voltage in Time Domain, Pwr-Return to Structure
	Connect the differential voltage probe "+" input to the STR1-main RETURN power line and the differential voltage probe "-" input to the satellite structure. Measure the differential voltage ripple with an oscilloscope (BW ≥ 50 MHz) in adequate

	time-/amplitude scaling. “return”: DB01-J06 pin 28 "satellite structure": Connector Bracket DB01
	Plot Sinus41 and Sinus42: Measured voltage ripple: U-amb = 529 mVpp
	Photos: 5880
Date/Time:	Remove the oscilloscope connection from the test adapter. 01.12./20h45

8.7 Tests on Satellite Ground Line

Test Step No., Executant		Description/Comments
26		**Test on Satellite Ground Line**: This test is deleted!

8.8 Conducted Emission Tests on Signal Lines

Test Step No., Executant		Description/Comments
		CE TEST ON SIGNAL LINES In additional see paragraph 5 “Test Requirements”. The power lines under test accessability and the current-/voltage probe arrangements are described in the previouse chapter for “Reference/Ambient- measurements” and will not be repeated here! **Before starting an EMC test:** **Ckeck that all the EMC adapter connectors are mechanically secured against dis-connecting by accident or stress!** **Pay special attention and take precautions when clamping and unclamping the measurement transducers (e.g. voltage- and current probes) to and from the EMC adapter wires. Wires may breake under stress.** **Note that the signal lines are shielded and the shield is rooted via the connector shells. So, when installing the EMC adapters and later performing the measure-ments, take care and measures that the shielding path is conductively not inter-rupted!**
27		**CE Test on TC Signal Lines (RWL1)**
		The measurement shall be done on the **Torque_Cmd SUP/RET** lines.
27.1	☑	NOISIEST Mode
	OP	If not already done, switch into NOISIEST mode according to chapter 7.1 ‘’B4’’ and confirm the mode
	OP	Command TC to: 500 acdcording to sect.7.1, B6 Command TDC to: Clockwise Remark: The plots have to be recorded in less than 15 min. If not B6 has to be started again. Record and check setup information and give values to the EMC team.
Date/Time		03/12./14h15
27.2	☑	CE Current in Frequency Domain, Common Mode
		Measure the current from 30 Hz to 50 MHz and compare the results with the limit of 77 dBµA.
	Passed	Plot 100: 30 Hz – 10 kHz, CM-NomMode, TC-signal Plot 101: 10 kHz – 1 MHz, CM- NomMode, TC-signal Plot 102: 1 MHz – 30 MHz, CM- NomMode, TC-signal Plot 102: 30 MHz – 50 MHz, CM- NomMode, TC-signal
		Test passed without limits exceeding
		Photos: 5881
		Remove the current probe.

Test Step No., Executant	Description/Comments
Date/Time	03.12./14:20
27.3 ☑	CE Voltage in Time Domain Common Mode (between return line and structure ground) **Procedure Variation**
	Measure the voltage ripple with an oscilloscope (BW ≥ 50 MHz). The voltage ripple shall be compared to 300 mVpp. This test is *for information only* because no requirement limit exists. The structure ground has been accessed via the structure of the connectror bracket DB02.
~~**For info only**~~	Plot 3_12_3: Measured voltage ripple: U-amb = 55 mVpp (>/< 300 mVpp)
	Photos: 5886
	Remove the oscilloscope connection from the test adapter.
Dare/Time:	03.12./14:25
28	**CE Test on TDC Signal Lines (RWL1)**
	The measurement shall be done on the **Torque_Direction_Cmd SUP/RET** lines.
28.1 ☑	NOISIEST Mode
OP	If not already done, switch into NOISIEST mode according to chapter 7.1 ''B4'' and confirm the mode.
OP	Command TC to: 500 acdcording to sect.7.1, B6 Command TDC to: Clockwise Remark: The plots have to be recorded in less than 15 min. If not B6 has to be started again. Record and check setup information and give values to the EMC team.
Date/Time	03.12./14h25
28.2 ☑	CE Current in Frequency Domain, Common Mode
	Measure the current from 30 Hz to 50 MHz and compare the results with the limit of 77 dBµA.
Passed	Plot 103: 30 Hz – 10 kHz, CM-ambient, TDC-signal Plot 104: 10 kHz – 1 MHz, CM-ambient, TDC -signal Plot 105: 1 MHz – 30 MHz, CM-ambient, TDC -signal Plot 106: 30 MHz – 50 MHz, CM-ambient, TDC -signal
	No limits exceeding
	Photos: 5887
	Remove the current probe.
Date/Time	03.12./14h34
28.3 ☑	CE Voltage in Time Domain Common Mode (between return line and structure ground) **Procedure Variation**
	Measure the voltage ripple with an oscilloscope (BW ≥ 50 MHz). The voltage ripple shall be compared to 300 mVpp. This test is *for information only* because no requirement limit exists.

Test Step No., Executant	Description/Comments
 **For info only**	Plot 3_12_4: Measured voltage ripple: u-amb = 190 mVpp (>/< 300 mVpp)
	Photos: 5887
Date/Time	Remove the oscilloscope connection from the test adapter. 03./12./14h35

29		**CE Test on <u>ACC/1553</u> Signal Lines (MILBUS)**
		The measurement shall be done on the **MILBUS** lines. The signal lines are accessible via the test adapter ACC-J23 (Figure 6-10). “MIL1553_A_Nom”: ACC-J23 pin 1 “MIL1553Rtn_A_Nom”: ACC-J23 pin 11
Date/Time		03.12./14h40
29.1	☑	<u>NOISIEST Mode</u>
	OP	If not already done switch into NOISIEST mode according to chapter 7.1 ‘’B4’’ and confirm the mode.
	OP	Verify that MILBUS is active. Record and check setup information and give values to the EMC team.
Date/Time		
29.2	☑	<u>CE Voltage in Time Domain Differential Mode</u>
		Set the voltage probe on the MILBUS signal lines in accordance to the DM voltage set-up and measure the voltage ripple with an oscilloscope (BW ≥ 50 MHz).
	 ~~**Info**~~	Plot 3_12_6 and 3_12_7 (zoom): Measured voltage ripple: U-amb = 6 Vpp
		Photos: 5888
Date/Time		Remove the oscilloscope connection from the test adapter. 03.12./14h55

8.9 Conducted Susceptibility Tests on Signal Lines

Test Step No., Executant		Description/Comments
		CS TEST ON SIGNAL LINES In additional see paragraph 5.6: "CS Voltage on Signal Lines (optional)", paragraph 6.1.2: "Signal line Test Adapter" and paragraph 6.3: "Arrangements for Signal Lines". **Before starting an EMC test:** **Ckeck that all the EMC adapter connectors are mechanically secured against disconnecting by accident or stress!** **Pay special attention and take precautions when clamping and unclamping the measurement transducers (e.g. voltage- and current probes) to and from the EMC adapter wires. Wires may breake under stress.** **Note that the signal lines are shielded and the shield is rooted via the connector shells. So, when installing the EMC adapters and later performing the measurements, take care and measures that the shielding path is conductively not interrupted!**
30		**CS Test on TC Signal Lines (RWL1)**
30.1		SENSITIVE Mode
	OP	If not already done, switch into SENSITIVE mode according to chapter 7.1 ‘’C’’ and confirm the mode
Date/Time		
30.2	**OP/ EMC**	Arrangement of the test equipment: 1) Arrange the test adapter according paragraph 6.3: "Arrangements for Signal Lines" and the test equipment according to Figure 5-5: TC Test Adapter arrangement for CS, DB02 level" and Figure 5-6: TDC Test Adapter arrangement for CS, DB02 level". 2) I-AC injection and -monitoring on “Motor-Current-Monitor-Return” (MCM/TC_RET (32).): Fix current injection and -monitoring devices to MCM/TC_RET (32). Monitor the injected current with a spectrum analyser. 3) Voltage “Torque-Command-Return to Structure”: The voltage between TC-RET (32) and Structure (connector shell) shall be measured with a differential voltage probe and an oscilloscope. 4) Voltage “Torque-Command” (TC+ (16) to TC-RET (32)): This TC command (500) shall be set by Sattelite/EGSE operational responsibilities. 5) Voltage “Torque-Direction-Command” (MCM/TC_RET (32) to TDC + (17)): The TDC command (CLOCKWISE) shall be set by Sattelite/EGSE operational responsibilities. 6) Voltage “Motor-Current-Monitor” (MCM (12) to MCM-RET (32)): This voltage (0,5105 V) shall be recorded and checked prior and during test by Sattelite/EGSE operational responsibilities.

Test Step No., Executant		Description/Comments
30.3	**OP**	Setup Conditions for TC/TDC Signal lines The TC command shall be set to ''**500**'' leading to a read out TLM (MCM) close to 0.5105V. The TDC command shall be set to: **Clockwise direction** Record and check status information and give values to the EMC team. Actual TLM values: 1) Command TC to: 500 acdcording to sect.7.1, B6 2) Command TDC to: Clockwise Remark: The plots / measurements have to be recorded in less than 15 min. If not B6 has to be started again. 3) Mot Cur. TLM "MCM to MCM-RET: ''…'' (shall be 0,5105 V)
		Actual TLM values: 1) I-inj = 0 mArms 2) TC-RET (7) to Structure (connector shell) = xxx mVpp Osci Plot DXXX: 3) Torque-Command" (TC+ (16) to TC-RET (32)) = xxx V 4) Motor-Current-Monitor" (MCM (12) to MCM-RET (32)) = xxx V
30.4		CS testing: 1) Adjust CS test frequency. Set the test voltage Voltage "Torque-Command-Return to Structure" to **2 Vpp** by slowly increasing the induced current I-AC. 2) At the same time monitor the injected current with a spectrum analyser. In any case the injected current shall not exceed **100 mApp (91 dBµA)** even if the 2 Vpp test voltage cannot be reached! The 100 mApp limit applies only for the injected spectral frequency; amplitudes of other signal parts of the emission spectrum are not relevant. 3) Give test status information to the Sattelite/EGSE operational responsibilities.
30.5	**OP/ EMC**	Susceptibility evaluation: - The RWL shall not exhibit any failures malfunctions or unintended responses when submitted to the injected signals. - The motor current TLM shall keep inside the range +/-12.5 mV around the value without noise injection when submitted to the injected signals. 4) Failure status and MCM voltage shall be monitored prior and during test by Sattelite/EGSE operational responsibilities. 5) Give susceptibility status information to the EMC test team.
		Susceptibility evaluation: - The motor current TLM (MCM (12) to MCM-RET (32)) shall keep inside the range +/-12.5 mV around the value without noise injection when submitted to the injected signals. 6) The motor current (MCM) voltage shall be monitored prior and during test with a DMM

Test Step No., Executant		Description/Comments
		by the EMC team. 7) Fill in the table **Error! Reference source not found.**below for each tested frequency and check the susceptibility criteria. 8) If susceptibility can be detected, reduce the induced voltage level to find the threshold value of susceptibility. 9) If no susceptibility can be detected, go on testing with the next frequency.
		Plot 0xx:
		Photos:

Time	f [kHz]	I-inj [dBµA]	Voltage "TC-RET to STRUC-TURE" [Vpp]	"TC to TC-RET" [V]	Mot Cur. TLM "MCM to MCM-RET" [V]	Observations /Comments
	0	**91 dBµA max**	**2 Vpp max**	**Xxxx V nom**	**Xxxx V nom**	
	50					
	100					
	200					
	400					
	600					
	800					
	1000					
	2000					
	3000					
	4000					
	5000					
	6000					
	7000					
	8000					
	9000					
	10000					
	12500					
	15000					
	17500					
	20000					
	25000					
	30000					
	35000					
	40000					
	45000					
	47500					
	48500					
	50000					

Table 8-1: Frequency Table for CS Injection on TC Line

Test Step No., Executant		Description/Comments
31		**CS Test on TDC Signal Lines (RWL1)**
31.1		SENSITIVE Mode
	OP	If not already done, switch into SENSITIVE mode according to chapter 7.1 ''C'' and confirm the mode
Date/Time		
31.2	**OP/ EMC**	Arrangement of the test equipment: 1) Arrange the test equipment according to Figure 5-6: TDC Test Adapter arrangement for CS, DB02 level". 2) I-AC injection and -monitoring on "Motor-Current-Monitor-Return" and "Torque Direction Command" (MCM/TC_RET (32) and TDC+ (17)): Fix current injection and -monitoring devices to MCM/TC_RET (32) and TDC+ (17). Monitor the injected current with an oscilloscope. 3) I-ac monitoring of "Torque Direction Comman" (TDC+ (17)): Fix current monitoring devices to TDC+ (17). Monitor the injected current with a frequency analyser. 4) Voltage "Torque-Command-Return to Structure": The voltage between TC-RET (32) and Structure (connector shell) shall be measured with a differential voltage probe and an oscilloscope. 5) Voltage "Torque-Direction-Command" (TDC+ (17) to TC-RET (32)): The TDC command CLOCKWISE shall be given by Sattelite/EGSE operational responsibilities. 6) Voltage "Torque-Command" (TC+): The TC command (500) shall be given by the SATELLITE/EGSEoperational responsibilities. 7) Voltage "Motor-Current-Monitor" (MCM (12) to MCM-RET (32)): This voltage (0,50105V) shall be recorded and checked prior and during test by SATELLITE/EGSEoperational responsibilities.
31.3	**OP/ EMC**	Setup Conditions for TC/TDC Signal lines The TC command shall be set to ''500'' leading to a read out TLM (MCM) close to 0.5105V. The TDC command shall be set to: **CLOCKWISE direction** Record and check status information and give values to the EMC team. Actual TLM values: 1) Command TC to: 500 acdcording to sect.7.1, B6 2) Command TDC to: Clockwise Remark: The plots / measurements have to be recorded in less than 15 min. If not B6 has to be started again. 3) Mot Cur. TLM "MCM to MCM-RET: ''…'' (shall be 0,5105 V)

Test Step No., Executant		Description/Comments
		Actual TLM values: 1) I-inj = 0 mArms 2) TC-RET (32) to Structure (connector shell) = xxx mVpp Osci Plot DXXX: 3) Torque-Command" (TC+ (16) to TC-RET (32)) = xxx V 4) Motor-Current-Monitor" (MCM (12) to MCM-RET (32)) = xxx V
31.4		CS testing: 1) Adjust CS test frequency. Set the test voltage Voltage "Torque-Command-Return to Structure" to **2 Vpp** by slowly increasing the induced current I-AC. 2) At the same time monitor the injected current on both, the MCM-RET line and TDC+ line with an oscilloscope. To avoid overtesting the injected current shall not exceed **tbd** mApp (**tbd** dBµA) even if the 2 Vpp test voltage cannot be reached! The **tbd** mApp limit applies only for the injected frequency. 3) At the same time monitor the current on the TDC+ line alone with a spectrum analyser. In any case the current shall not exceed **100 mApp (91 dBµA)** even if the 2 Vpp test voltage cannot be reached! The 100 mApp limit applies only for the injected spectral frequency; amplitudes of other signal parts of the emission spectrum are not relevant. 4) Give test status information to the SATELLITE/EGSEoperational responsibilities.
31.5	**OP**	Susceptibility evaluation: - The RWL shall not exhibit any failures malfunctions or unintended responses when submitted to the injected signals. - The motor current TLM shall keep inside the range +/-12.5 mV around the value without noise injection when submitted to the injected signals. 5) Failure status and MCM voltage shall be monitored prior and during test by SATELLITE/EGSEoperational responsibilities. 6) Give susceptibility status information to the EMC test team.
		Susceptibility evaluation: - The motor current TLM (MCM (12) to MCM-RET (32)) shall keep inside the range +/-12.5 mV around the value without noise injection when submitted to the injected signals. 7) The motor current (MCM) voltage shall be monitored prior and during test with a DMM be the EMC team. 8) Fill in the table **Error! Reference source not found.**below for each tested frequency and check the susceptibility criteria. 9) If susceptibility can be detected, reduce the induced voltage level to find the threshold value of susceptibility. 10) If no susceptibility can be detected, go on testing with the next frequency.
		Plot 0xx:

Test Step No., Executant	Description/Comments
	Photos:

Time	f [kHz]	I-inj, TC-RET and TDC+ [App]	I-TDC+, [dBµA]	Voltage "TC-RET to STRUC-TURE" [Vpp]	"TDC to TC-RET" [V]	Mot Cur. TLM "MCM to MCM-RET" [V]	Observati-ons/Comments
	0	**xxx App max**	**91 dBµA max**	**2 Vpp max**	**Xxxx V nom**	**Xxxx V nom**	
	50						
	100						
	200						
	400						
	600						
	800						
	1000						
	2000						
	3000						
	4000						
	5000						
	6000						
	7000						
	8000						
	9000						
	10000						
	12500						
	15000						
	17500						
	20000						
	25000						
	30000						
	35000						
	40000						
	45000						
	47500						
	48500						
	50000						

Table 8-2: Frequency Table for CS Injection on TDC Line

8.10 De-Installation of Test Adapters from Power- and Signal Lines

Test Step No/ Info		Description/Comments
32		**DE-INSTALL TEST ADAPTER ON POWER- AND SIGNAL LINES LINES** Use information of paragraph 6.4 for deinstallation.

9 SUMMARY SHEETS

9.1 Procedure Variation Summary

	Test Change	Curr. No.: Date: Page 1 of 1	
Test designation Herschel PFM EMC CE Test	Test Procedure **HP-2-ASED-TP-0155**	Issue 1, dated 30.11.07	Rev. 1
Step 2.1, Step 3.1, Step 4.1, Step 5.1, Step 6.1, Step 7.1, Step 8.1, Step 9.1, Step 10.1, Step 11.1.	Voltage not measured as spacecraft switched OFF		
Step 12.3	The voltage between the Torque Cmd Return line (Pin 32) and the structure (DB02 Connector bracket) shall be measured and NOT the differential voltage between Torque Cmd supply and return.		
Step 13.3	The voltage between the Torque Direction Cmd Return line (Pin 37) and the structure (DB02 Connector bracket) shall be measured, and NOT the differential voltage between Torque Direction Cmd supply and return.		
Step 21.6	Test Step deleted. Test was not performed correctly. During PTR it was agreed by all parties that this step has not to be repeated since DM current in step 21.4 and DM voltage in step 21.5 showed low levels.		
Sect. 8.9	The tests of sect 8.9 'Conducted Susceptibility Tests on Signal Lines' were not performed. According the EMC test requirement spec H-P-2-ASP-TS-0819, sect. 7.6.1, the object of this test is to check if relevant CE tests are not successful. All CE test were successful.		
Prepared by: Clemens Kalde	Resp. Test Leader M. Hopfgarten	Project Engineer	
PA/QA			

Table 9-1: Procedure Variation Sheet

9.2 Non Conformance Report (NCR) Summary

NCR - No.	NCR - Title	Date	Open Closed	PA sig.
	No NCR was created!			

Table 9-2: Non- Conformance Record Sheet

9.3 Sign-off Sheet

	Date	Signature
Test Manager	5.12.07	
Operator	5.12.07	
PA Responsible	05.12.07	
ESA Representative		

END OF DOCUMENT

	Name	Dep./Comp.		Name	Dep./Comp.
	Alberti von Mathias Dr.	ASG23		Schmidt Thomas	AED15
	Baldock Richard	FAE12		Schuler Günter	ASA42
	Barlage Bernhard	AED13		Schweickert Gunn	ASG23
	Bayer Thomas	ASA42	x	Sonn Nico	ASG51
	Brune Holger	ASA45		Steininger Eric	AED32
	Edelhoff Dirk	AED2	x	Stritter Rene	AED11
	Fehringer Alexander	ASG13		Suess Rudi	OTN/ASA44
x	Fricke Wolfgang Dr.	AED 65	x	Theunissen Martijn	DSSA
	Geiger Hermann	ASA42	x	Vascotto Riccardo	AED11
	Grasl Andreas	OTN/ASA44		Wagner Klaus	ASG23
	Grasshoff Brigitte	AET12	x	Wietbrock Walter	AET12
x	Hamer Simon	Terma		Wöhler Hans	ASG23
	Hendrikse Jeffrey	HE Space		Wössner Ulrich	ASE252
	Hendry David	Terma		Zumstein Armin	ASQ42
	Hengstler Reinhold	ASA42			
	Hinger Jürgen	ASG23			
x	Hohn Rüdiger	AED65			
	Hölzle Edgar Dr.	AED32			
x	Hopfgarten Michael				
	Huber Johann	ASA42			
	Hund Walter	ASE252			
x	Idler Siegmund	AED312			
	Ivády von András	FAE12			
	Jahn Gerd Dr.	ASG23			
x	Kalde Clemens	ASM2			
	Kameter Rudolf	OTN/ASA42	x	ESA/ESTEC	ESA
	Kettner Bernhard	AET42	x	Thales Alenia Space Cannes	TAS-F
	Knoblauch August	AET32	x	Thales Alenia Space Torino	TAS-I
x	Koelle Markus	ASA43			
x	Koppe Axel	AED312		**Instruments:**	
x	Kroeker Jürgen	AED65	x	MPE (PACS)	MPE
x	La Gioia Valentina	Terma	x	RAL (SPIRE)	RAL
	Lang Jürgen	ASE252	x	SRON (HIFI)	SRON
	Langenstein Rolf	AED15			
	Langfermann Michael	ASA41			
	Martin Olivier	ASA43		**Subcontractors:**	
x	Maukisch Jan	ASA43		Austrian Aerospace	AAE
x	Much Christoph	ASA43		Austrian Aerospace	AAEM
	Müller Jörg	ASA42		BOC Edwards	BOCE
x	Müller Martin	ASA43		Dutch Space Solar Arrays	DSSA
	Peltz Heinz-Willi	ASG13		EADS Astrium Sub-Subsyst. &	ASSE
	Pietroboni Karin	AED65		EADS CASA Espacio	CASA
	Platzer Wilhelm	AED2		EADS CASA Espacio	ECAS
	Reichle Konrad	ASA42		European Test Services	ETS
	Runge Axel	OTN/ASA44		Patria New Technologies Oy	PANT
	Sauer Maximilian Dr.	AED65		SENER Ingenieria SA	SEN
x	Schink Dietmar	AED32		Thales Alenia Space, Antwerp	TAS-ETCA

Annex 2

Test Result Frequency Domain Plots, Time Domain Plots and Photos of Test Set-up

Content:

Plots in frequency and time domain and photos of the test set up.

These test results were already distributed as files on a CD.

Annex 1

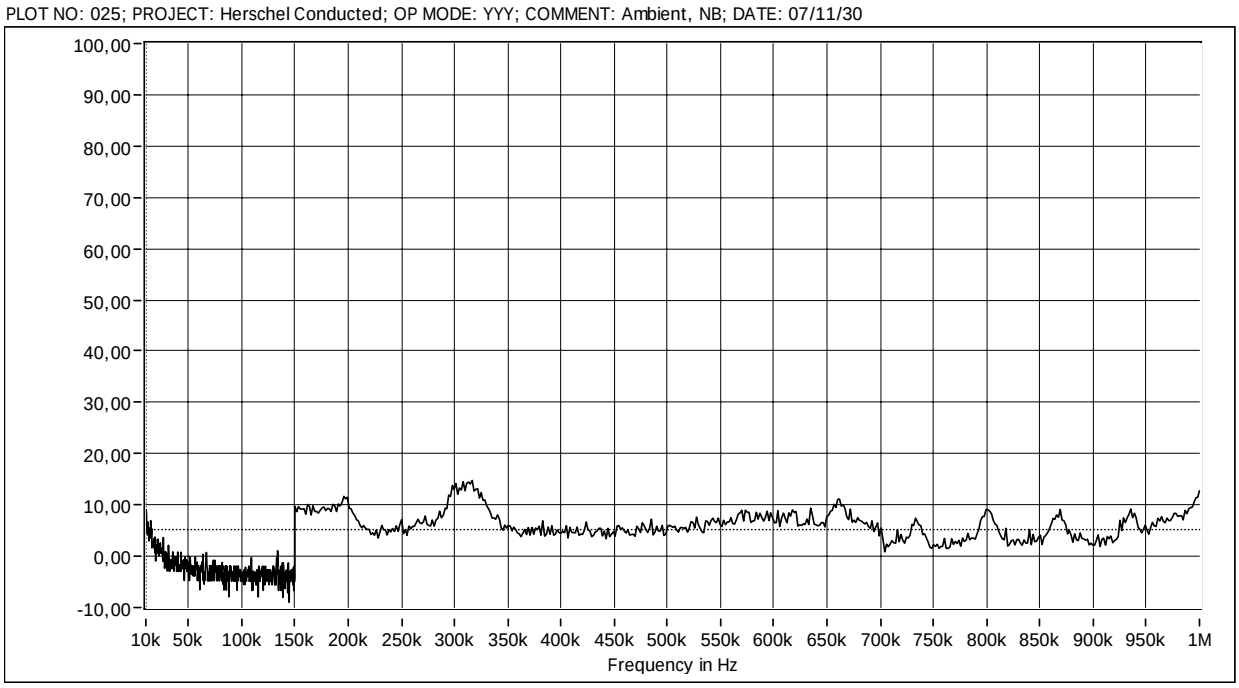
Content: Test Result Plots and Photos

One directory for each test day, each containing files of frequency domain plots (FD Plots), time domain plots (TD Plots) and photos of the test set-up.

Directory names:

- Test 30.11.07

Plots:



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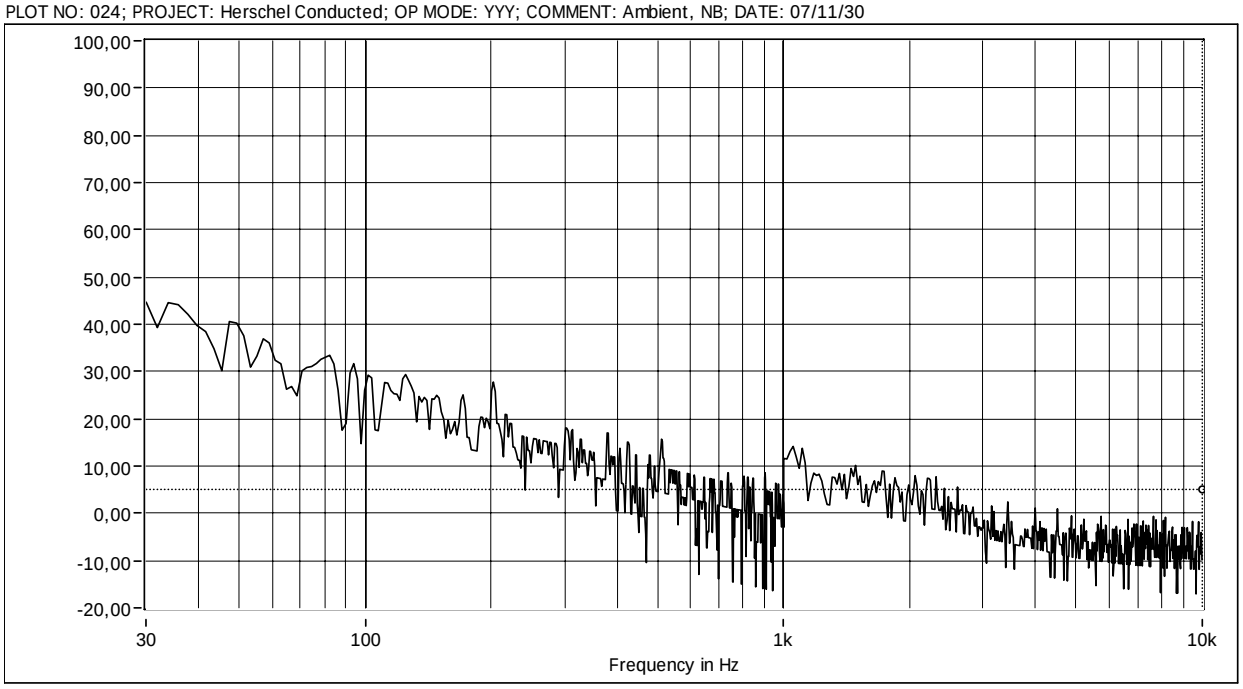

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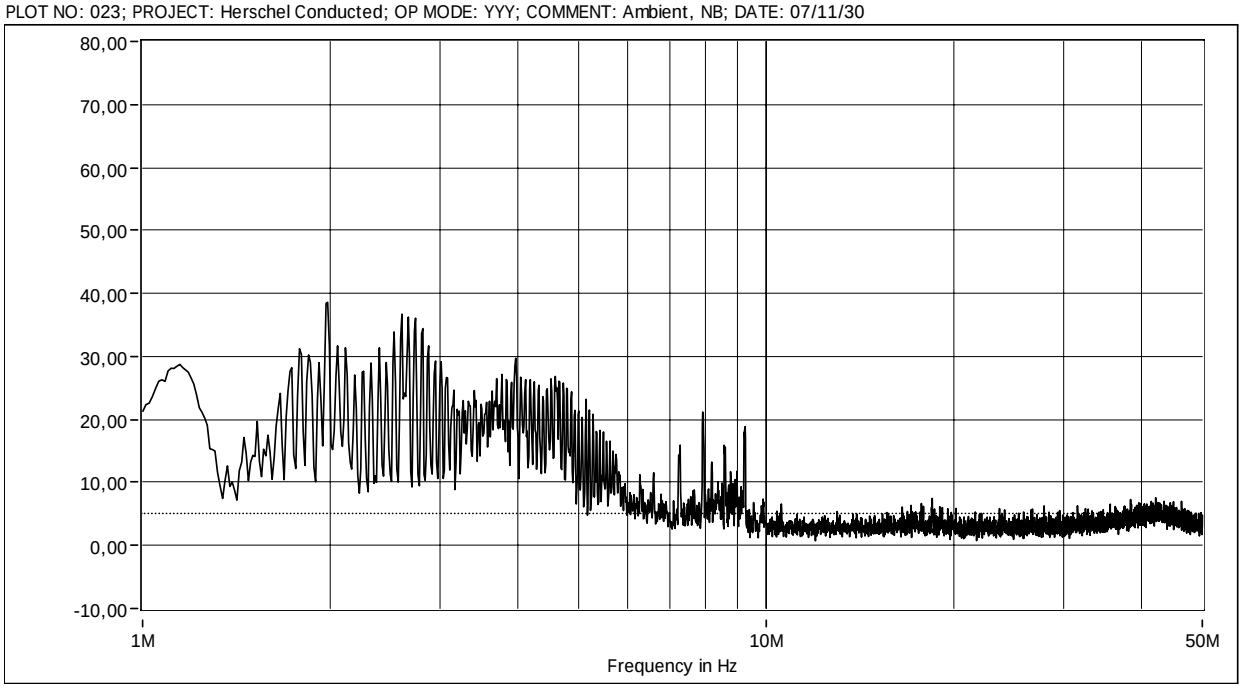

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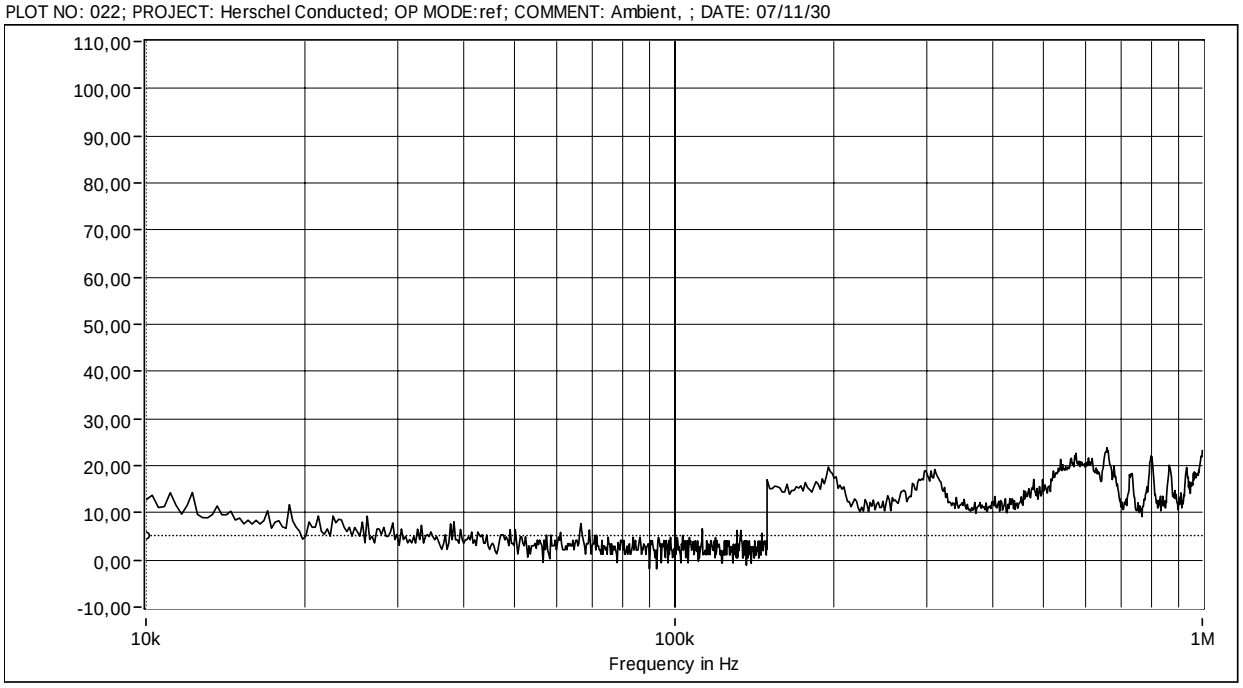

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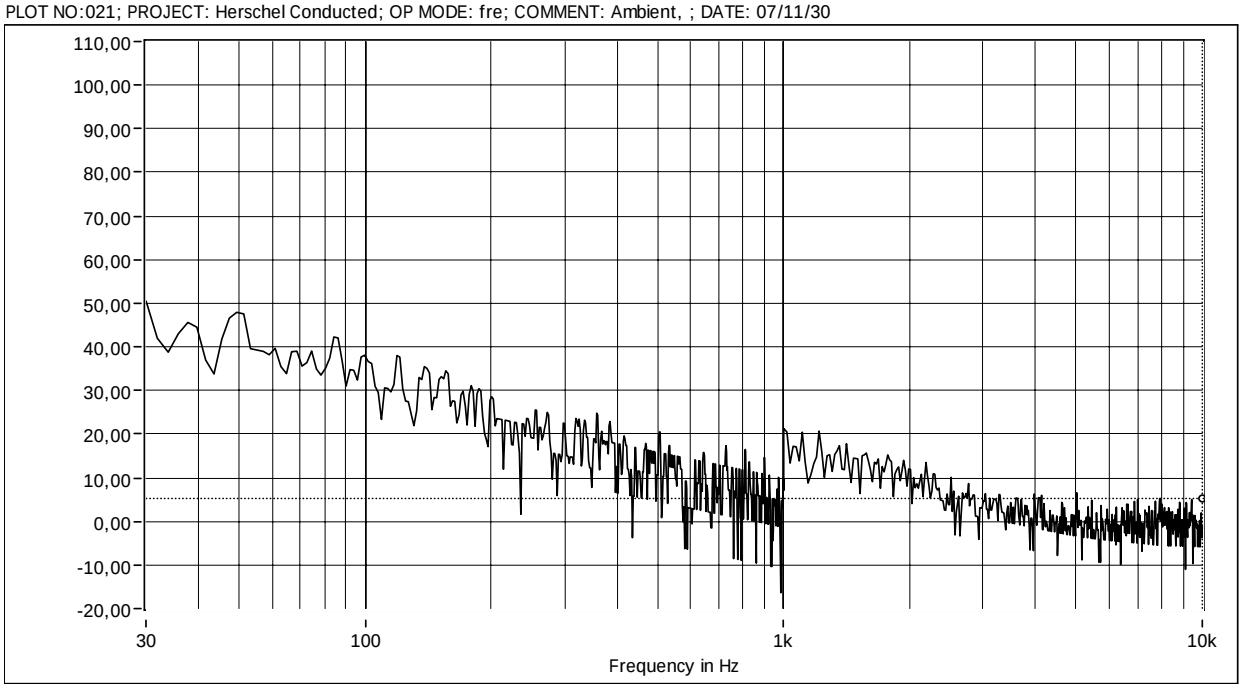

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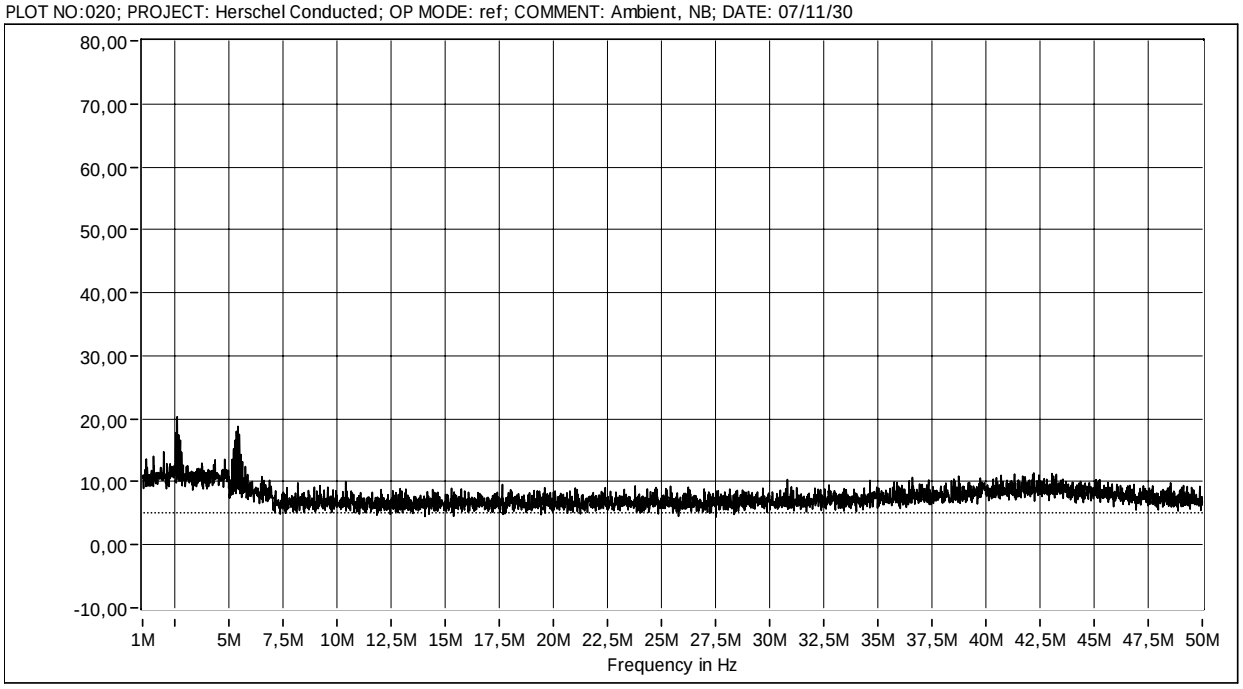

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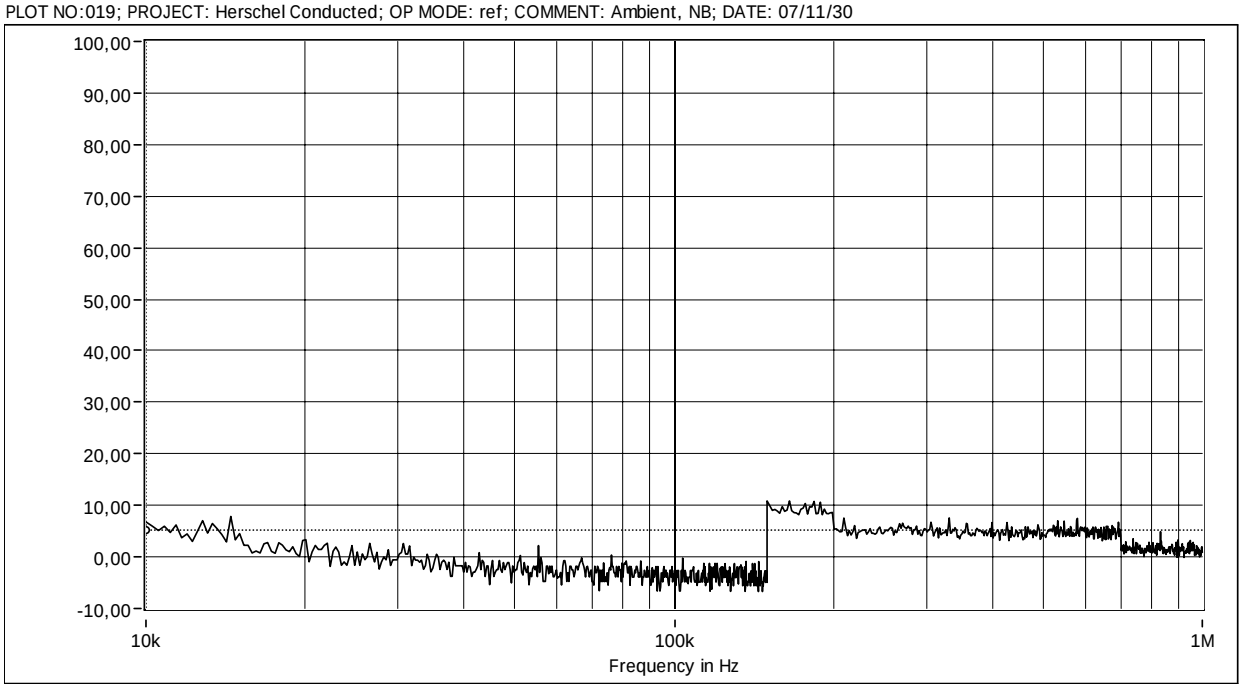

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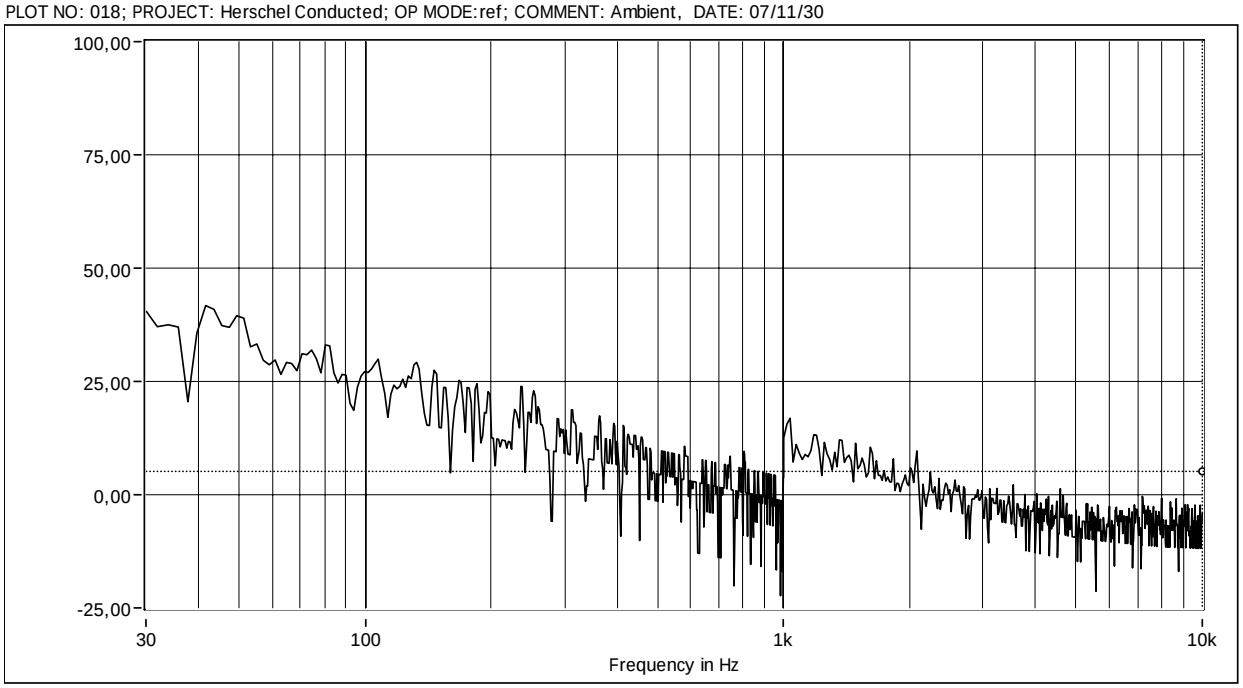

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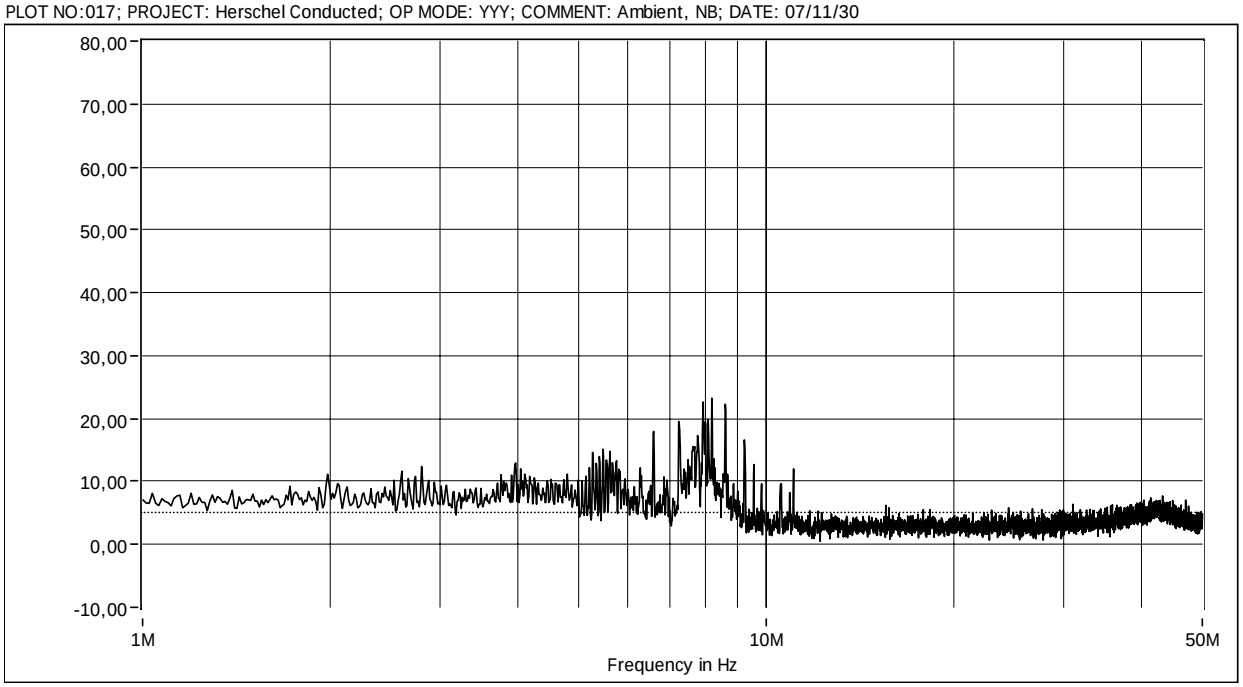

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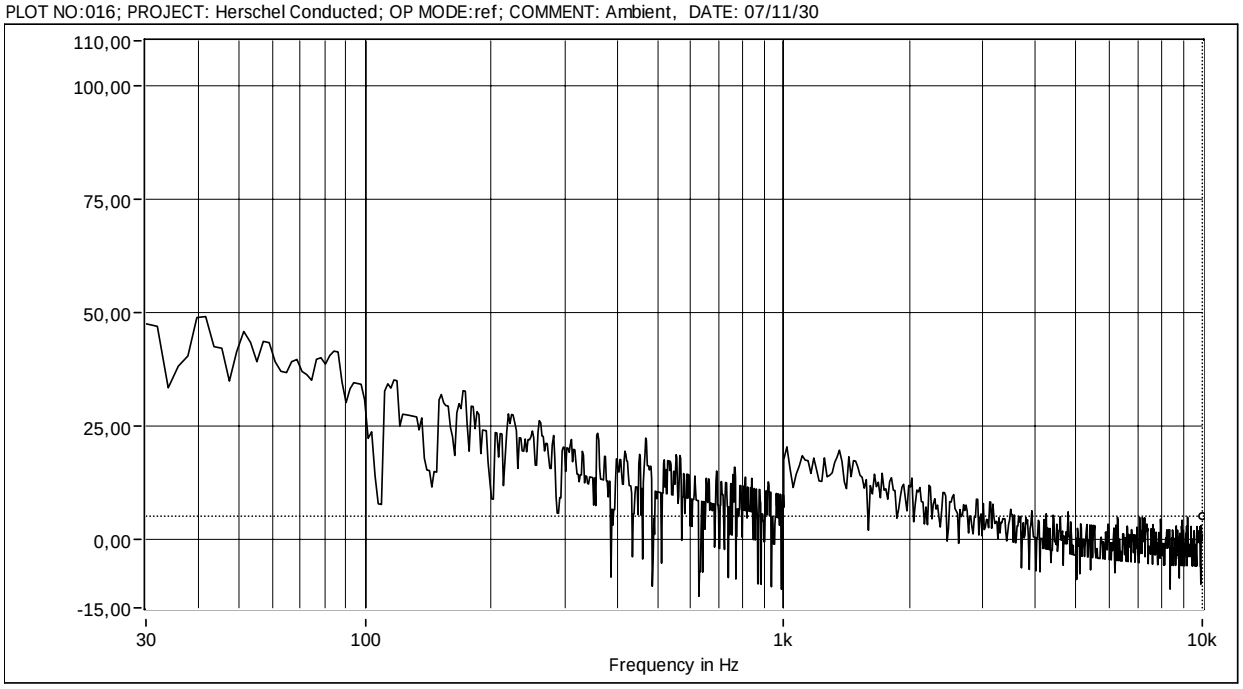

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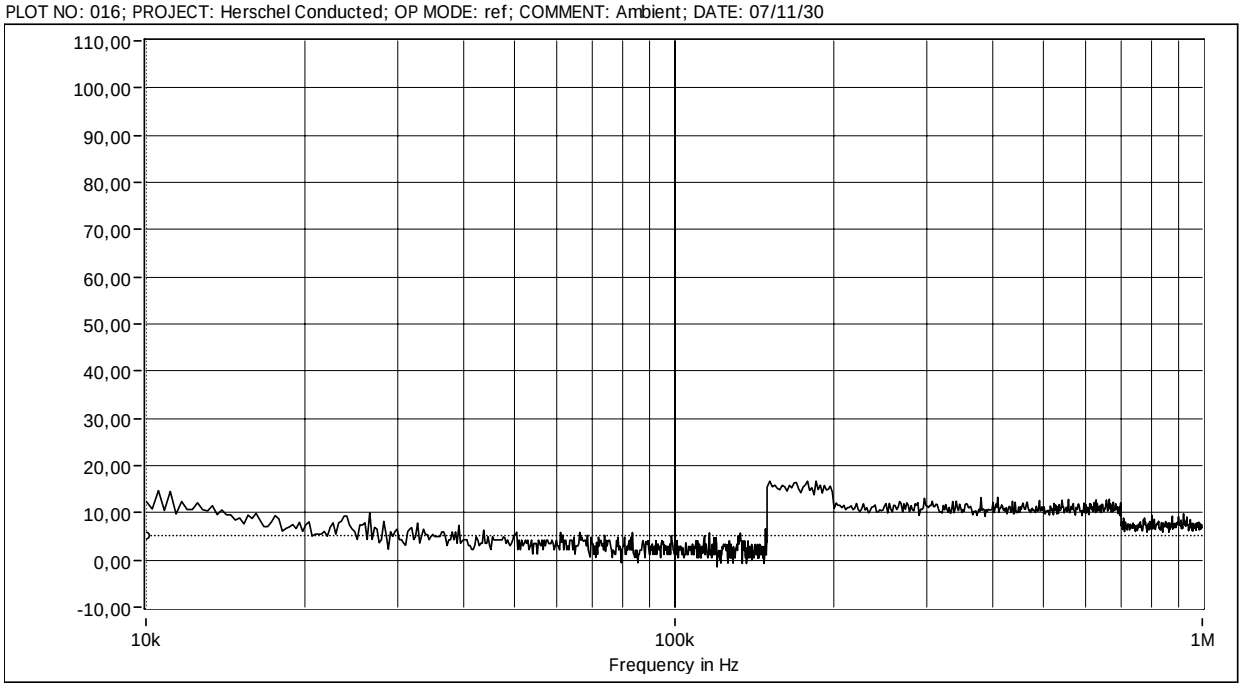

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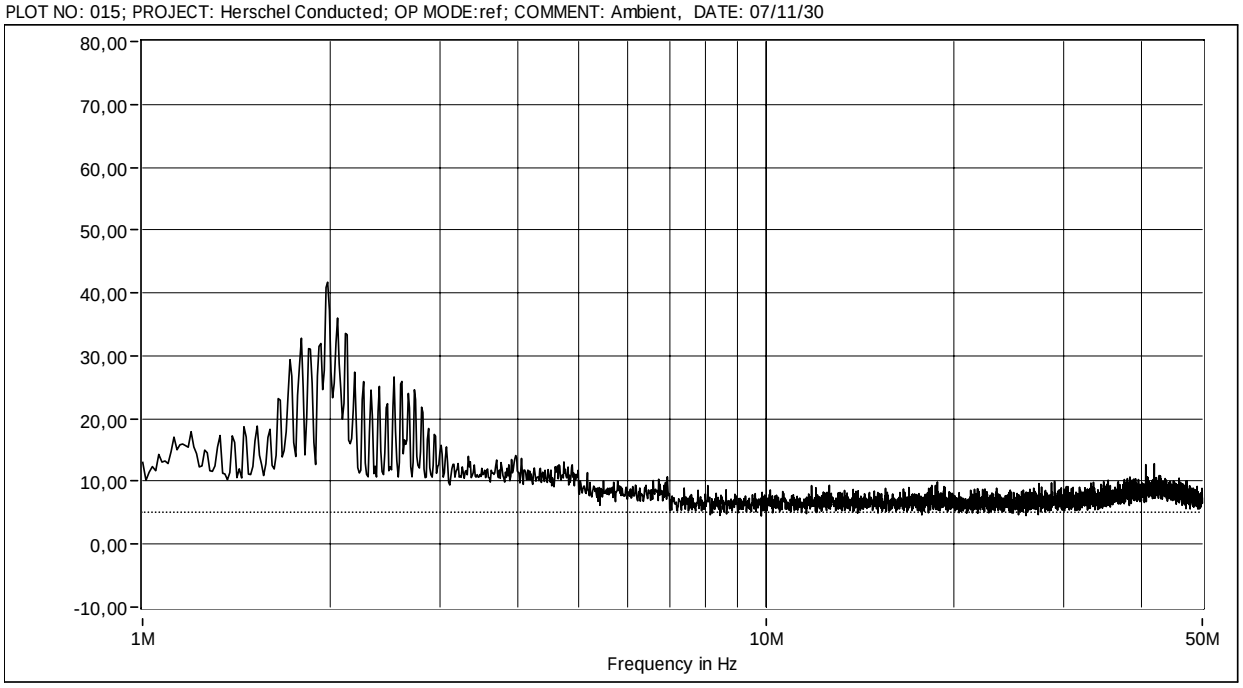

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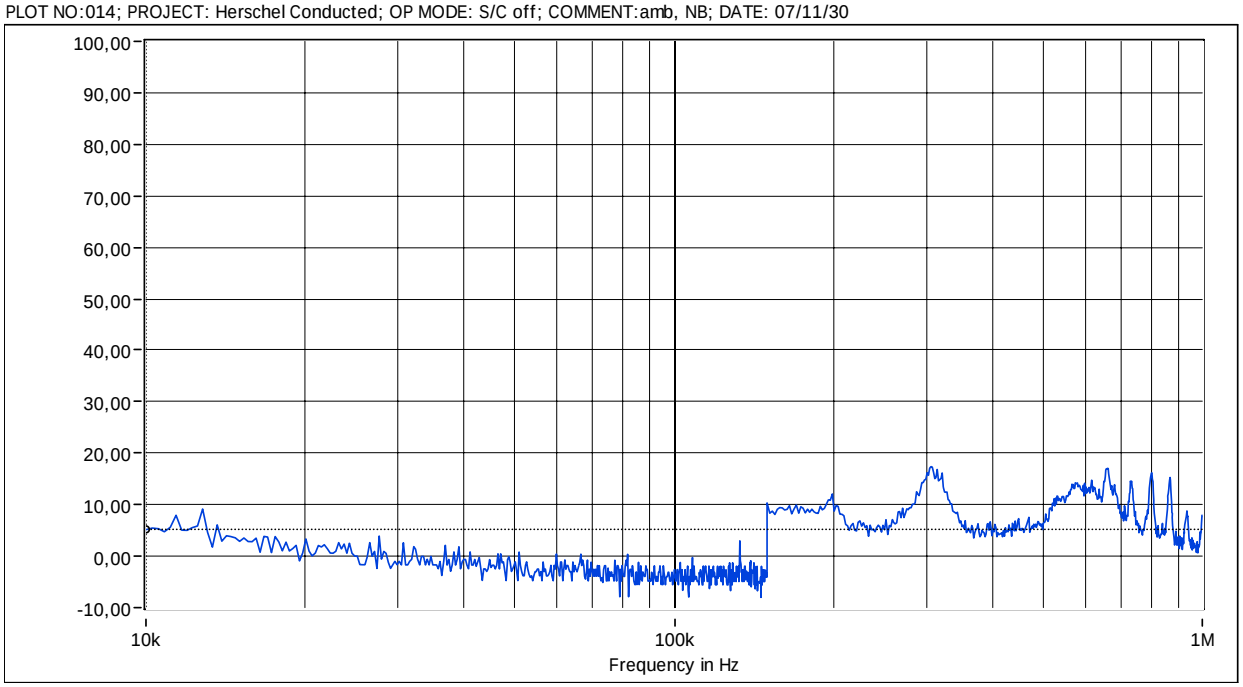

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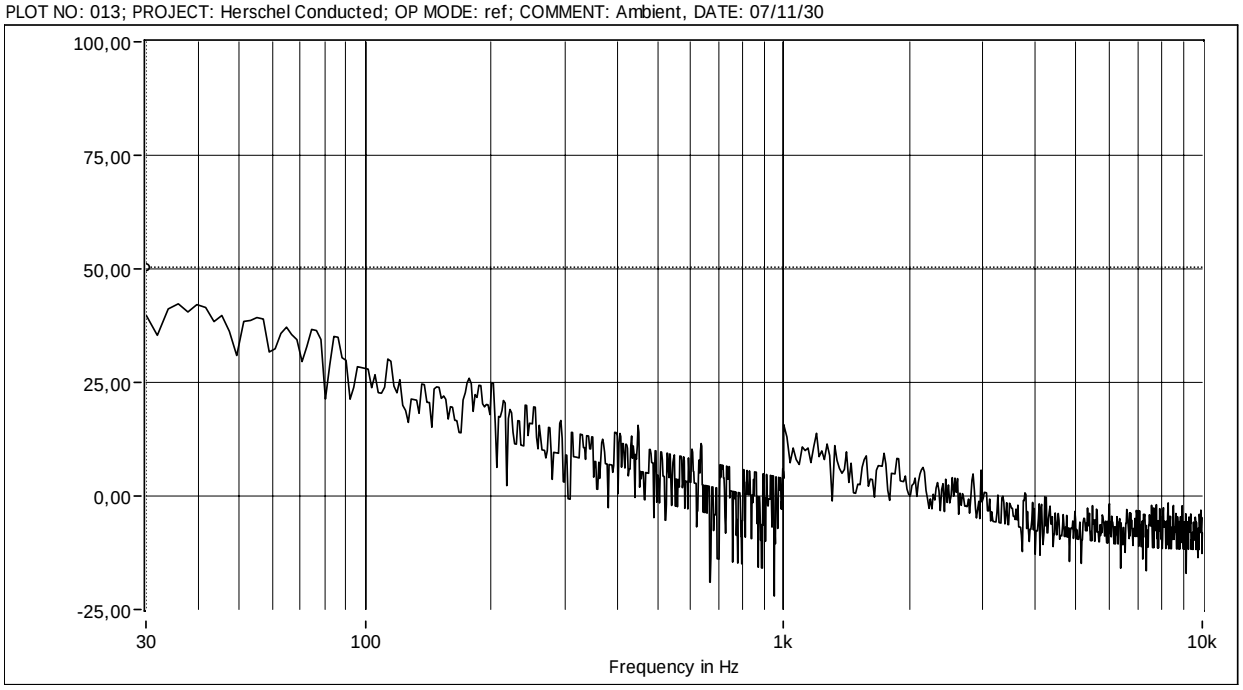

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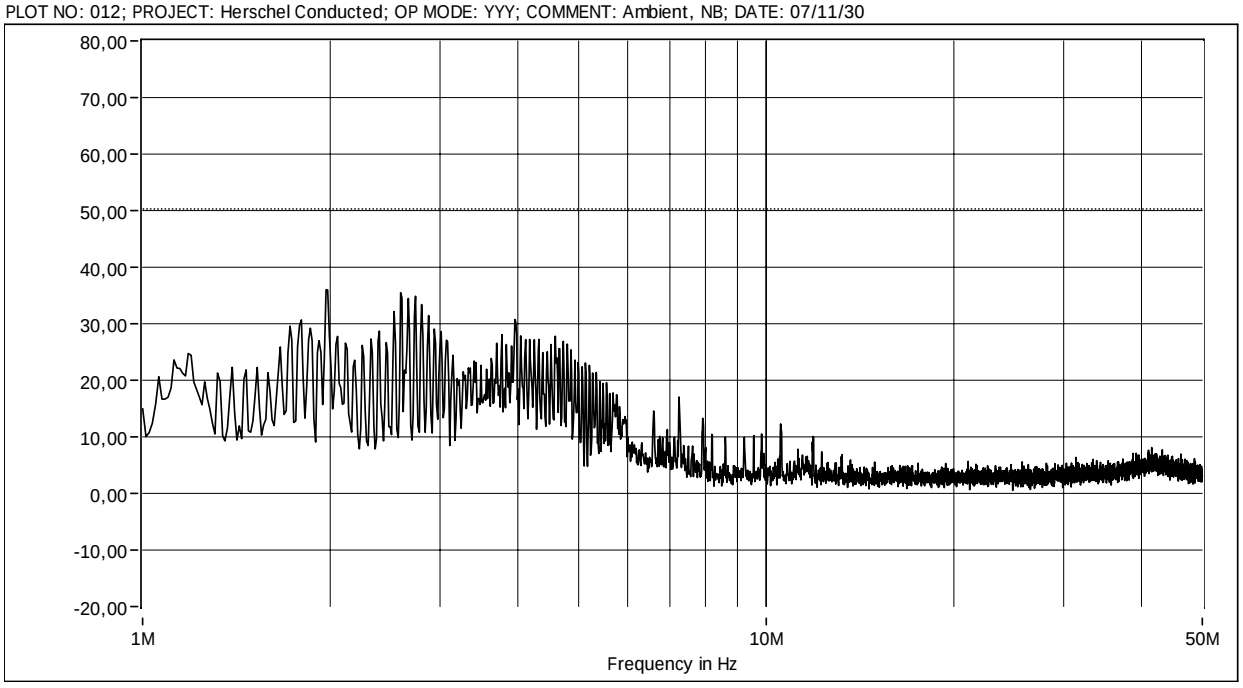

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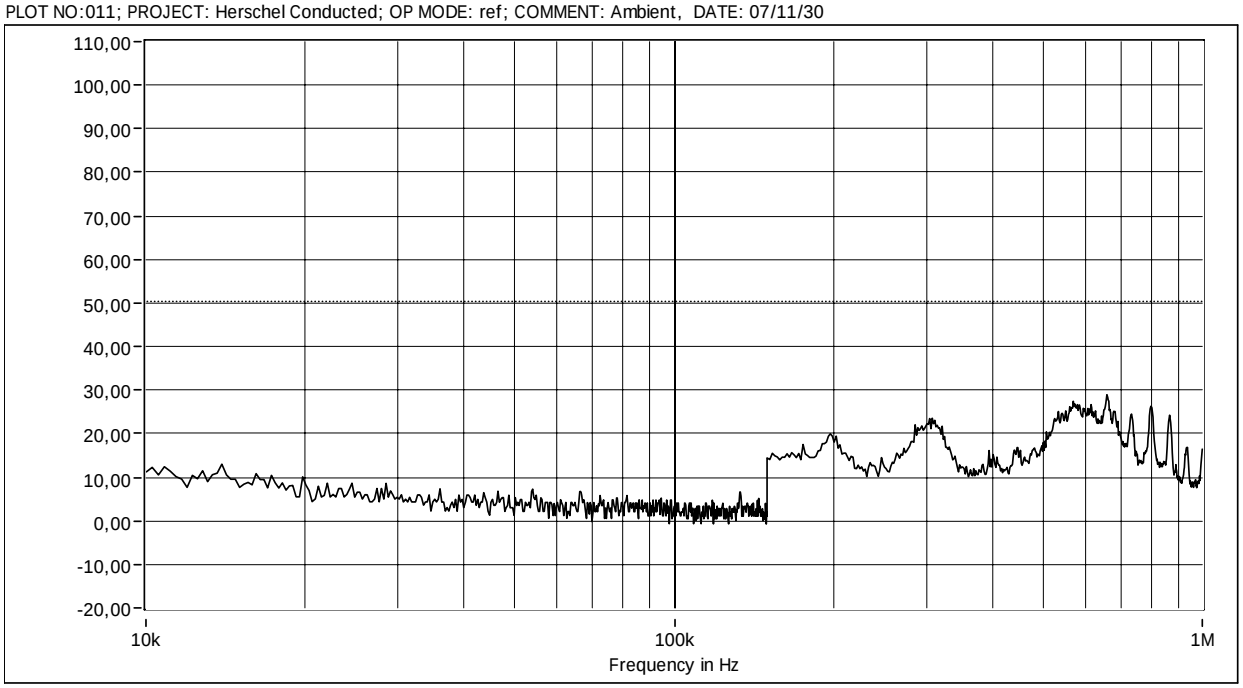

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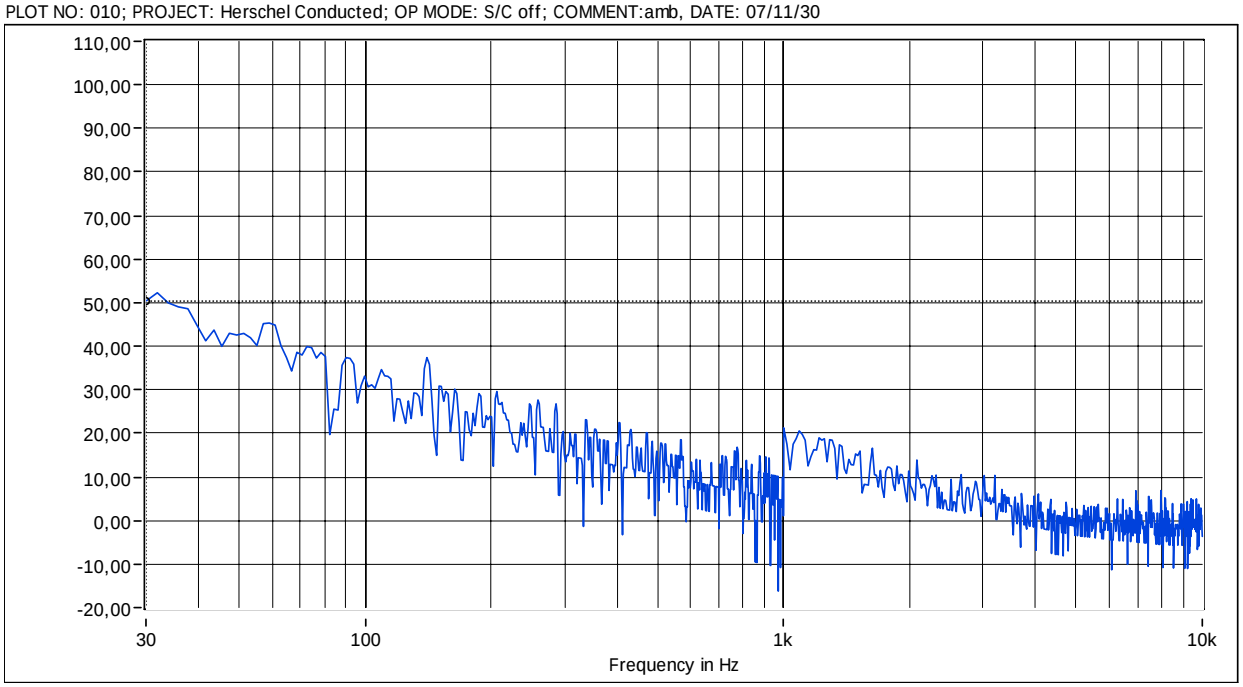
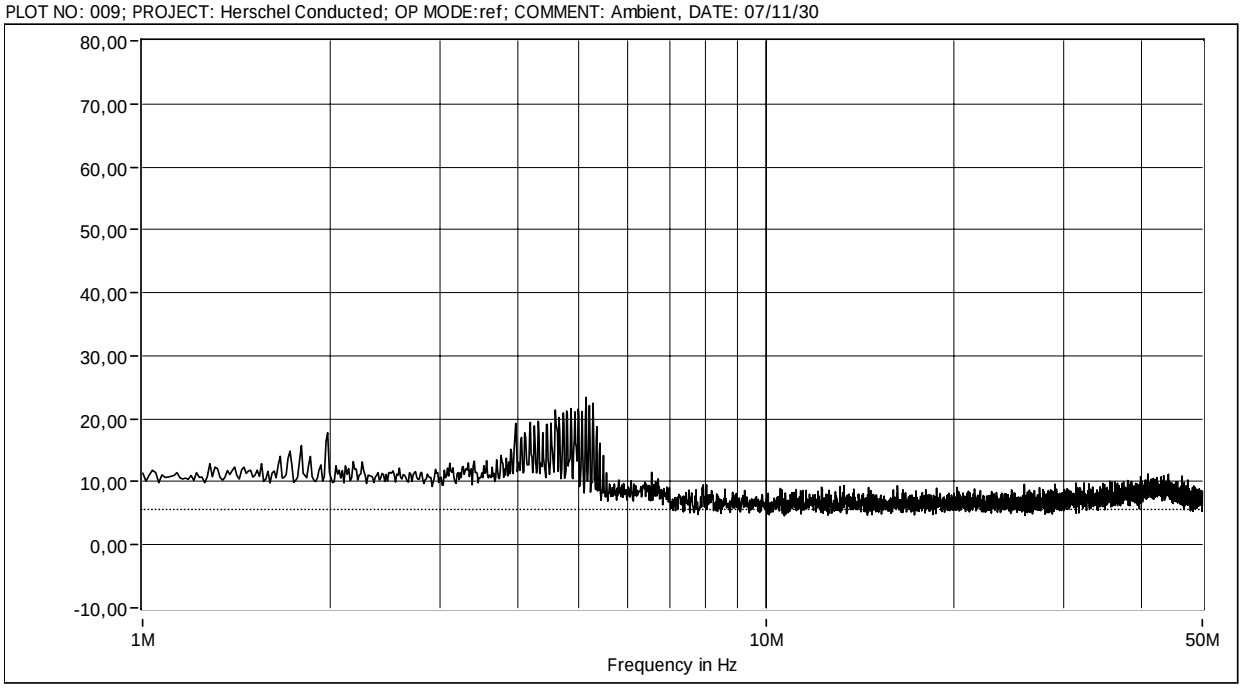
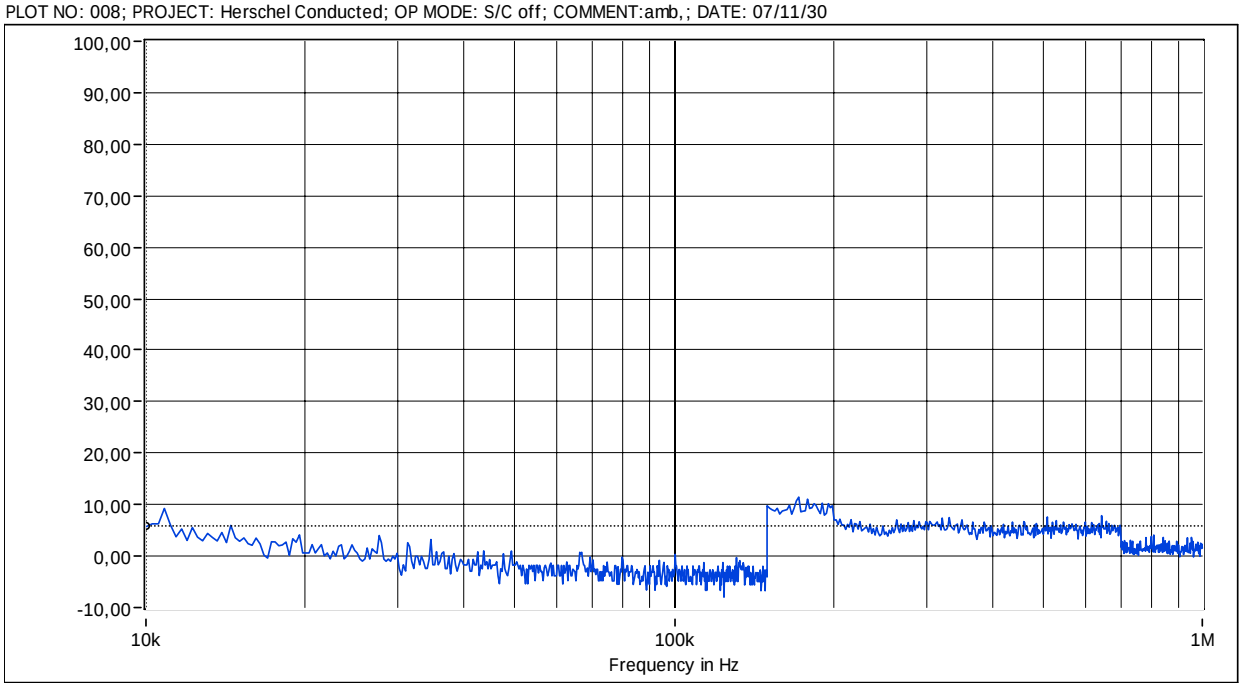


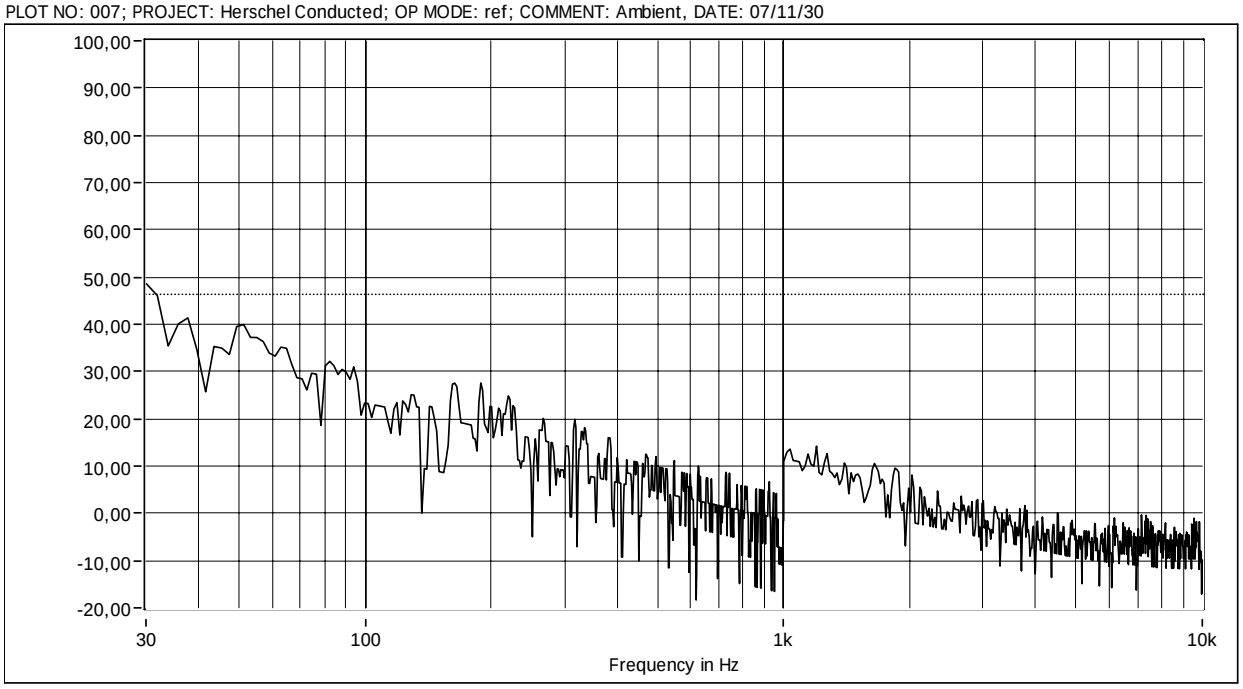
PLOT NO: 009; PROJECT: Herschel Conducted; OP MODE:ref; COMMENT: Ambient, DATE: 07/11/30



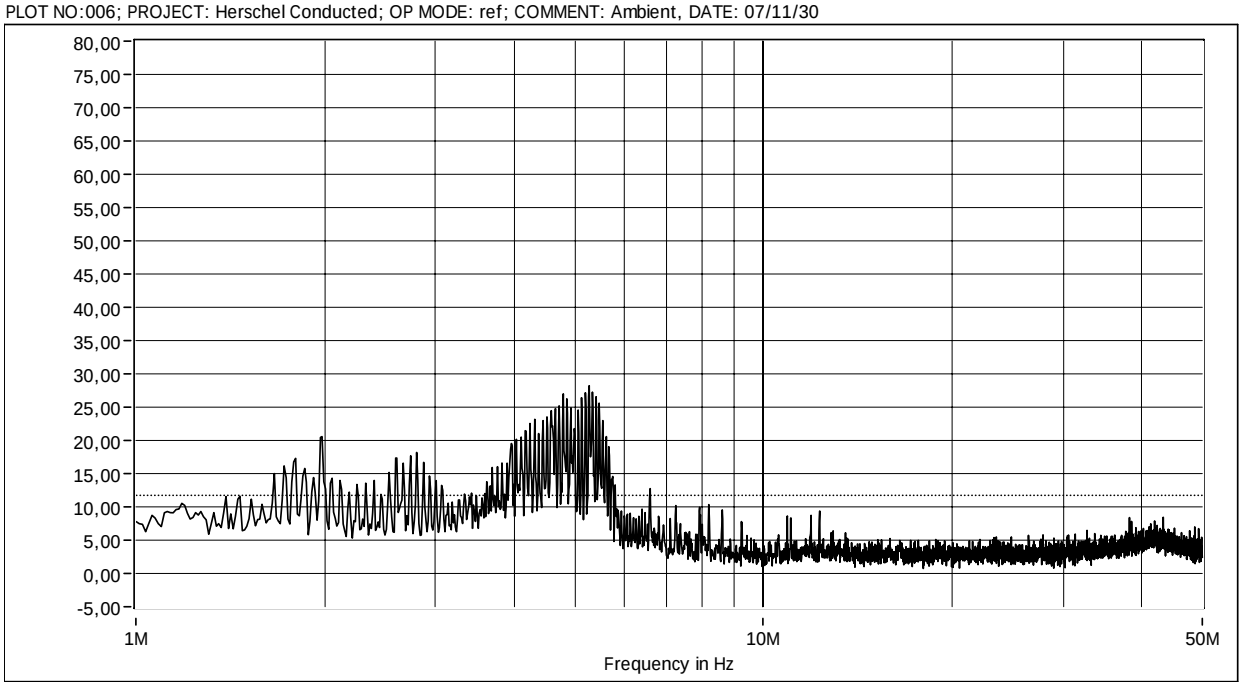
PLOT NO: 008; PROJECT: Herschel Conducted; OP MODE: S/C off; COMMENT:amb,; DATE: 07/11/30



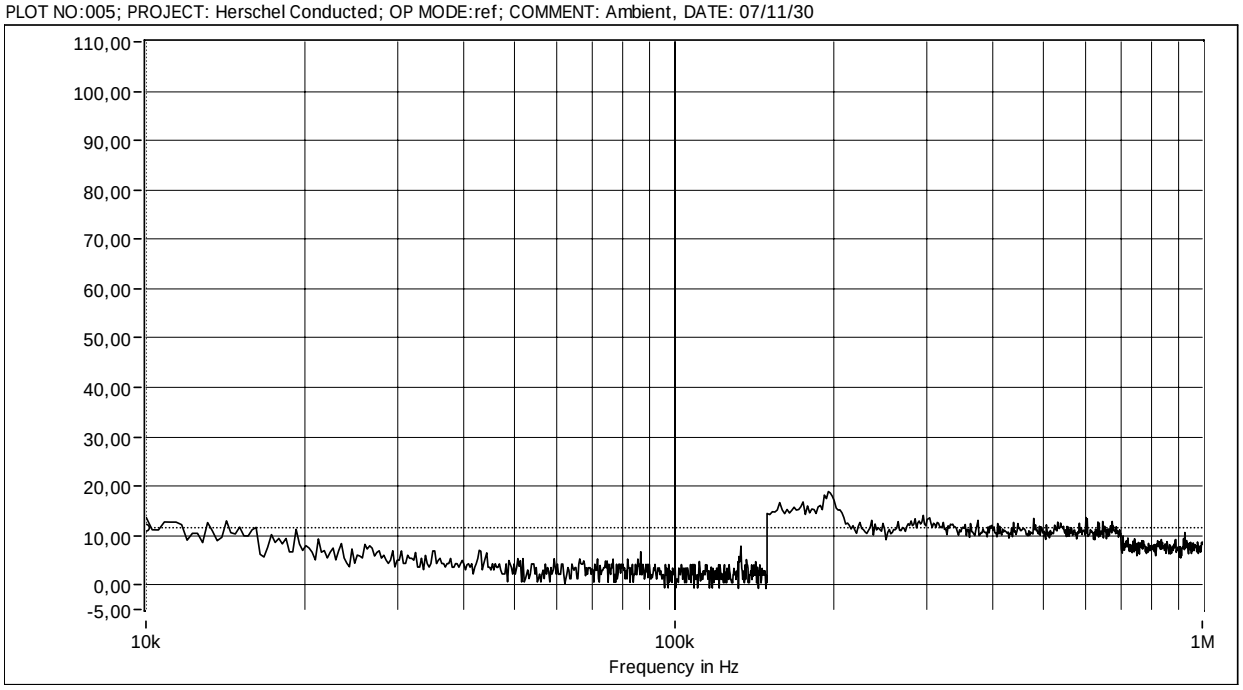
PLOT NO: 007; PROJECT: Herschel Conducted; OP MODE: ref; COMMENT: Ambient, DATE: 07/11/30



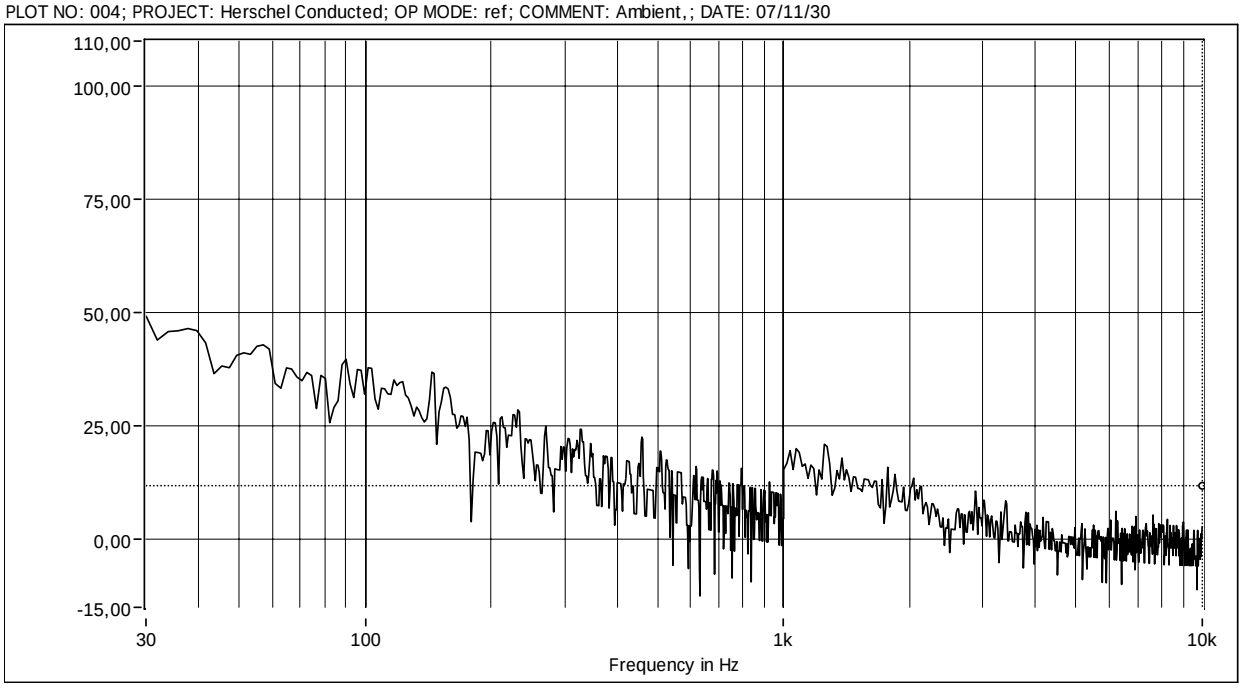
PLOT NO:006; PROJECT: Herschel Conducted; OP MODE: ref; COMMENT: Ambient, DATE: 07/11/30



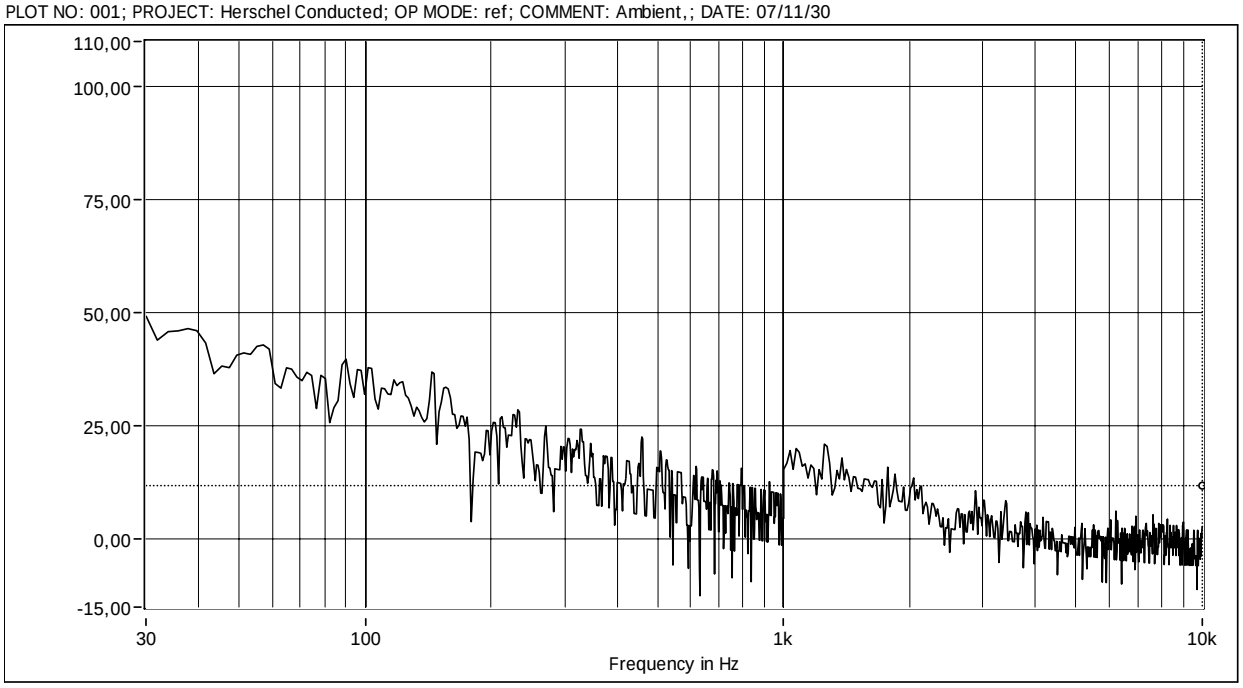
PLOT NO:005; PROJECT: Herschel Conducted; OP MODE:ref; COMMENT: Ambient, DATE: 07/11/30



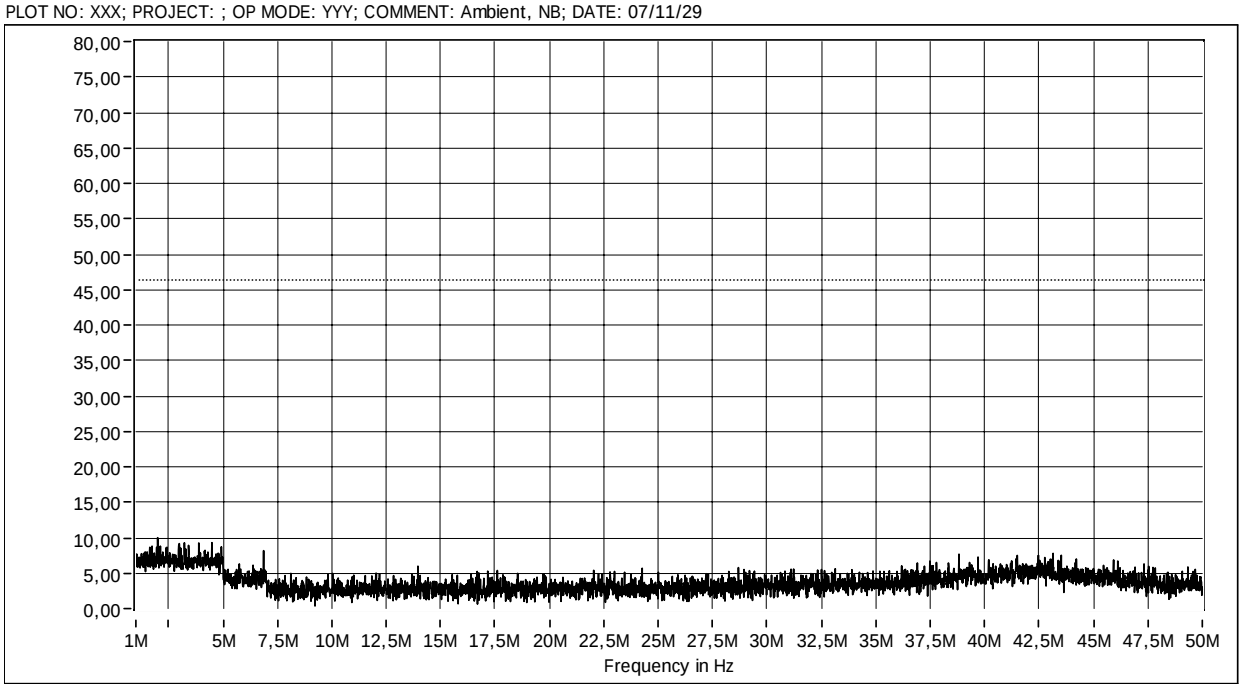
PLOT NO: 004; PROJECT: Herschel Conducted; OP MODE: ref; COMMENT: Ambient,; DATE: 07/11/30



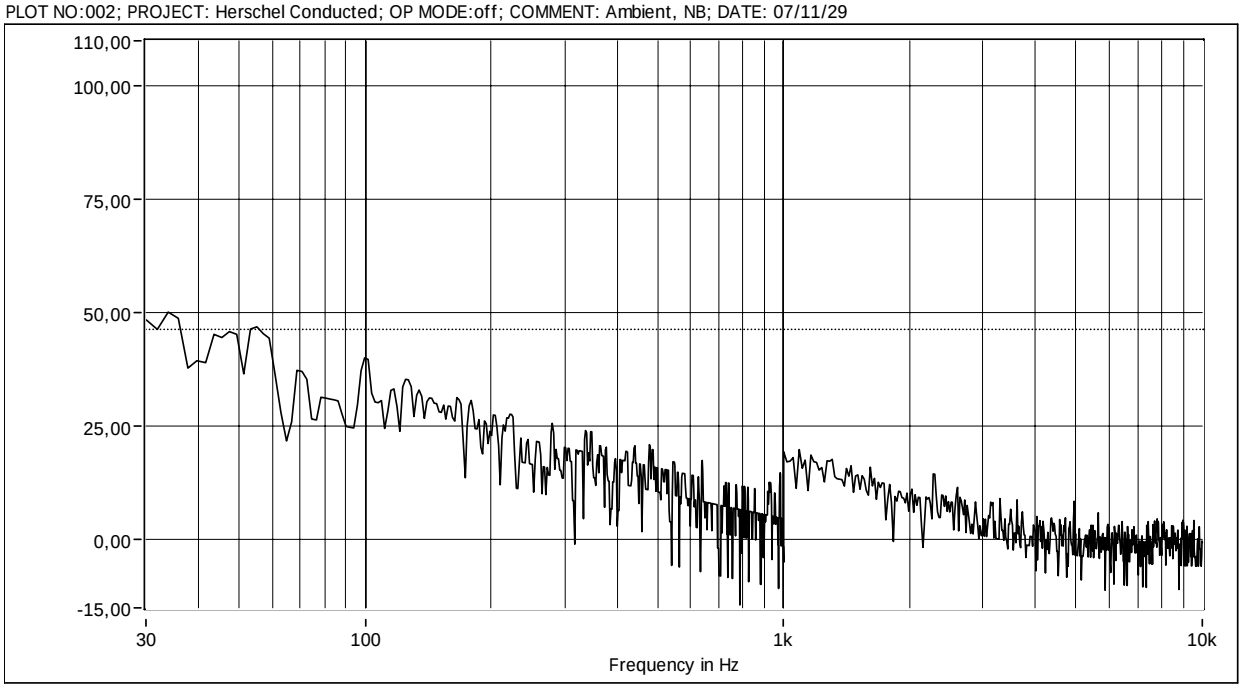
PLOT NO: 001; PROJECT: Herschel Conducted; OP MODE: ref; COMMENT: Ambient,; DATE: 07/11/30



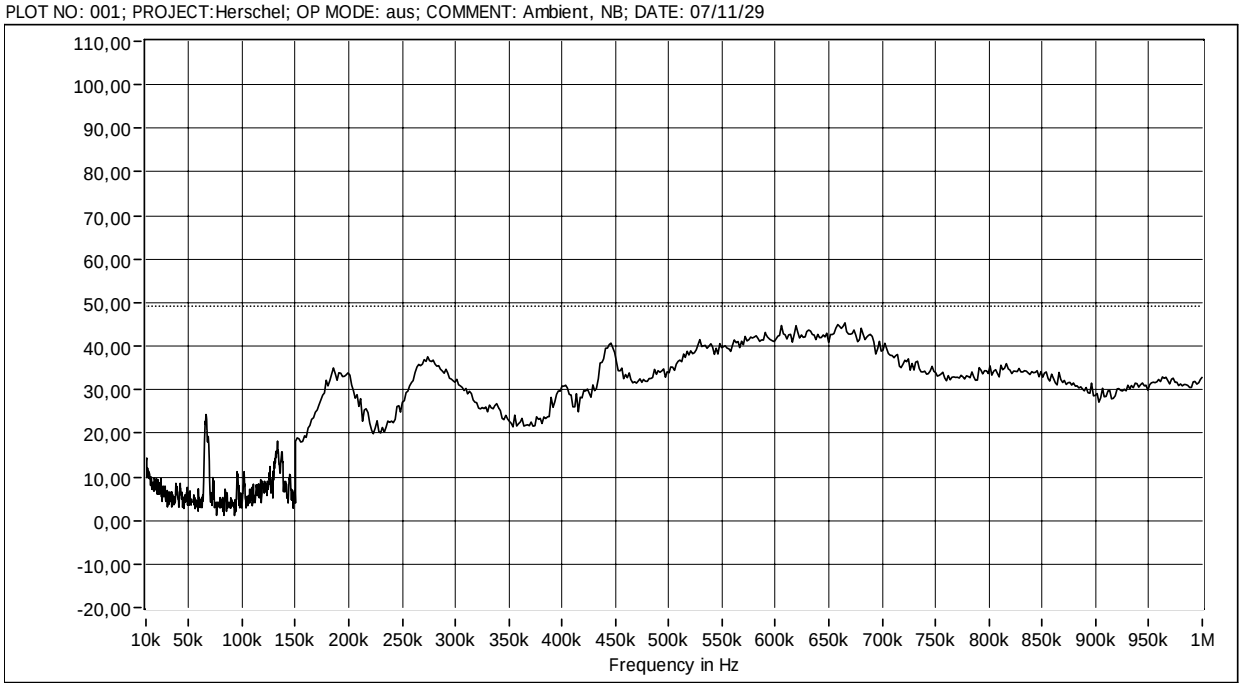
PLOT NO: XXX; PROJECT: ; OP MODE: YYY; COMMENT: Ambient, NB; DATE: 07/11/29



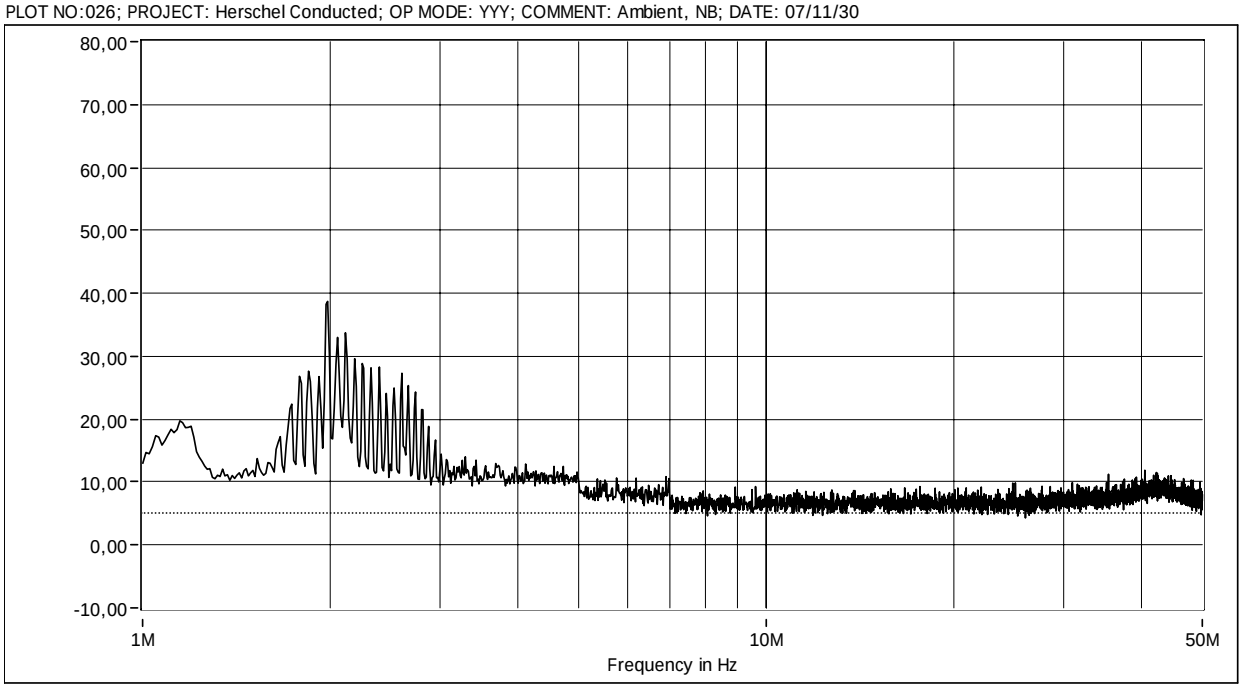
PLOT NO:002; PROJECT: Herschel Conducted; OP MODE:off; COMMENT: Ambient, NB; DATE: 07/11/29

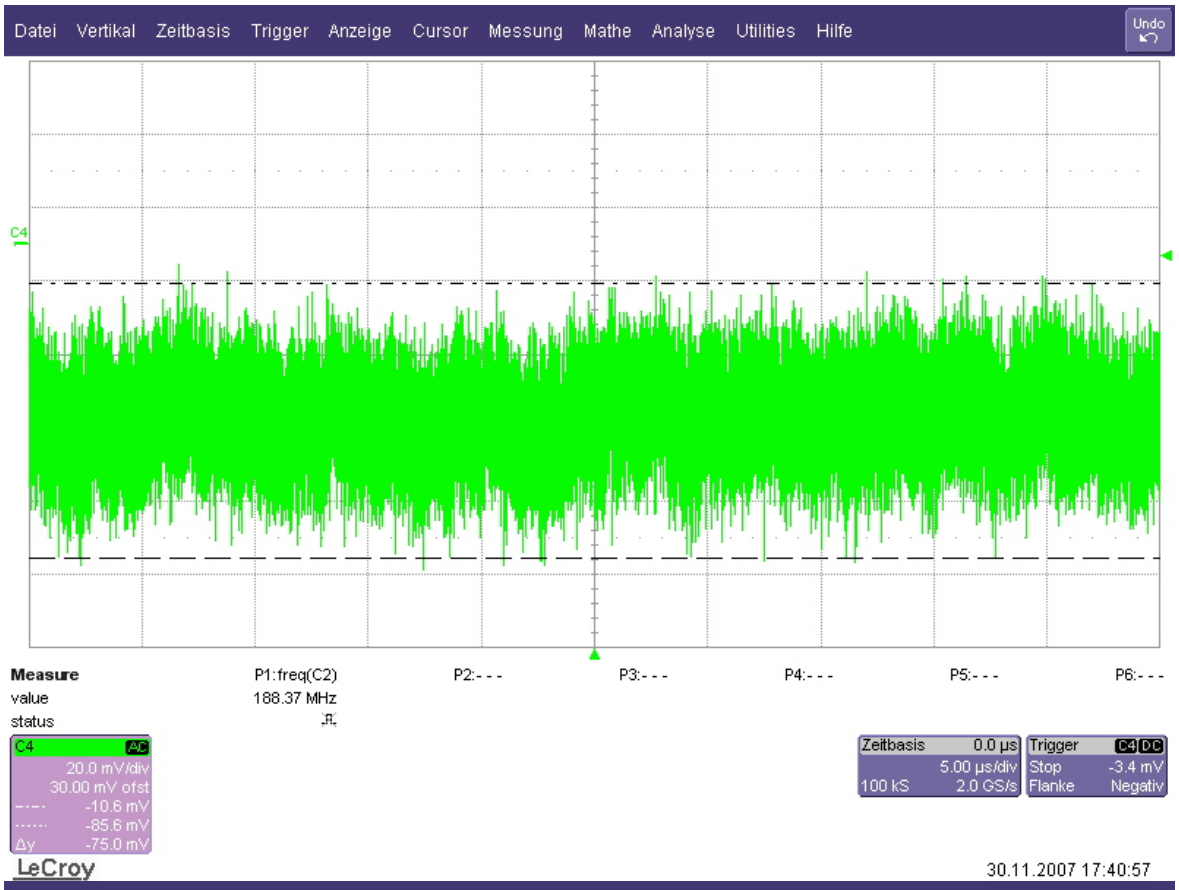


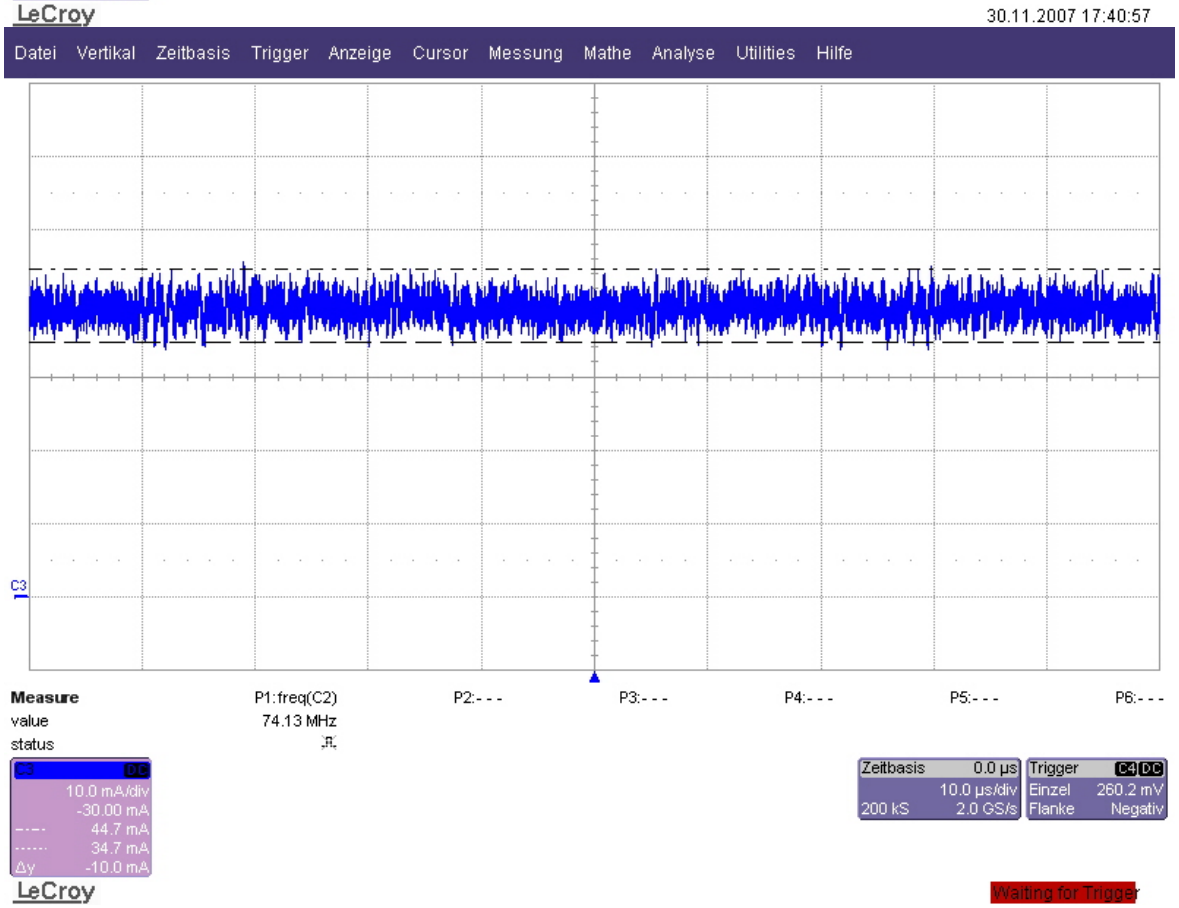
PLOT NO: 001; PROJECT:Herschel; OP MODE: aus; COMMENT: Ambient, NB; DATE: 07/11/29

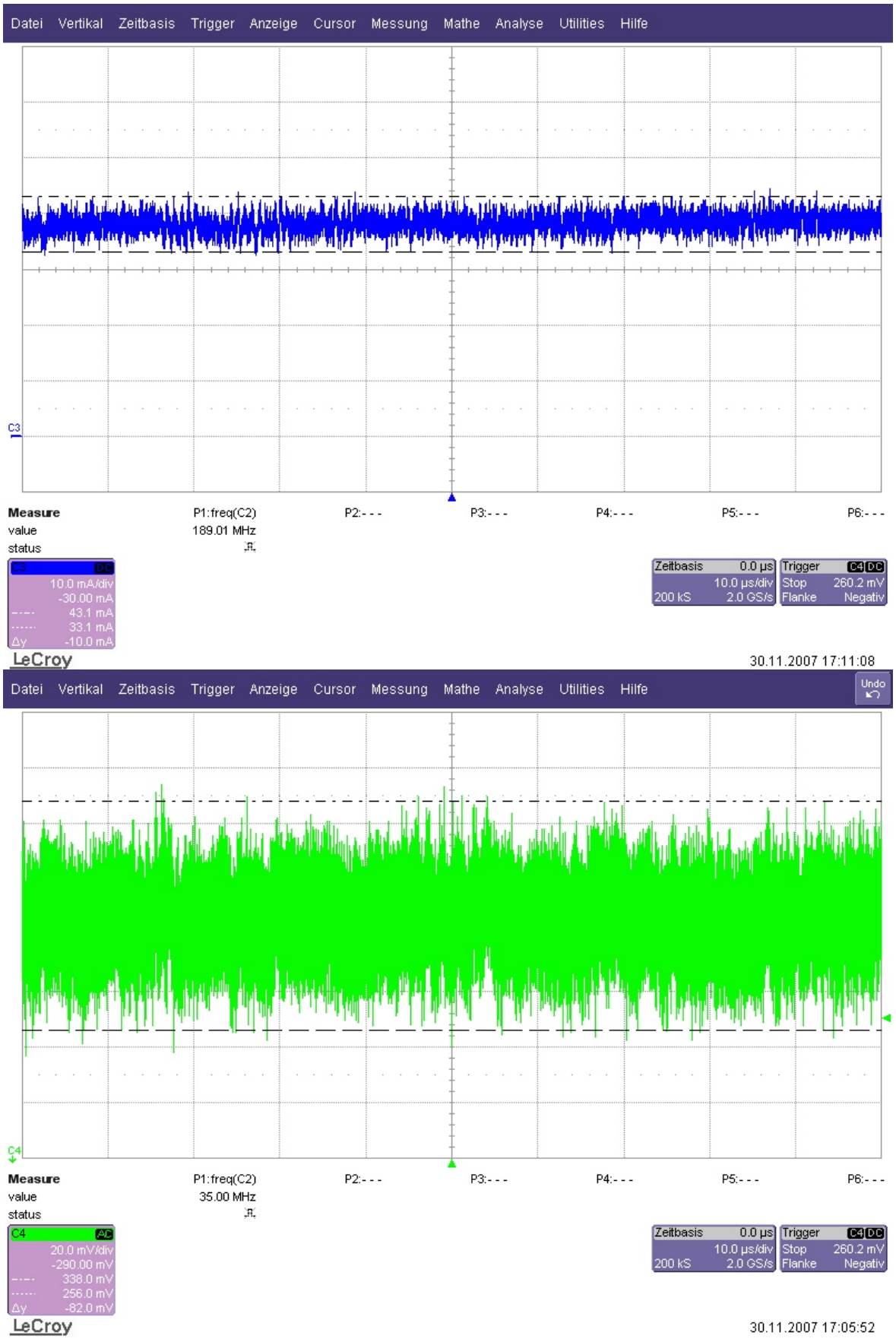


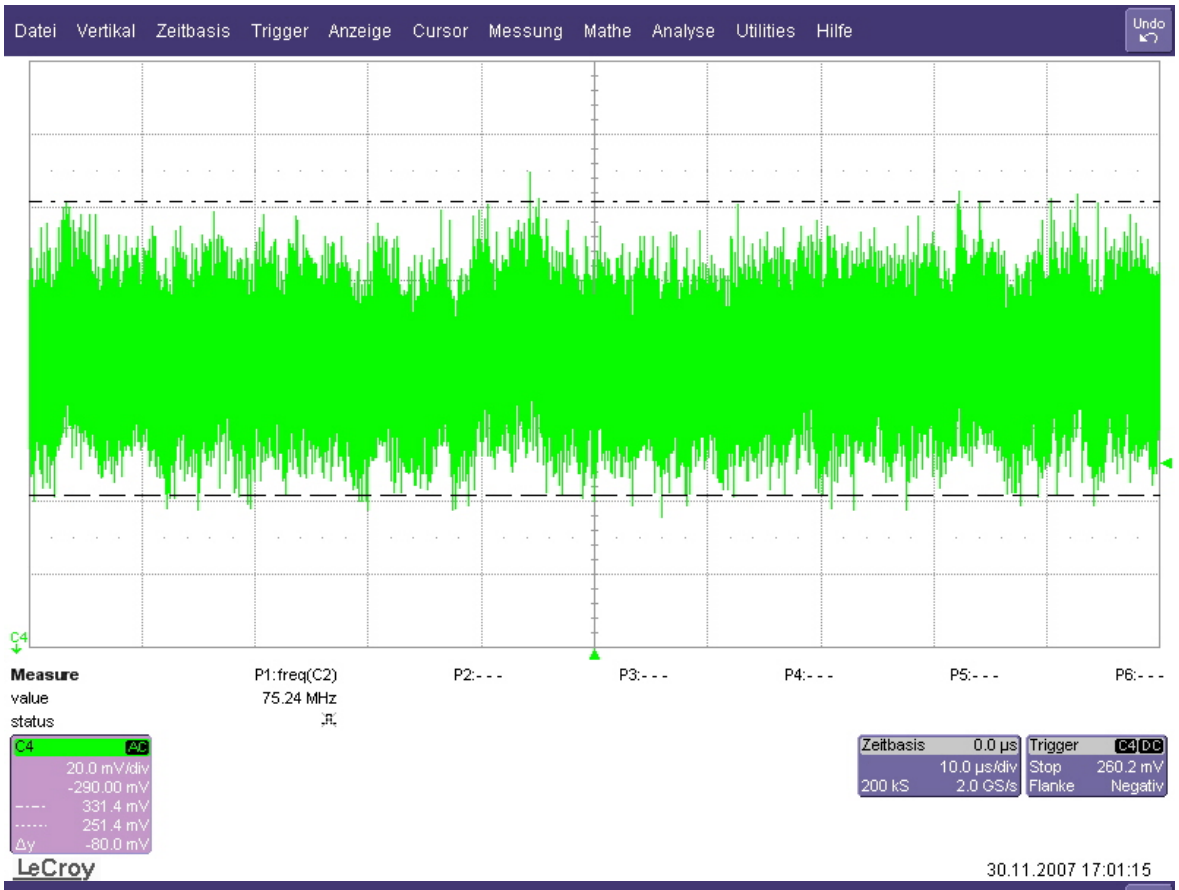
PLOT NO:026; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, NB; DATE: 07/11/30

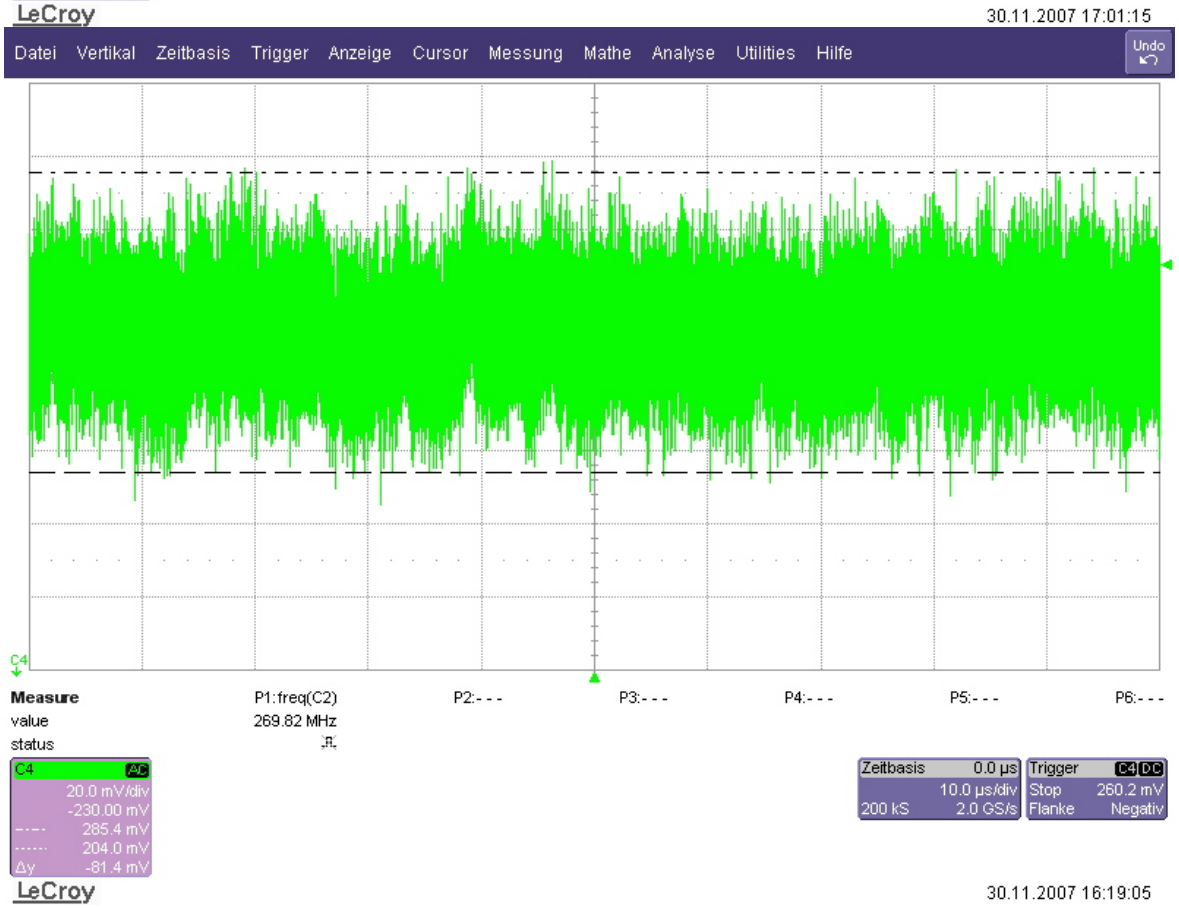


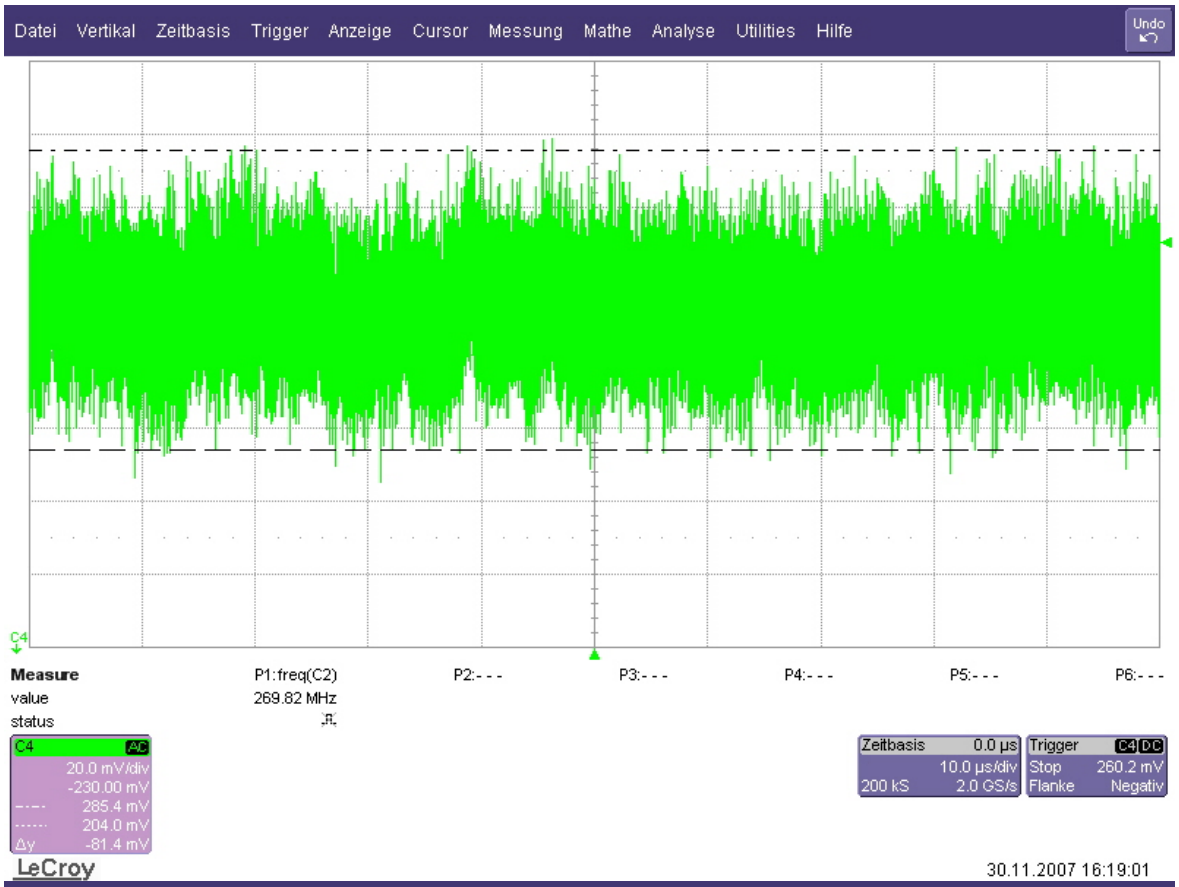

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
Undo
C4
Measure
value
status
P1:freq(C2)
188.37 MHz
P2:- - -
P3:- - -
P4:- - -
P5:- - -
P6:- - -
C4 DC
20.0 mV/div
30.00 mV ofst
-10.6 mV
-85.6 mV
Δy -75.0 mV
Zeitbasis 0.0 µs
5.00 µs/div
100 kS 2.0 GS/s
Trigger C4 DC
Stop -3.4 mV
Flanke Negativ
LeCroy
30.11.2007 17:40:57

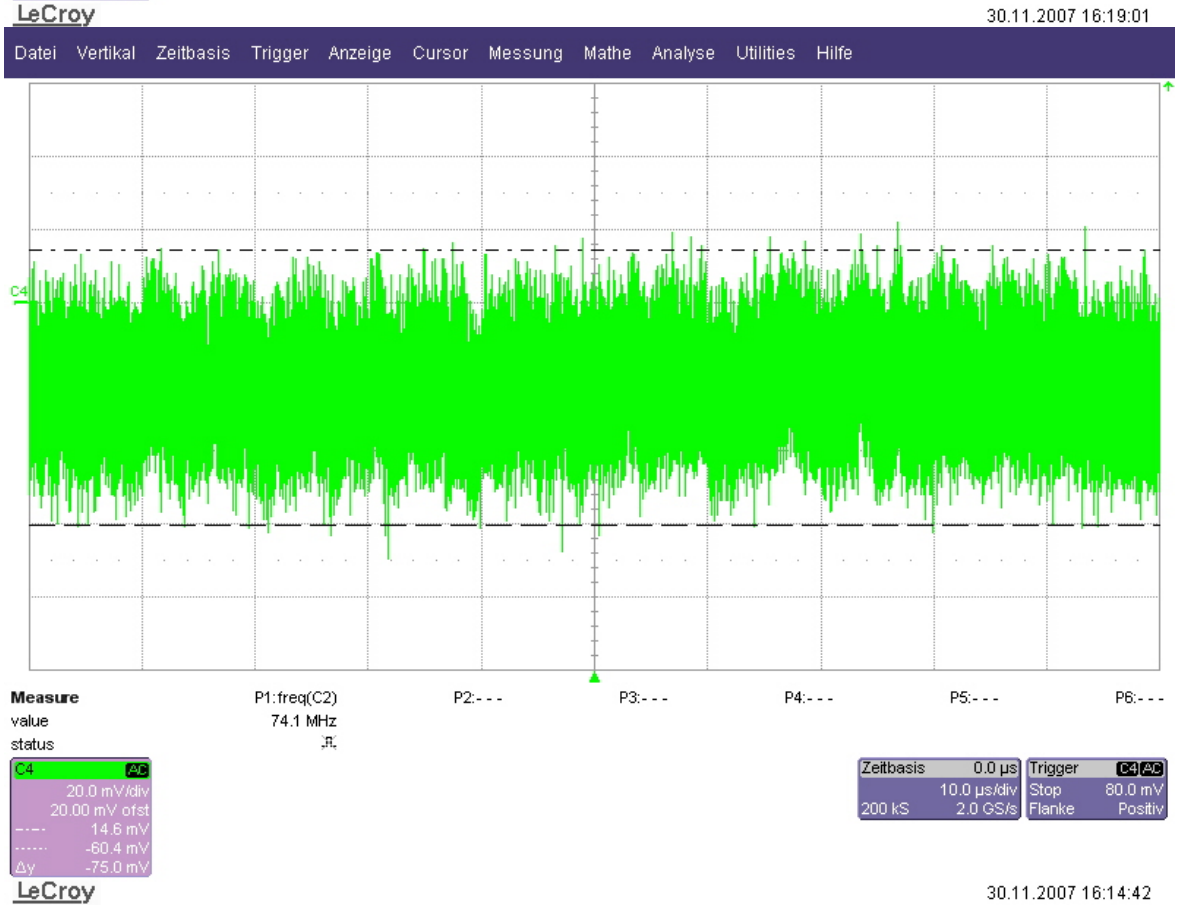

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
Measure
value
status
P1:freq(C2)
74.13 MHz
P2:- - -
P3:- - -
P4:- - -
P5:- - -
P6:- - -
C3 DC
10.0 mA/div
-30.00 mA
44.7 mA
34.7 mA
Δy -10.0 mA
Zeitbasis 0.0 µs
10.0 µs/div
200 kS 2.0 GS/s
Trigger C4 DC
Einzel 260.2 mV
Flanke Negativ
LeCroy
Waiting for Trigger

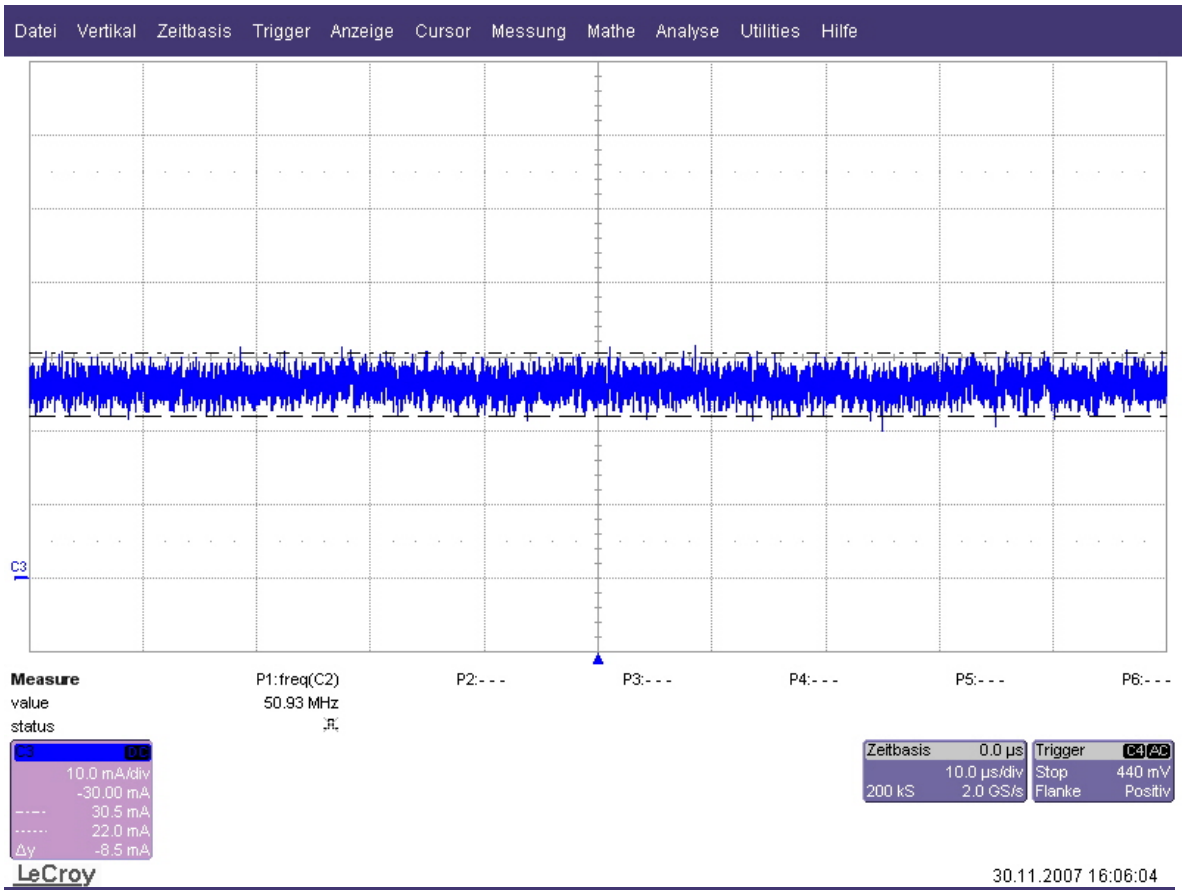

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
Measure
value
status
P1:freq(C2)
189.01 MHz
P2:- - -
P3:- - -
P4:- - -
P5:- - -
P6:- - -
C3 DC
10.0 mA/div
-30.00 mA
43.1 mA
33.1 mA
Δy -10.0 mA
Zeitbasis 0.0 µs
10.0 µs/div
200 kS 2.0 GS/s
Trigger C4 DC
Stop 260.2 mV
Flanke Negativ
LeCroy
30.11.2007 17:11:08
Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
Undo
C4
Measure
value
status
P1:freq(C2)
35.00 MHz
P2:- - -
P3:- - -
P4:- - -
P5:- - -
P6:- - -
C4 AC
20.0 mV/div
-290.00 mV
338.0 mV
256.0 mV
Δy -82.0 mV
Zeitbasis 0.0 µs
10.0 µs/div
200 kS 2.0 GS/s
Trigger C4 DC
Stop 260.2 mV
Flanke Negativ
LeCroy
30.11.2007 17:05:52

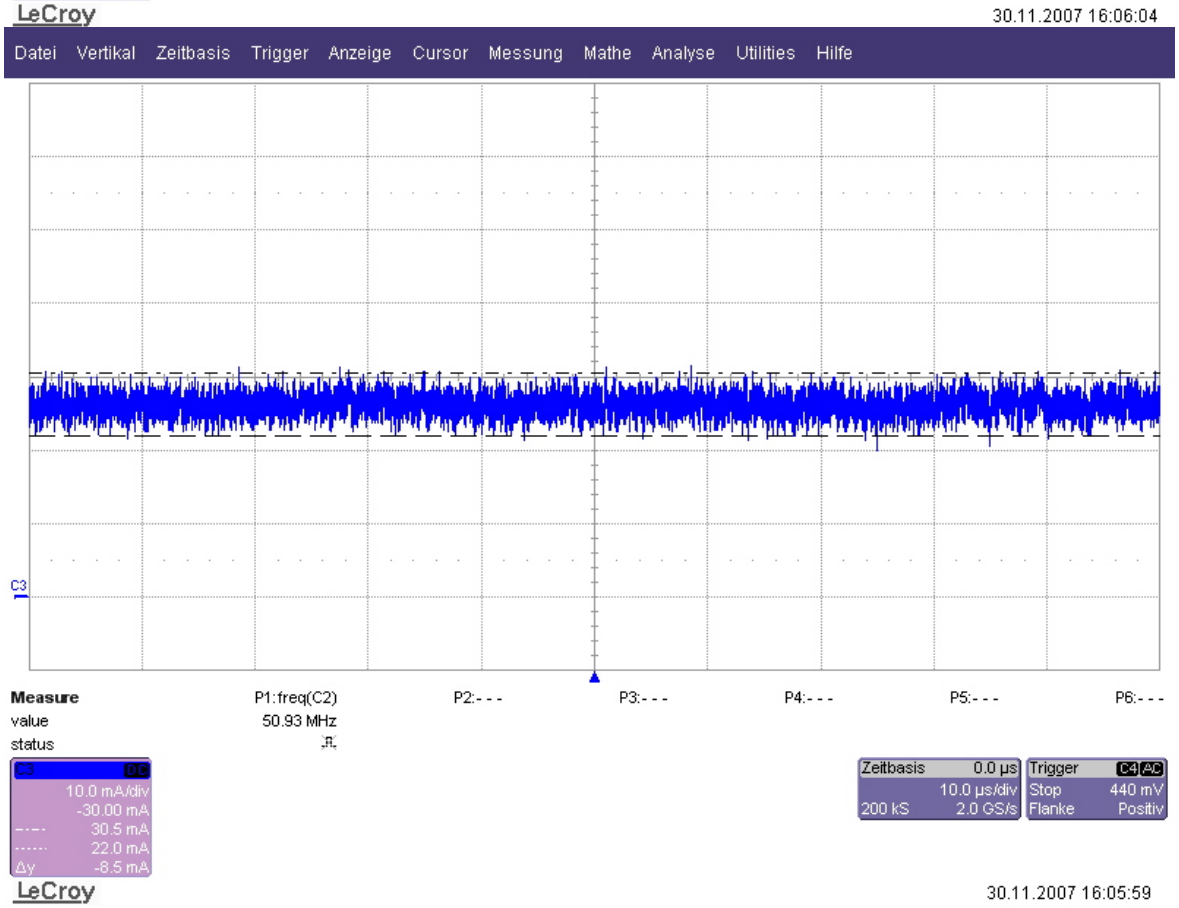

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
Undo
C4
Measure
value
status
P1:freq(C2)
75.24 MHz
P2:- - -
P3:- - -
P4:- - -
P5:- - -
P6:- - -
C4 AC
20.0 mV/div
-290.00 mV
331.4 mV
251.4 mV
Δy -80.0 mV
Zeitbasis 0.0 µs
10.0 µs/div
200 kS 2.0 GS/s
Trigger C4 DC
Stop 260.2 mV
Flanke Negativ
LeCroy
30.11.2007 17:01:15

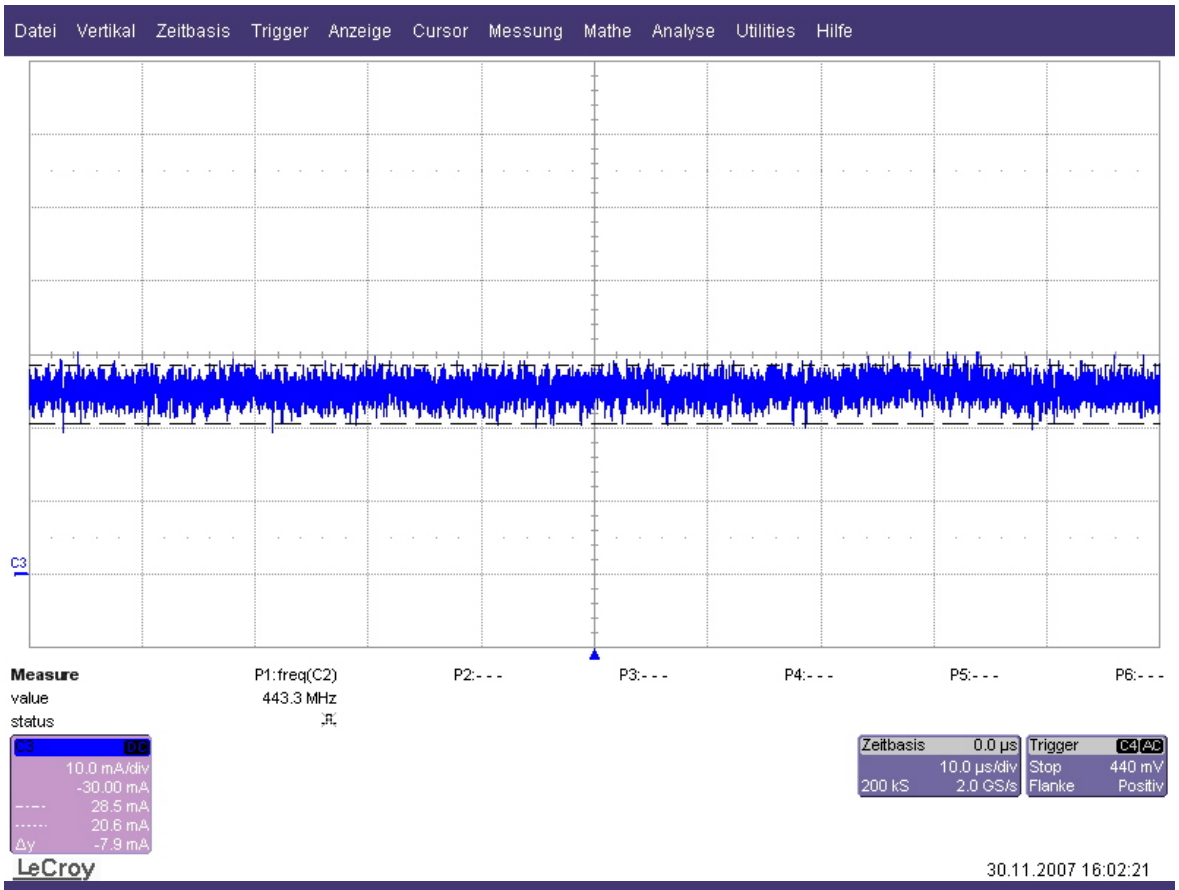

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
Undo
C4
Measure
value
status
P1:freq(C2)
269.82 MHz
P2:- - -
P3:- - -
P4:- - -
P5:- - -
P6:- - -
C4 AC
20.0 mV/div
-230.00 mV
285.4 mV
204.0 mV
Δy -81.4 mV
Zeitbasis 0.0 µs
10.0 µs/div
200 kS 2.0 GS/s
Trigger C4 DC
Stop 260.2 mV
Flanke Negativ
LeCroy
30.11.2007 16:19:05

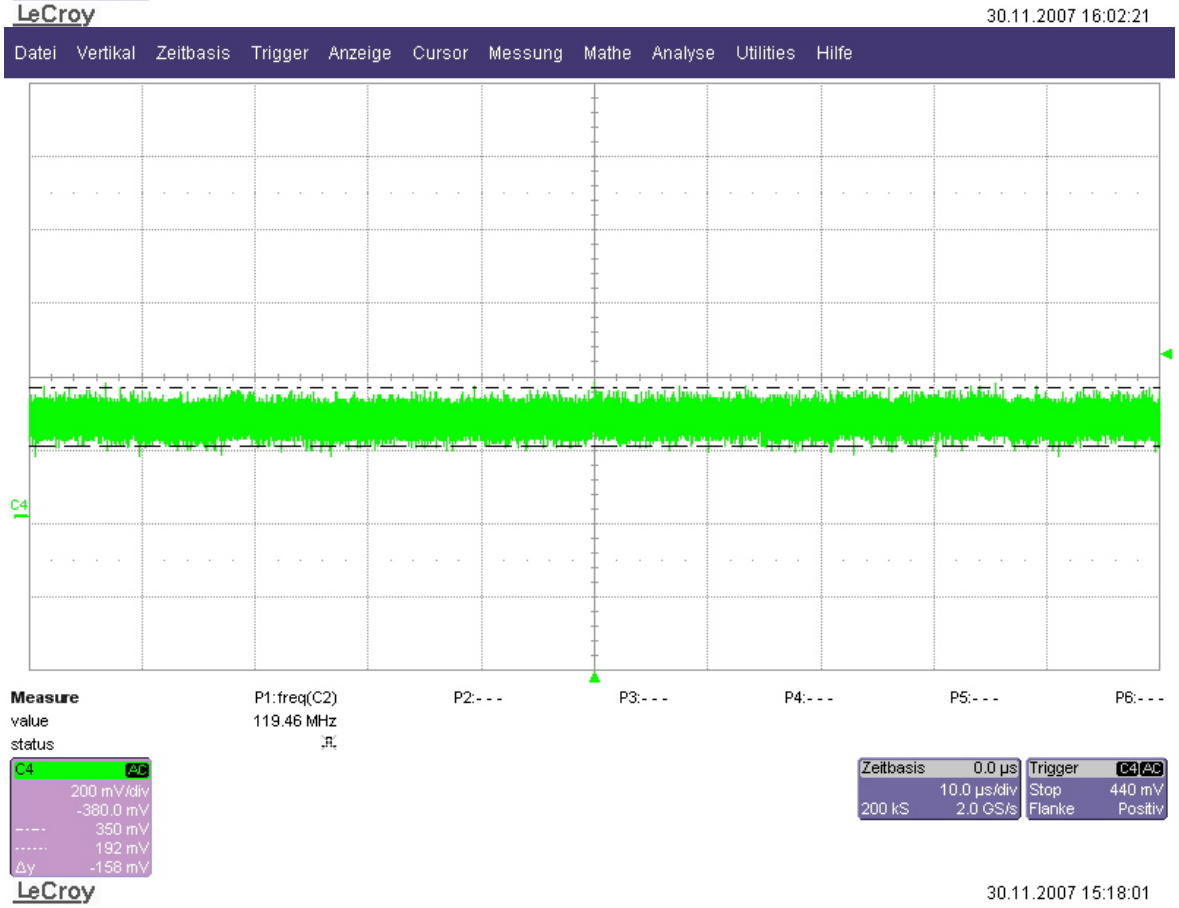

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
Undo
C4
Measure
value
status
P1:freq(C2)
269.82 MHz
P2:- - -
P3:- - -
P4:- - -
P5:- - -
P6:- - -
C4 AC
20.0 mV/div
-230.00 mV
285.4 mV
204.0 mV
Δy -81.4 mV
Zeitbasis 0.0 µs
10.0 µs/div
200 kS 2.0 GS/s
Trigger C4 DC
Stop 260.2 mV
Flanke Negativ
LeCroy
30.11.2007 16:19:01

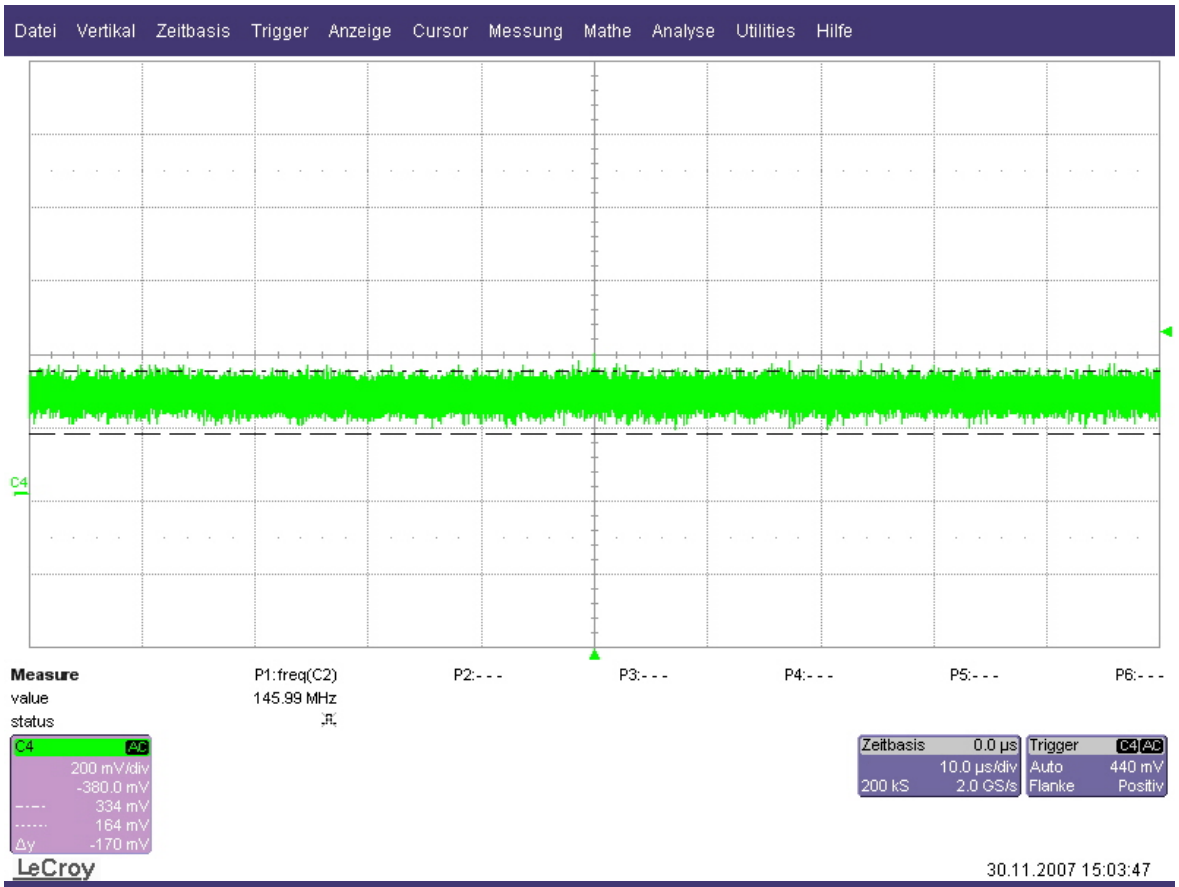

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure
value
status
P1:freq(C2)
74.1 MHz
P2:- - -
P3:- - -
P4:- - -
P5:- - -
P6:- - -
C4 AC
20.0 mV/div
20.00 mV ofst
14.6 mV
-60.4 mV
Δy -75.0 mV
Zeitbasis 0.0 µs
10.0 µs/div
200 kS 2.0 GS/s
Trigger C4 AC
Stop 80.0 mV
Flanke Positiv
LeCroy
30.11.2007 16:14:42

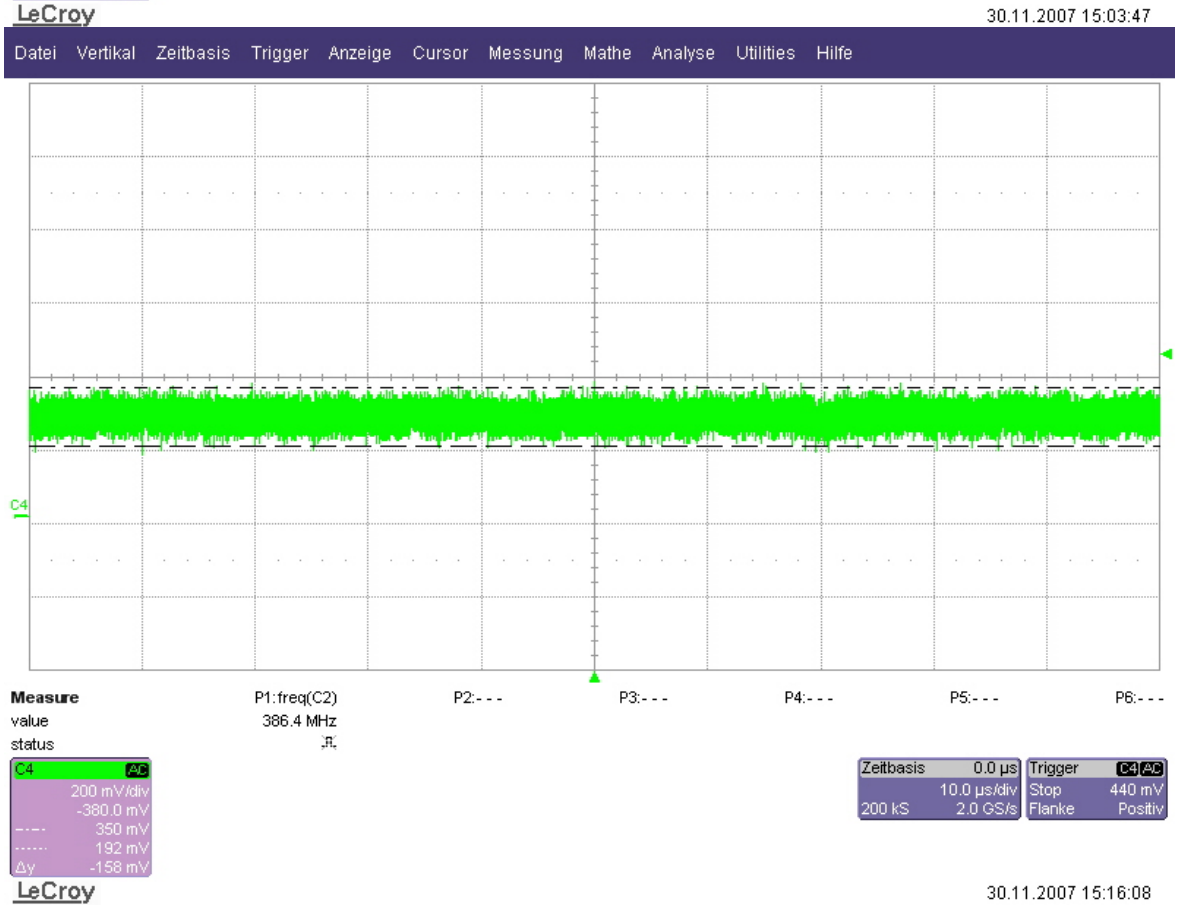

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 50.93 MHz
status
C3 DC
10.0 mA/div
-30.00 mA
30.5 mA
22.0 mA
Δy -8.5 mA
Zeitbasis 0.0 µs
10.0 µs/div
200 kS 2.0 GS/s
Trigger C4 AC
Stop 440 mV
Flanke Positiv
LeCroy
30.11.2007 16:06:04

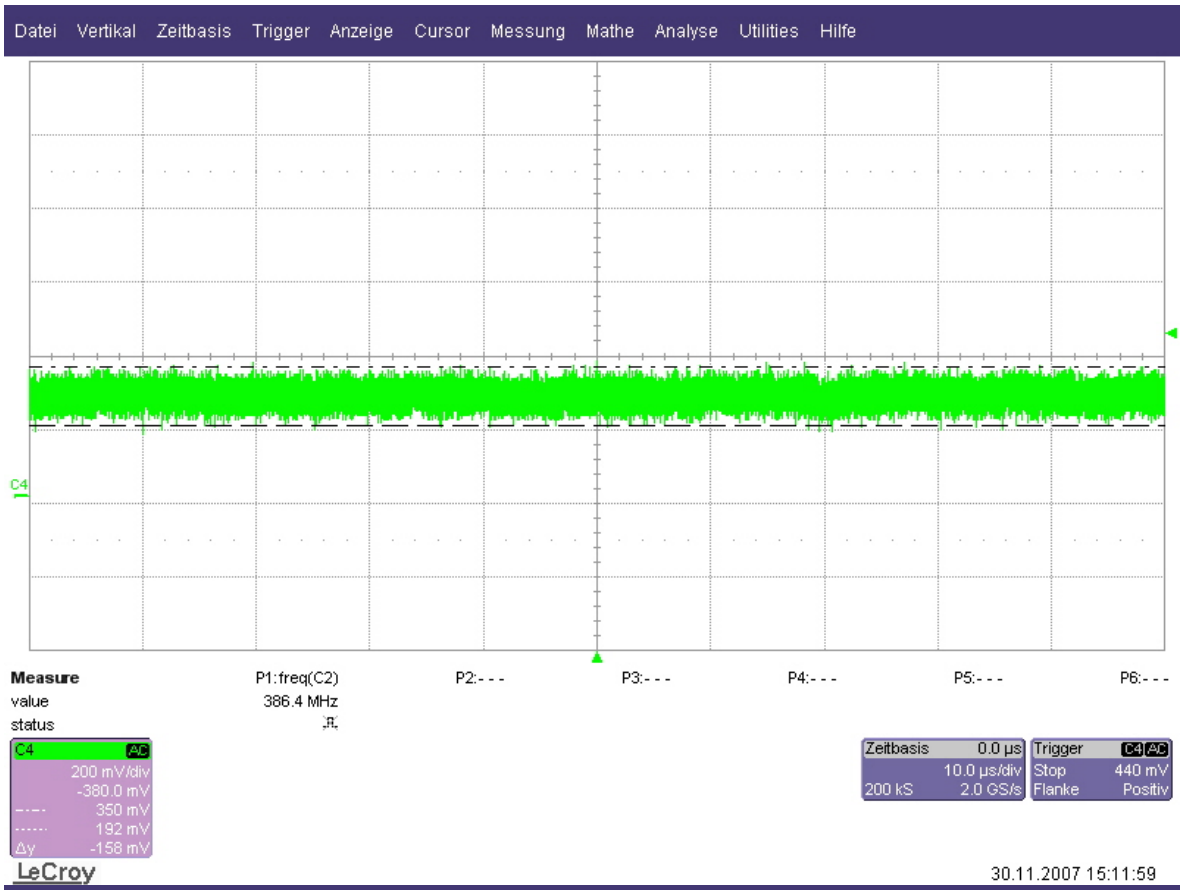

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 50.93 MHz
status
C3 DC
10.0 mA/div
-30.00 mA
30.5 mA
22.0 mA
Δy -8.5 mA
Zeitbasis 0.0 µs
10.0 µs/div
200 kS 2.0 GS/s
Trigger C4 AC
Stop 440 mV
Flanke Positiv
LeCroy
30.11.2007 16:05:59

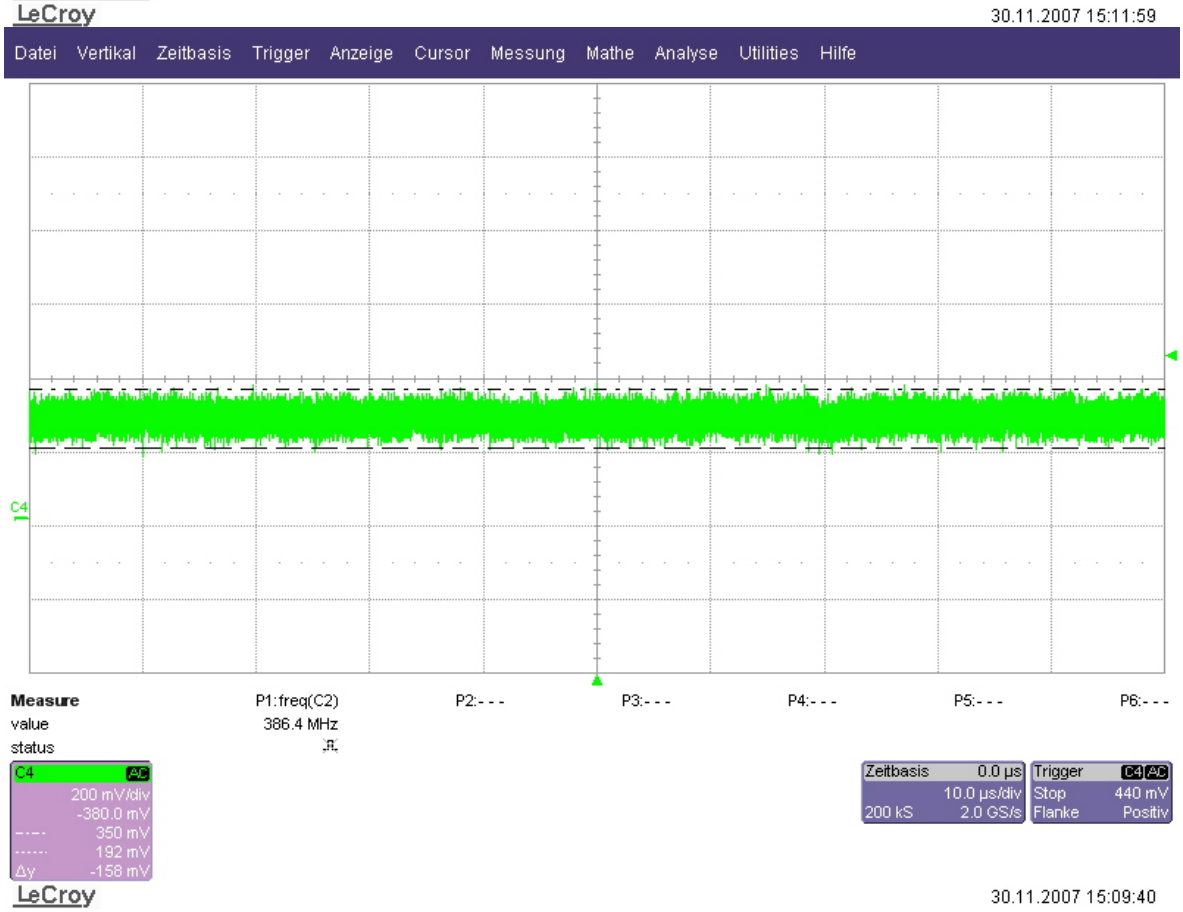

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
Measure
value
status
P1:freq(C2)
443.3 MHz
P2:- - -
P3:- - -
P4:- - -
P5:- - -
P6:- - -
C3 DC
10.0 mA/div
-30.00 mA
28.5 mA
20.6 mA
Δy -7.9 mA
Zeitbasis 0.0 µs
10.0 µs/div
200 kS 2.0 GS/s
Trigger C4 AC
Stop 440 mV
Flanke Positiv
LeCroy
30.11.2007 16:02:21

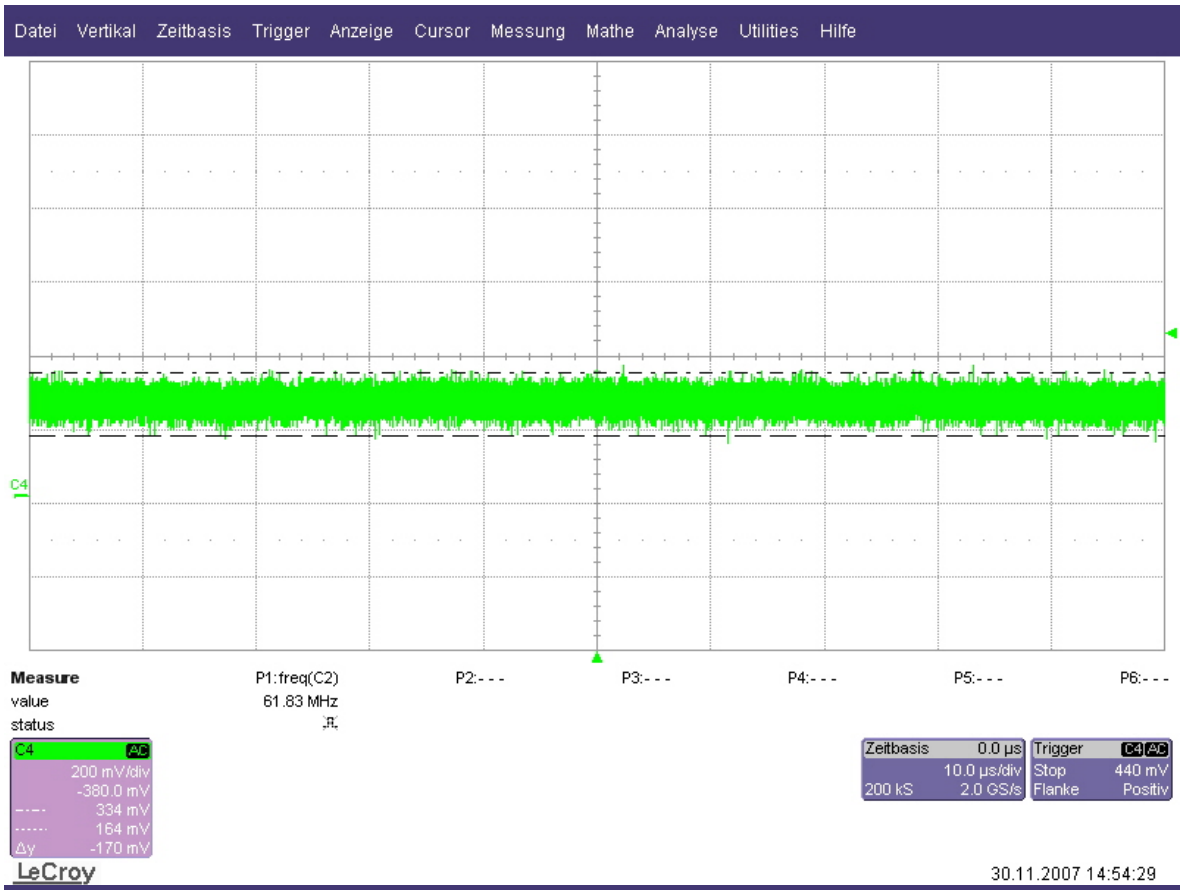

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure
value
status
P1:freq(C2)
119.46 MHz
P2:- - -
P3:- - -
P4:- - -
P5:- - -
P6:- - -
C4 AC
200 mV/div
-380.0 mV
350 mV
192 mV
Δy -158 mV
Zeitbasis 0.0 µs
10.0 µs/div
200 kS 2.0 GS/s
Trigger C4 AC
Stop 440 mV
Flanke Positiv
LeCroy
30.11.2007 15:18:01

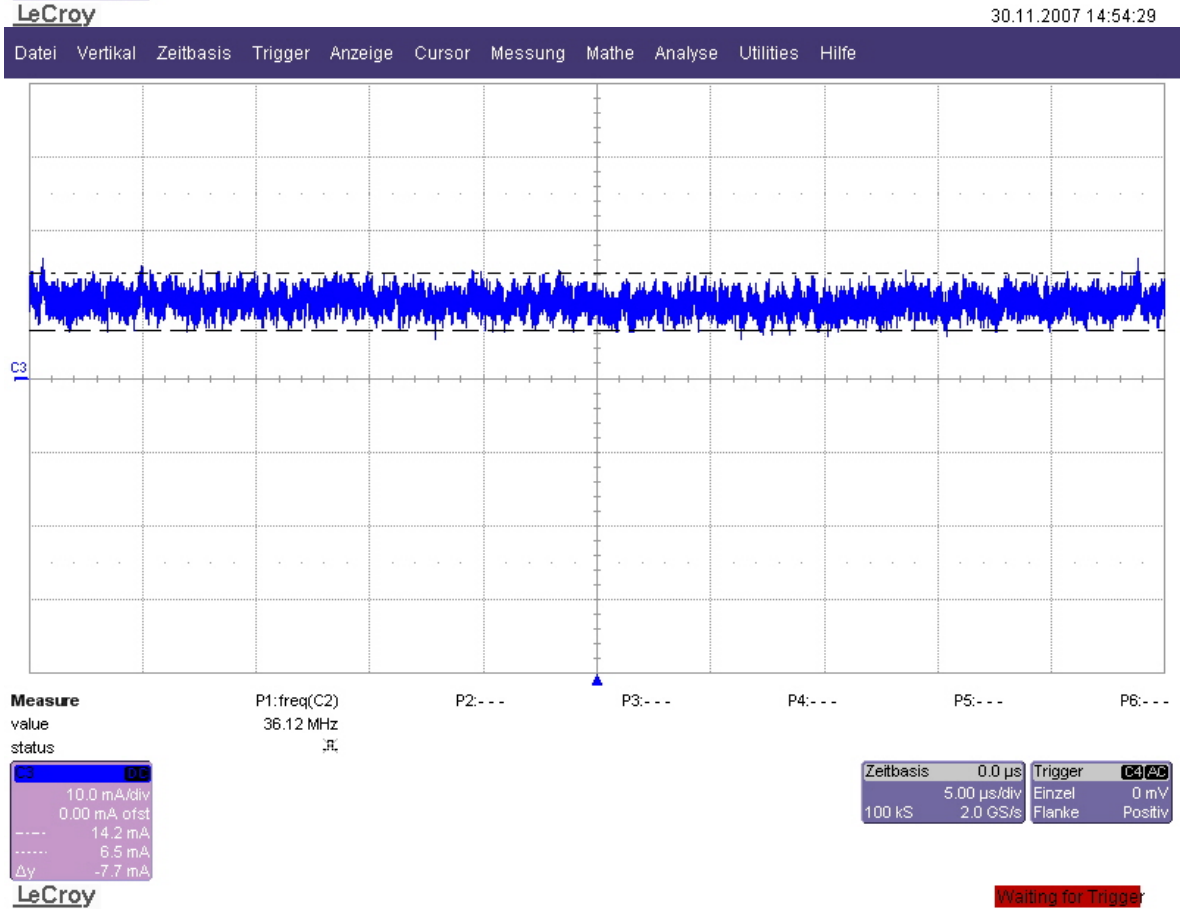

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 145.99 MHz
status
C4 AC
200 mV/div
-380.0 mV
334 mV
164 mV
Δy -170 mV
Zeitbasis 0.0 µs
10.0 µs/div
200 kS 2.0 GS/s
Trigger C4 AC
Auto 440 mV
Flanke Positiv
LeCroy
30.11.2007 15:03:47


Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 386.4 MHz
status
C4 AC
200 mV/div
-380.0 mV
350 mV
192 mV
Δy -158 mV
Zeitbasis 0.0 µs
10.0 µs/div
200 kS 2.0 GS/s
Trigger C4 AC
Stop 440 mV
Flanke Positiv
LeCroy
30.11.2007 15:16:08

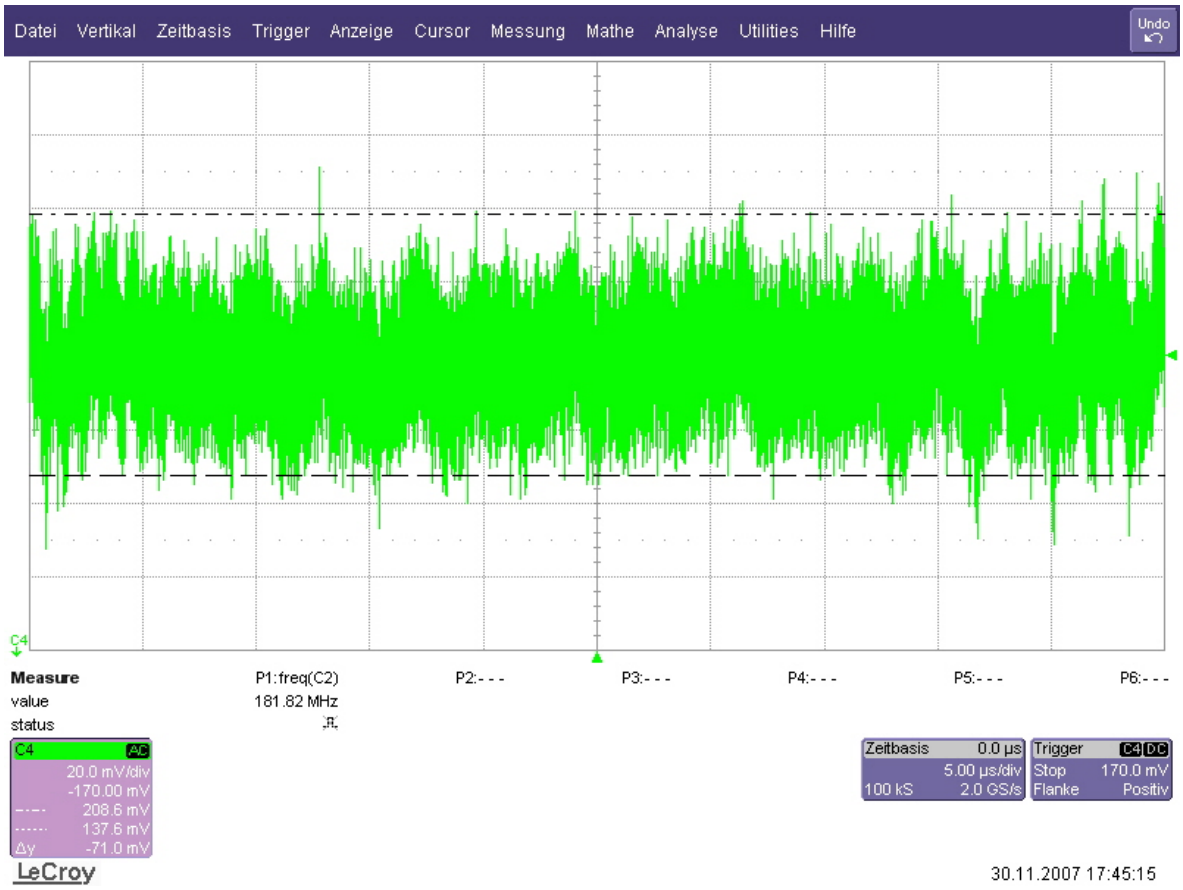

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 386.4 MHz
status
C4 200 mV/div -380.0 mV 350 mV 192 mV Δy -158 mV
Zeitbasis 0.0 µs 10.0 µs/div 200 kS 2.0 GS/s
Trigger C4 Stop 440 mV Flanke Positiv
LeCroy
30.11.2007 15:11:59


Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 386.4 MHz
status
C4 200 mV/div -380.0 mV 350 mV 192 mV Δy -158 mV
Zeitbasis 0.0 µs 10.0 µs/div 200 kS 2.0 GS/s
Trigger C4 Stop 440 mV Flanke Positiv
LeCroy
30.11.2007 15:09:40

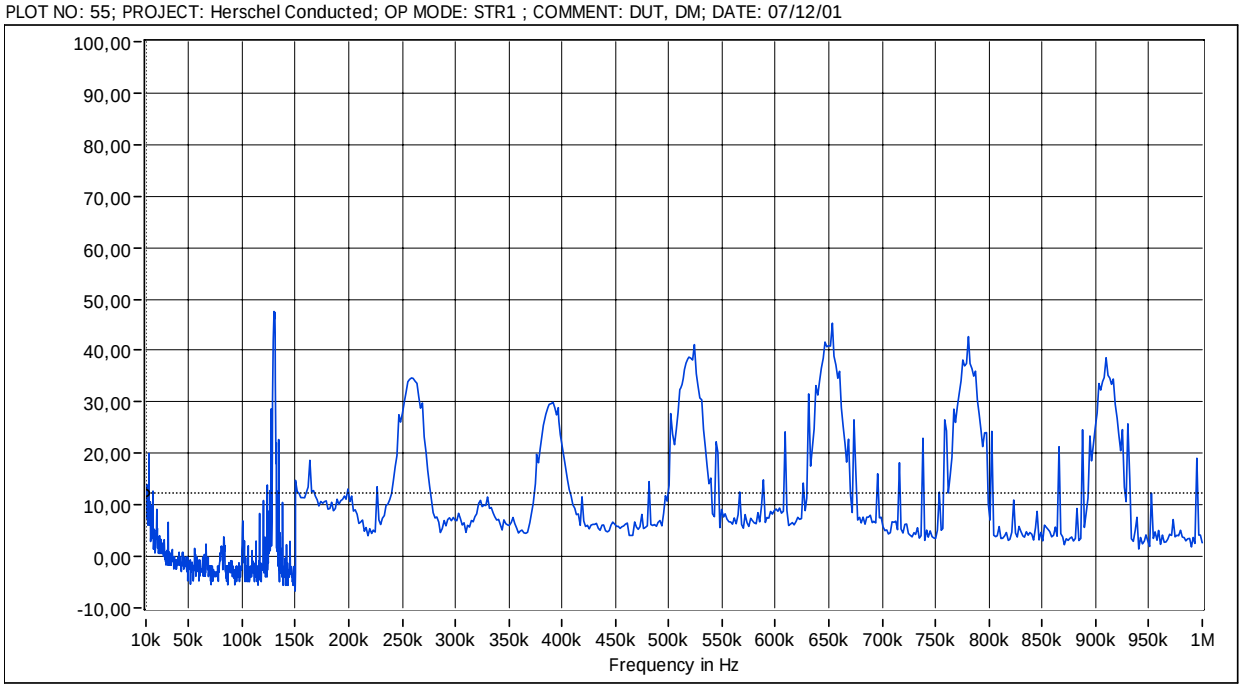

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 61.83 MHz
status
C4 AC
200 mV/div
-380.0 mV
334 mV
164 mV
Δy -170 mV
Zeitbasis 0.0 µs
10.0 µs/div
200 kS 2.0 GS/s
Trigger C4 AC
Stop 440 mV
Flanke Positiv
LeCroy
30.11.2007 14:54:29


Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 36.12 MHz
status
C3 DC
10.0 mA/div
0.00 mA ofst
14.2 mA
6.5 mA
Δy -7.7 mA
Zeitbasis 0.0 µs
5.00 µs/div
100 kS 2.0 GS/s
Trigger C4 AC
Einzel 0 mV
Flanke Positiv
LeCroy
Waiting for Trigger

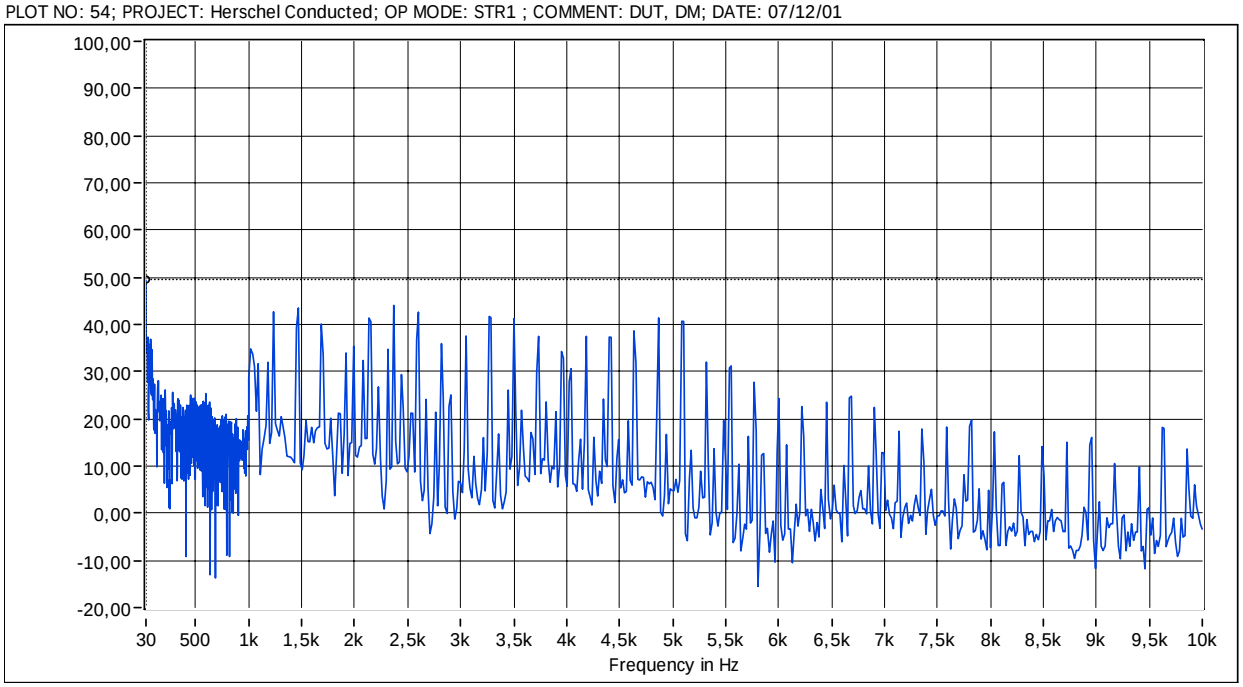

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C1
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 138.8 MHz
status
C1 DC50
10.0 mV/div
0.00 mV ofst
10.1 mV
-10.2 mV
Δy -20.3 mV
Zeitbasis 0 µs
50.0 µs/div
500 kS 1.0 GS/s
Trigger C4 DC
Stop 0 mV
Flanke Positiv
LeCroy
30.11.2007 11:27:21
Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C1
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 49.1 MHz
status
C1 DC50
10.0 mV/div
0.00 mV ofst
11.0 mV
-11.1 mV
Δy -22.1 mV
Zeitbasis 0 µs
50.0 µs/div
500 kS 1.0 GS/s
Trigger C4 DC
Einzel 0 mV
Flanke Positiv
LeCroy
Waiting for Trigger


Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
Undo
C4
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 181.82 MHz
status
C4 AC
20.0 mV/div
-170.00 mV
208.6 mV
137.6 mV
Δy -71.0 mV
Zeitbasis 0.0 µs
5.00 µs/div
100 kS 2.0 GS/s
Trigger C4 DC
Stop 170.0 mV
Flanke Positiv
LeCroy
30.11.2007 17:45:15

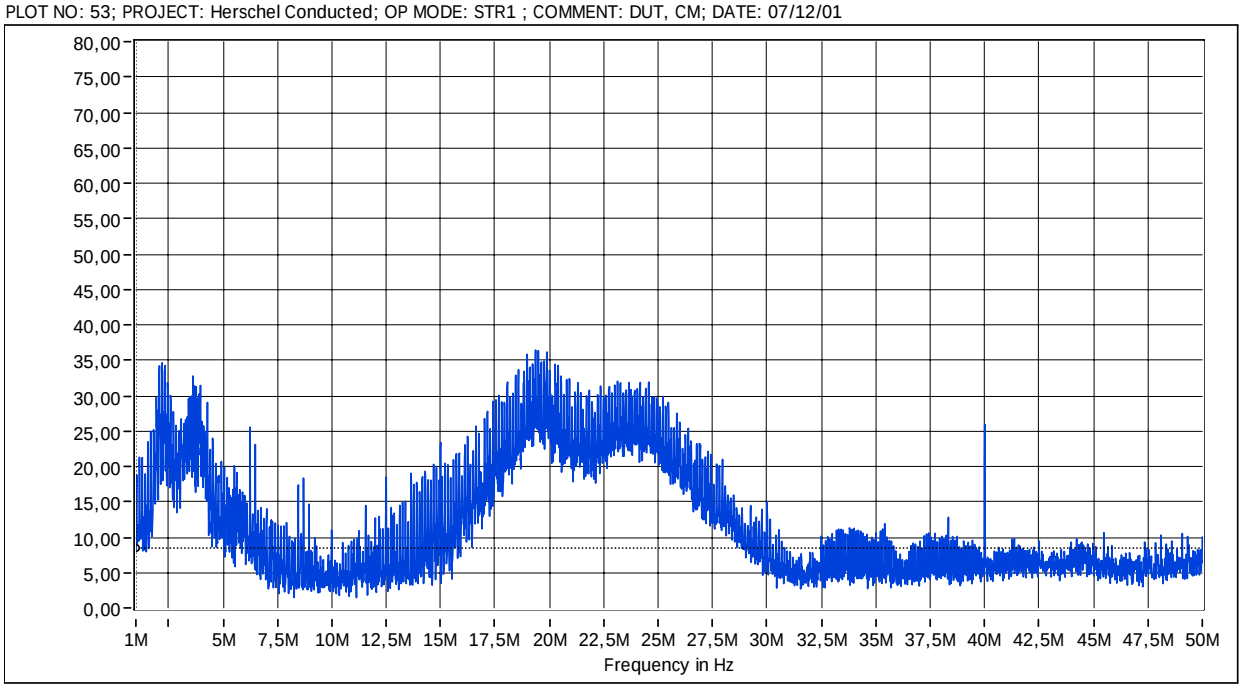
PLOT NO: 55; PROJECT: Herschel Conducted; OP MODE: STR1 ; COMMENT: DUT, DM; DATE: 07/12/01



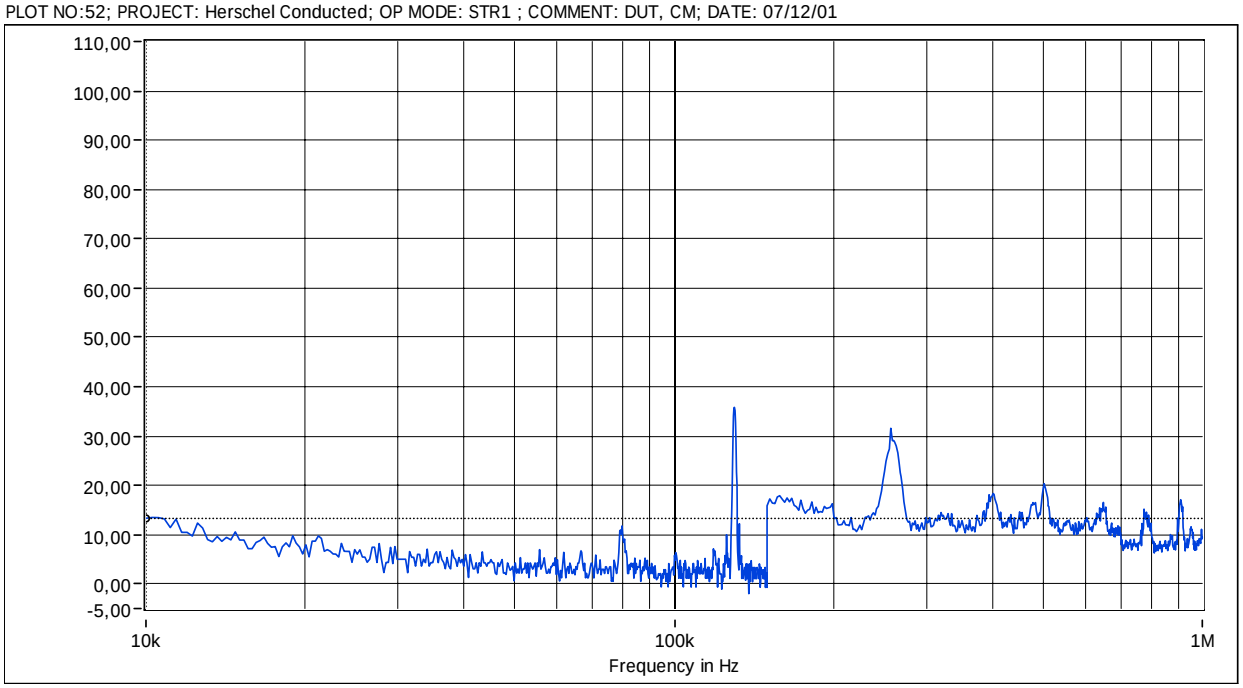
PLOT NO: 54; PROJECT: Herschel Conducted; OP MODE: STR1 ; COMMENT: DUT, DM; DATE: 07/12/01



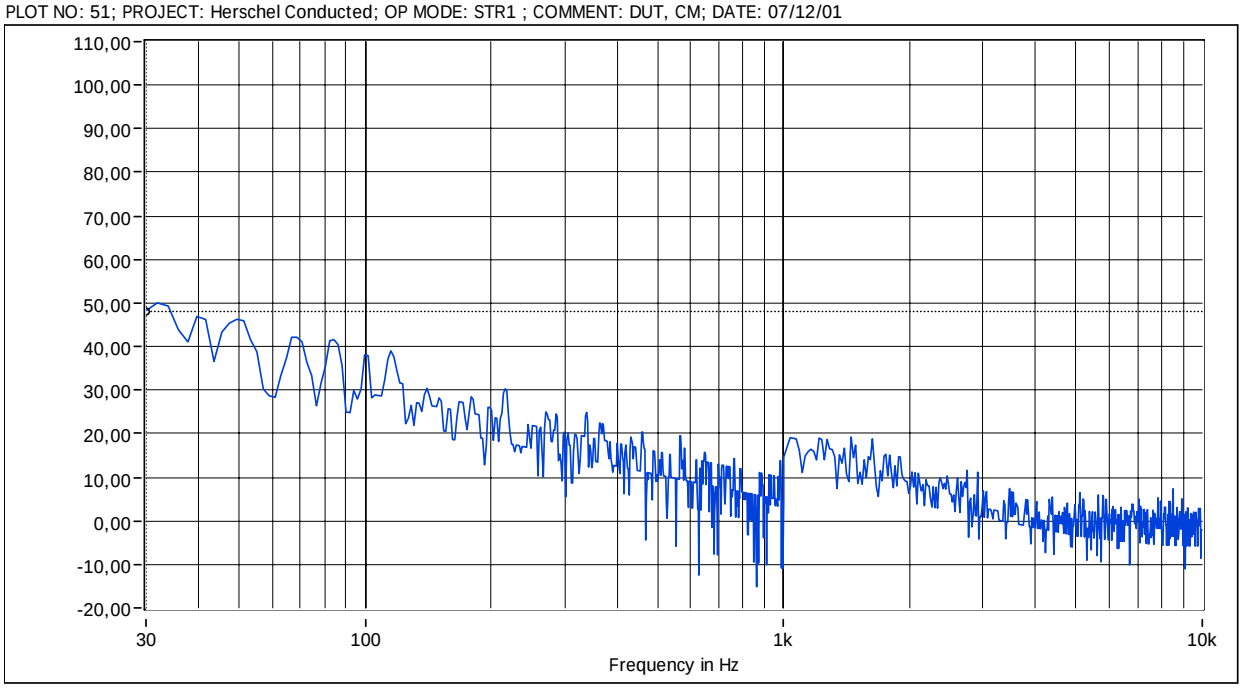
PLOT NO: 53; PROJECT: Herschel Conducted; OP MODE: STR1 ; COMMENT: DUT, CM; DATE: 07/12/01



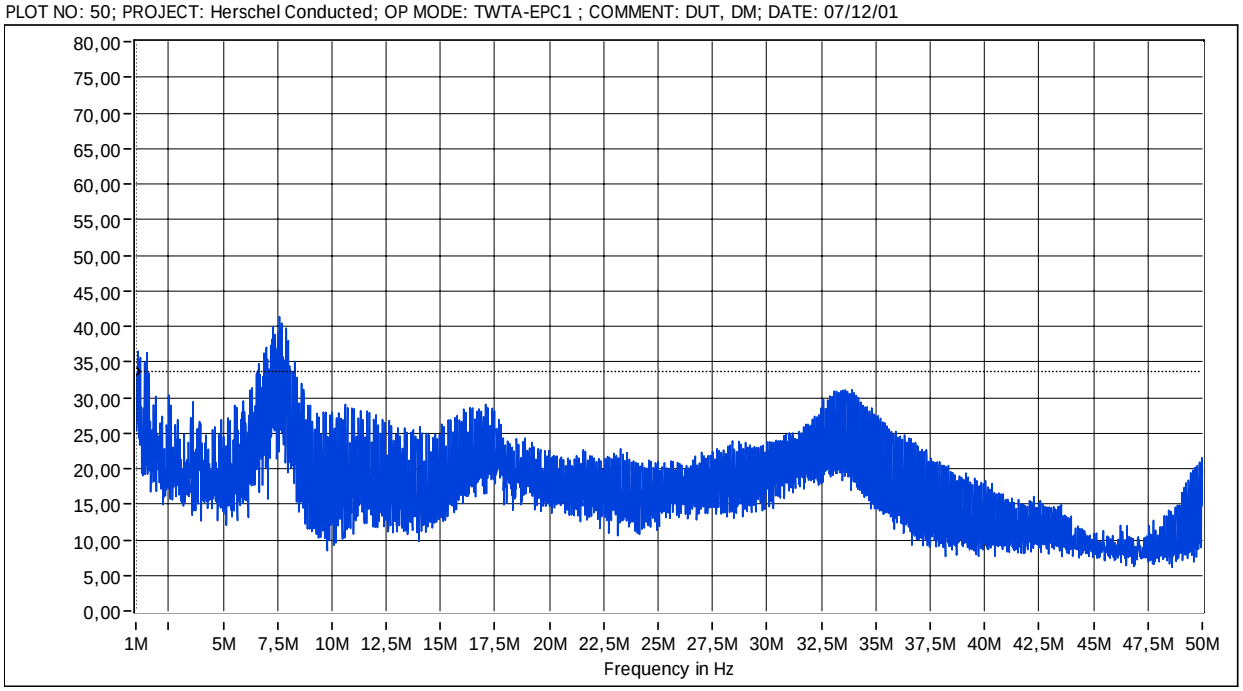
PLOT NO:52; PROJECT: Herschel Conducted; OP MODE: STR1 ; COMMENT: DUT, CM; DATE: 07/12/01



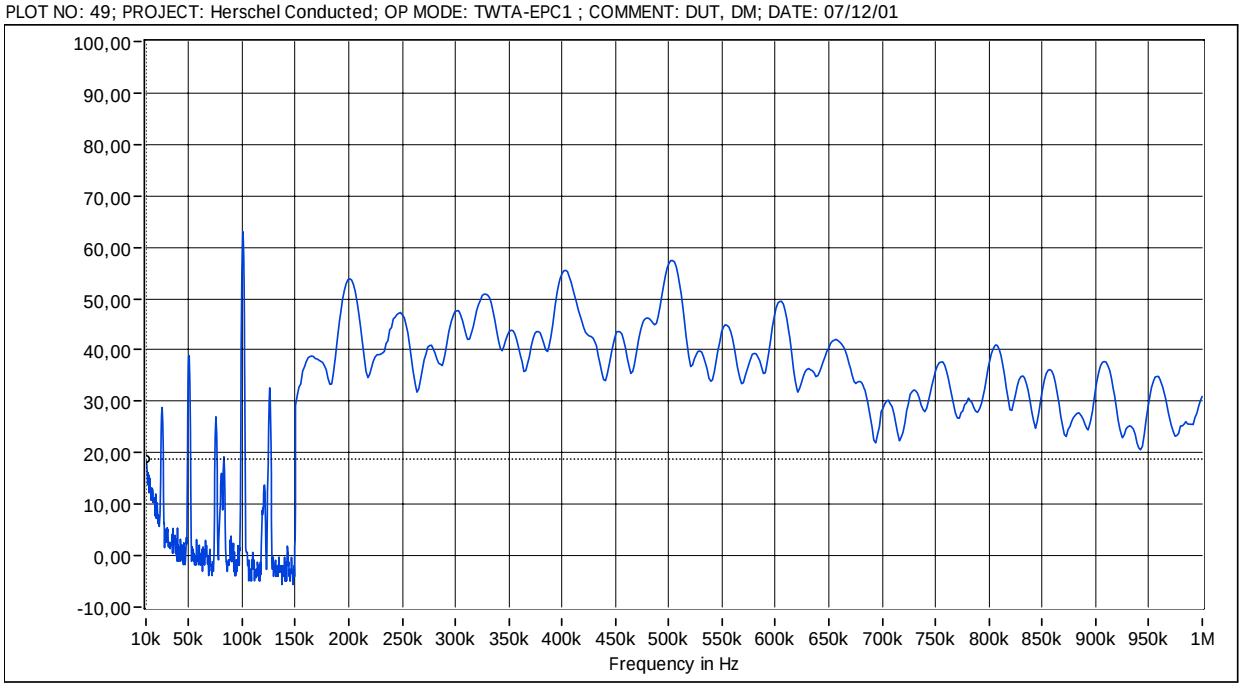
PLOT NO: 51; PROJECT: Herschel Conducted; OP MODE: STR1 ; COMMENT: DUT, CM; DATE: 07/12/01



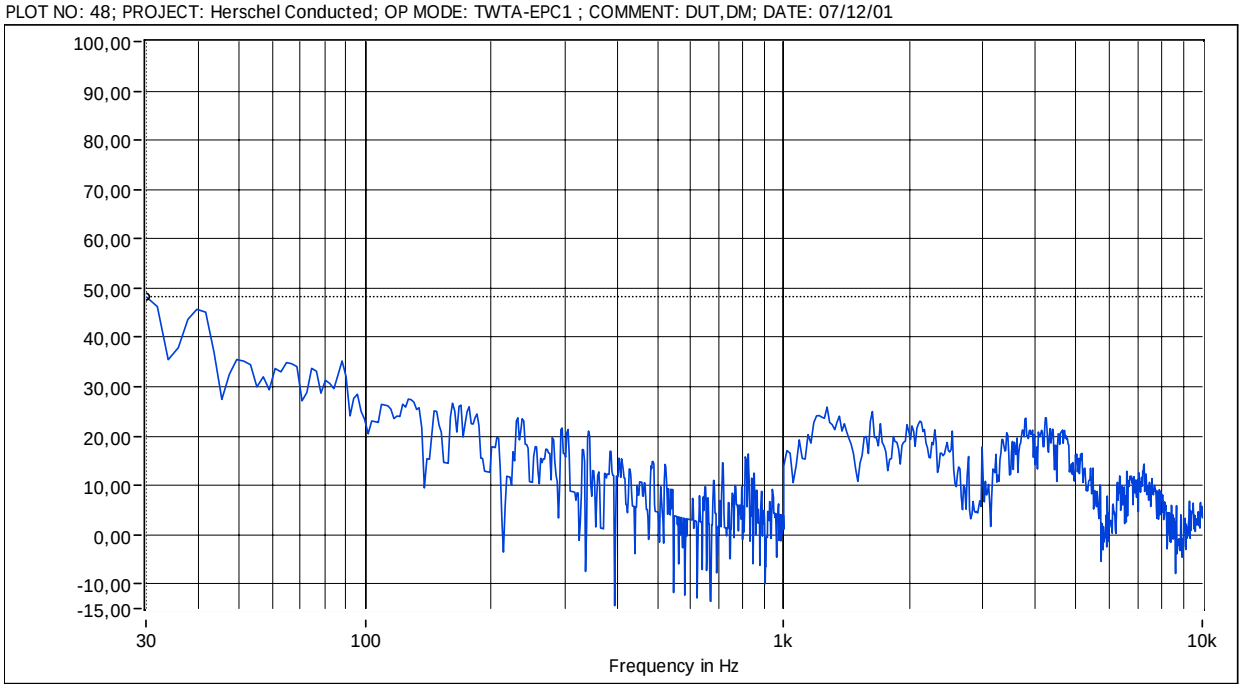
PLOT NO: 50; PROJECT: Herschel Conducted; OP MODE: TWTA-EPC1 ; COMMENT: DUT, DM; DATE: 07/12/01



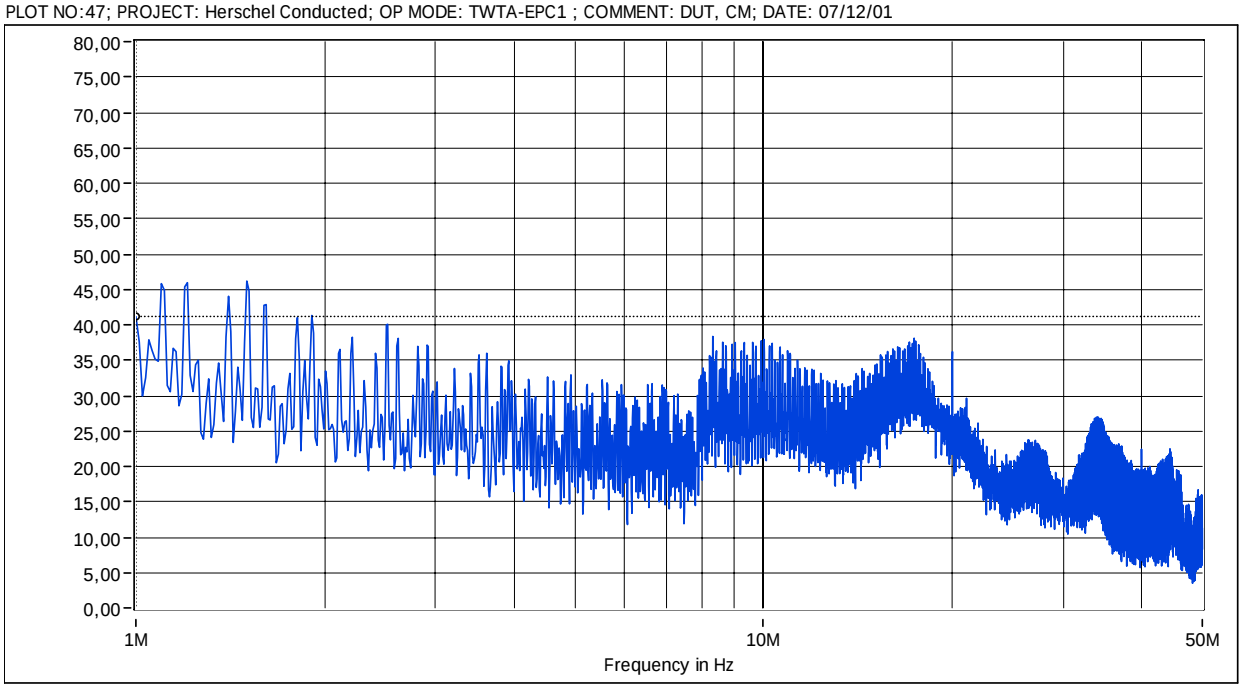
PLOT NO: 49; PROJECT: Herschel Conducted; OP MODE: TWTA-EPC1 ; COMMENT: DUT, DM; DATE: 07/12/01



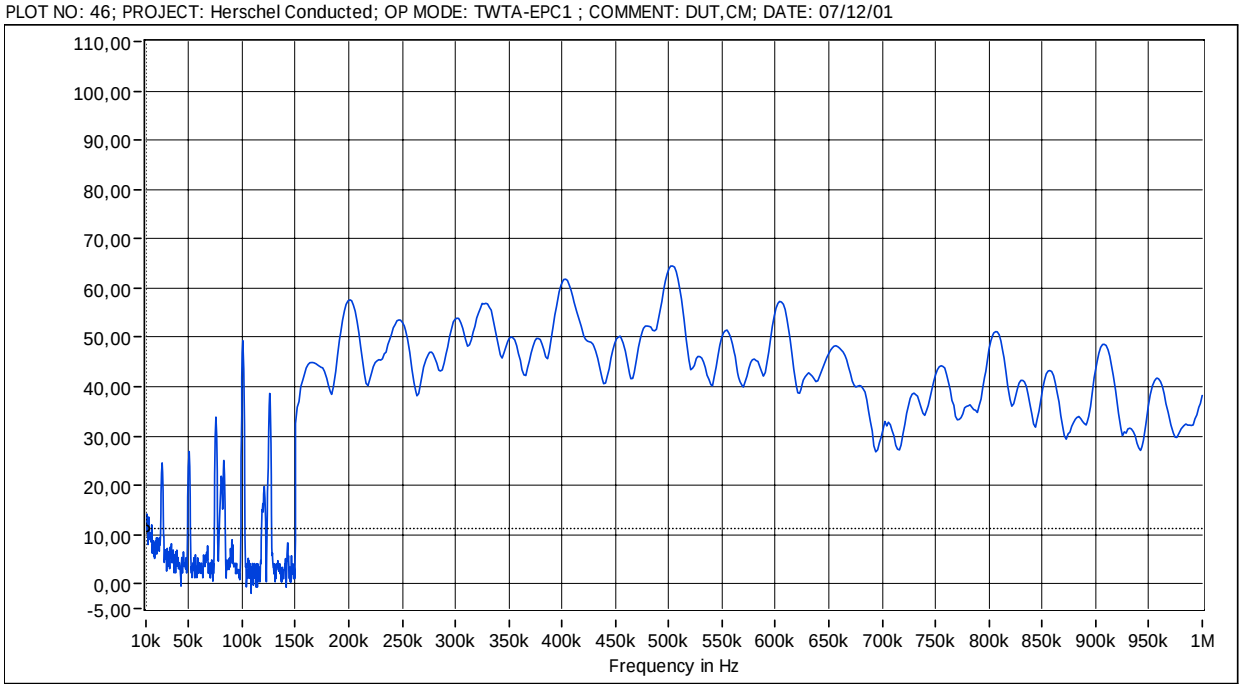
PLOT NO: 48; PROJECT: Herschel Conducted; OP MODE: TWTA-EPC1 ; COMMENT: DUT,DM; DATE: 07/12/01



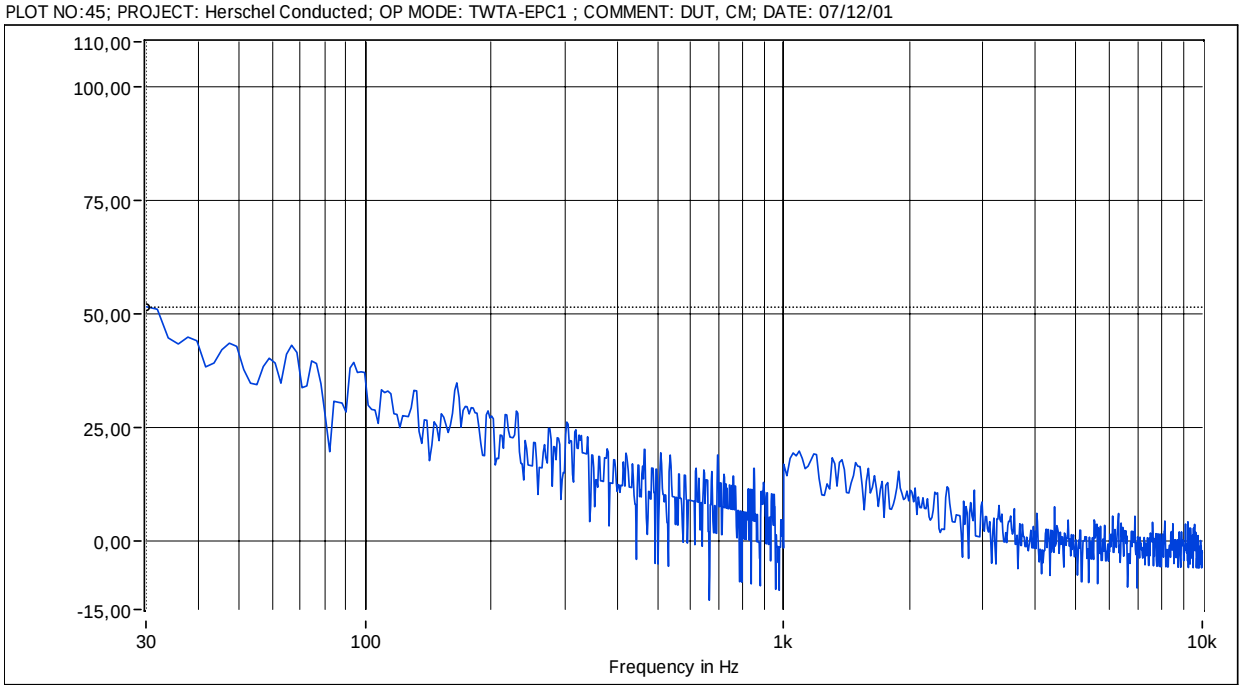
PLOT NO:47; PROJECT: Herschel Conducted; OP MODE: TWTA-EPC1 ; COMMENT: DUT, CM; DATE: 07/12/01



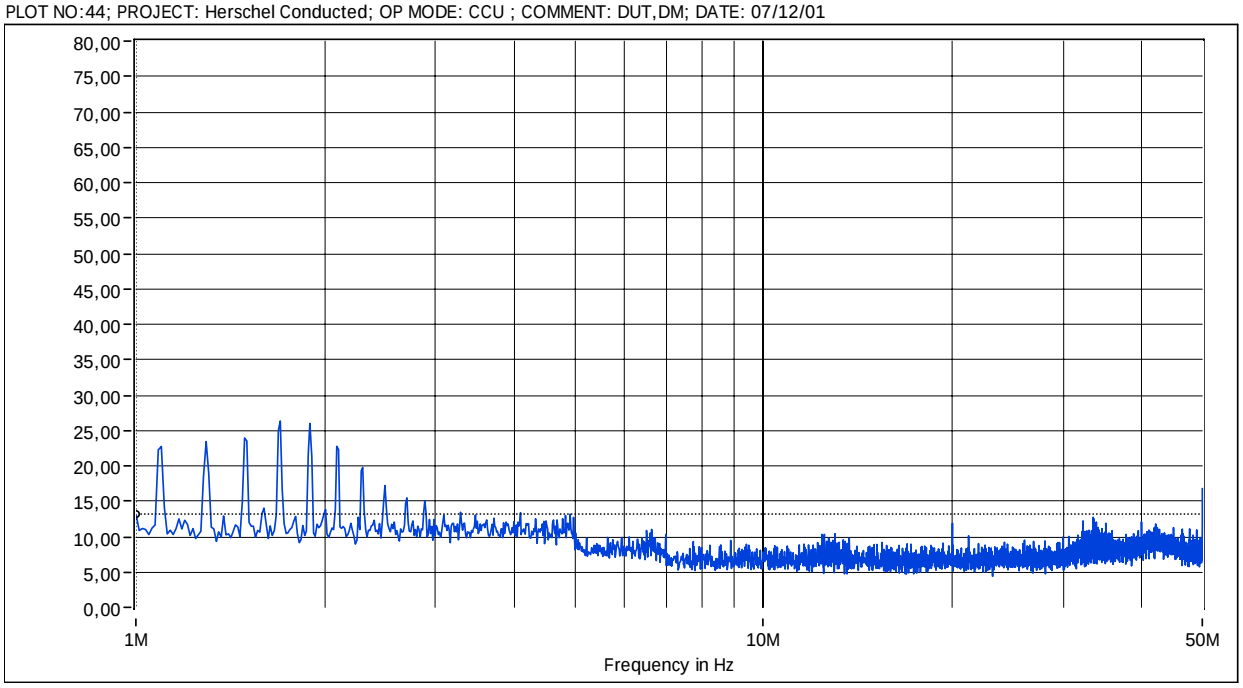
PLOT NO: 46; PROJECT: Herschel Conducted; OP MODE: TWTA-EPC1 ; COMMENT: DUT,CM; DATE: 07/12/01



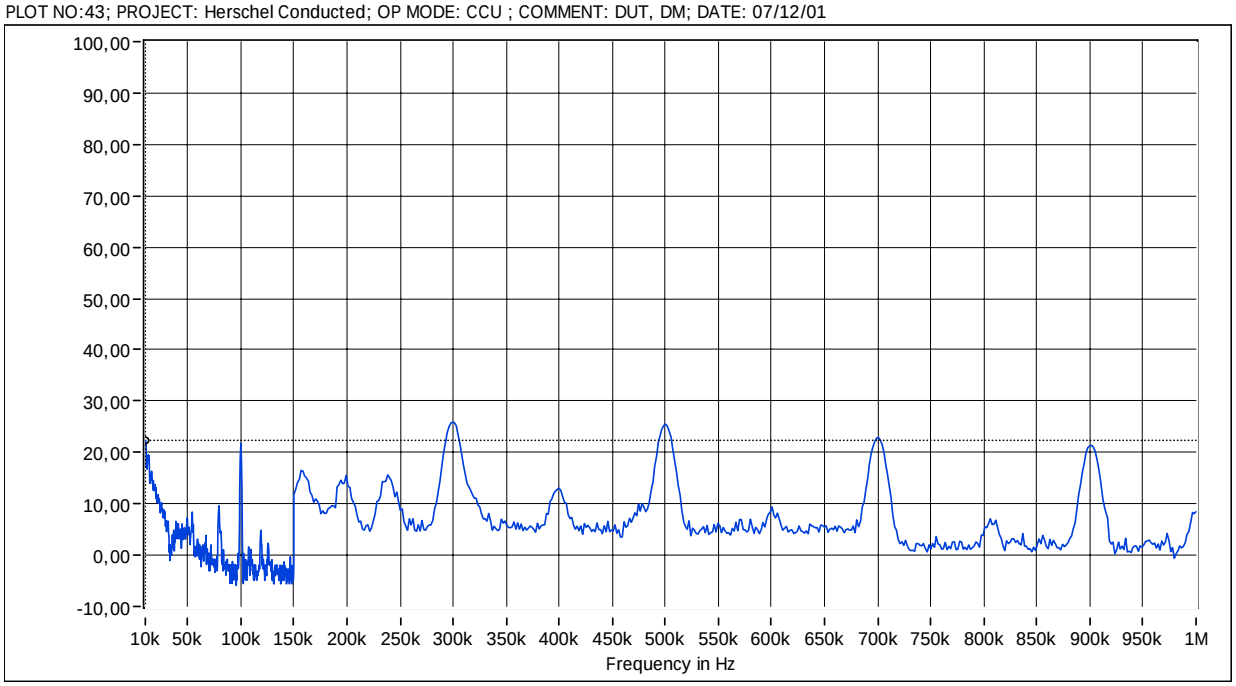
PLOT NO:45; PROJECT: Herschel Conducted; OP MODE: TWTA-EPC1 ; COMMENT: DUT, CM; DATE: 07/12/01



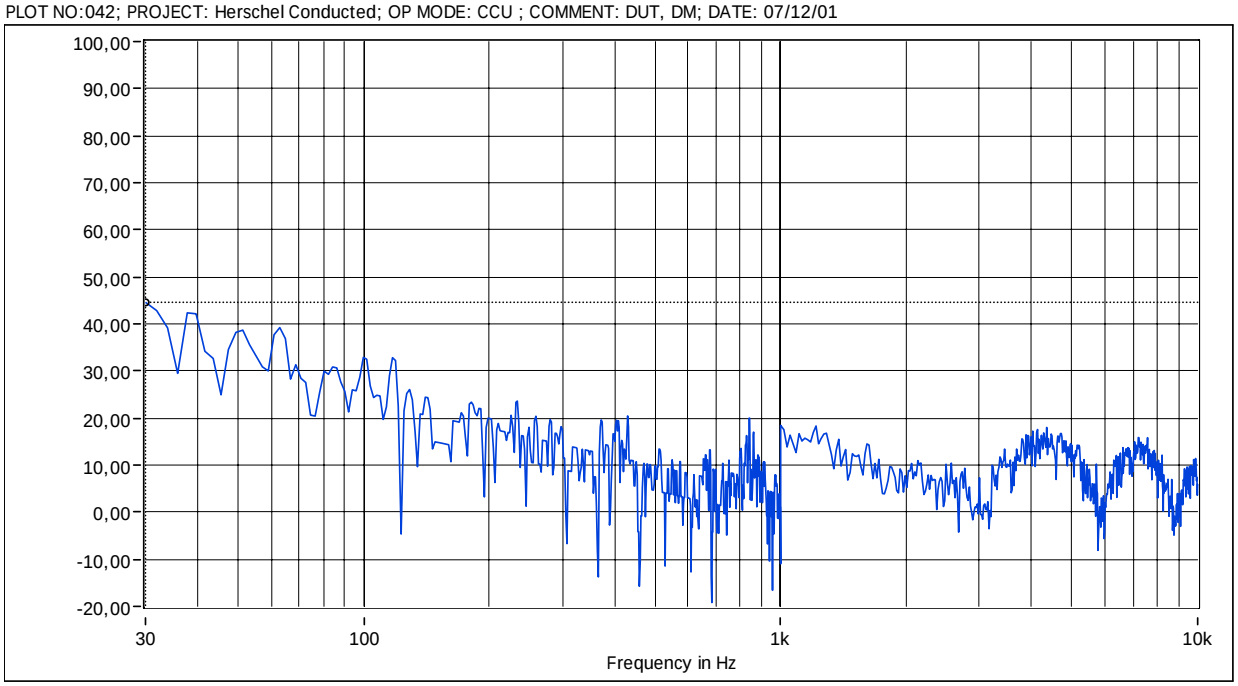
PLOT NO:44; PROJECT: Herschel Conducted; OP MODE: CCU ; COMMENT: DUT,DM; DATE: 07/12/01



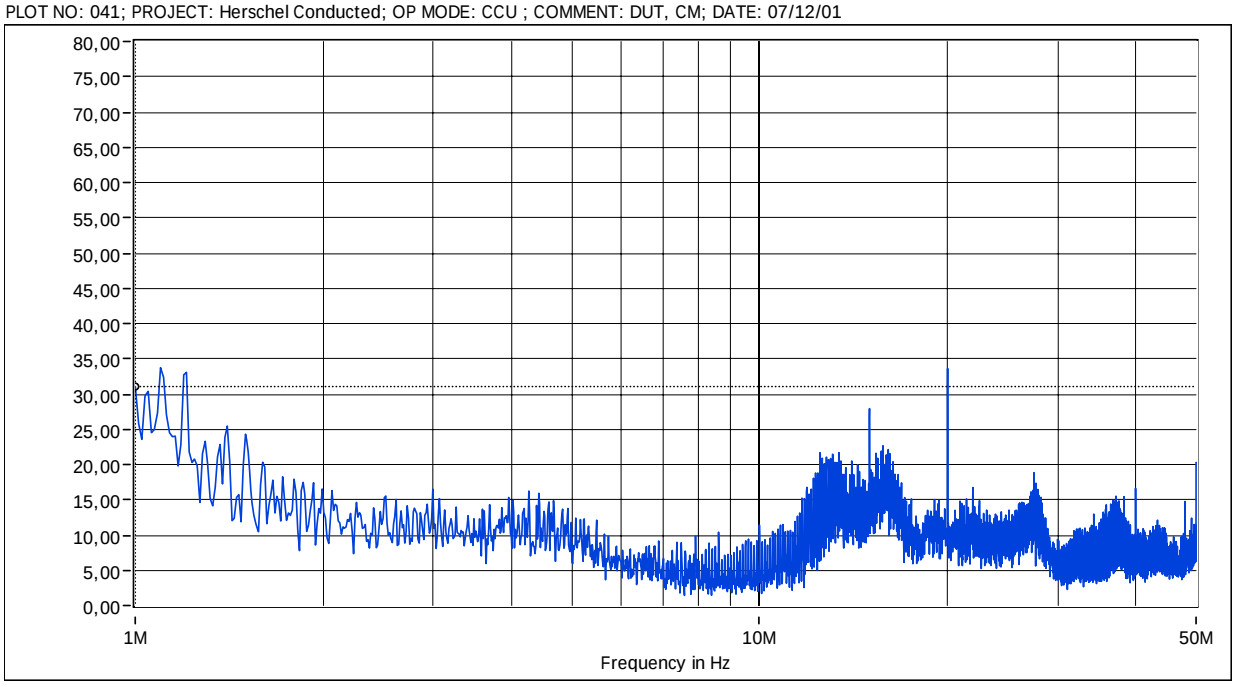
PLOT NO:43; PROJECT: Herschel Conducted; OP MODE: CCU ; COMMENT: DUT, DM; DATE: 07/12/01



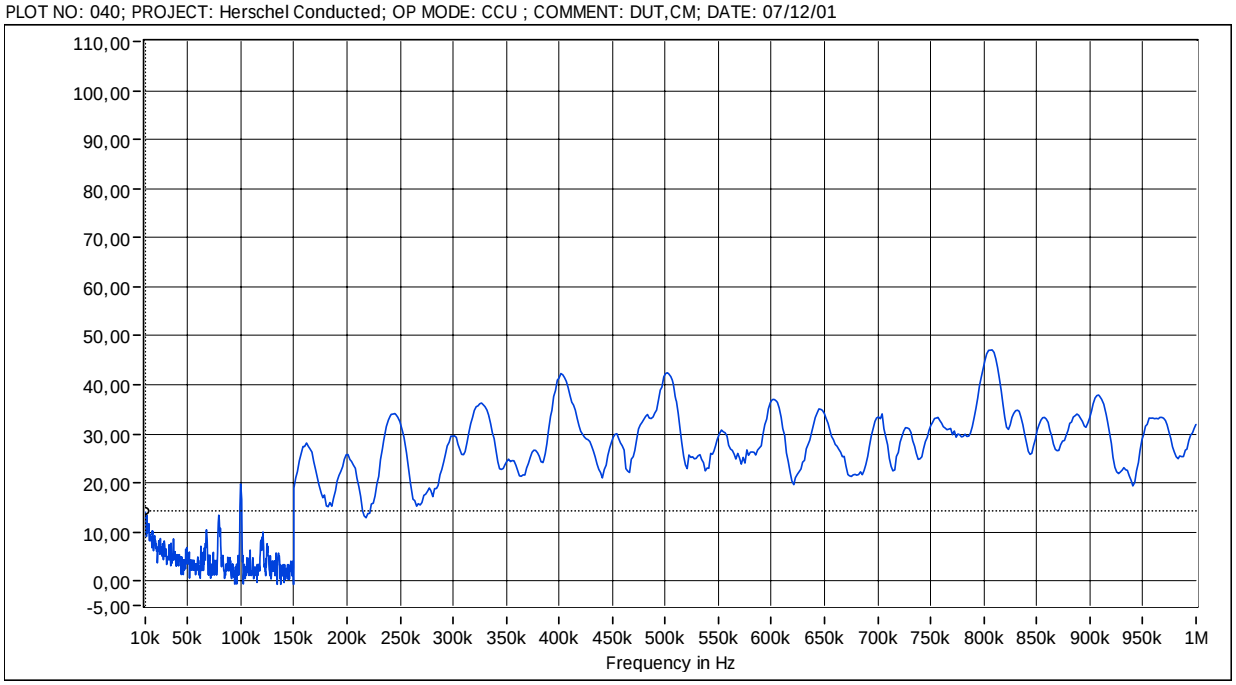
PLOT NO:042; PROJECT: Herschel Conducted; OP MODE: CCU ; COMMENT: DUT, DM; DATE: 07/12/01



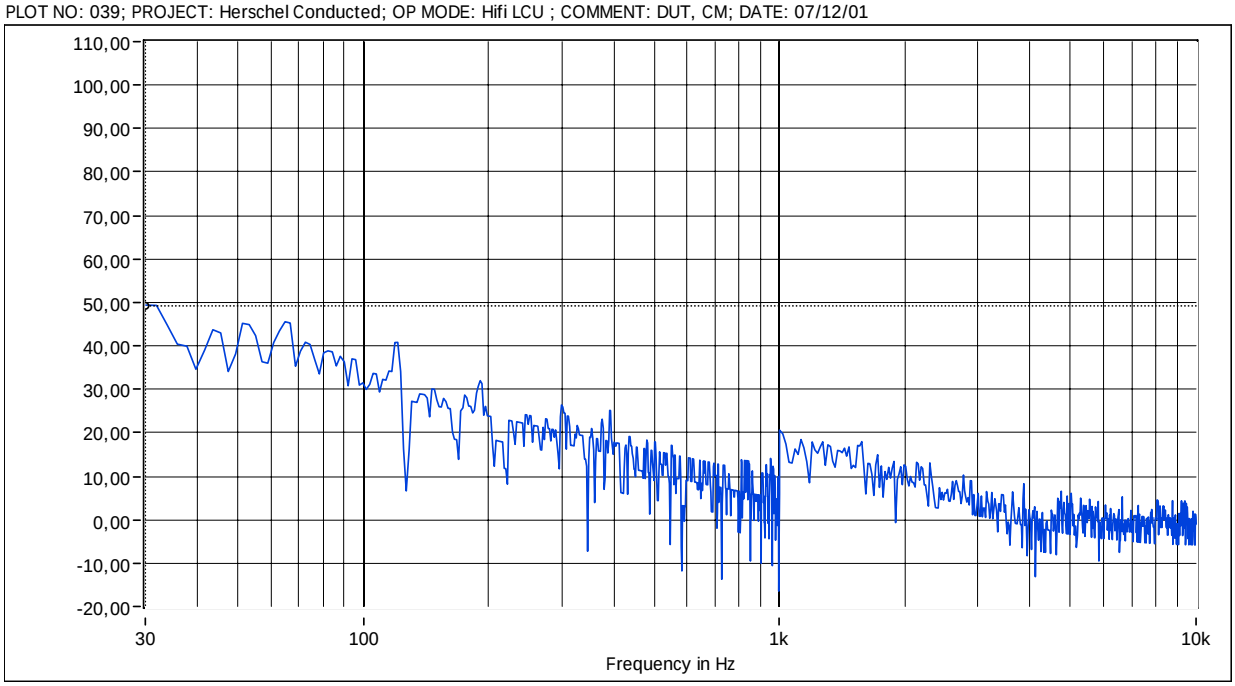
PLOT NO: 041; PROJECT: Herschel Conducted; OP MODE: CCU ; COMMENT: DUT, CM; DATE: 07/12/01



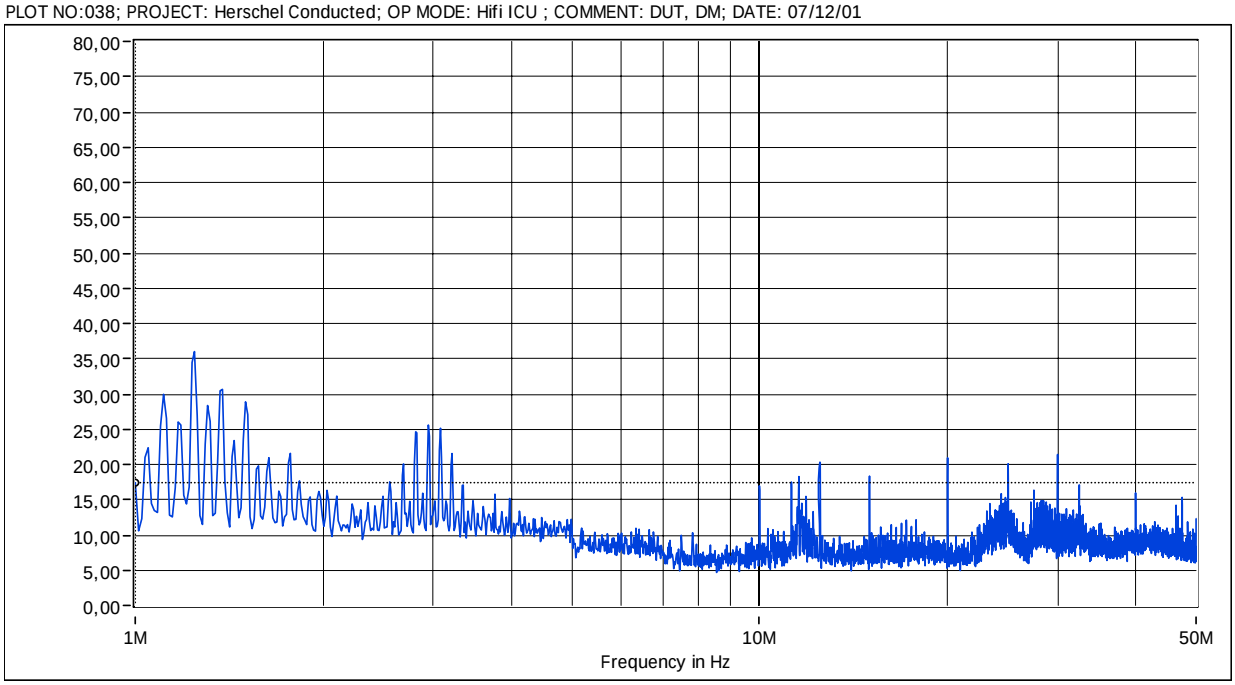
PLOT NO: 040; PROJECT: Herschel Conducted; OP MODE: CCU ; COMMENT: DUT,CM; DATE: 07/12/01



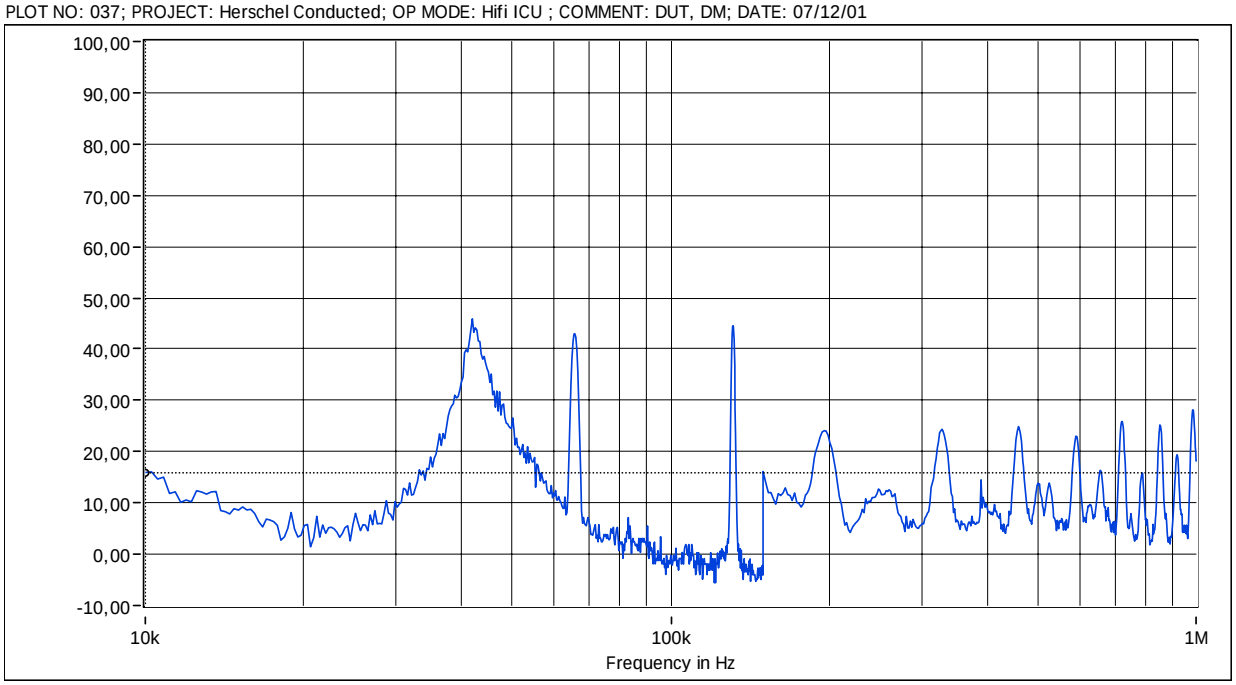
PLOT NO: 039; PROJECT: Herschel Conducted; OP MODE: Hifi LCU ; COMMENT: DUT, CM; DATE: 07/12/01



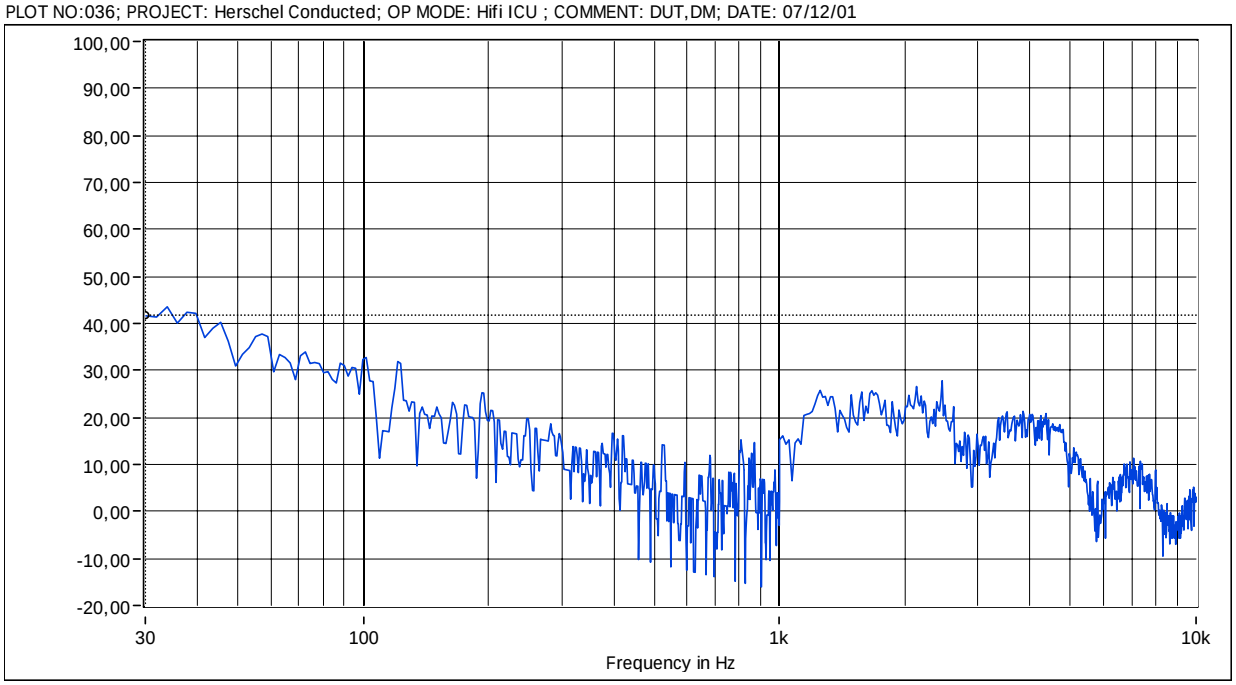
PLOT NO:038; PROJECT: Herschel Conducted; OP MODE: Hifi ICU ; COMMENT: DUT, DM; DATE: 07/12/01



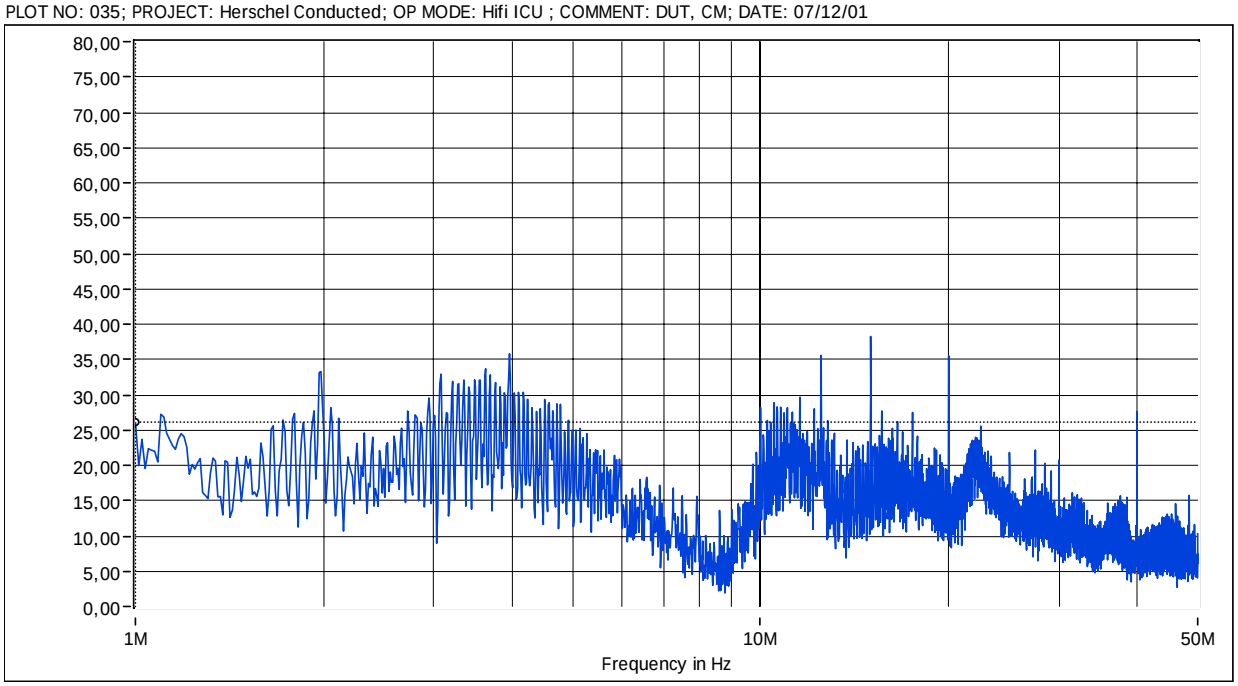
PLOT NO: 037; PROJECT: Herschel Conducted; OP MODE: Hifi ICU ; COMMENT: DUT, DM; DATE: 07/12/01



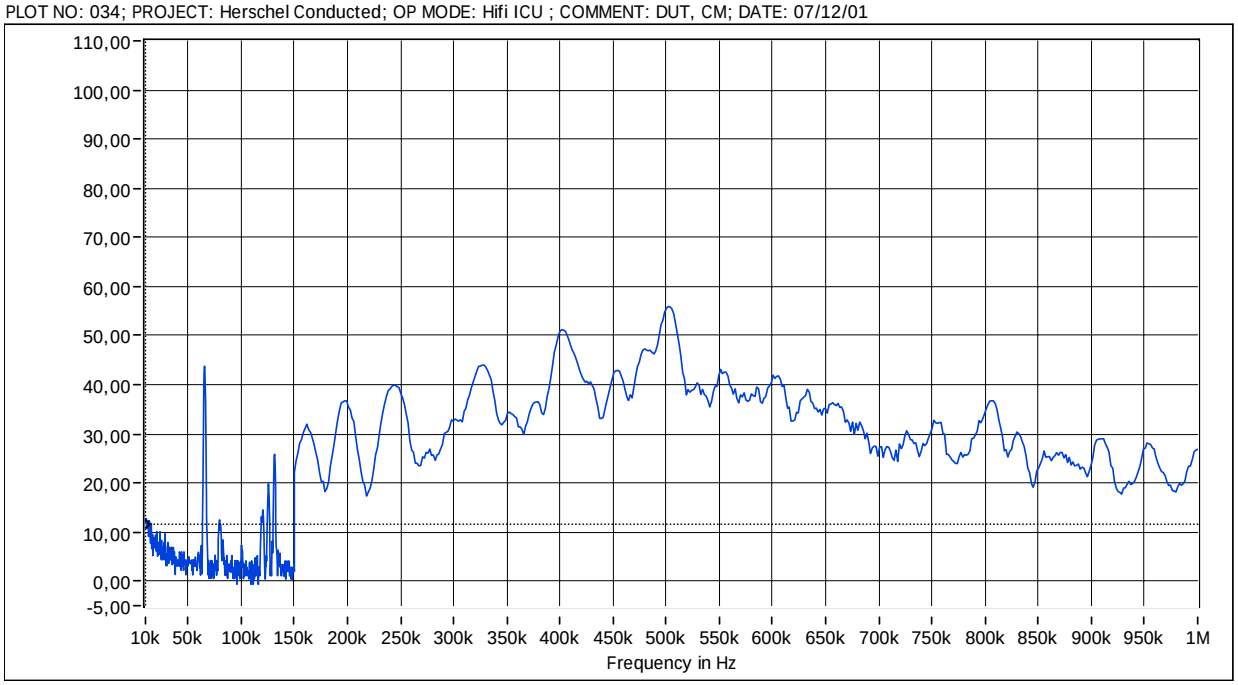
PLOT NO:036; PROJECT: Herschel Conducted; OP MODE: Hifi ICU ; COMMENT: DUT,DM; DATE: 07/12/01



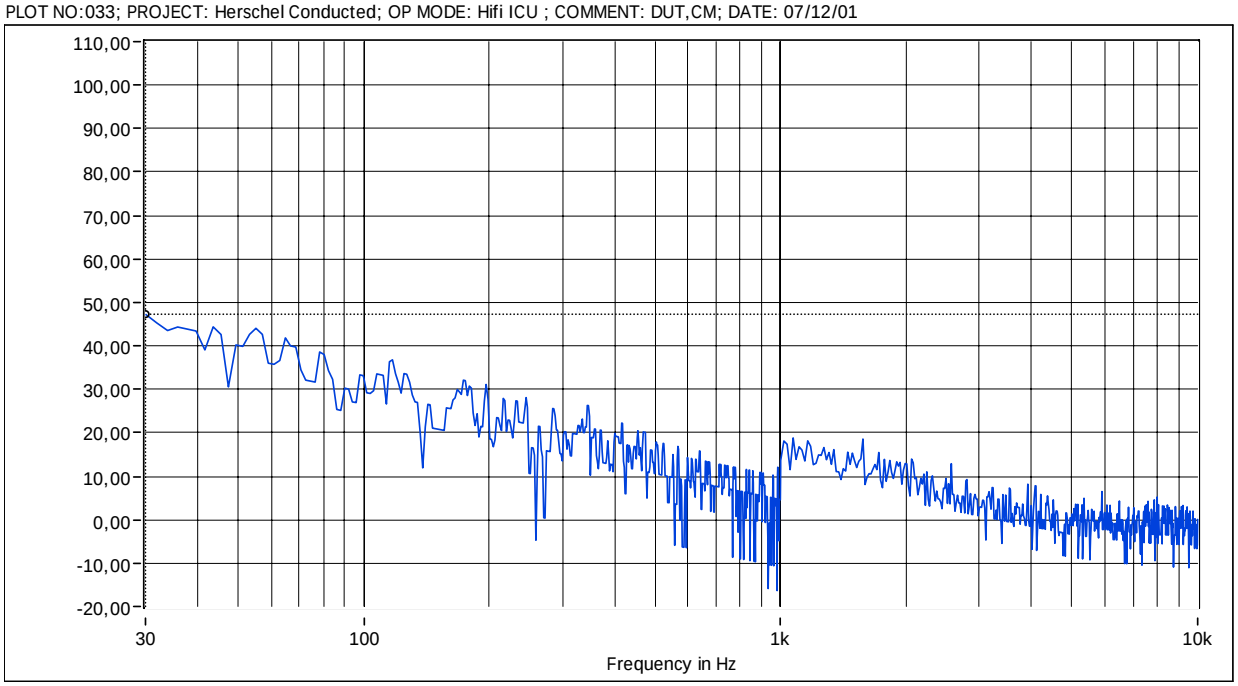
PLOT NO: 035; PROJECT: Herschel Conducted; OP MODE: Hifi ICU ; COMMENT: DUT, CM; DATE: 07/12/01



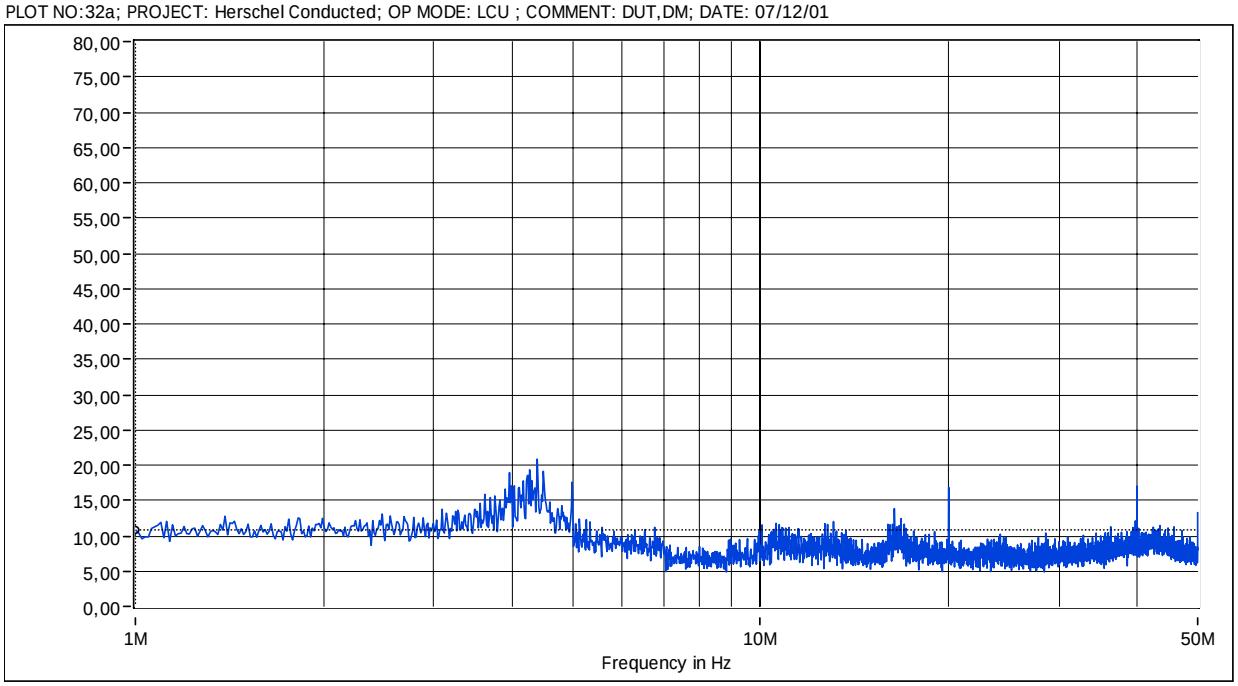
PLOT NO: 034; PROJECT: Herschel Conducted; OP MODE: Hifi ICU ; COMMENT: DUT, CM; DATE: 07/12/01



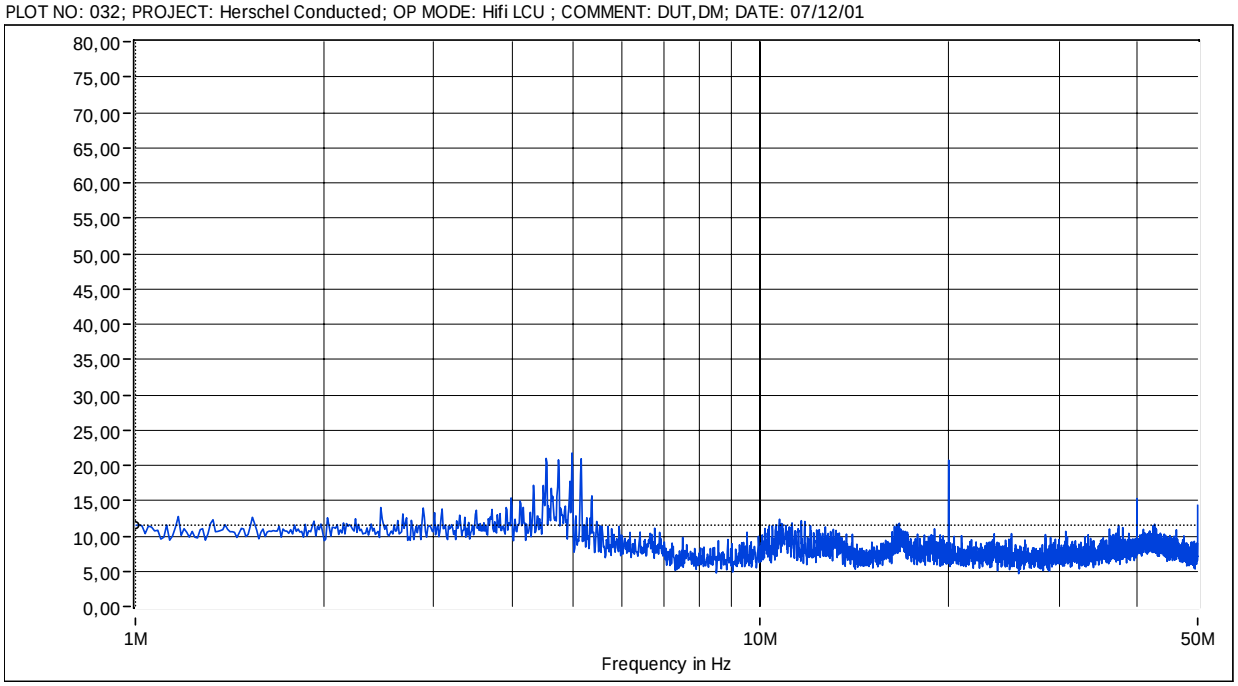
PLOT NO:033; PROJECT: Herschel Conducted; OP MODE: Hifi ICU ; COMMENT: DUT,CM; DATE: 07/12/01



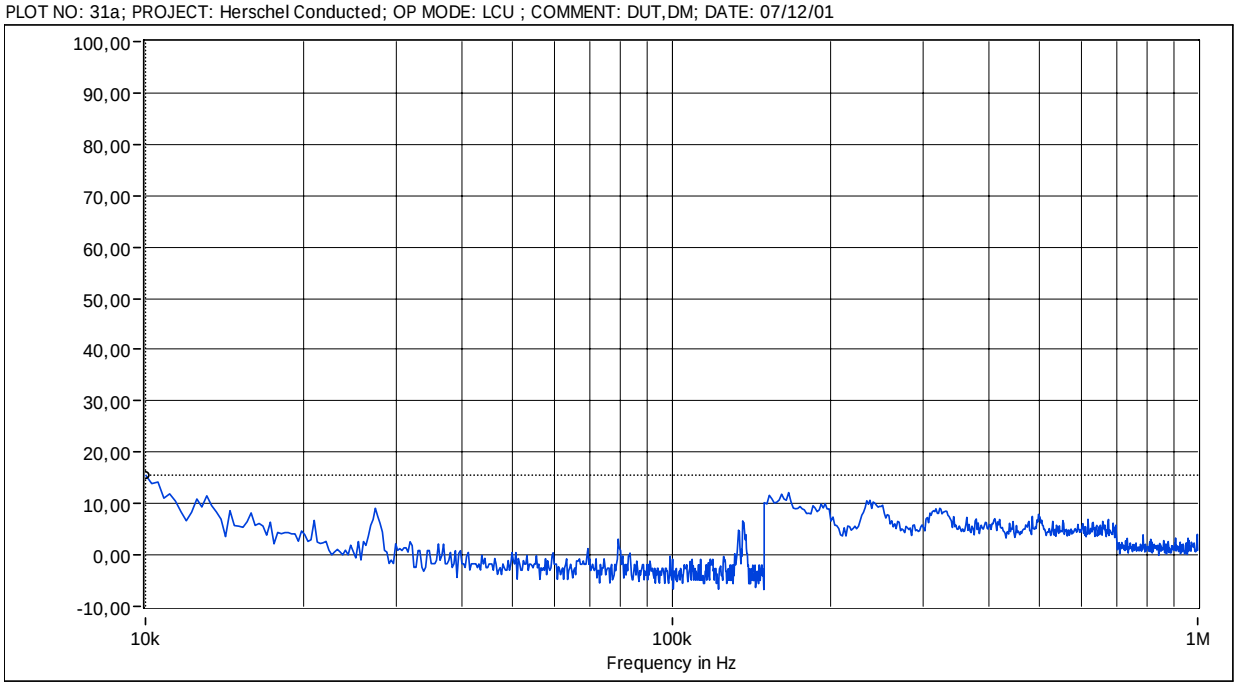
PLOT NO:32a; PROJECT: Herschel Conducted; OP MODE: LCU ; COMMENT: DUT,DM; DATE: 07/12/01



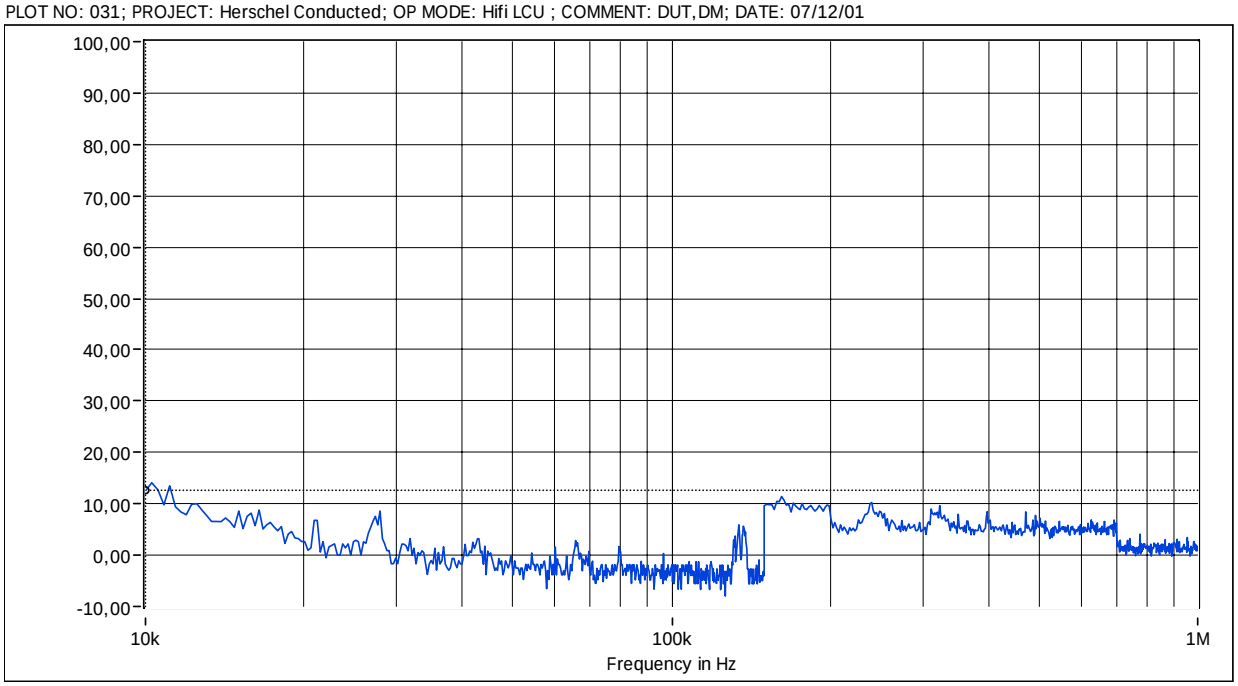
PLOT NO: 032; PROJECT: Herschel Conducted; OP MODE: Hifi LCU ; COMMENT: DUT,DM; DATE: 07/12/01



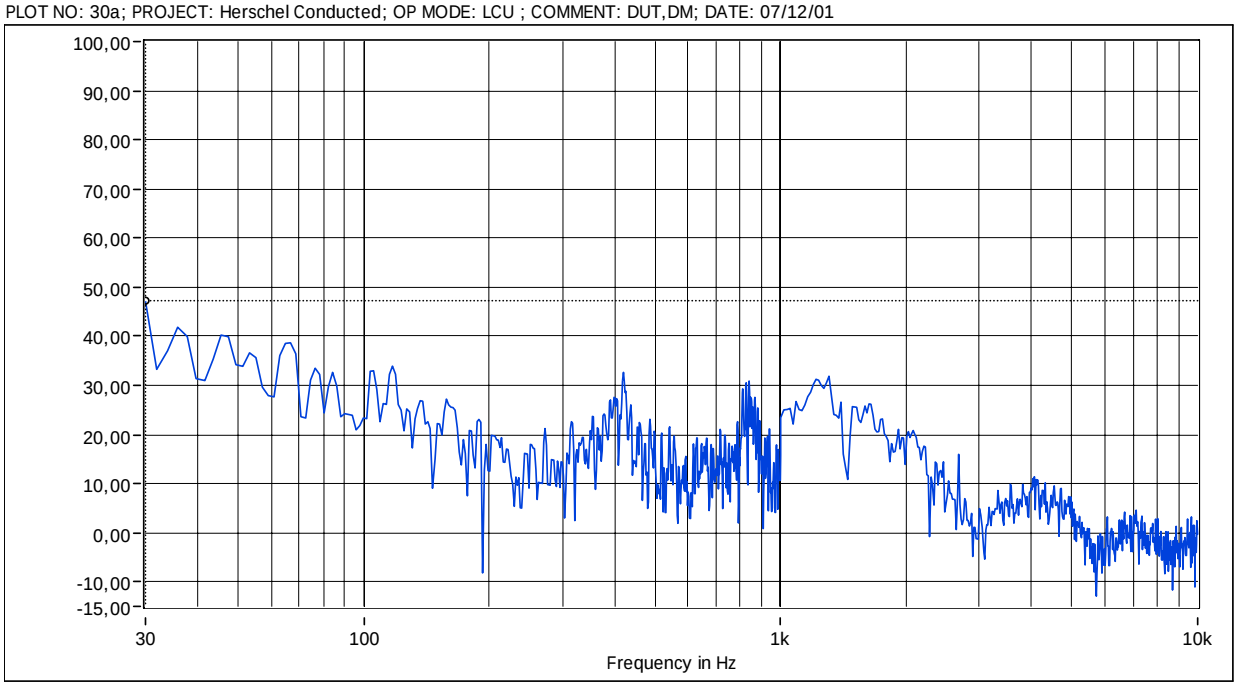
PLOT NO: 31a; PROJECT: Herschel Conducted; OP MODE: LCU ; COMMENT: DUT,DM; DATE: 07/12/01



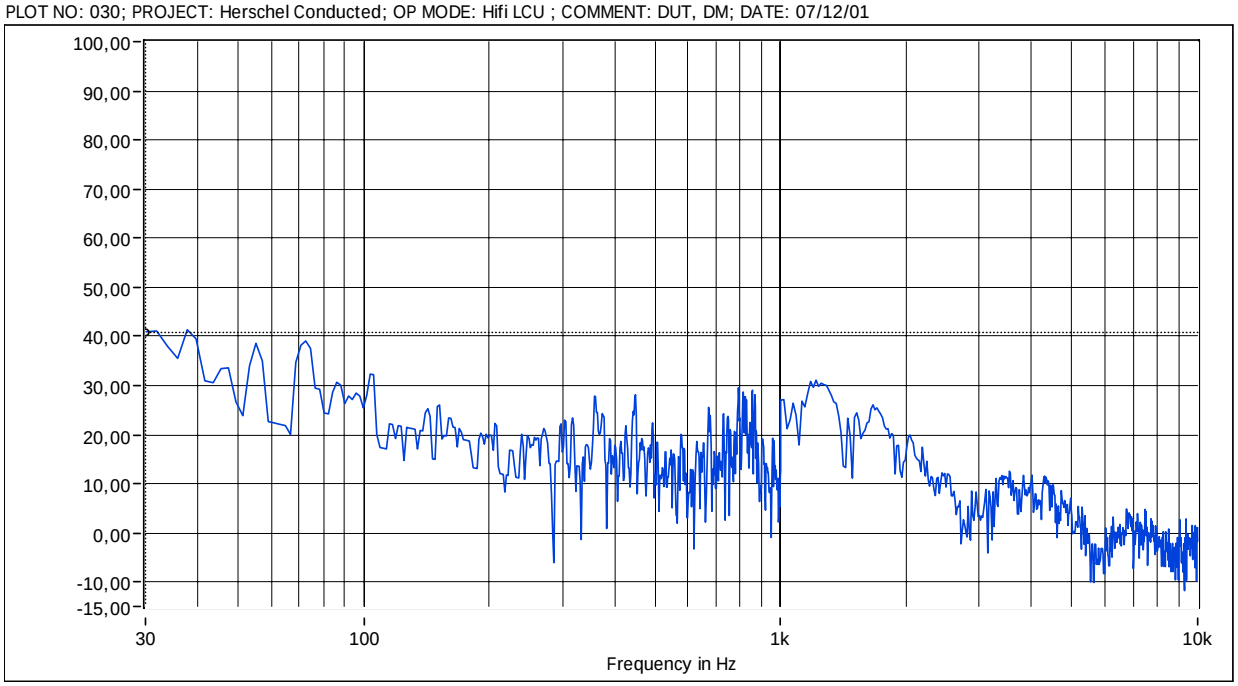
PLOT NO: 031; PROJECT: Herschel Conducted; OP MODE: Hifi LCU ; COMMENT: DUT,DM; DATE: 07/12/01



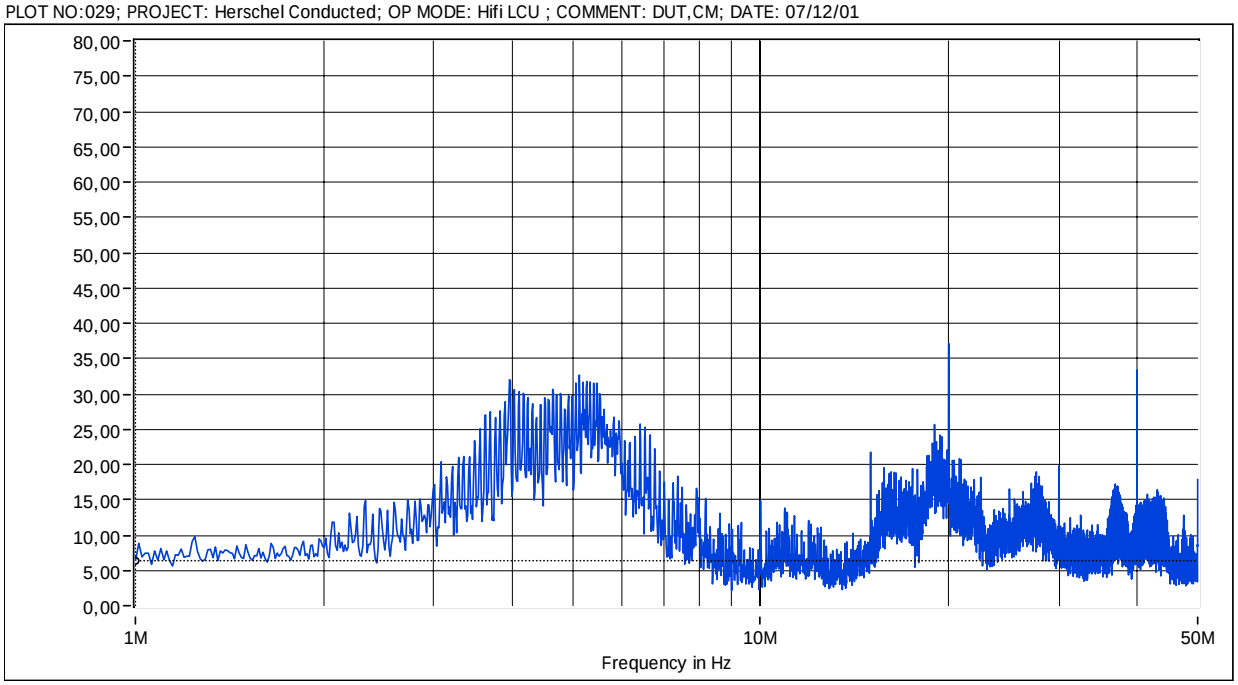
PLOT NO: 30a; PROJECT: Herschel Conducted; OP MODE: LCU ; COMMENT: DUT,DM; DATE: 07/12/01



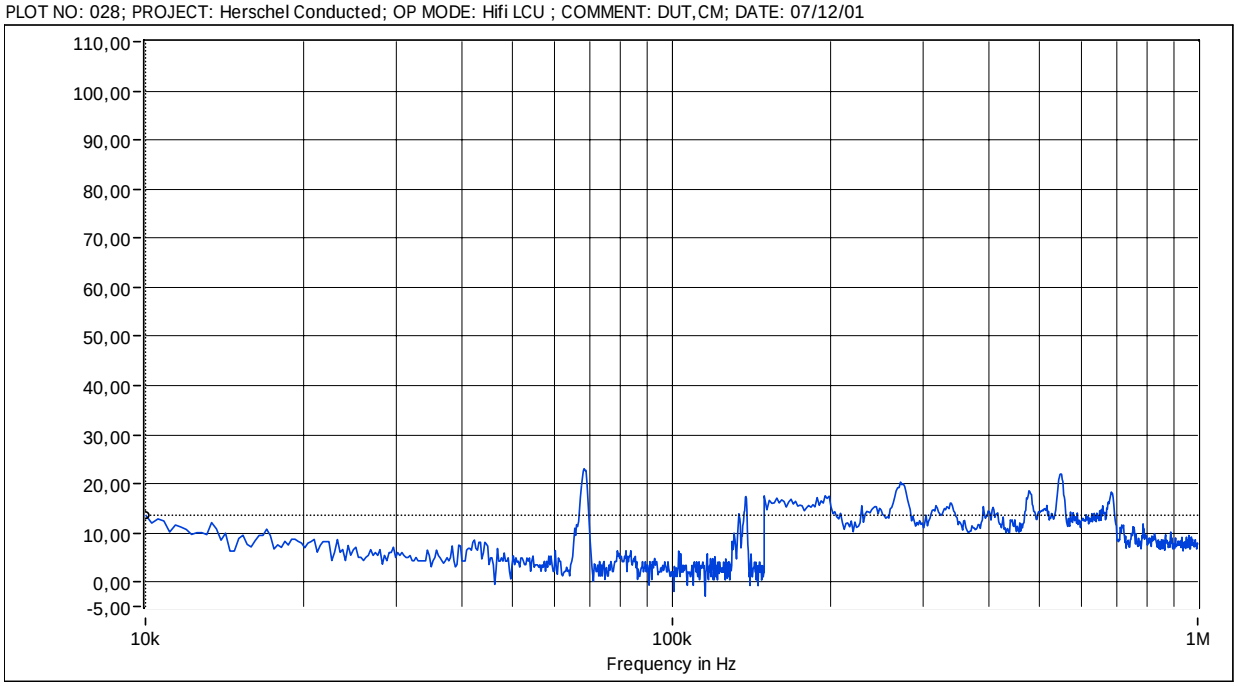
PLOT NO: 030; PROJECT: Herschel Conducted; OP MODE: Hifi LCU ; COMMENT: DUT, DM; DATE: 07/12/01



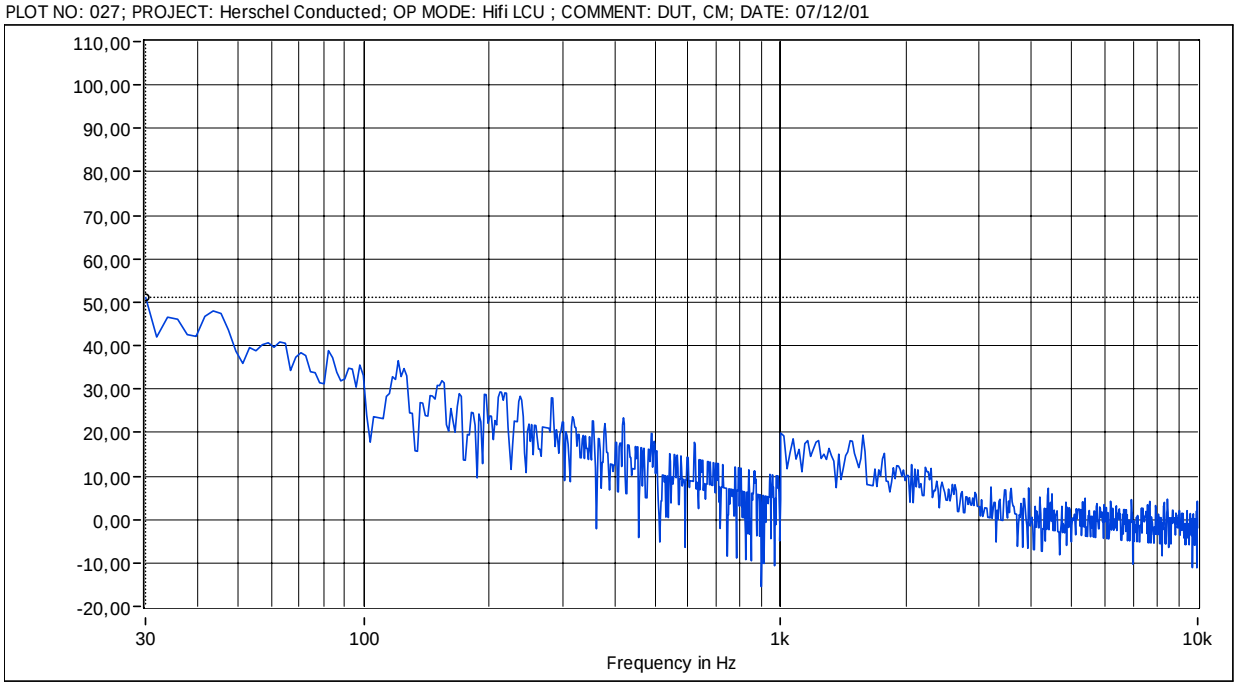
PLOT NO:029; PROJECT: Herschel Conducted; OP MODE: Hifi LCU ; COMMENT: DUT,CM; DATE: 07/12/01



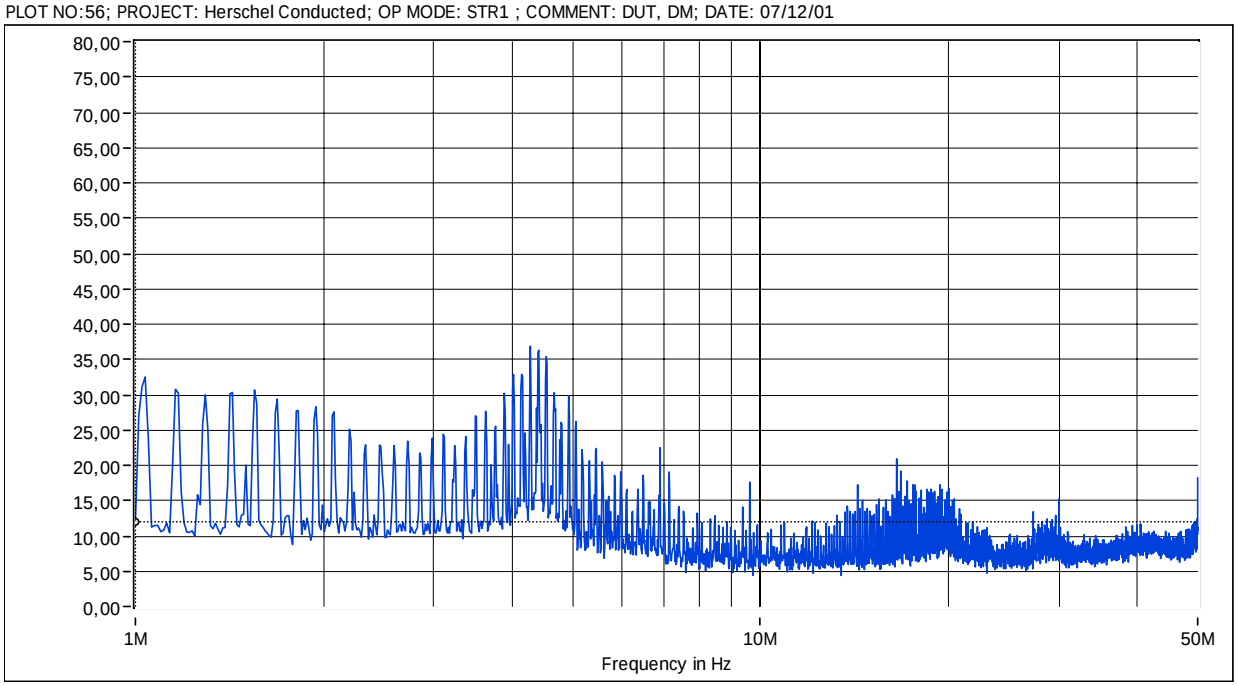
PLOT NO: 028; PROJECT: Herschel Conducted; OP MODE: Hifi LCU ; COMMENT: DUT,CM; DATE: 07/12/01

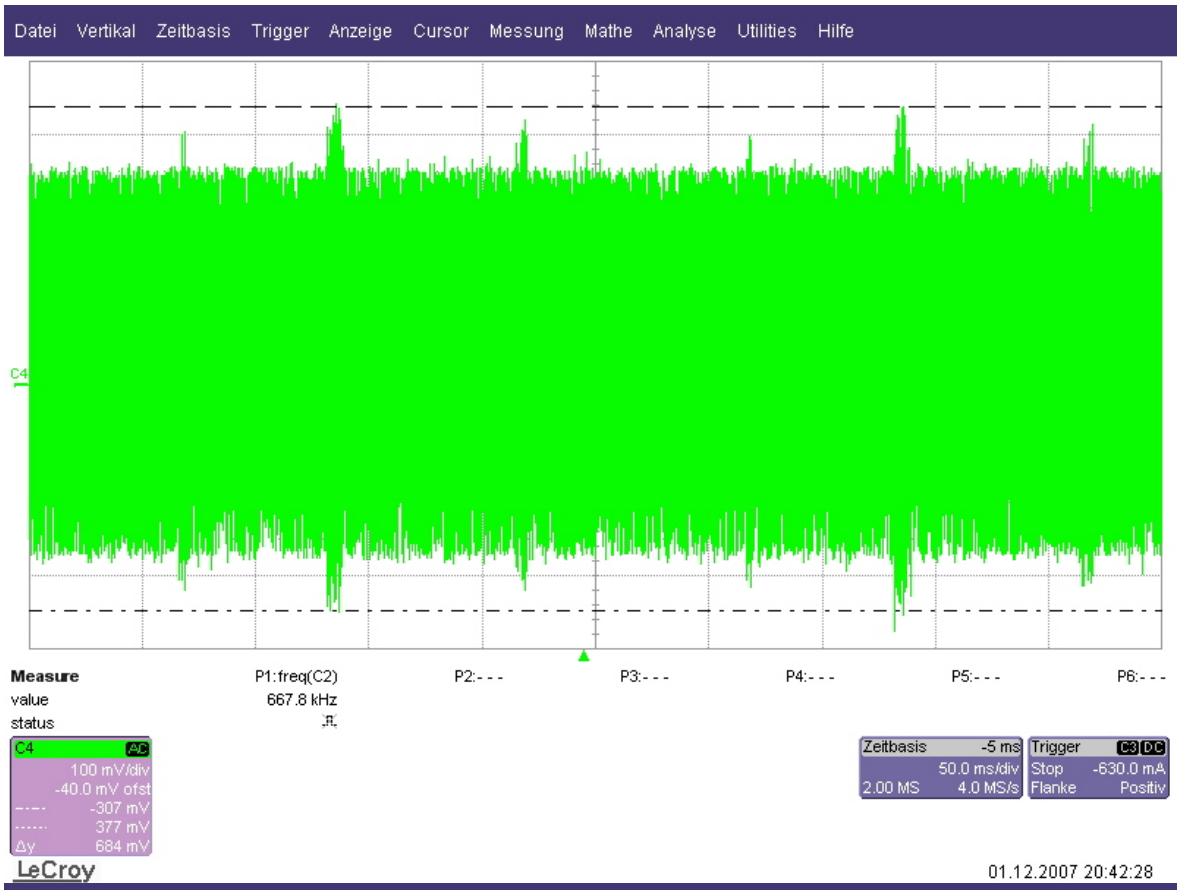


PLOT NO: 027; PROJECT: Herschel Conducted; OP MODE: Hifi LCU ; COMMENT: DUT, CM; DATE: 07/12/01

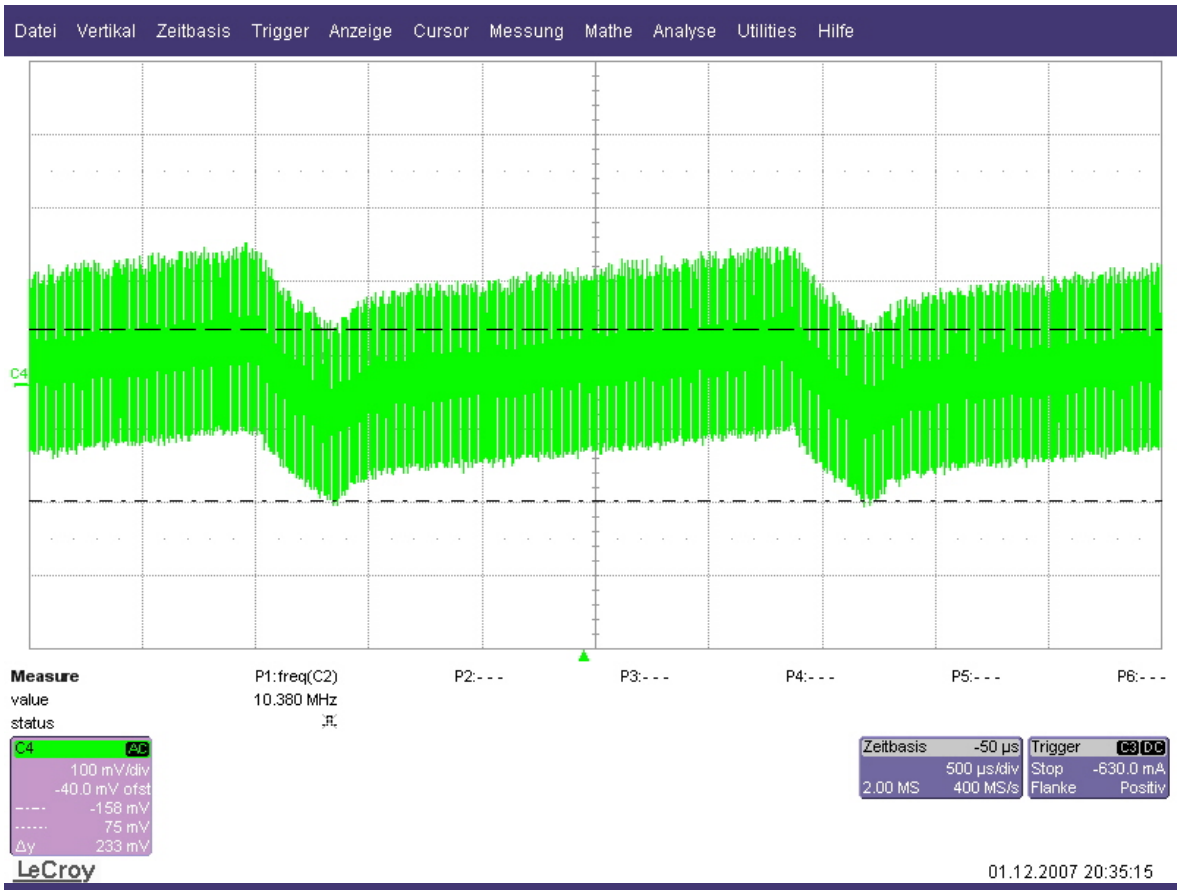


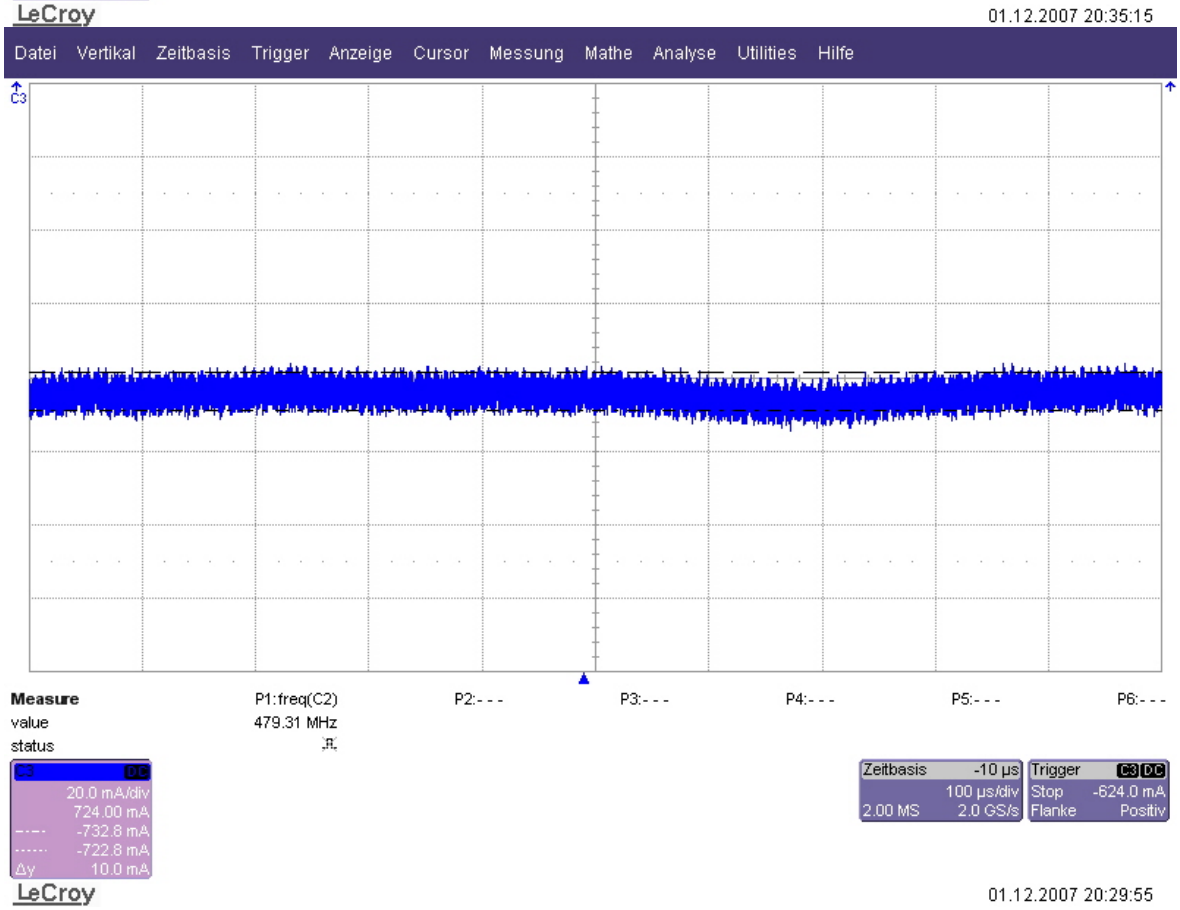
PLOT NO:56; PROJECT: Herschel Conducted; OP MODE: STR1 ; COMMENT: DUT, DM; DATE: 07/12/01

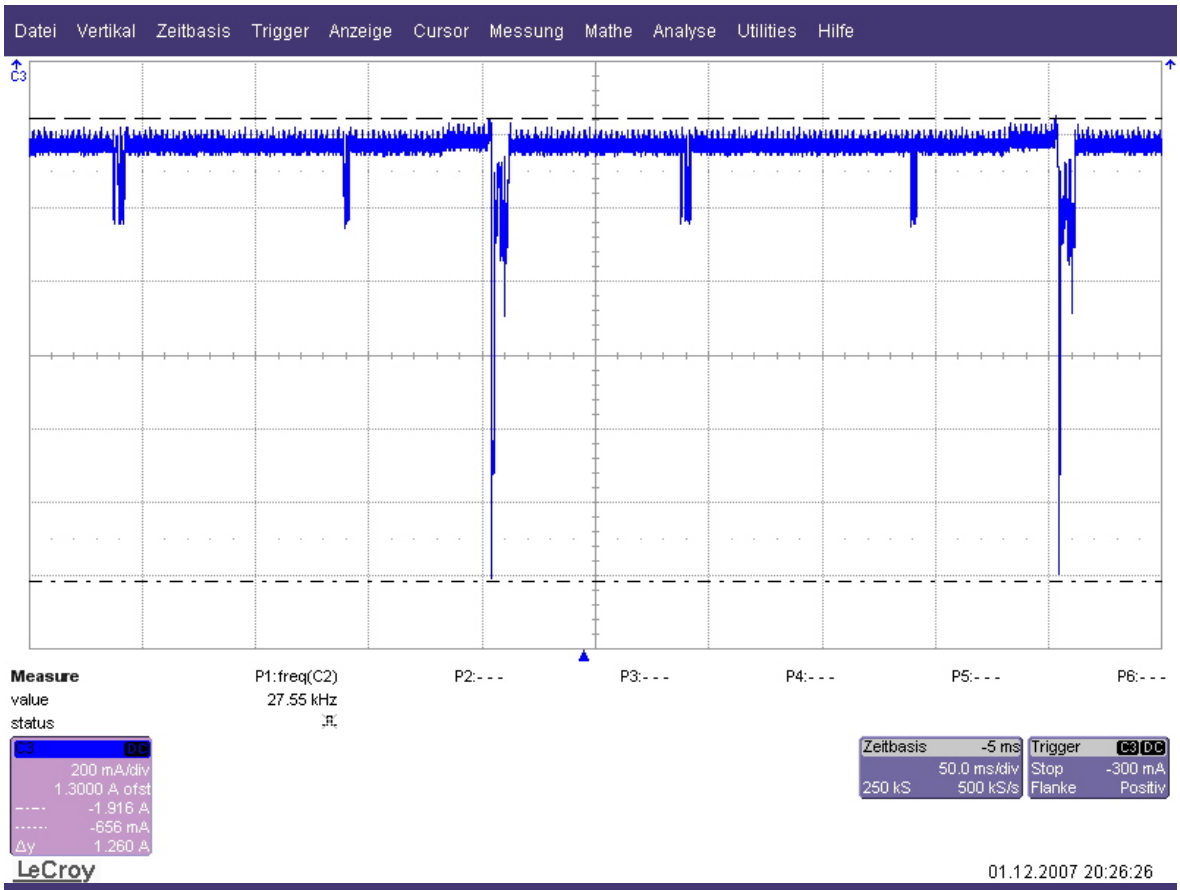


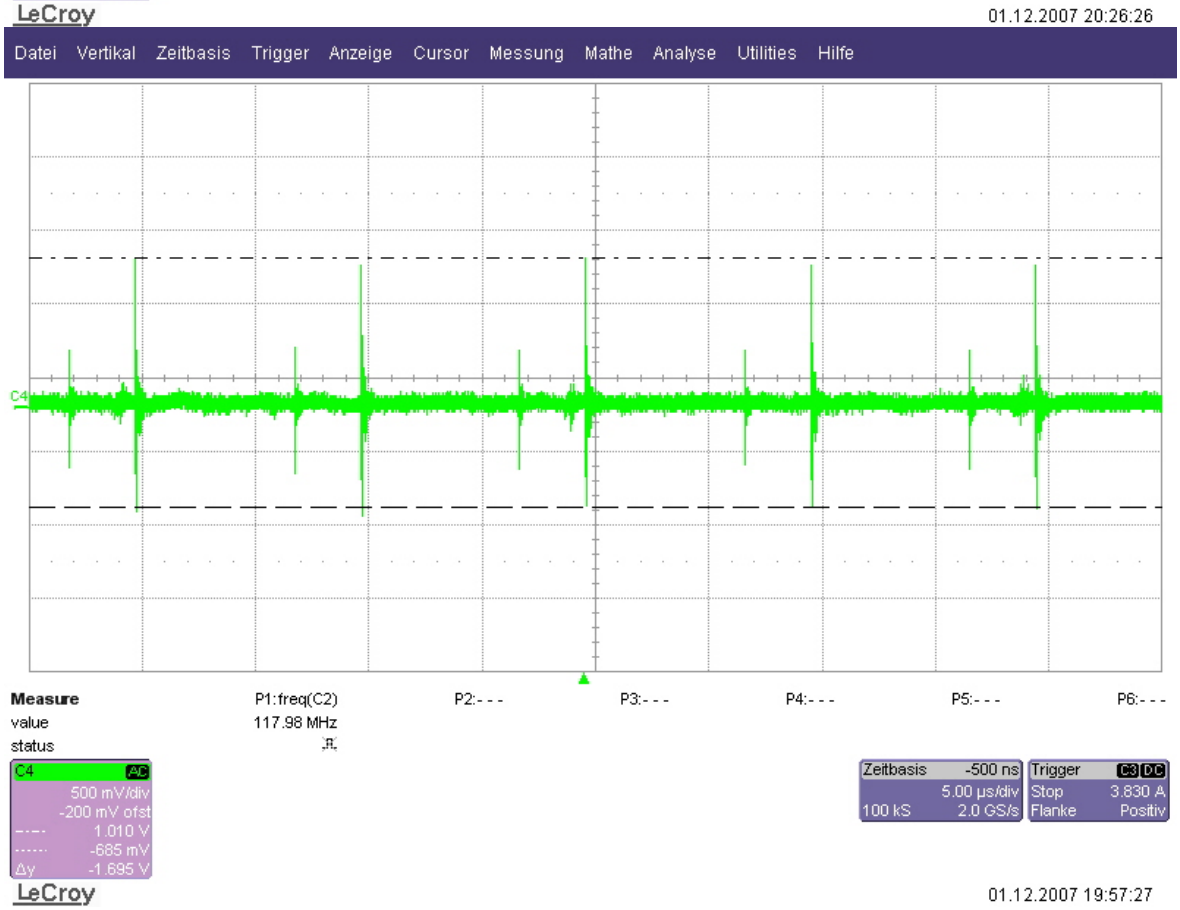

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 667.8 kHz
status
C4 AC
100 mV/div
-40.0 mV ofst
-307 mV
377 mV
Δy 684 mV
Zeitbasis -5 ms
50.0 ms/div
2.00 MS 4.0 MS/s
Trigger C3 DC
Stop -630.0 mA
Flanke Positiv
LeCroy
01.12.2007 20:42:28

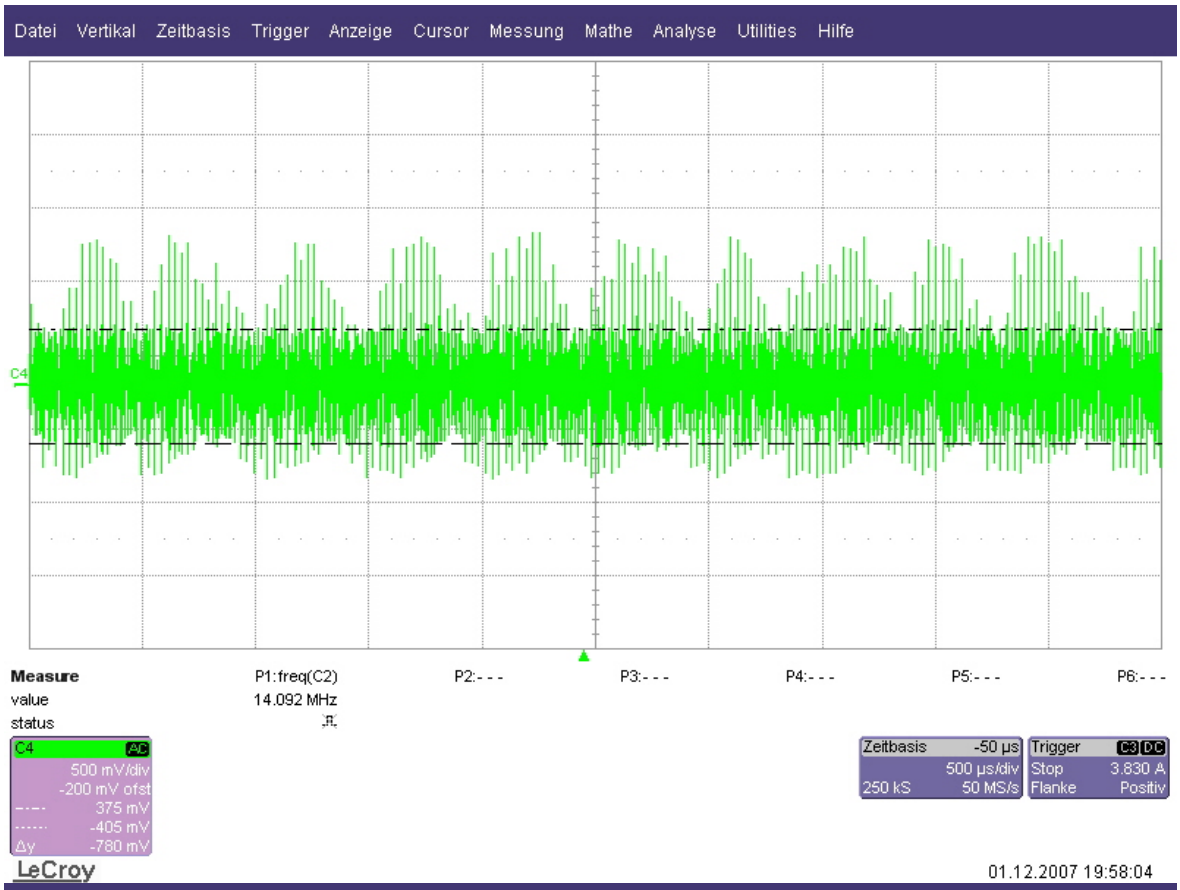

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 24.599 MHz
status
C4 AC
100 mV/div
-40.0 mV ofst
-97 mV
151 mV
Δy 248 mV
Zeitbasis -200 ns
2.00 µs/div
40.0 kS 2.0 GS/s
Trigger C3 DC
Stop -630.0 mA
Flanke Positiv
LeCroy
01.12.2007 20:35:48


Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure
value
status
P1:freq(C2)
10.380 MHz
P2:- - -
P3:- - -
P4:- - -
P5:- - -
P6:- - -
C4 AC
100 mV/div
-40.0 mV ofst
-158 mV
75 mV
Δy 233 mV
Zeitbasis -50 µs
500 µs/div
2.00 MS 400 MS/s
Trigger C3 DC
Stop -630.0 mA
Flanke Positiv
LeCroy
01.12.2007 20:35:15

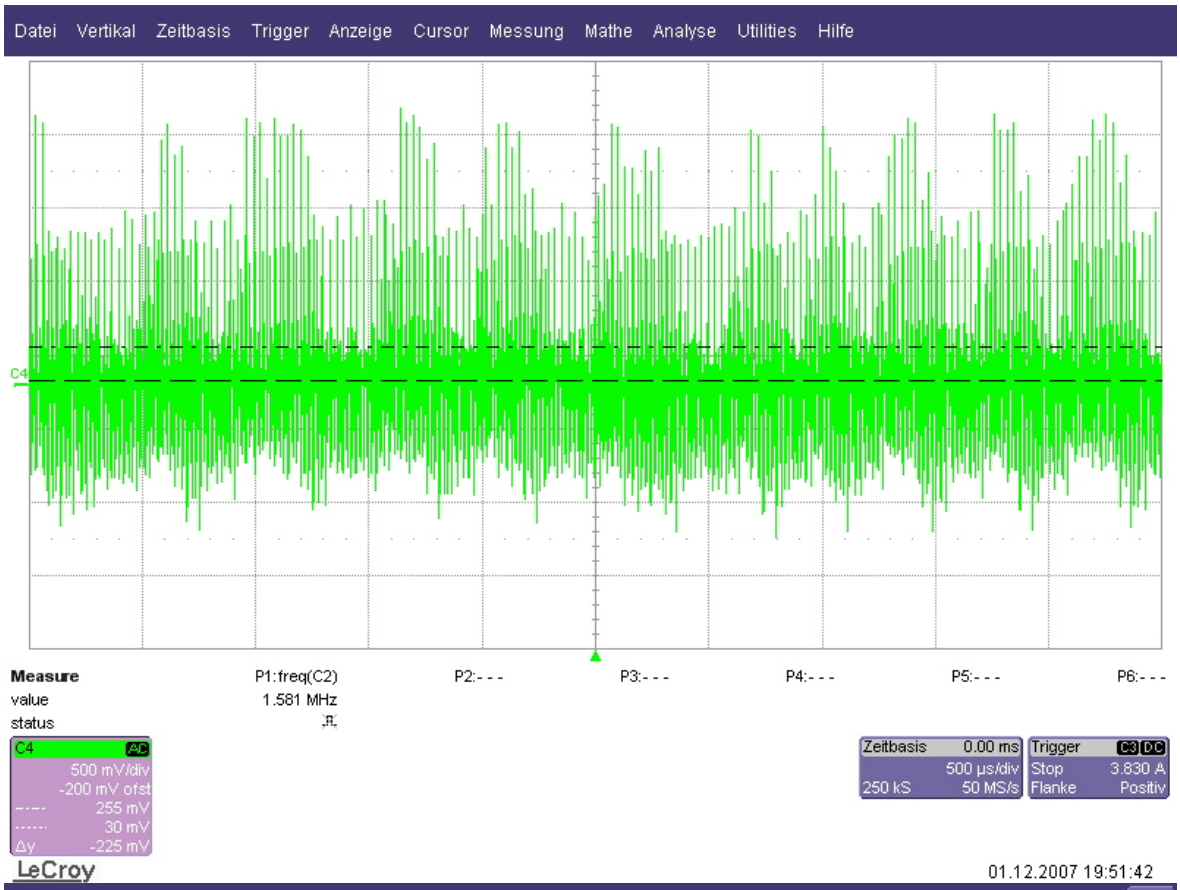

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
Measure
value
status
P1:freq(C2)
479.31 MHz
P2:- - -
P3:- - -
P4:- - -
P5:- - -
P6:- - -
C3 DC
20.0 mA/div
724.00 mA
-732.8 mA
-722.8 mA
Δy 10.0 mA
Zeitbasis -10 µs
100 µs/div
2.00 MS 2.0 GS/s
Trigger C3 DC
Stop -624.0 mA
Flanke Positiv
LeCroy
01.12.2007 20:29:55

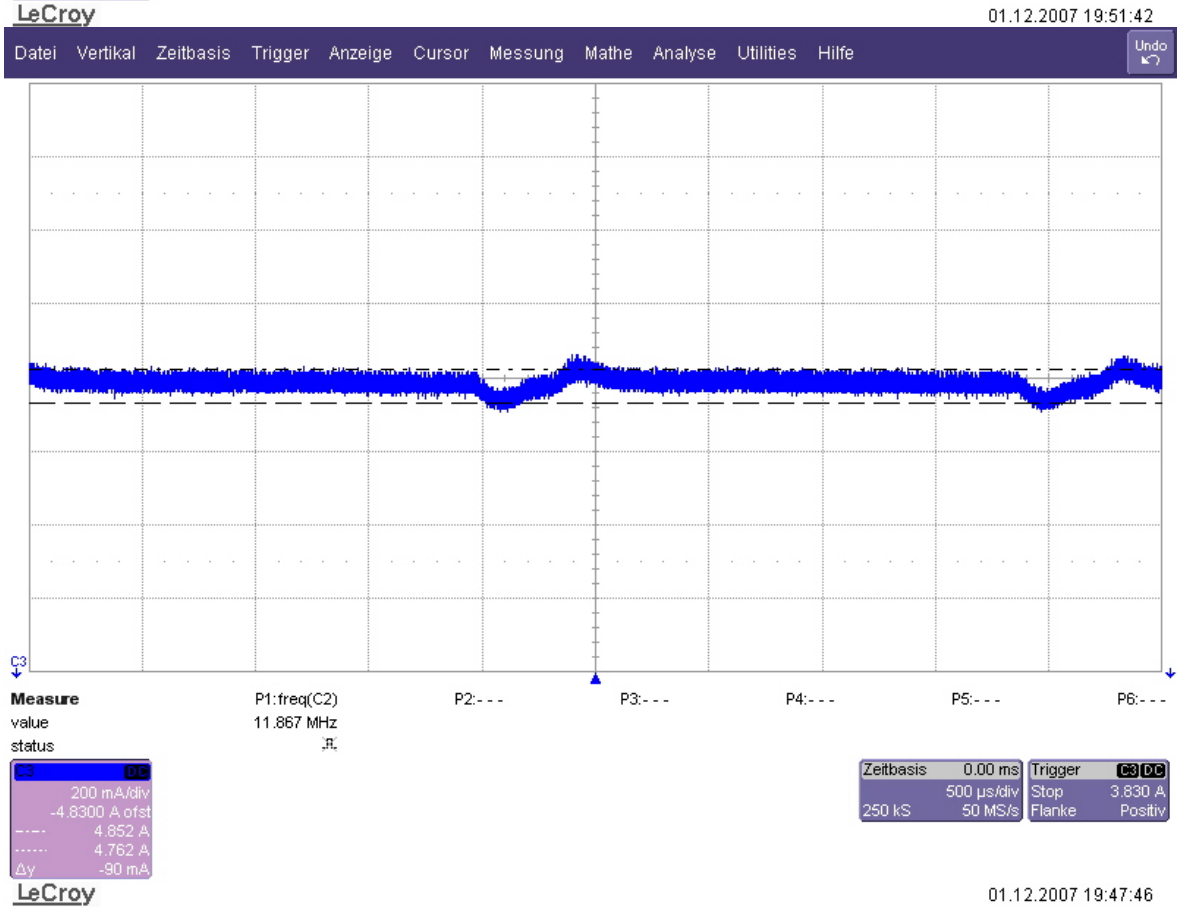

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 27.55 kHz
status
C3 DC
200 mA/div
1.3000 A ofst
-1.916 A
-656 mA
Δy 1.260 A
Zeitbasis -5 ms
50.0 ms/div
250 kS 500 kS/s
Trigger C3 DC
Stop -300 mA
Flanke Positiv
LeCroy
01.12.2007 20:26:26

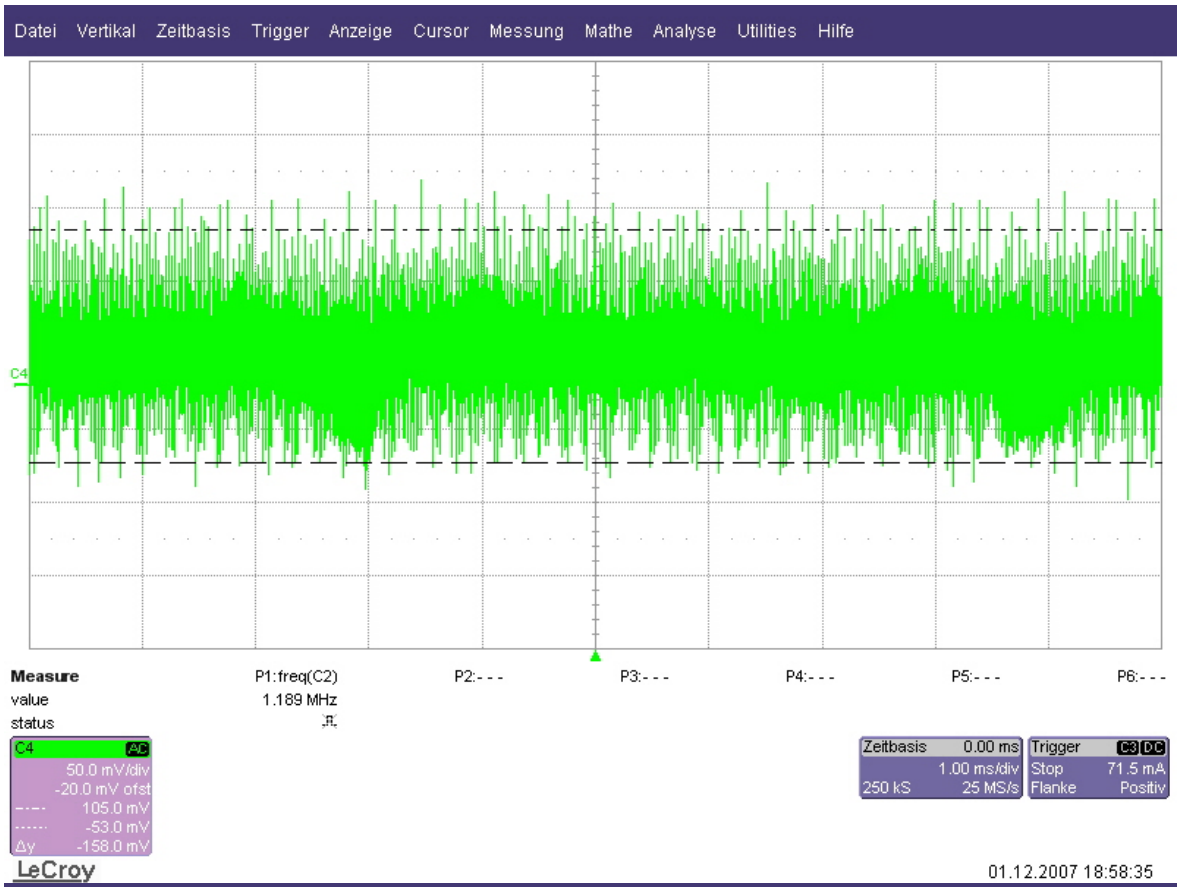

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 117.98 MHz
status
C4 AC
500 mV/div
-200 mV ofst
1.010 V
-685 mV
Δy -1.695 V
Zeitbasis -500 ns
5.00 µs/div
100 kS 2.0 GS/s
Trigger C3 DC
Stop 3.830 A
Flanke Positiv
LeCroy
01.12.2007 19:57:27

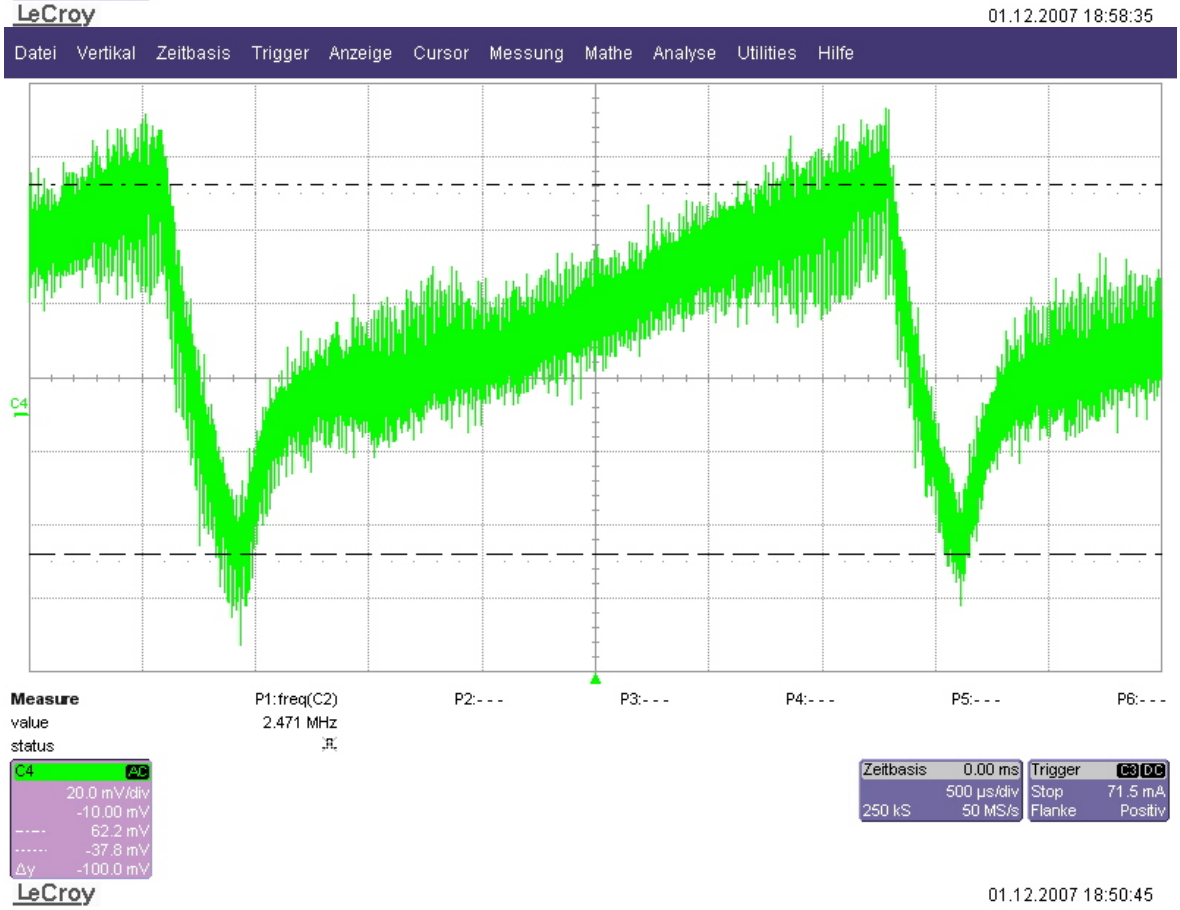

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 14.092 MHz
status
C4 AC
500 mV/div
-200 mV ofst
375 mV
-405 mV
Δy -780 mV
Zeitbasis -50 µs
500 µs/div
250 kS 50 MS/s
Trigger C3 DC
Stop 3.830 A
Flanke Positiv
LeCroy
01.12.2007 19:58:04

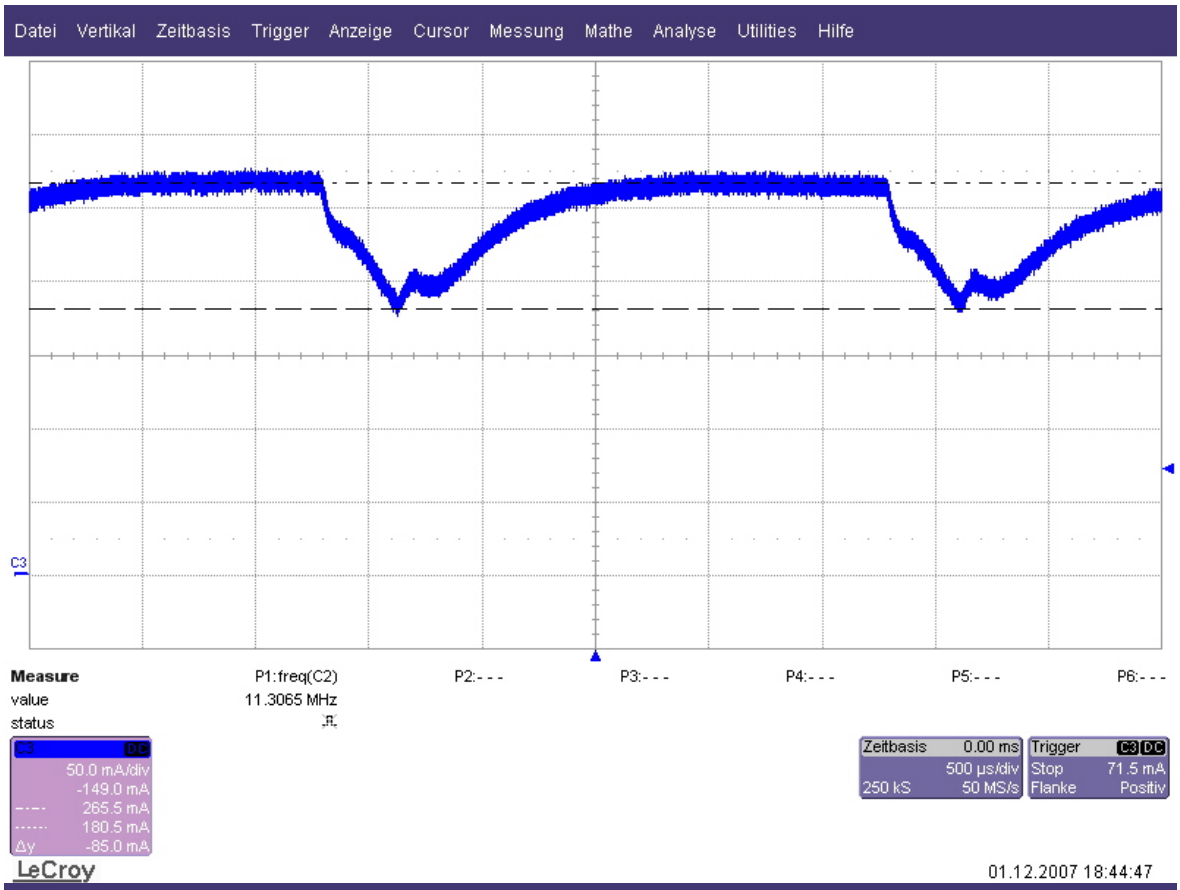

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 535.00 MHz
status
C4 AC
500 mV/div
-200 mV ofst
1.875 V
-875 mV
Δy -2.750 V
Zeitbasis -500 ns
5.00 µs/div
100 kS 2.0 GS/s
Trigger C3 DC
Stop 3.830 A
Flanke Positiv
LeCroy
01.12.2007 19:53:19

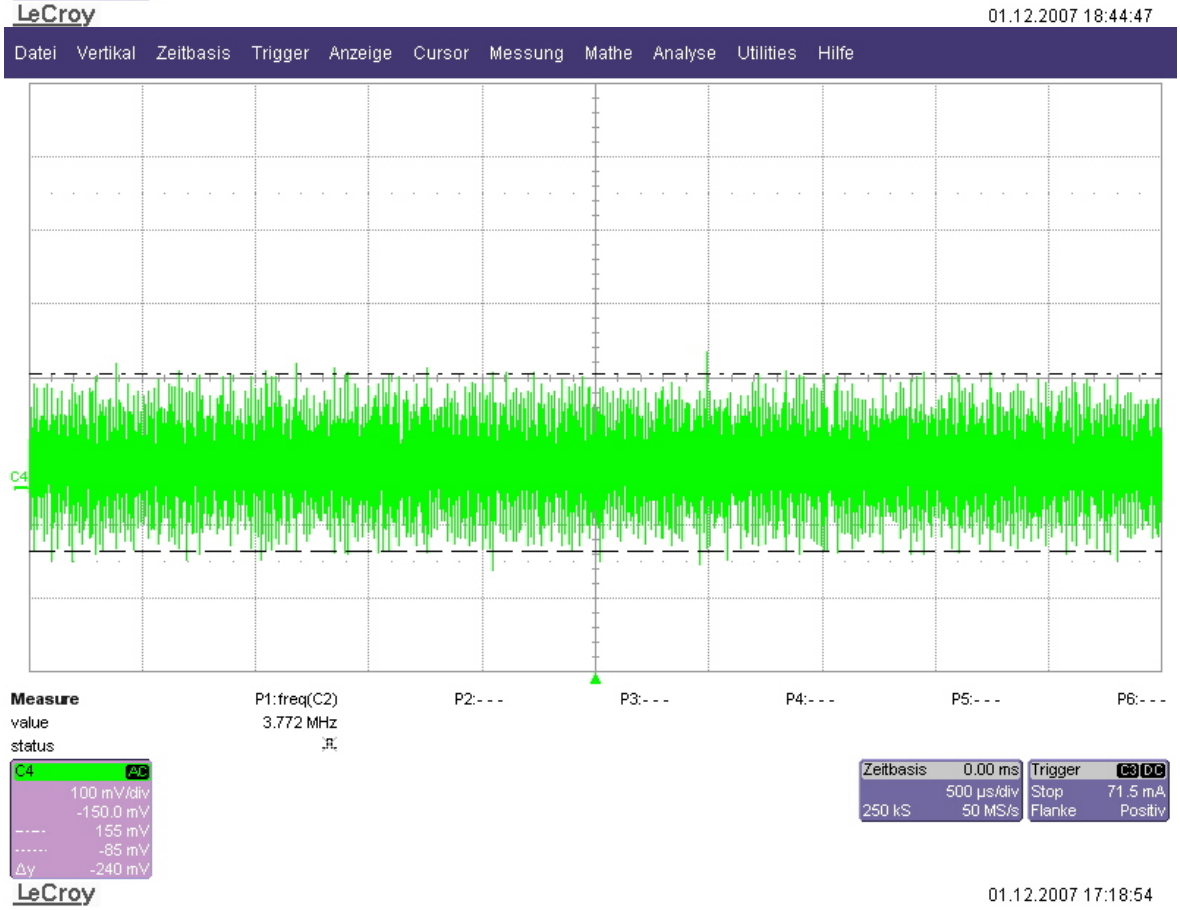

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure
value
status
P1:freq(C2)
1.581 MHz
P2:- - -
P3:- - -
P4:- - -
P5:- - -
P6:- - -
C4 AC
500 mV/div
-200 mV ofst
255 mV
30 mV
Δy -225 mV
Zeitbasis 0.00 ms
500 µs/div
250 kS 50 MS/s
Trigger C3 DC
Stop 3.830 A
Flanke Positiv
LeCroy
01.12.2007 19:51:42

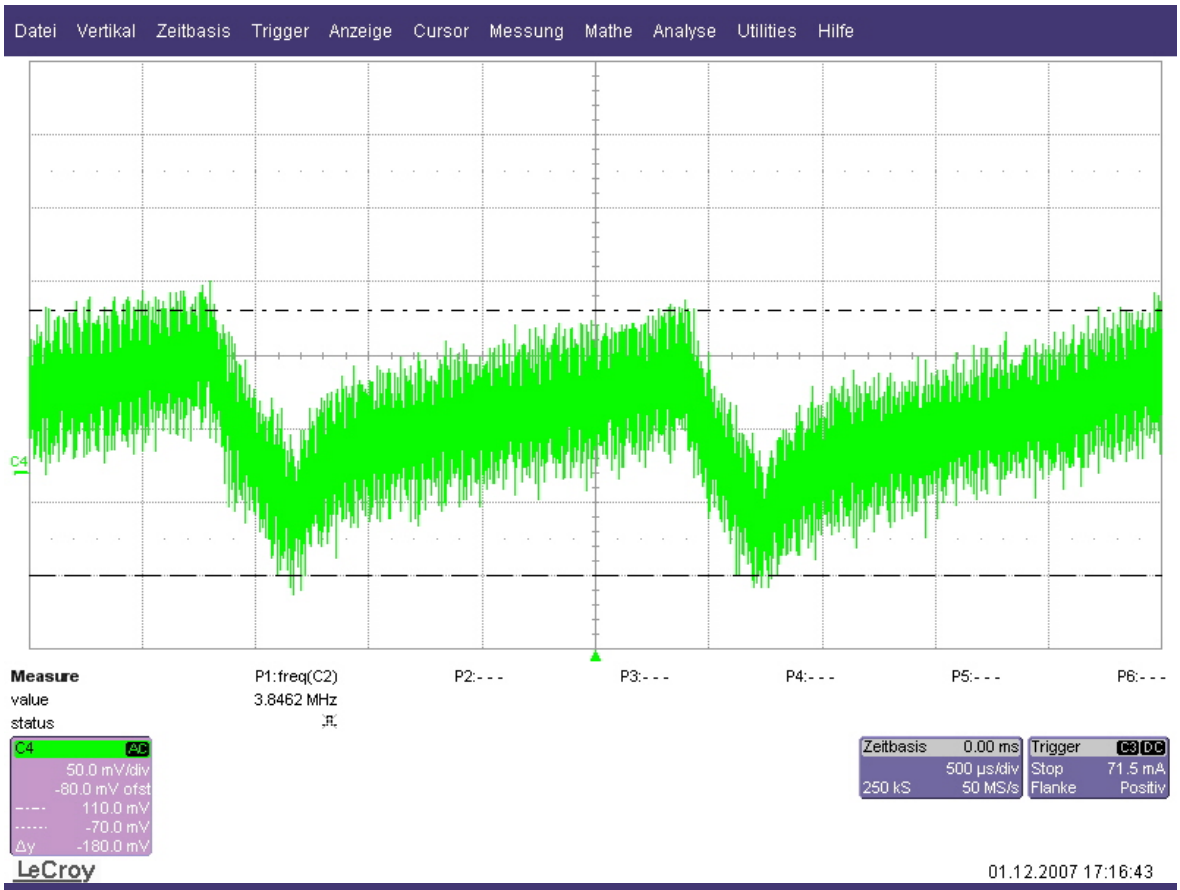

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
Undo
C3
Measure
value
status
P1:freq(C2)
11.867 MHz
P2:- - -
P3:- - -
P4:- - -
P5:- - -
P6:- - -
C3 DC
200 mA/div
-4.8300 A ofst
4.852 A
4.762 A
Δy -90 mA
Zeitbasis 0.00 ms
500 µs/div
250 kS 50 MS/s
Trigger C3 DC
Stop 3.830 A
Flanke Positiv
LeCroy
01.12.2007 19:47:46

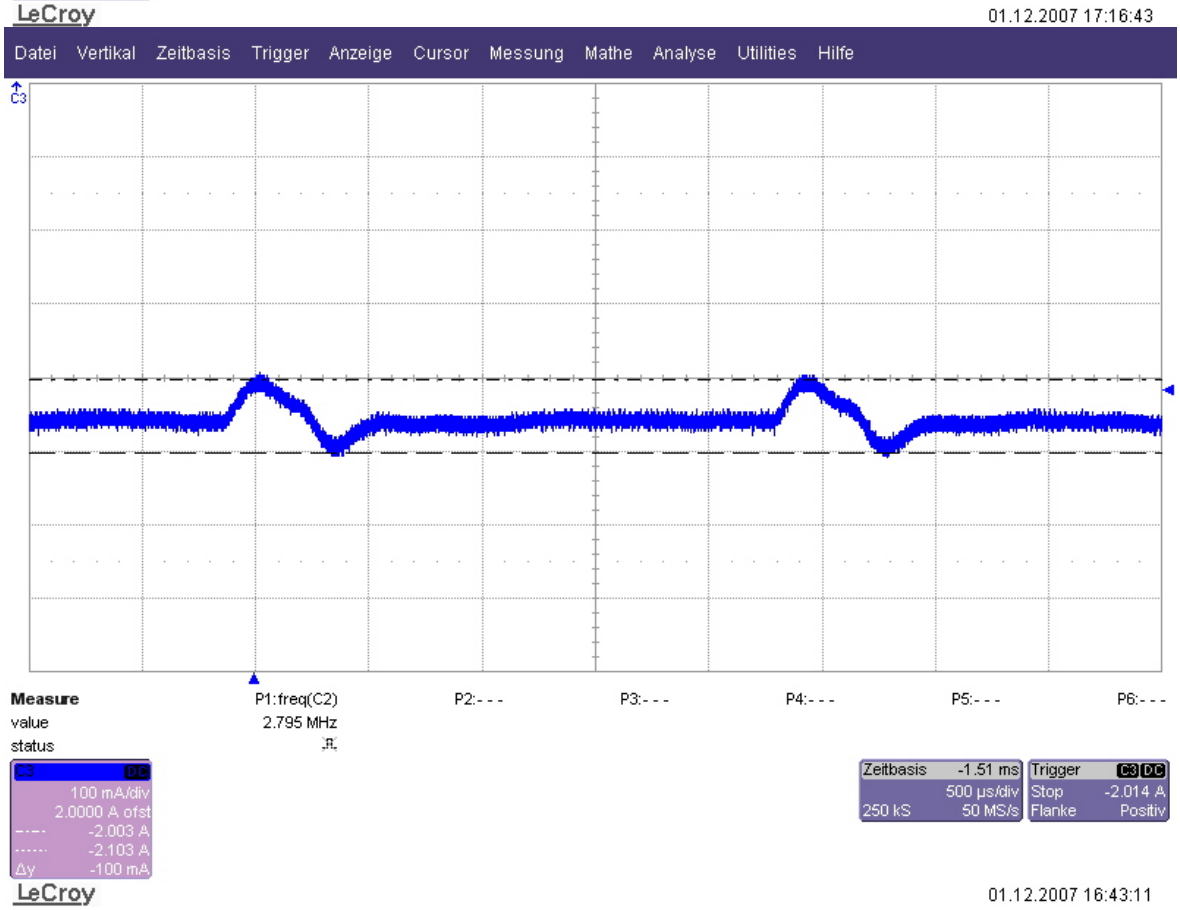

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure
value
status
P1:freq(C2)
1.189 MHz
P2:- - -
P3:- - -
P4:- - -
P5:- - -
P6:- - -
C4 AC
50.0 mV/div
-20.0 mV ofst
105.0 mV
-53.0 mV
Δy -158.0 mV
Zeitbasis 0.00 ms
1.00 ms/div
250 kS 25 MS/s
Trigger C3 DC
Stop 71.5 mA
Flanke Positiv
LeCroy
01.12.2007 18:58:35

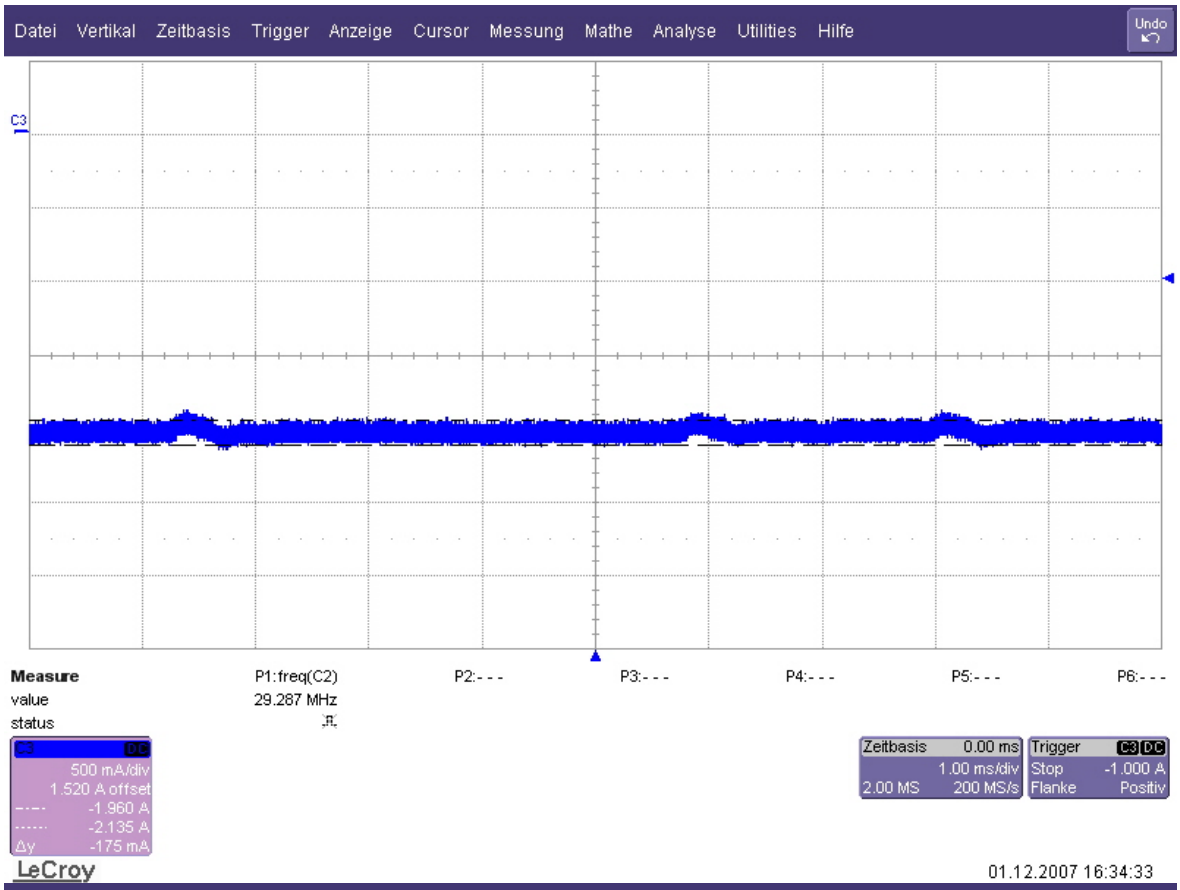

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure
value
status
P1:freq(C2)
2.471 MHz
P2:- - -
P3:- - -
P4:- - -
P5:- - -
P6:- - -
C4 AC
20.0 mV/div
-10.00 mV
62.2 mV
-37.8 mV
Δy -100.0 mV
Zeitbasis 0.00 ms
500 µs/div
250 kS 50 MS/s
Trigger C3 DC
Stop 71.5 mA
Flanke Positiv
LeCroy
01.12.2007 18:50:45

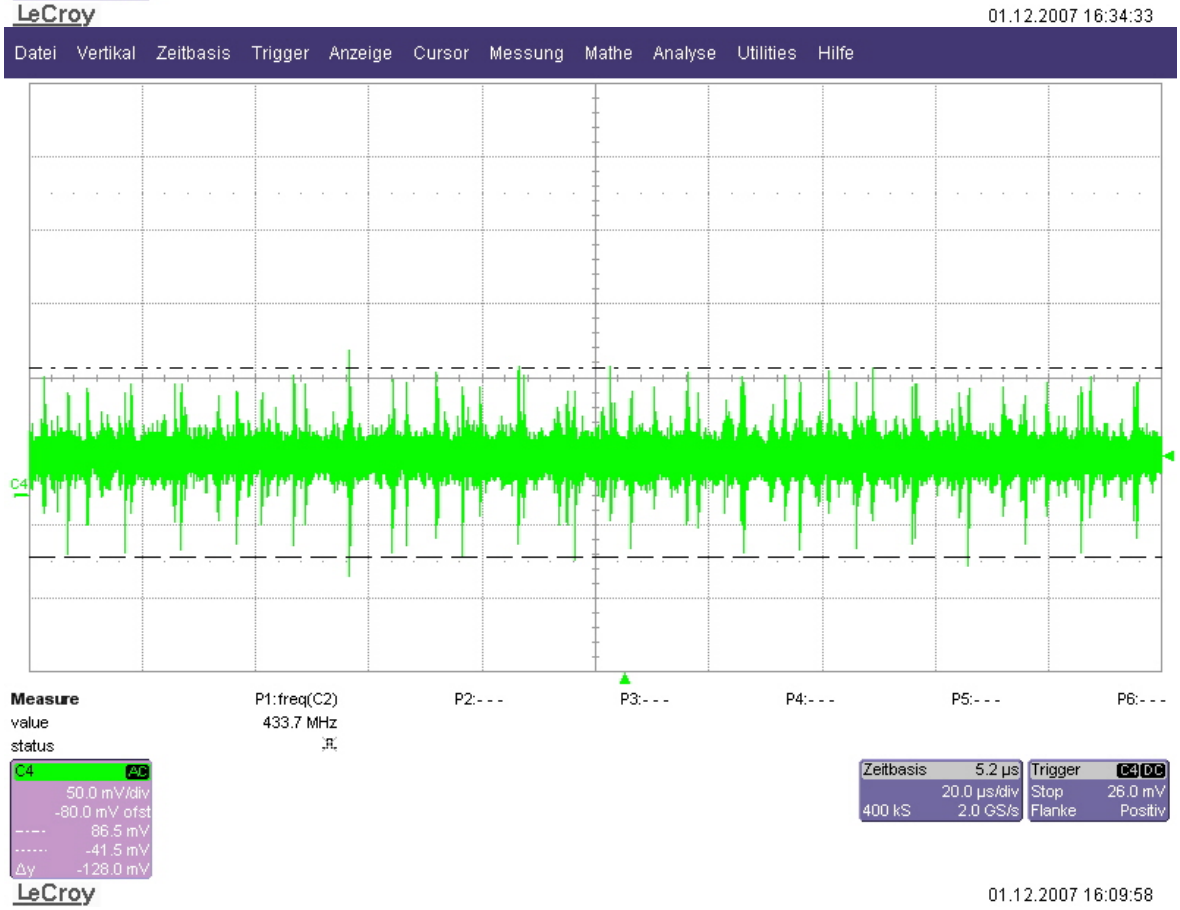

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 11.3065 MHz
status
C3 DC
50.0 mA/div
-149.0 mA
265.5 mA
180.5 mA
Δy -85.0 mA
Zeitbasis 0.00 ms
500 µs/div
250 kS 50 MS/s
Trigger C3 DC
Stop 71.5 mA
Flanke Positiv
LeCroy
01.12.2007 18:44:47


Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 3.772 MHz
status
C4 AC
100 mV/div
-150.0 mV
155 mV
-85 mV
Δy -240 mV
Zeitbasis 0.00 ms
500 µs/div
250 kS 50 MS/s
Trigger C3 DC
Stop 71.5 mA
Flanke Positiv
LeCroy
01.12.2007 17:18:54

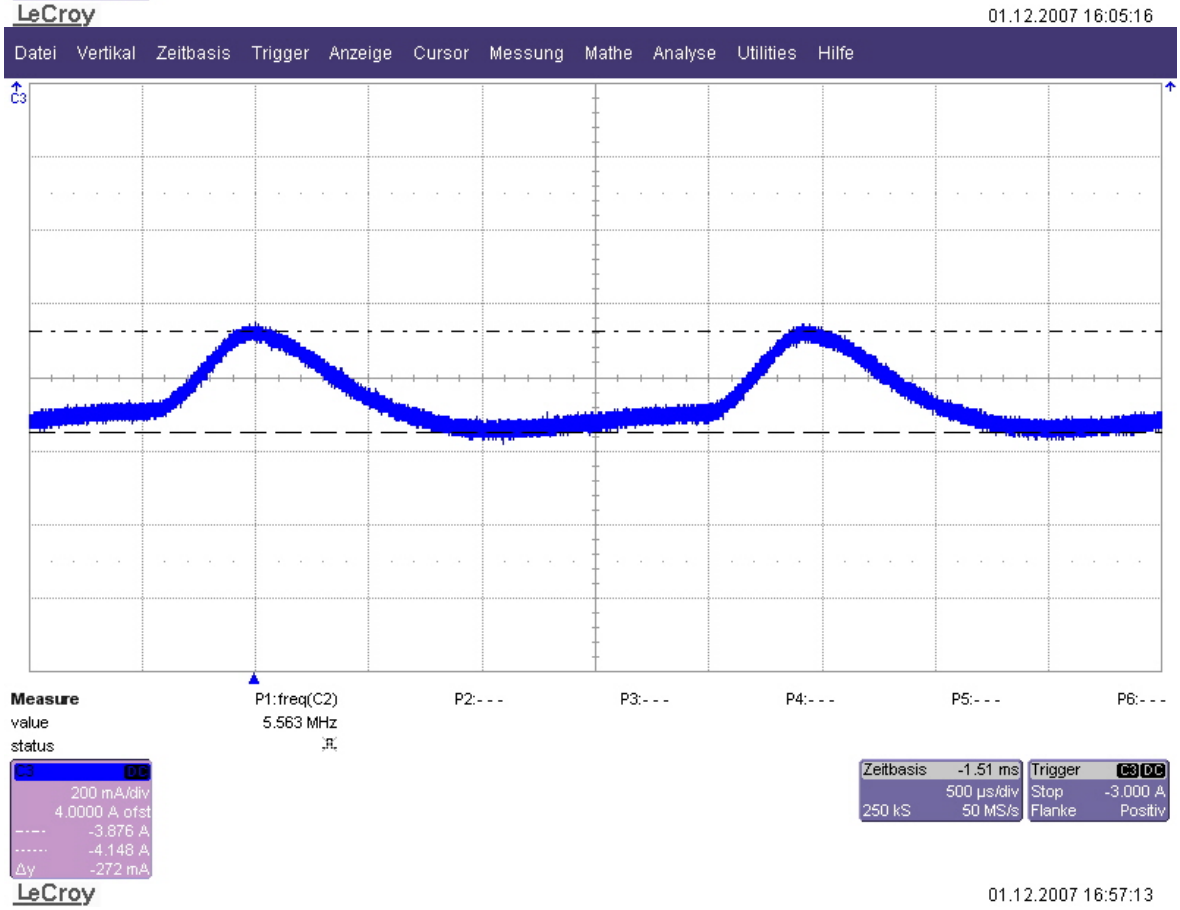

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure
value
status
P1:freq(C2)
3.8462 MHz
P2:- - -
P3:- - -
P4:- - -
P5:- - -
P6:- - -
C4 AC
50.0 mV/div
-80.0 mV ofst
110.0 mV
-70.0 mV
Δy -180.0 mV
Zeitbasis 0.00 ms
500 µs/div
250 kS 50 MS/s
Trigger C3 DC
Stop 71.5 mA
Flanke Positiv
LeCroy
01.12.2007 17:16:43

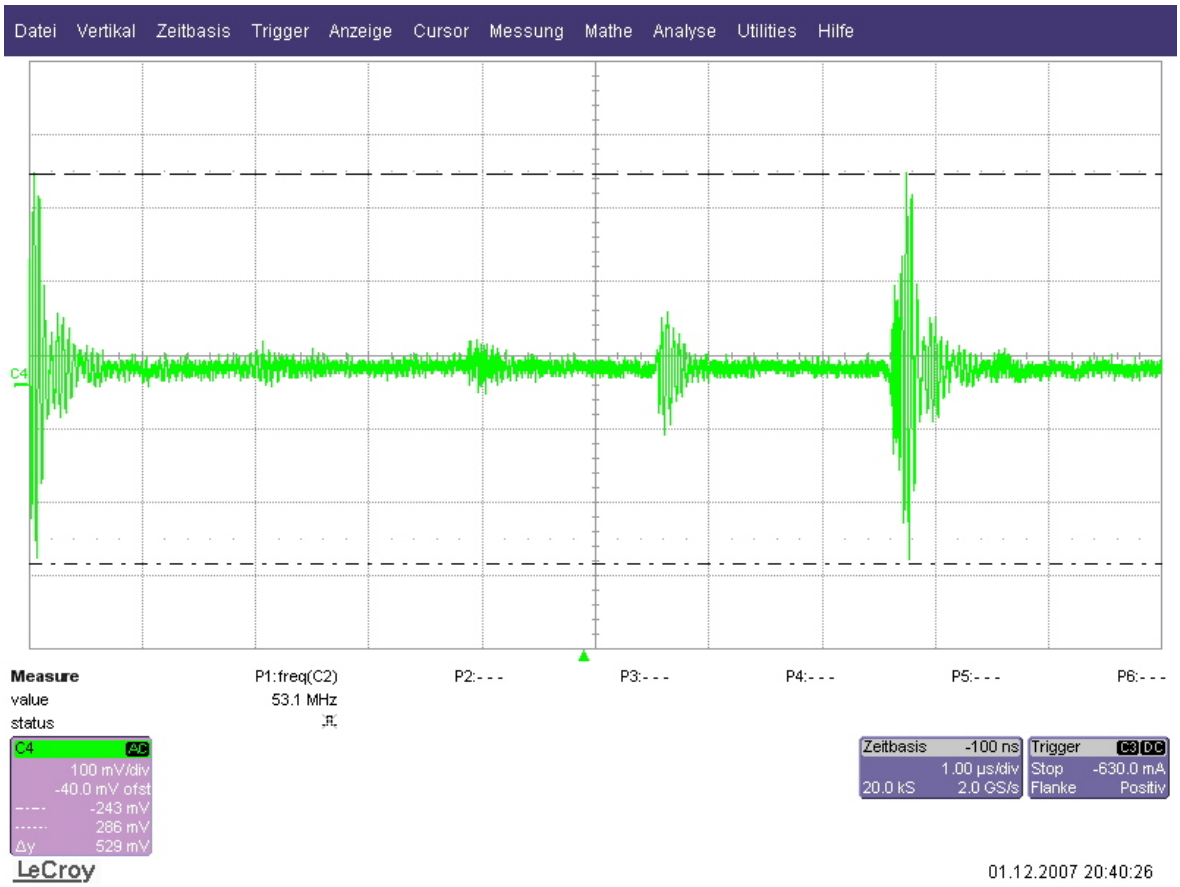

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
Measure
value
status
P1:freq(C2)
2.795 MHz
P2:- - -
P3:- - -
P4:- - -
P5:- - -
P6:- - -
C3 DC
100 mA/div
2.0000 A ofst
-2.003 A
-2.103 A
Δy -100 mA
Zeitbasis -1.51 ms
500 µs/div
250 kS 50 MS/s
Trigger C3 DC
Stop -2.014 A
Flanke Positiv
LeCroy
01.12.2007 16:43:11


Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
Undo
C3
Measure
value
status
P1:freq(C2)
29.287 MHz
P2:- - -
P3:- - -
P4:- - -
P5:- - -
P6:- - -
C3 DC
500 mA/div
1.520 A offset
-1.960 A
-2.135 A
Δy -175 mA
Zeitbasis 0.00 ms
1.00 ms/div
2.00 MS 200 MS/s
Trigger C3 DC
Stop -1.000 A
Flanke Positiv
LeCroy
01.12.2007 16:34:33

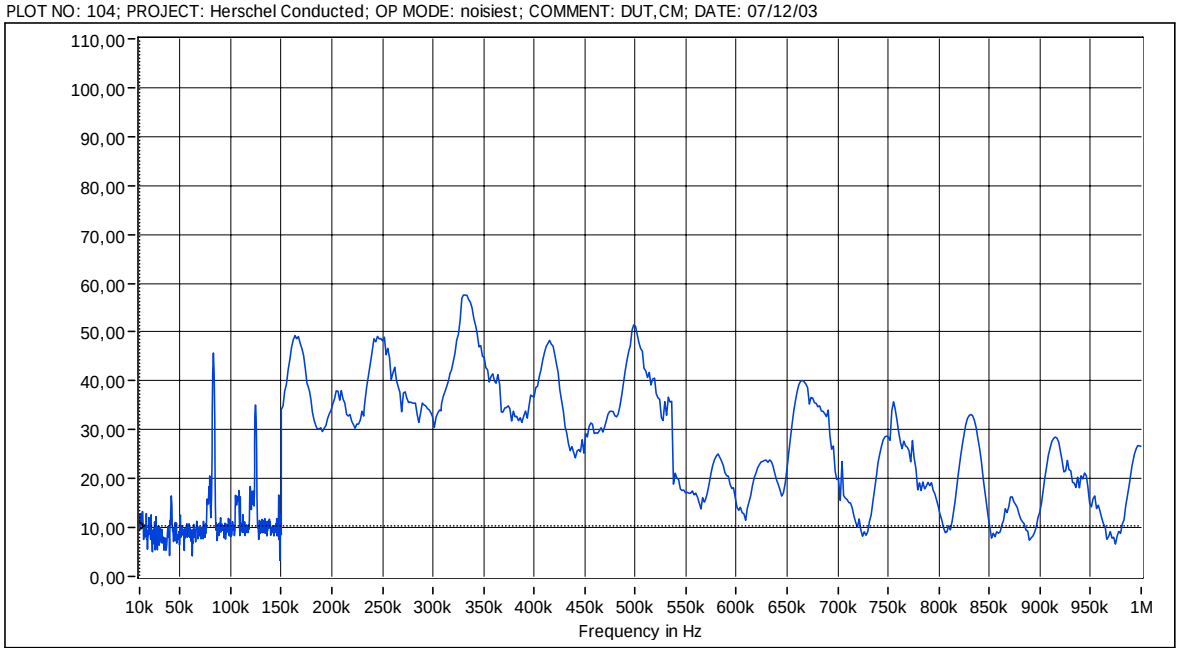

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure
value
status
P1:freq(C2)
433.7 MHz
P2:- - -
P3:- - -
P4:- - -
P5:- - -
P6:- - -
C4 AC
50.0 mV/div
-80.0 mV ofst
86.5 mV
-41.5 mV
Δy -128.0 mV
Zeitbasis 5.2 µs
20.0 µs/div
400 kS 2.0 GS/s
Trigger C4 DC
Stop 26.0 mV
Flanke Positiv
LeCroy
01.12.2007 16:09:58


Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 25.16 MHz
status
C4 AC
20.0 mV/div
-30.00 mV
83.2 mV
-18.6 mV
Δy -101.8 mV
Zeitbasis 130 µs
500 µs/div
2.00 MS 400 MS/s
Trigger C4 DC
Stop 26.0 mV
Flanke Positiv
LeCroy
01.12.2007 16:05:16

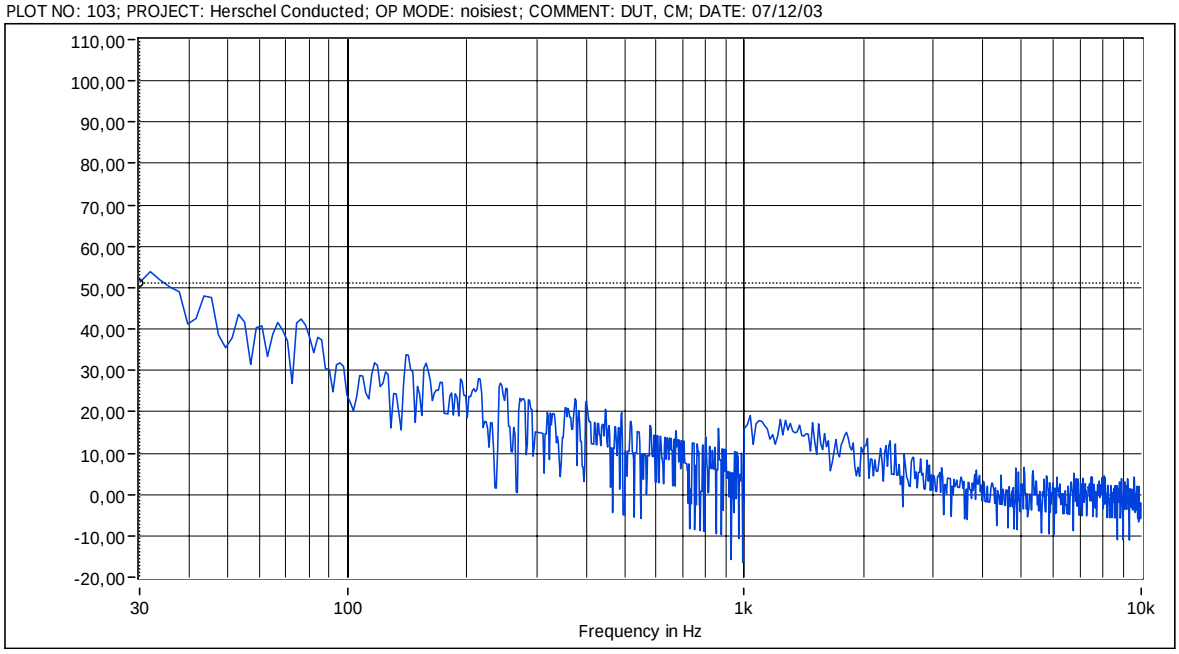

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 5.563 MHz
status
C3 DC
200 mA/div
4.0000 A ofst
-3.876 A
-4.148 A
Δy -272 mA
Zeitbasis -1.51 ms
500 µs/div
250 kS 50 MS/s
Trigger C3 DC
Stop -3.000 A
Flanke Positiv
LeCroy
01.12.2007 16:57:13



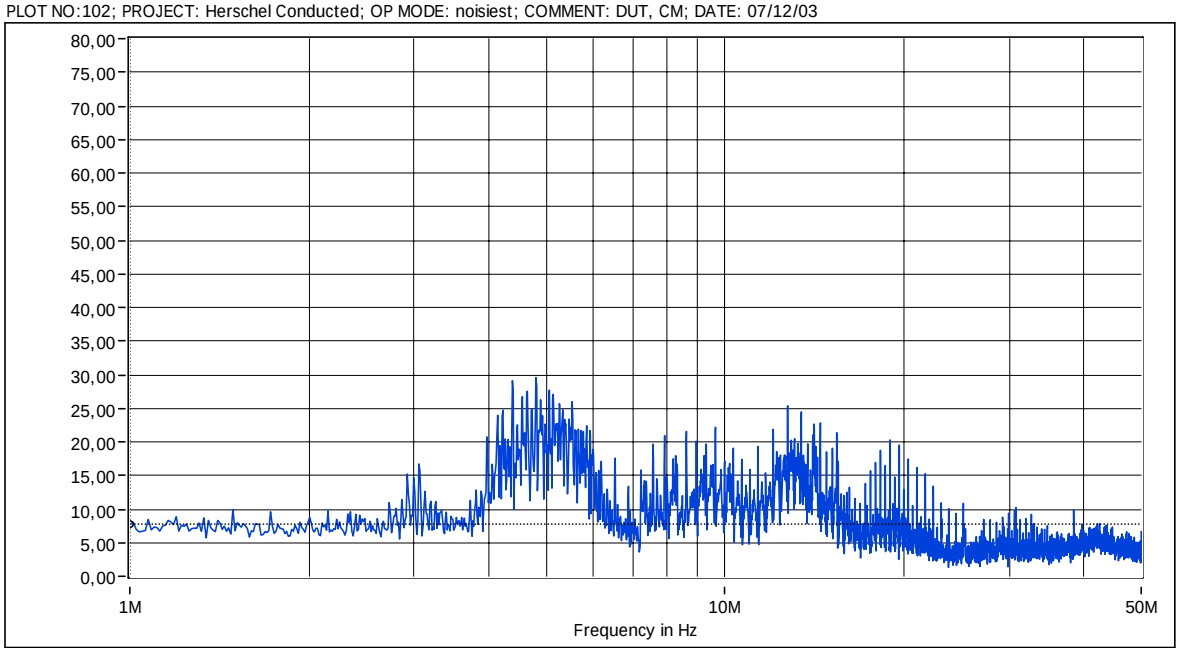
PLOT NO: 104; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT,CM; DATE: 07/12/03



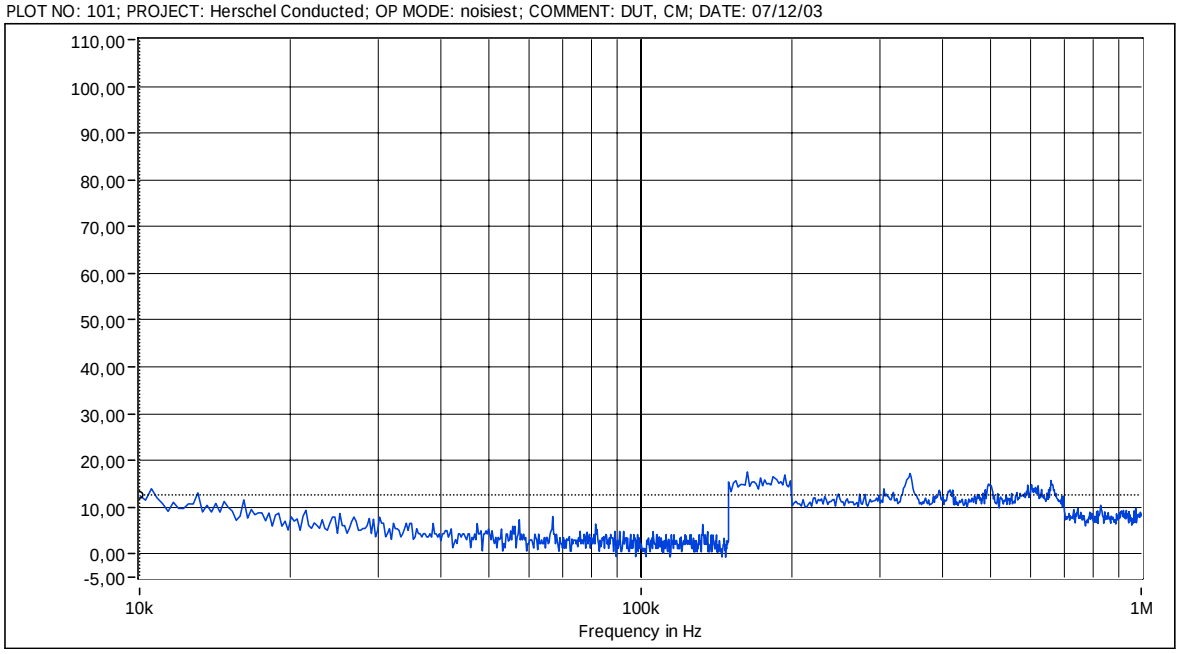
PLOT NO: 103; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT, CM; DATE: 07/12/03



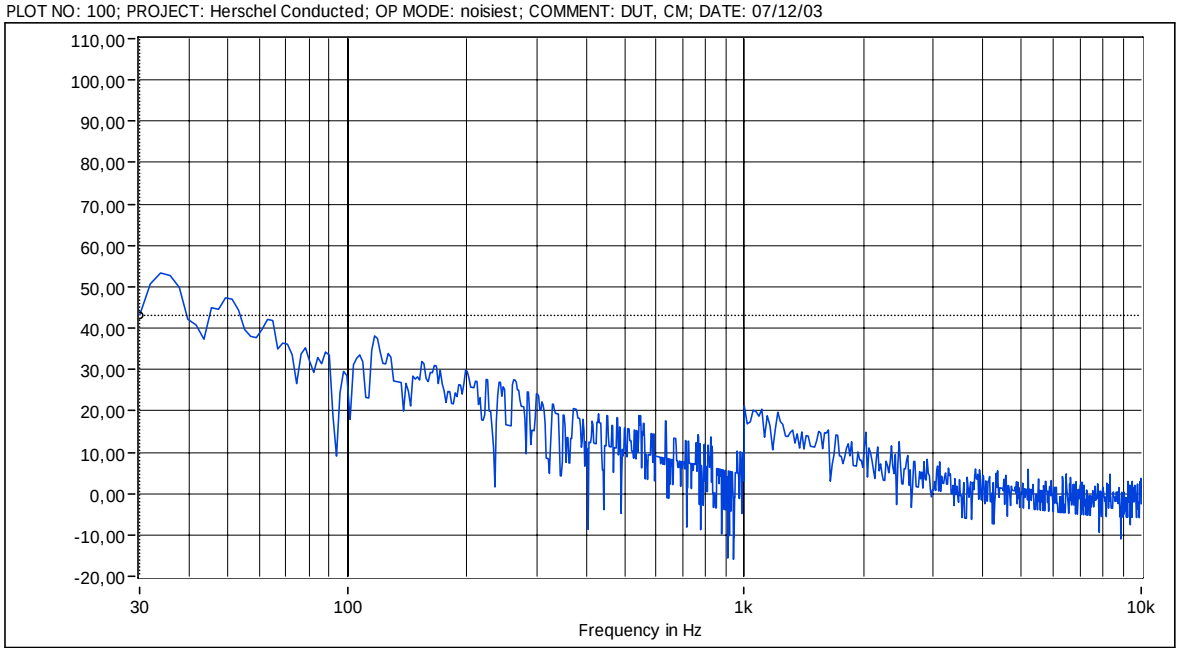
PLOT NO:102; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT, CM; DATE: 07/12/03



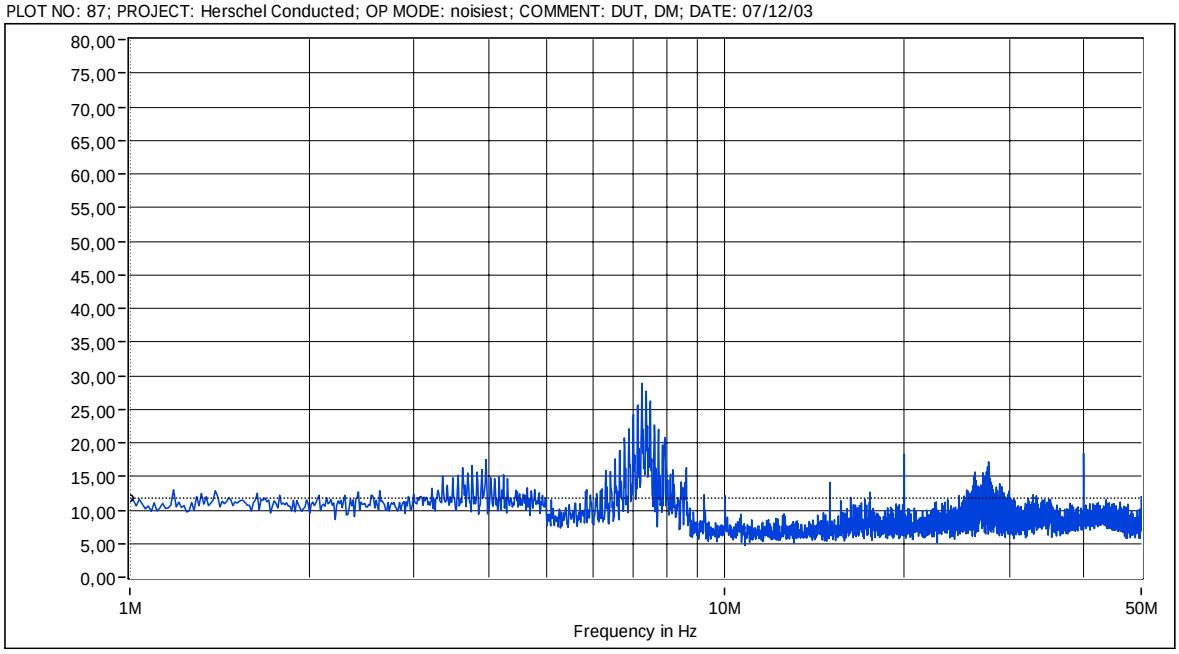
PLOT NO: 101; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT, CM; DATE: 07/12/03



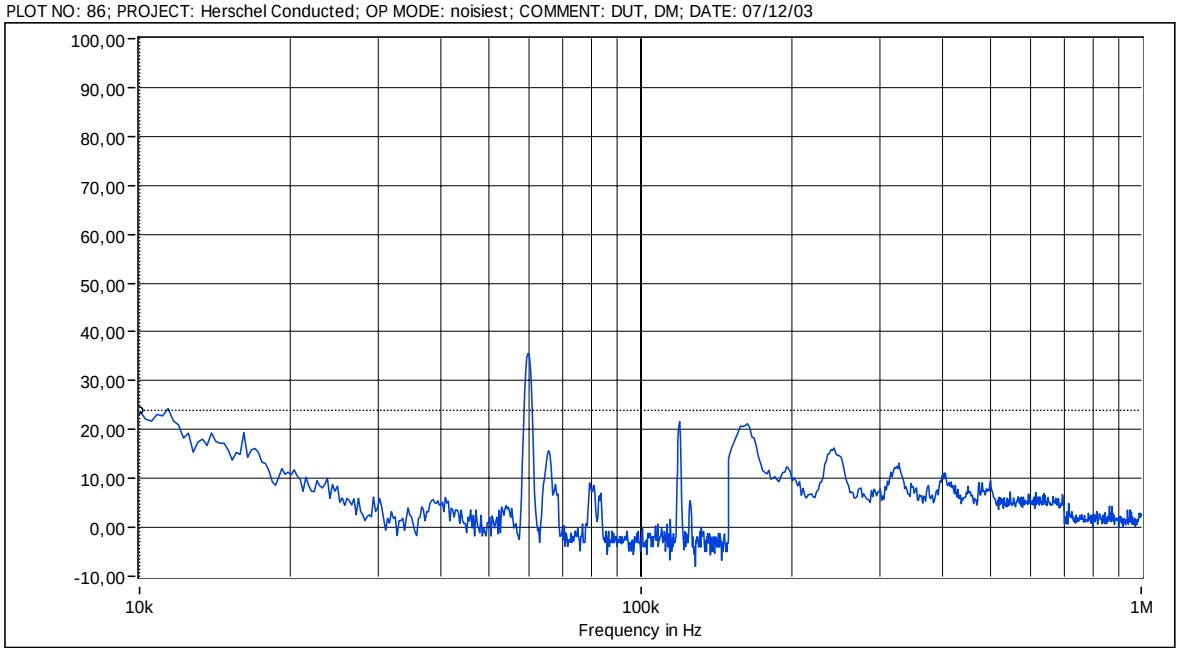
PLOT NO: 100; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT, CM; DATE: 07/12/03



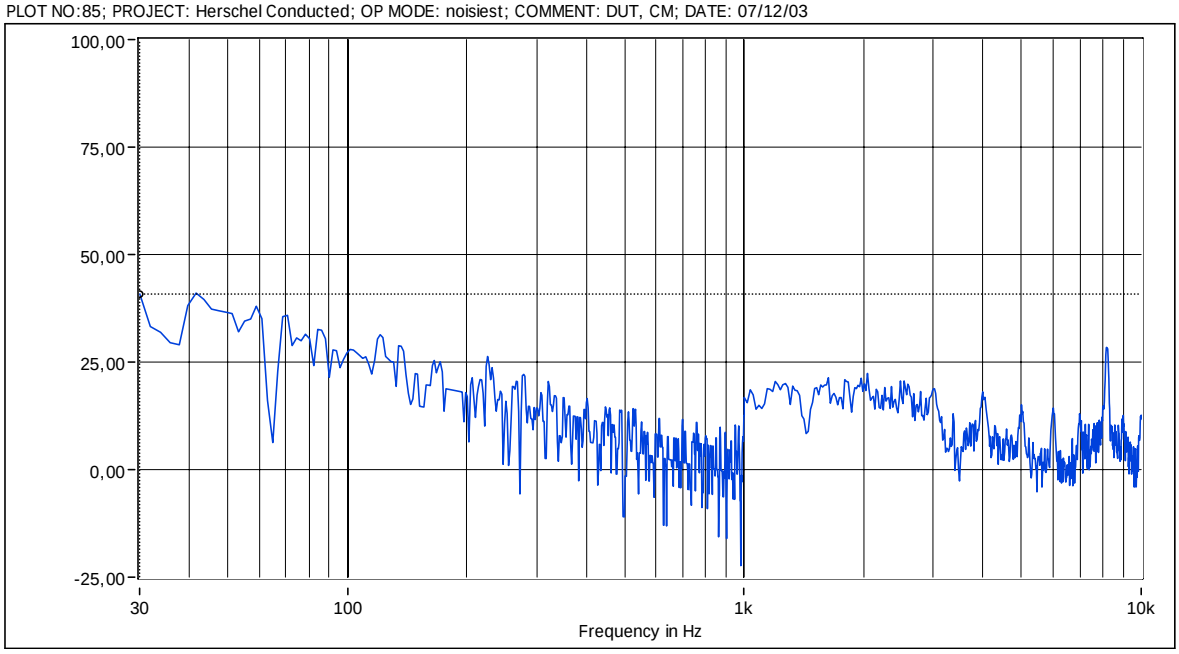
PLOT NO: 87; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT, DM; DATE: 07/12/03



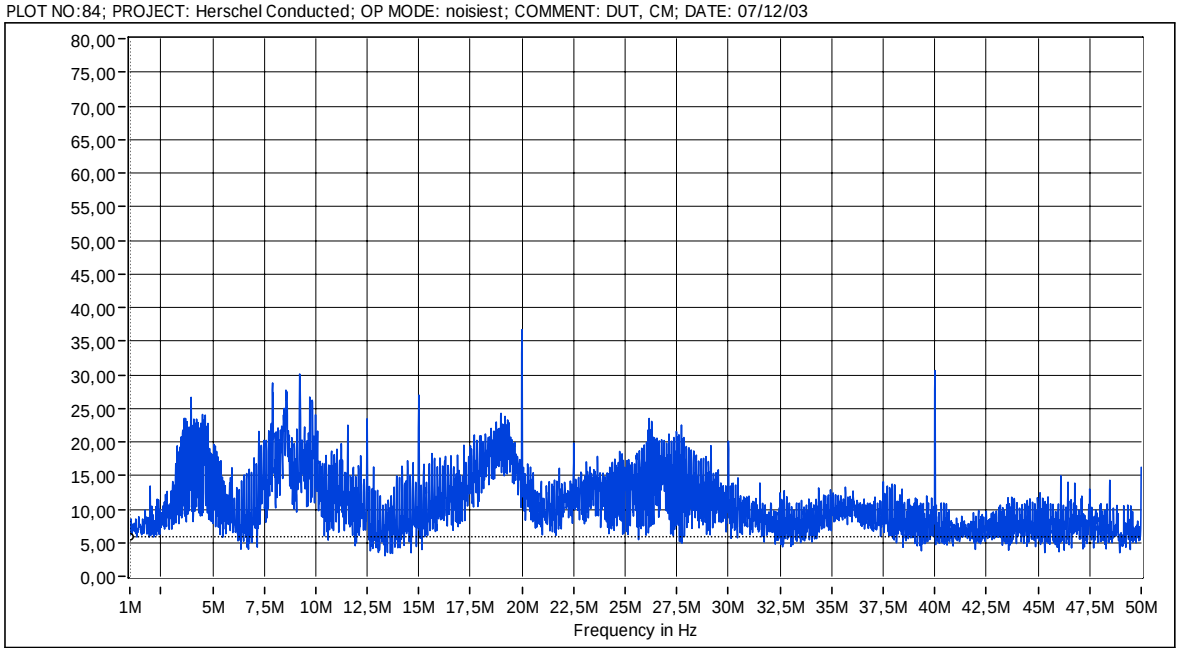
PLOT NO: 86; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT, DM; DATE: 07/12/03



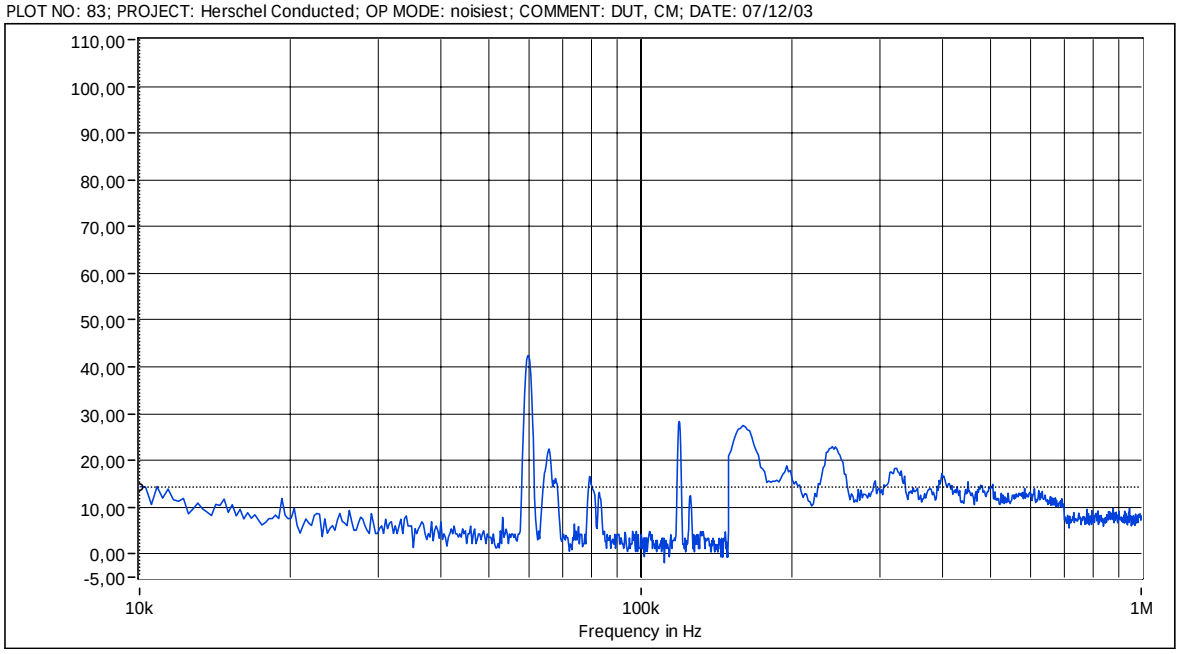
PLOT NO:85; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT, CM; DATE: 07/12/03



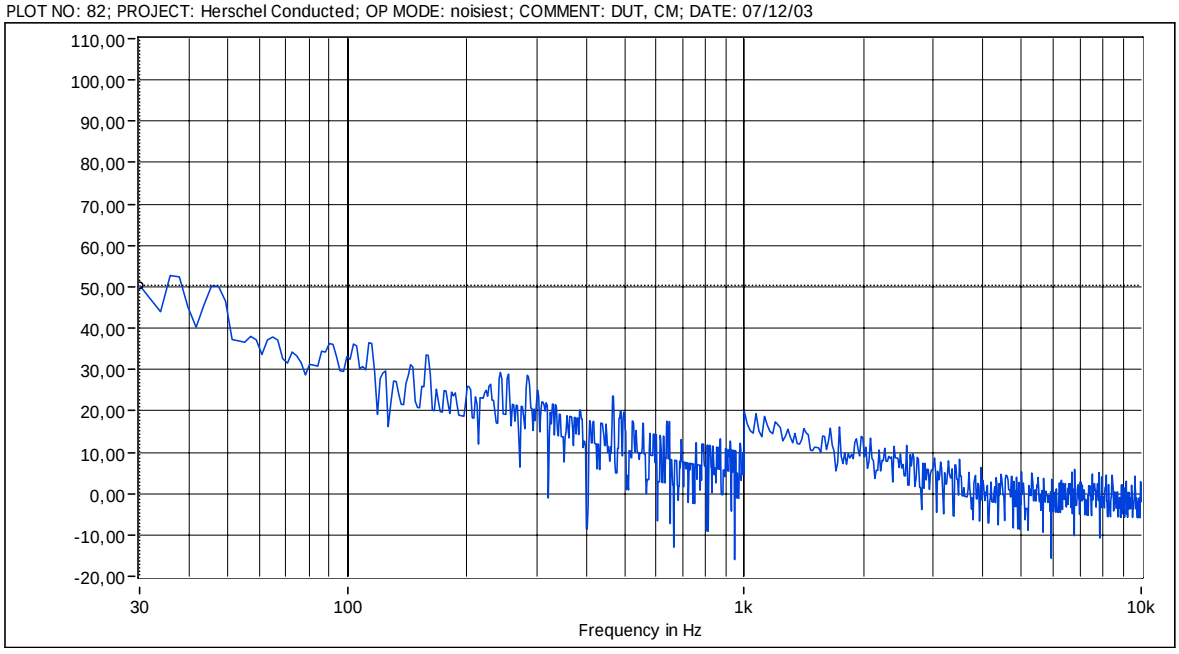
PLOT NO:84; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT, CM; DATE: 07/12/03



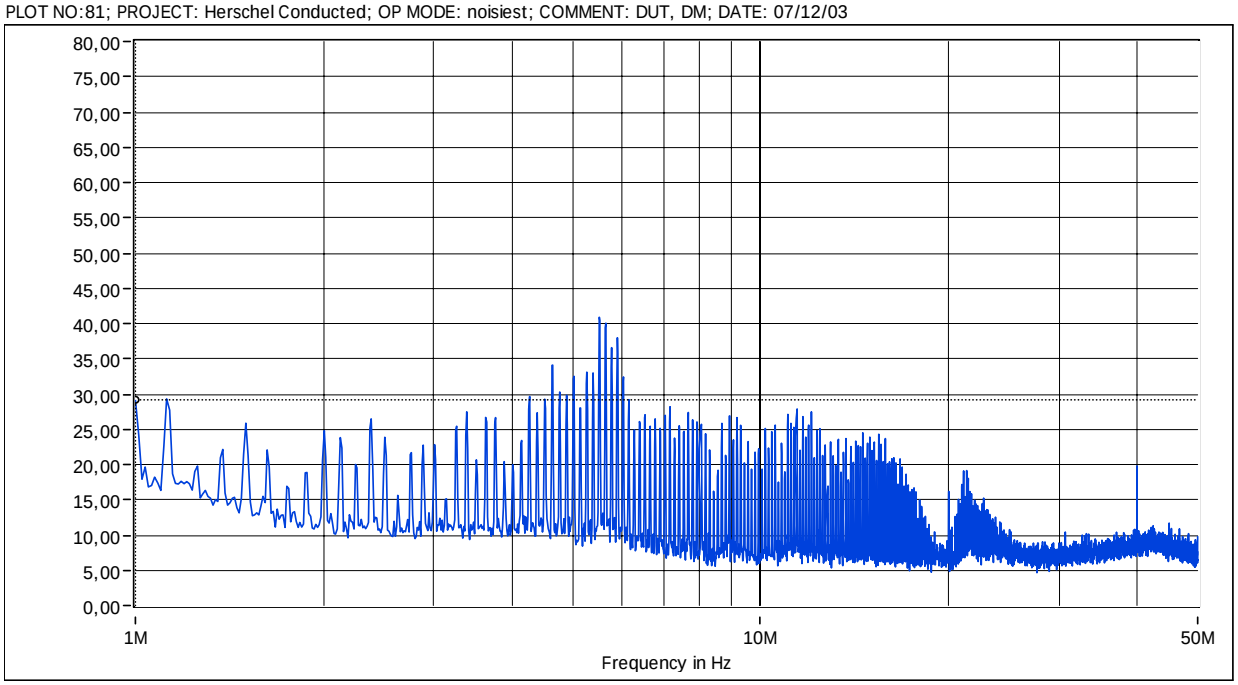
PLOT NO: 83; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT, CM; DATE: 07/12/03



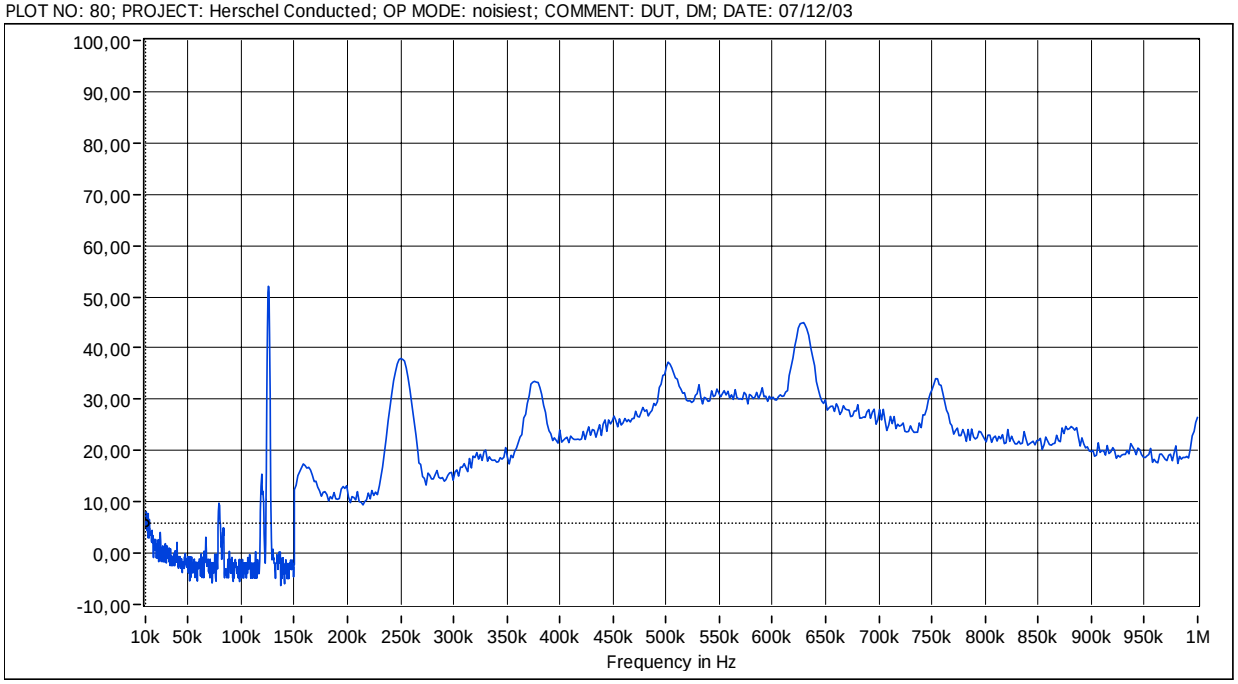
PLOT NO: 82; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT, CM; DATE: 07/12/03



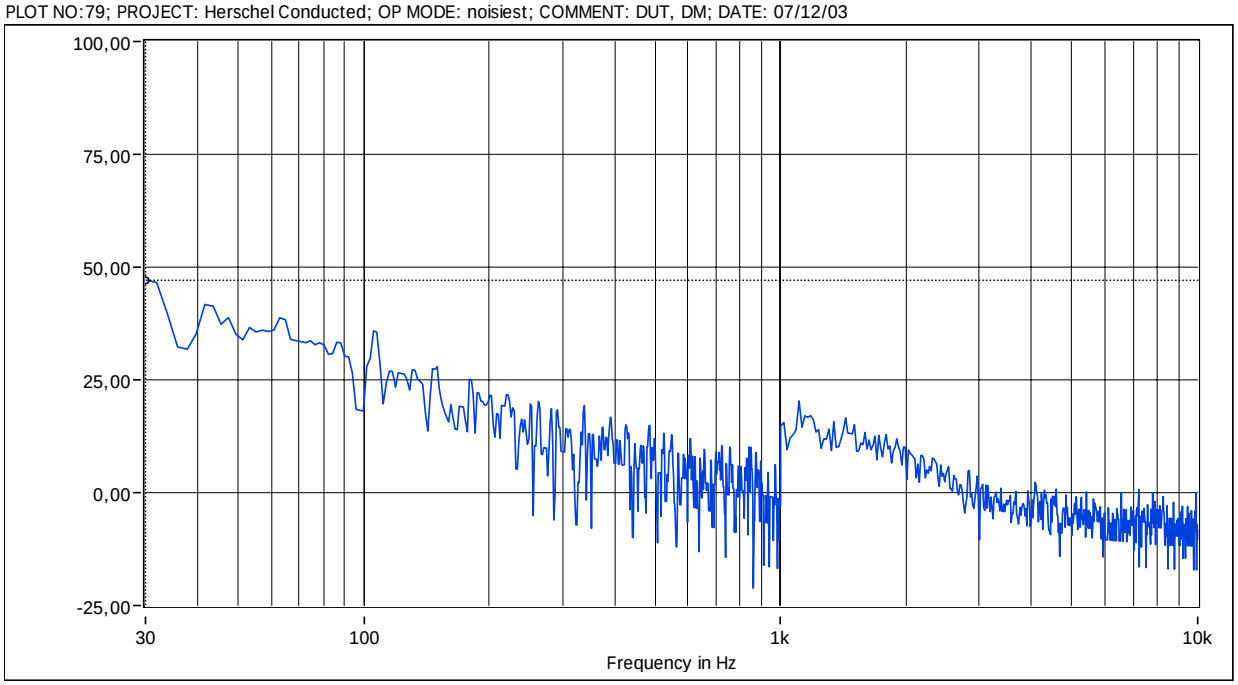
PLOT NO:81; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT, DM; DATE: 07/12/03



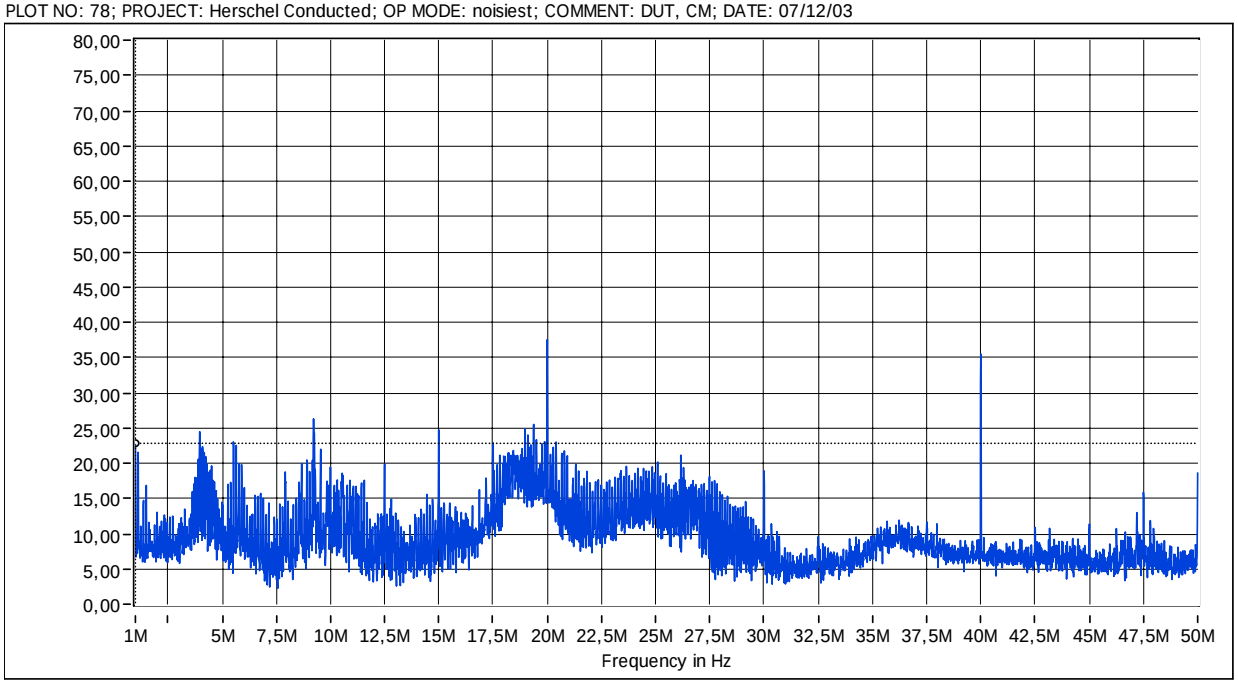
PLOT NO: 80; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT, DM; DATE: 07/12/03



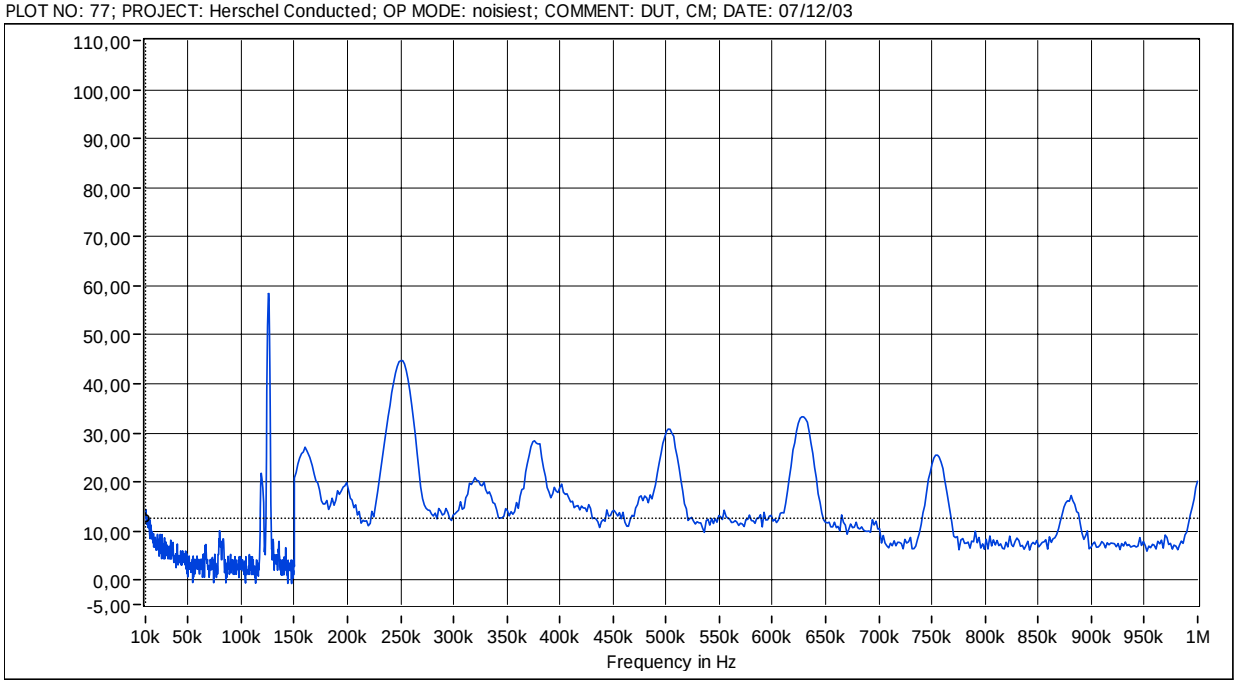
PLOT NO:79; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT, DM; DATE: 07/12/03



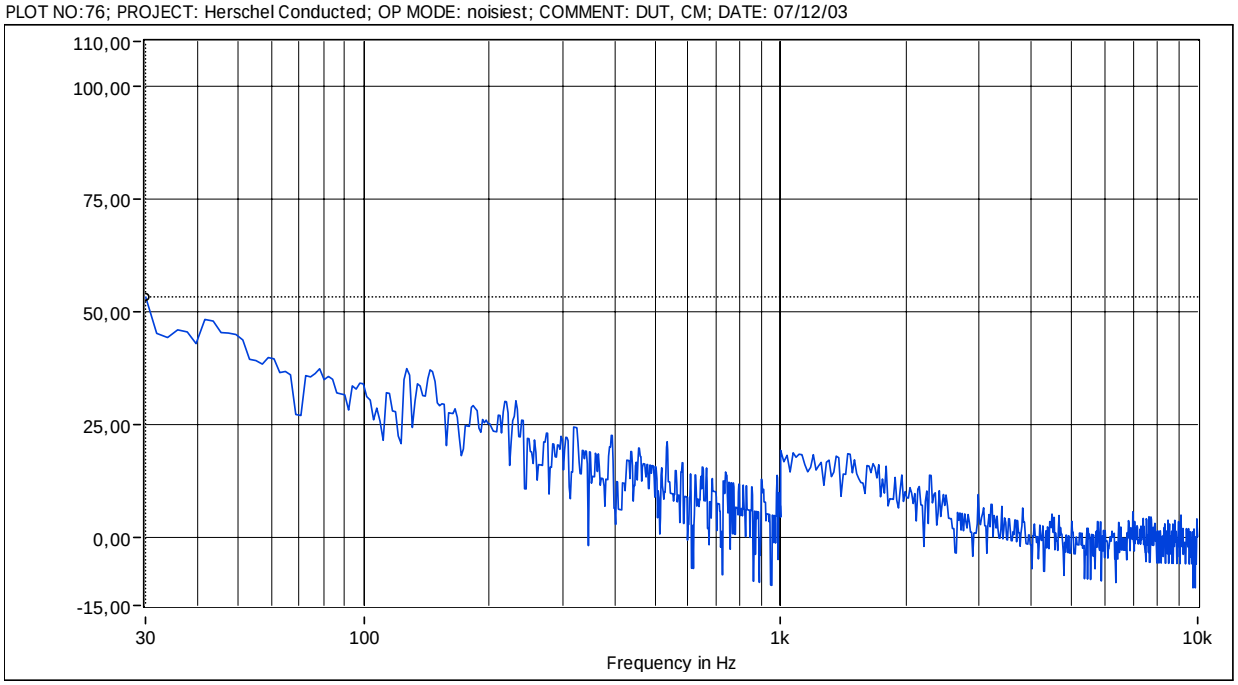
PLOT NO: 78; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT, CM; DATE: 07/12/03



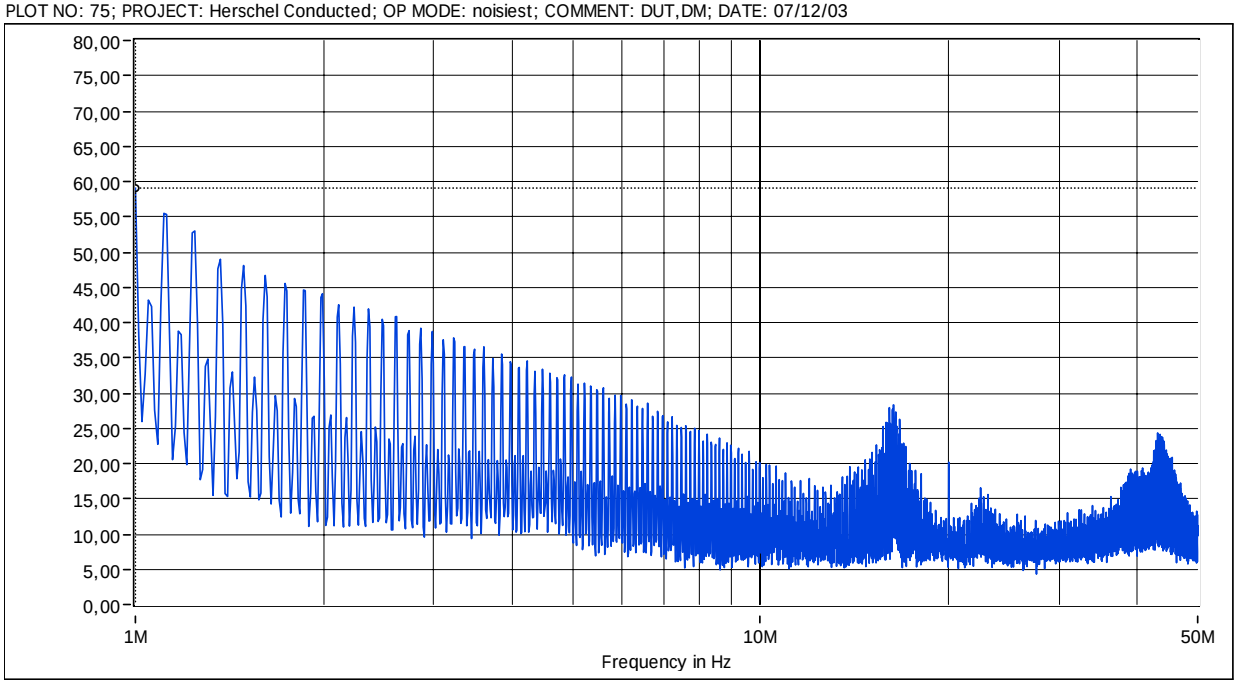
PLOT NO: 77; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT, CM; DATE: 07/12/03



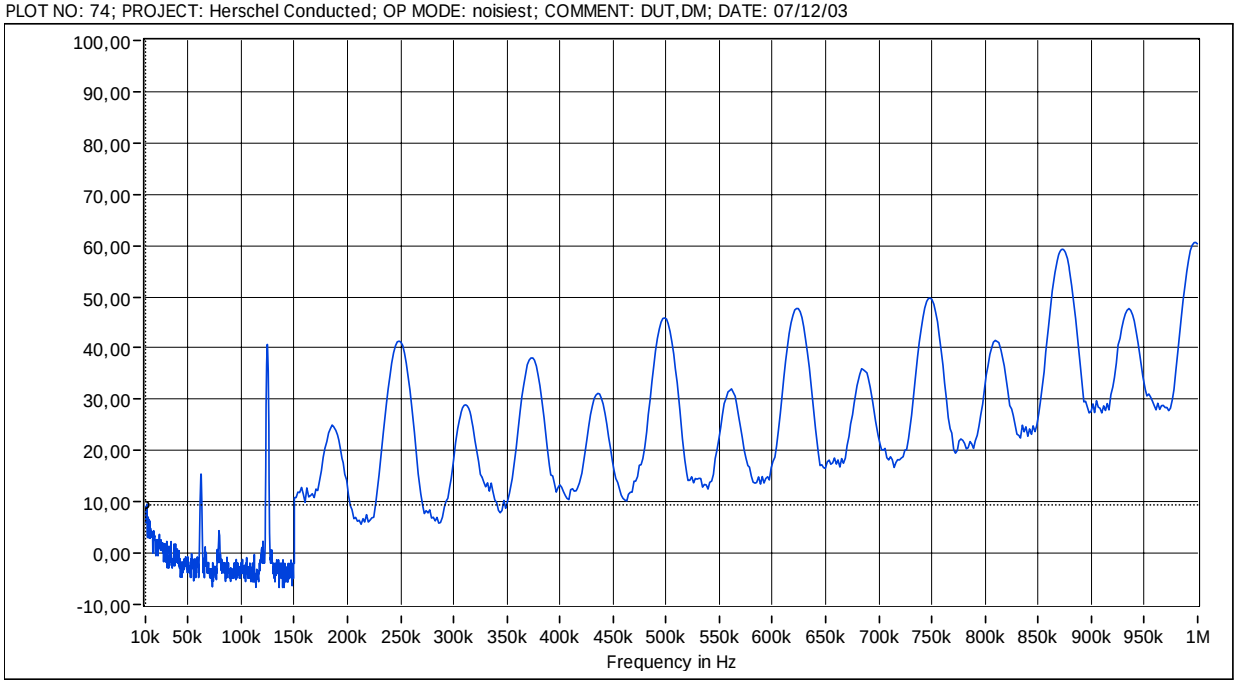
PLOT NO:76; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT, CM; DATE: 07/12/03



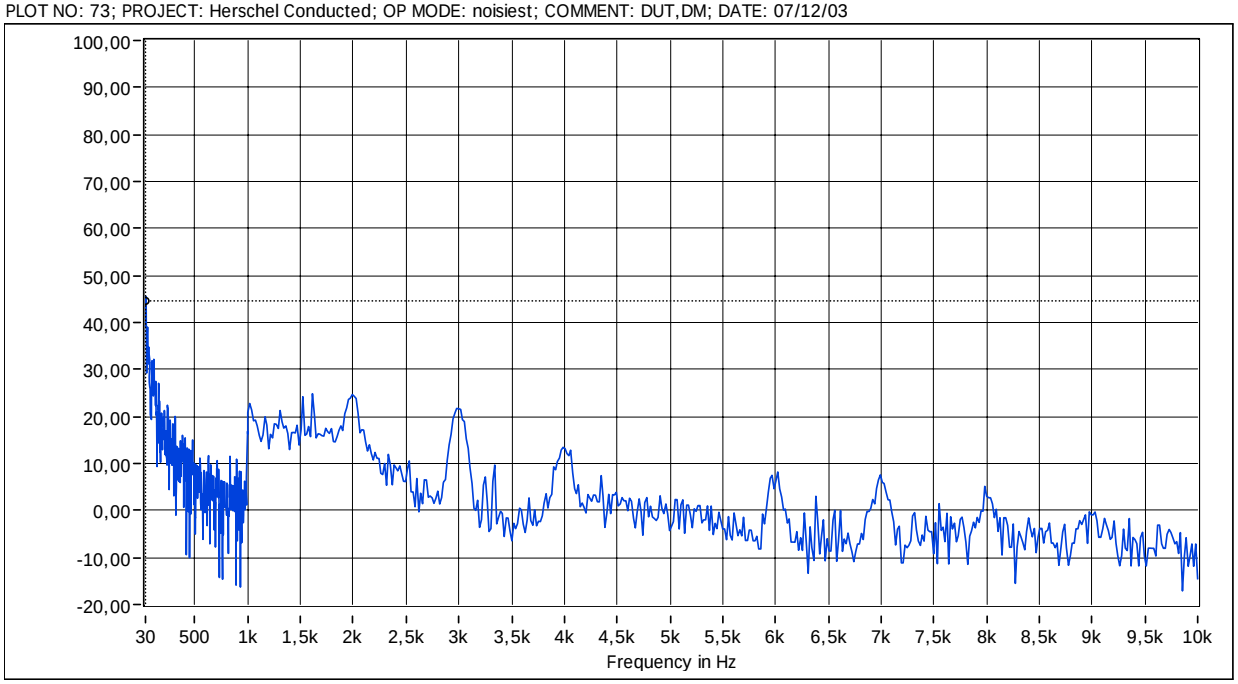
PLOT NO: 75; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT,DM; DATE: 07/12/03



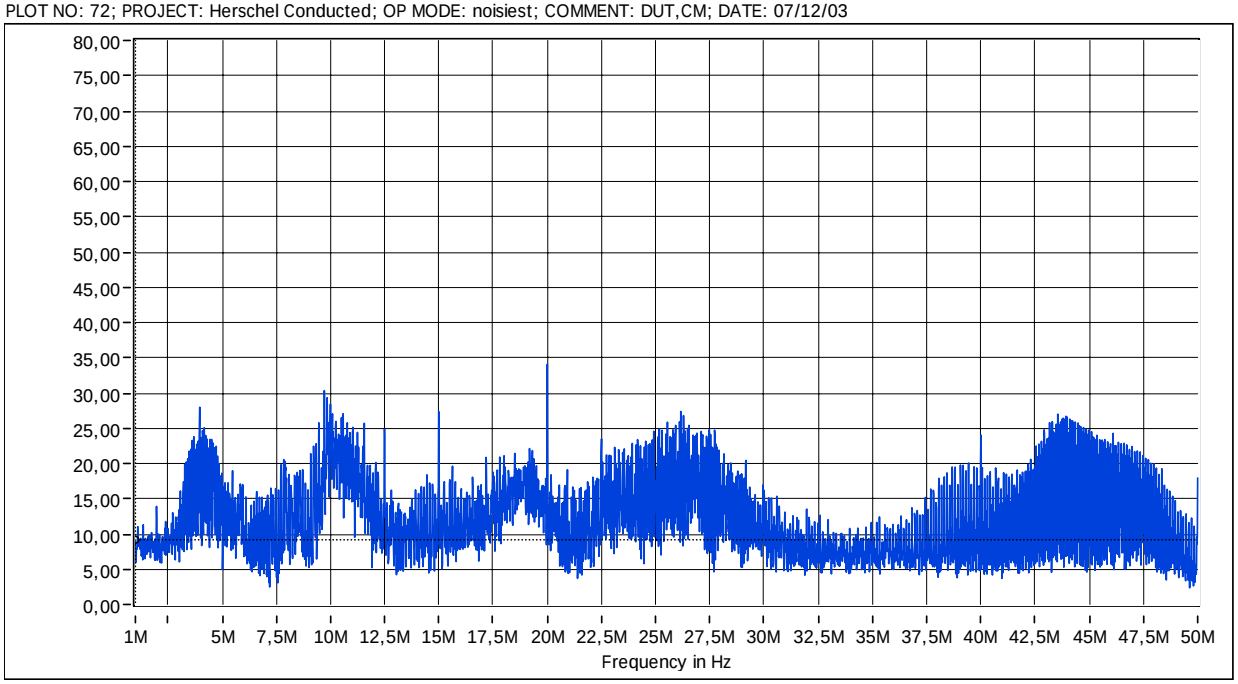
PLOT NO: 74; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT,DM; DATE: 07/12/03



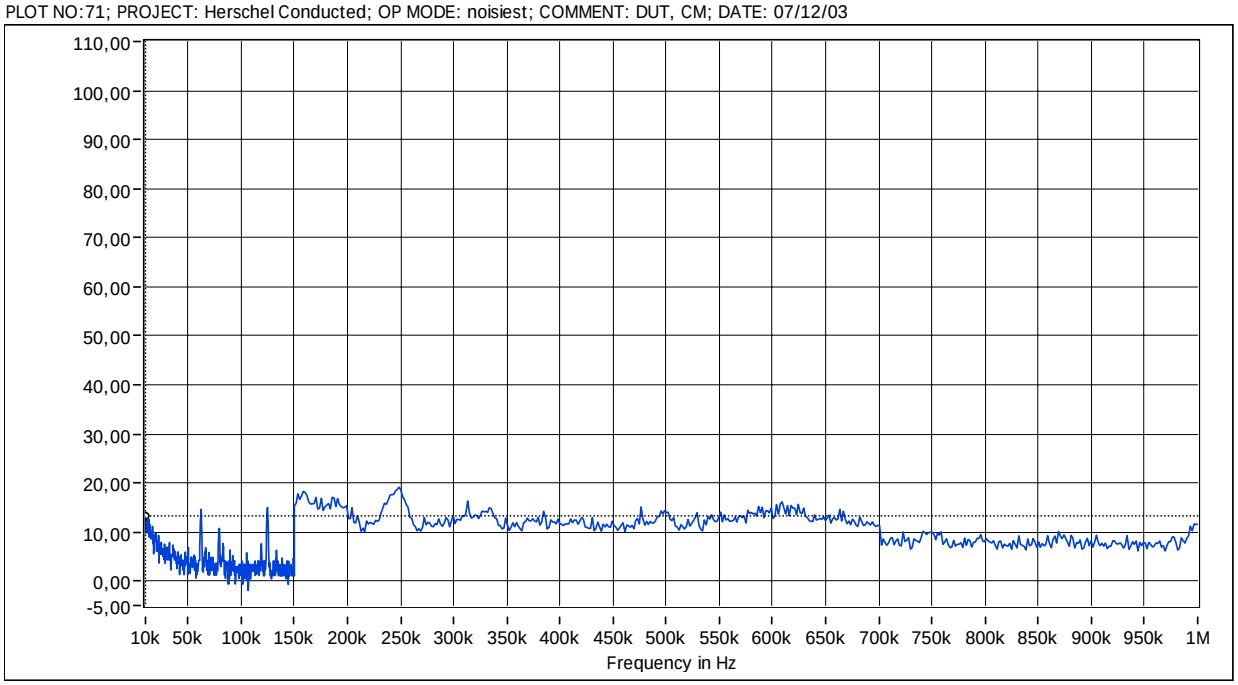
PLOT NO: 73; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT,DM; DATE: 07/12/03



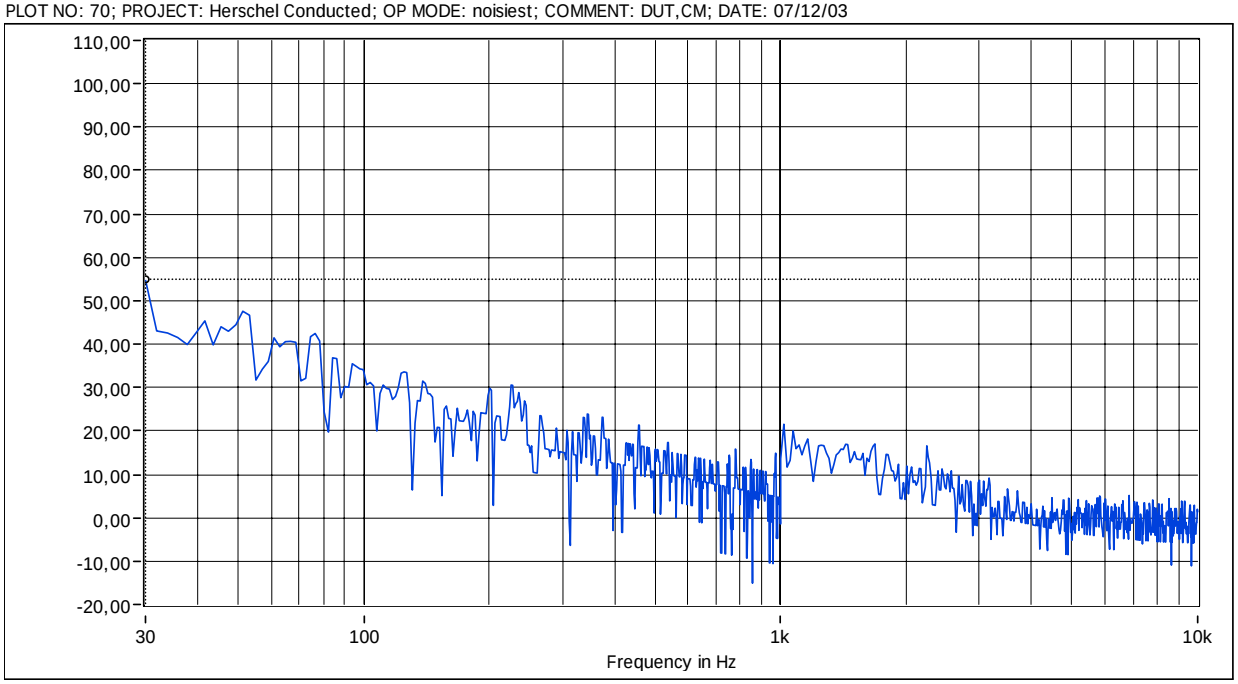
PLOT NO: 72; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT,CM; DATE: 07/12/03



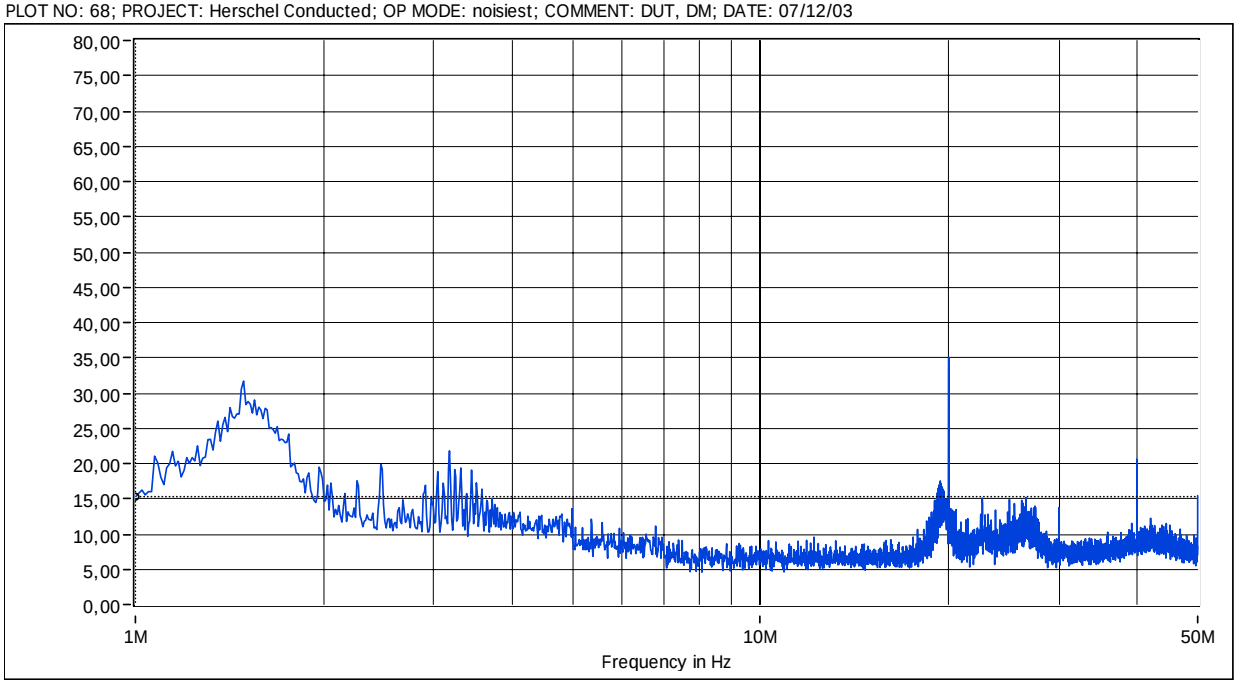
PLOT NO:71; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT, CM; DATE: 07/12/03



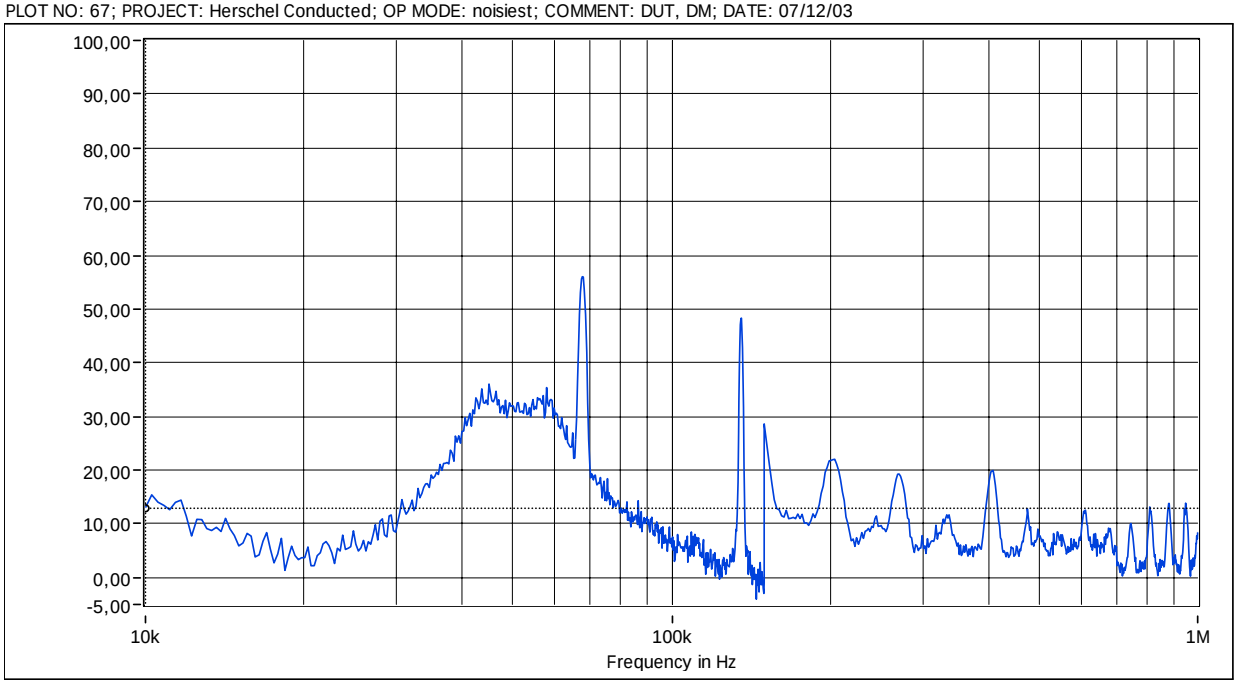
PLOT NO: 70; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT,CM; DATE: 07/12/03



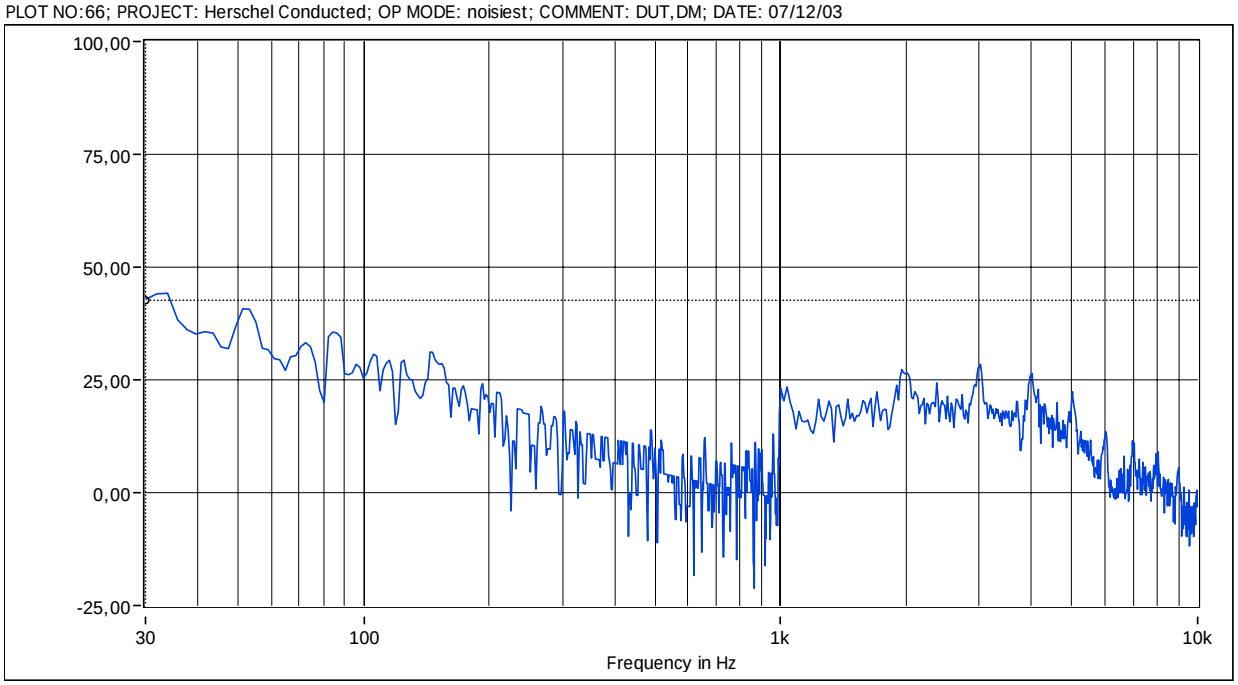
PLOT NO: 68; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT, DM; DATE: 07/12/03



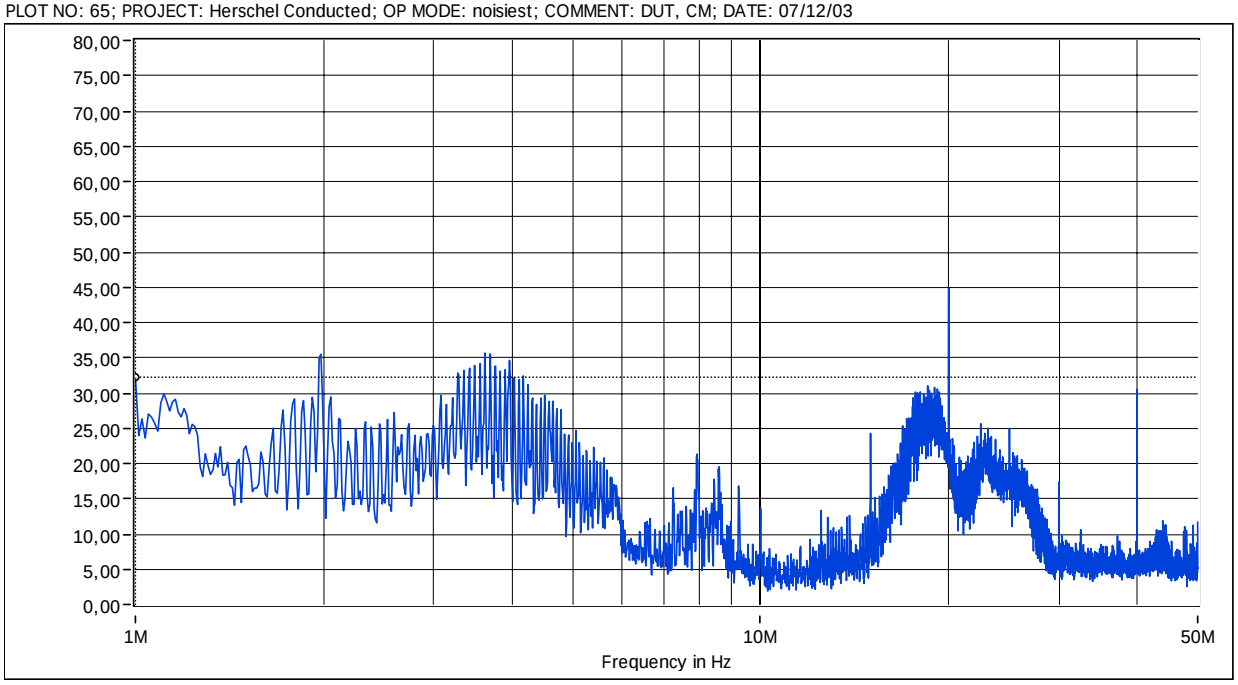
PLOT NO: 67; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT, DM; DATE: 07/12/03



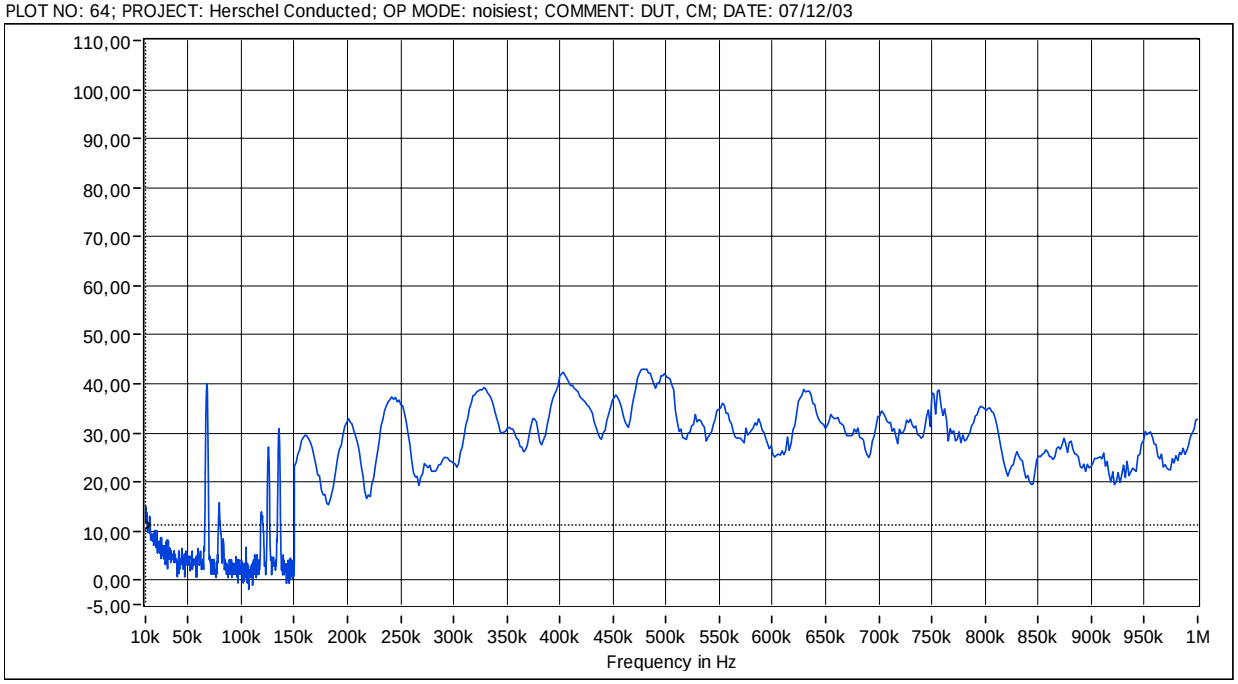
PLOT NO:66; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT,DM; DATE: 07/12/03



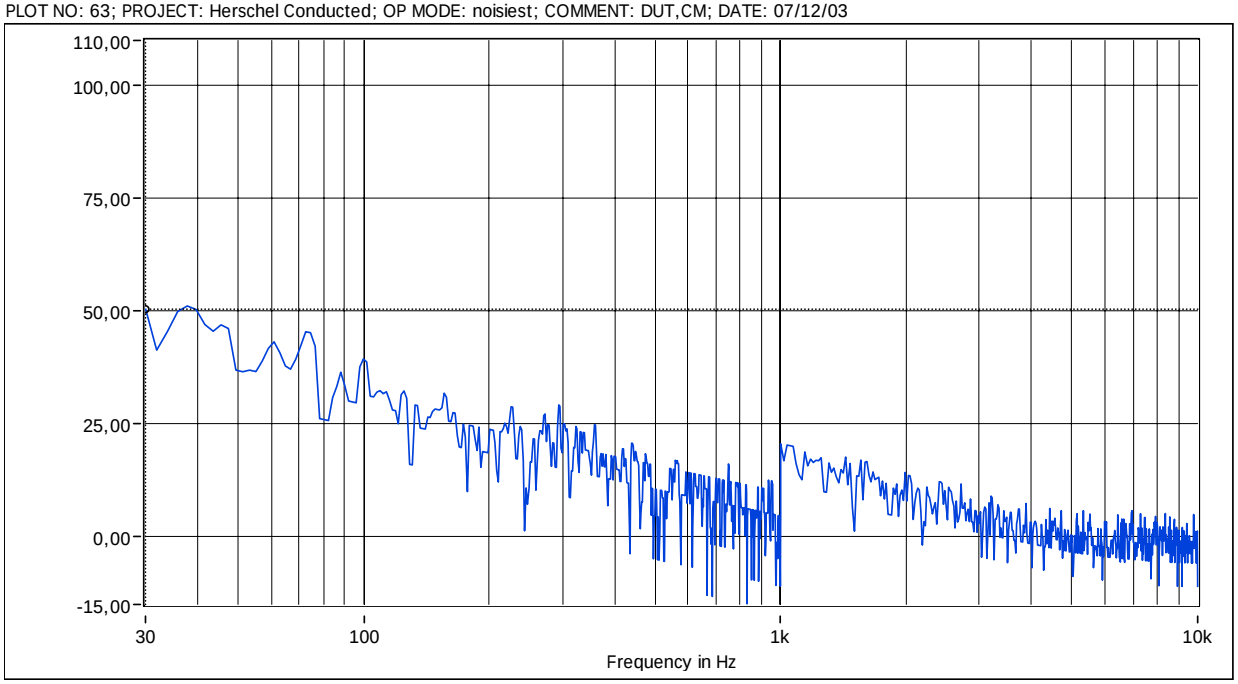
PLOT NO: 65; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT, CM; DATE: 07/12/03



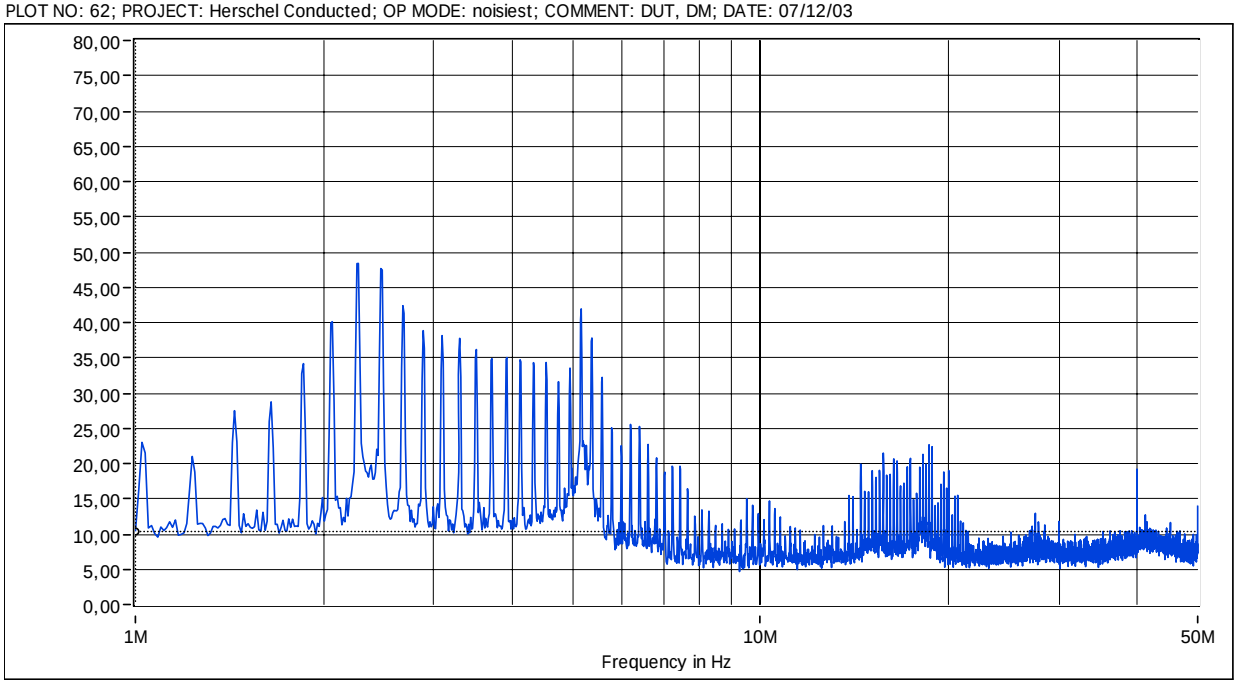
PLOT NO: 64; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT, CM; DATE: 07/12/03



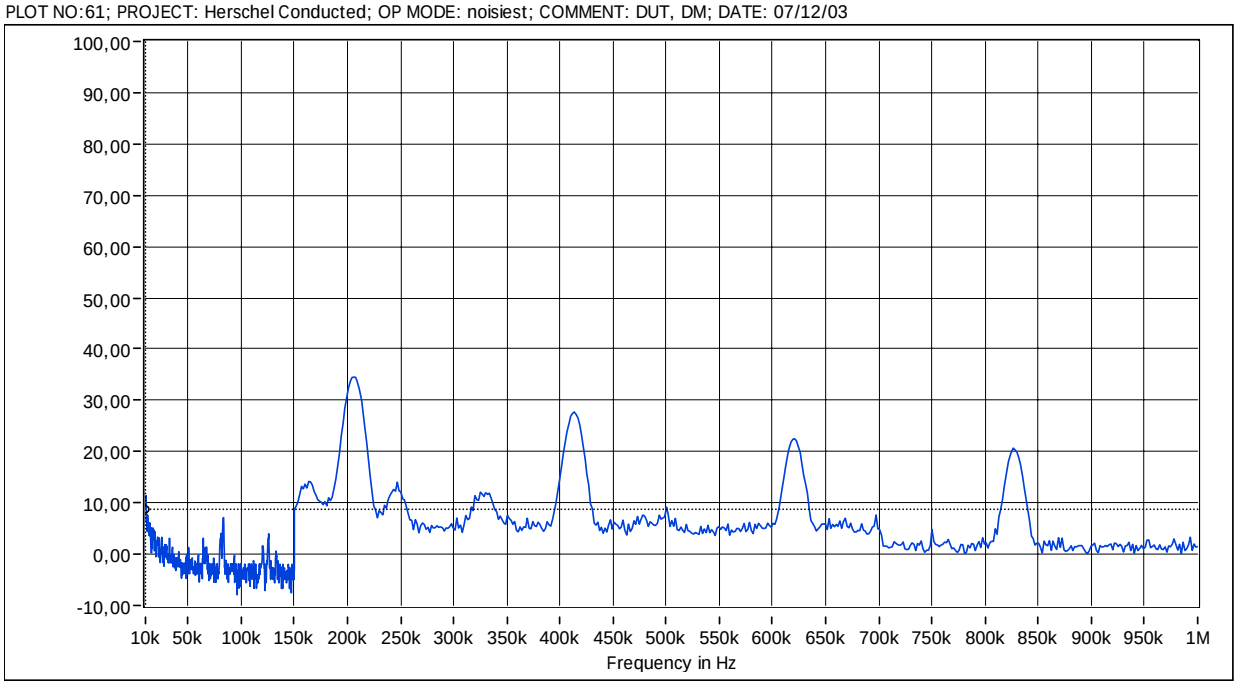
PLOT NO: 63; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT,CM; DATE: 07/12/03



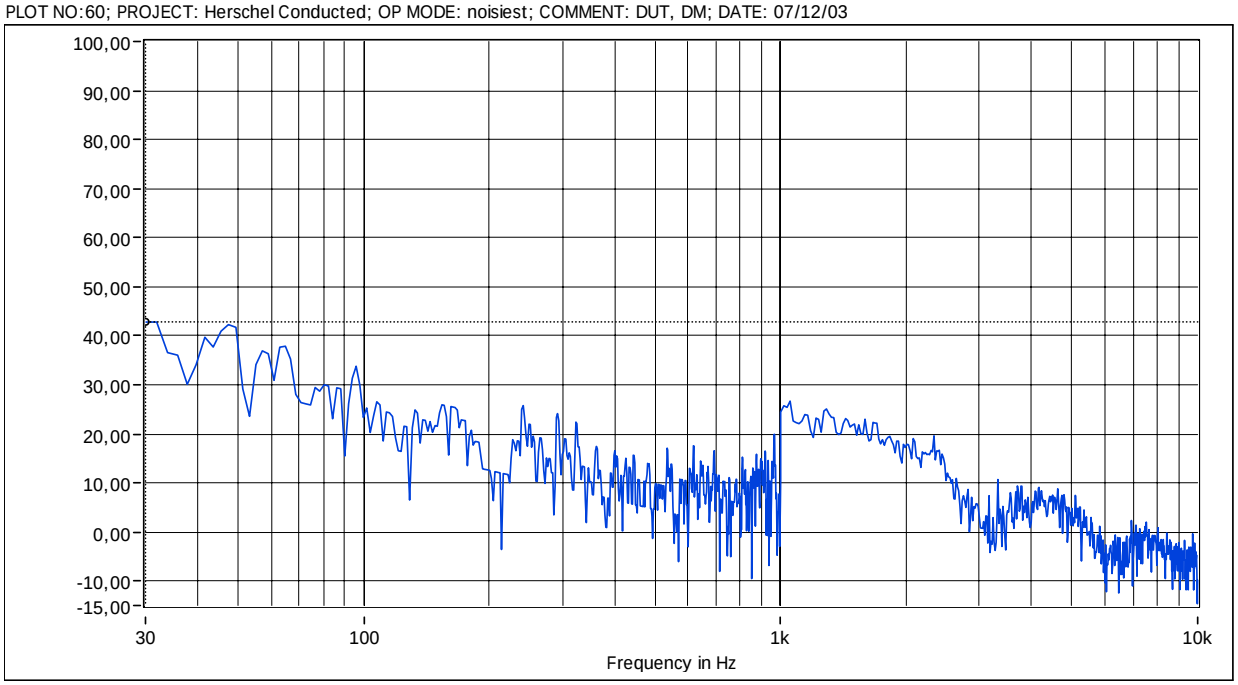
PLOT NO: 62; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT, DM; DATE: 07/12/03



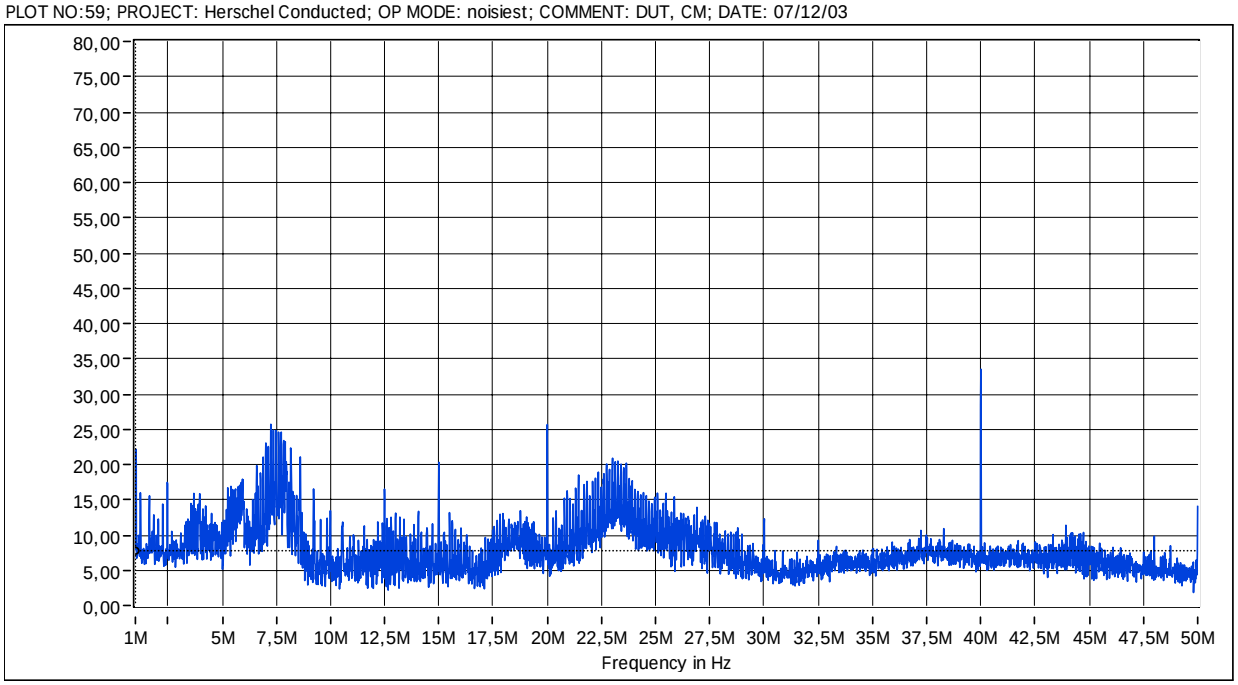
PLOT NO:61; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT, DM; DATE: 07/12/03



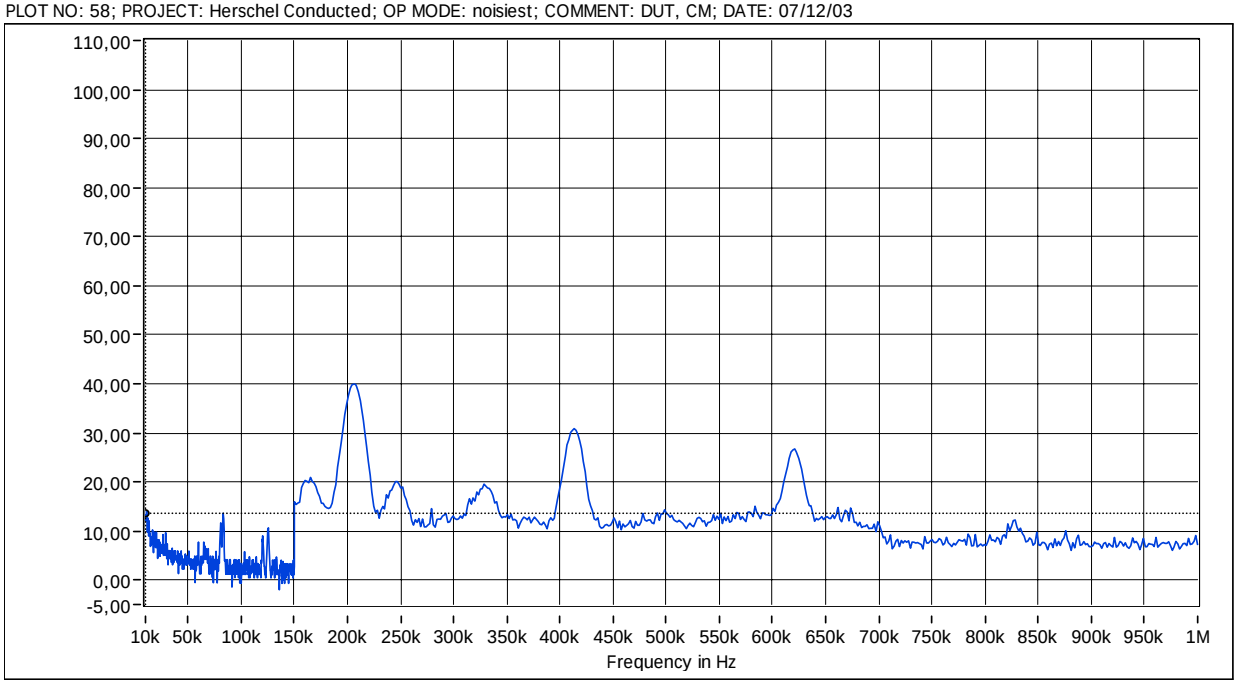
PLOT NO:60; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT, DM; DATE: 07/12/03



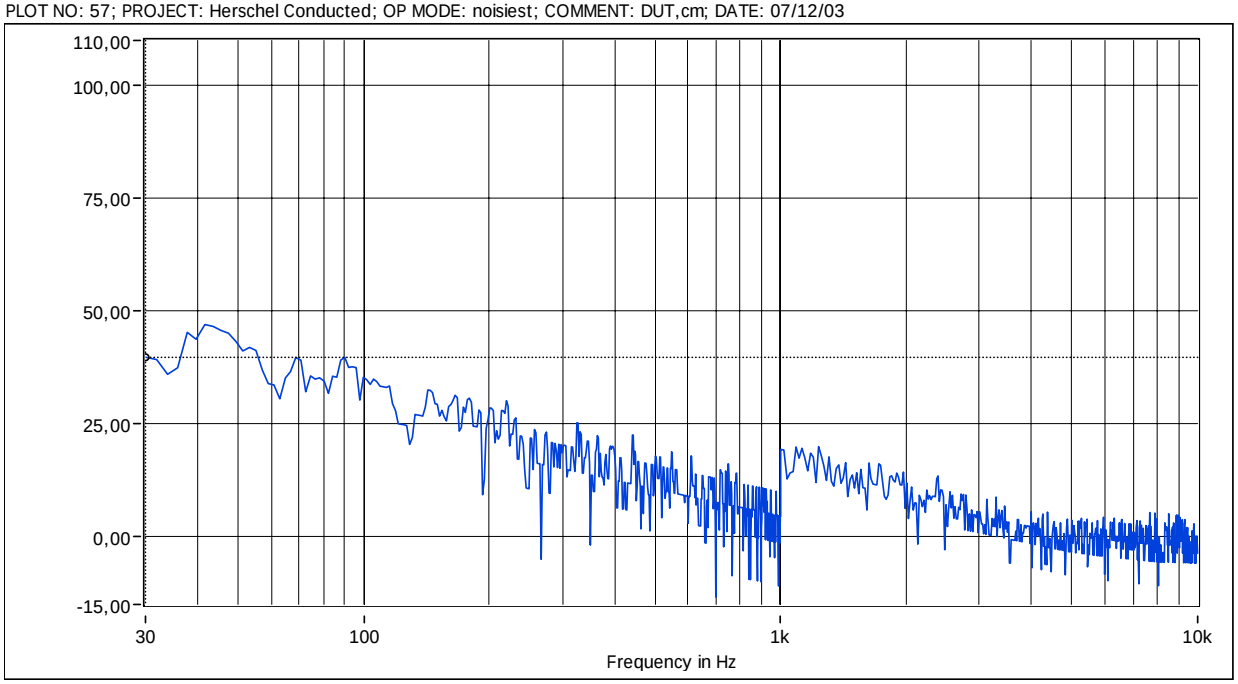
PLOT NO:59; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT, CM; DATE: 07/12/03



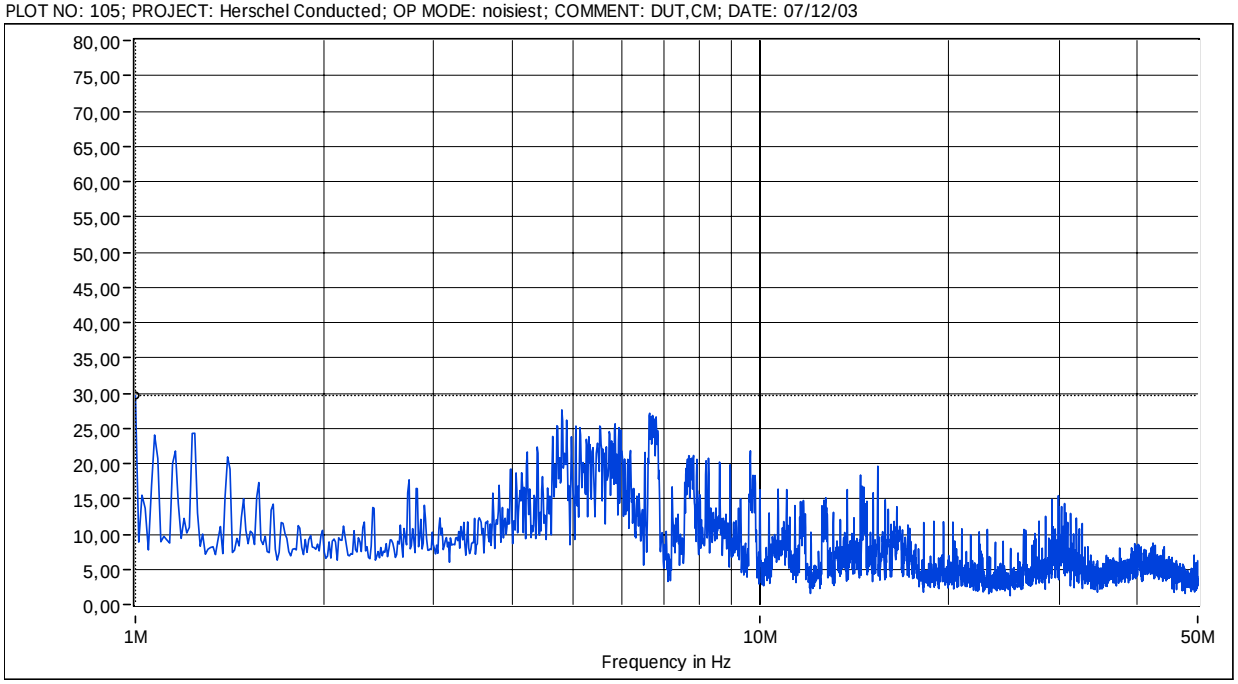
PLOT NO: 58; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT, CM; DATE: 07/12/03

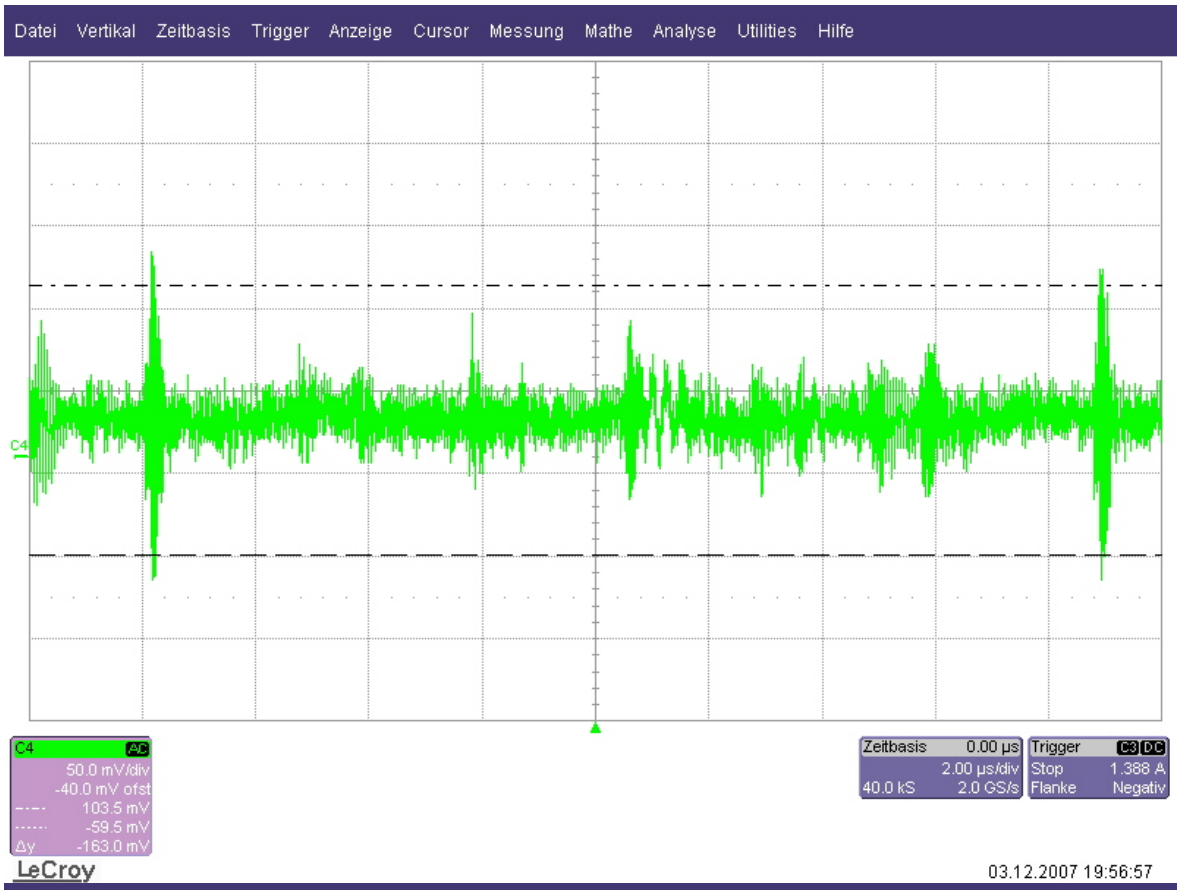


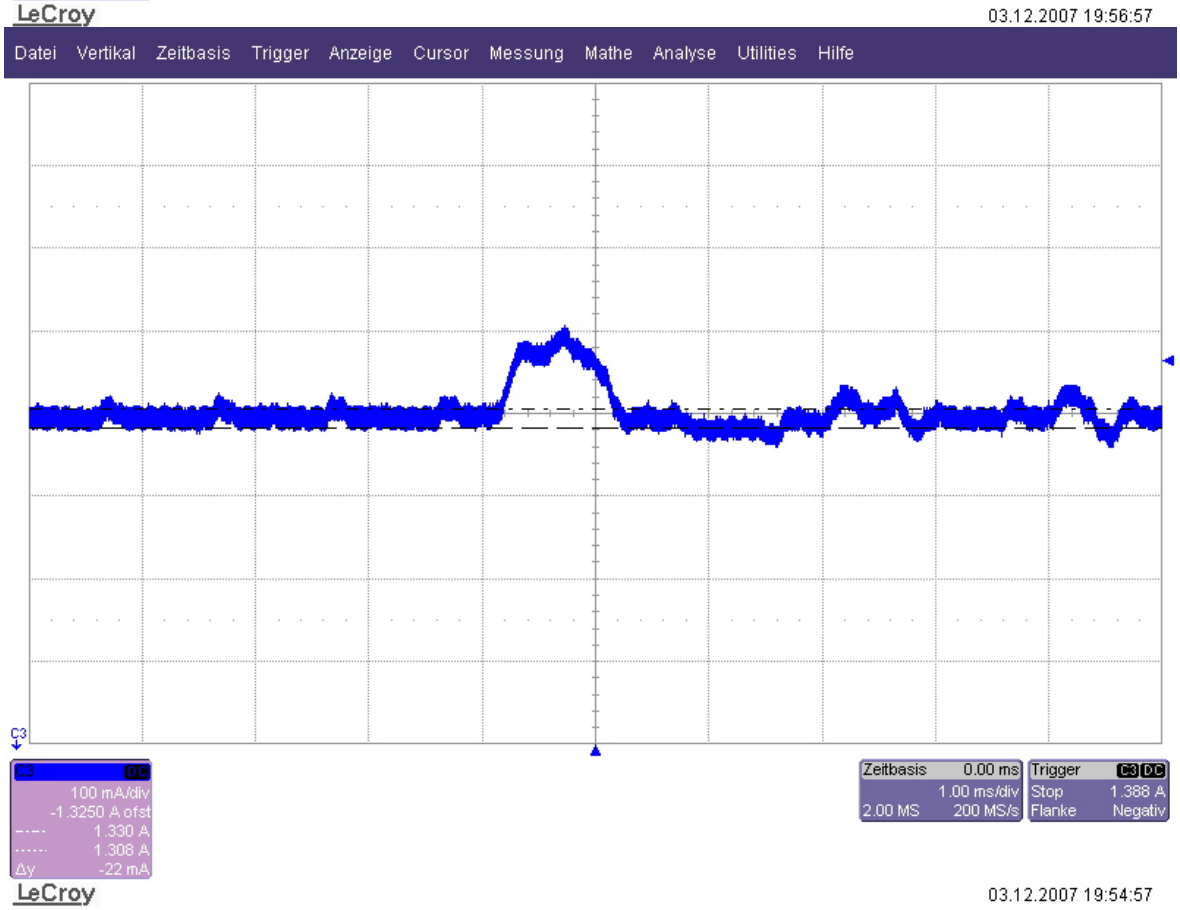
PLOT NO: 57; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT,cm; DATE: 07/12/03

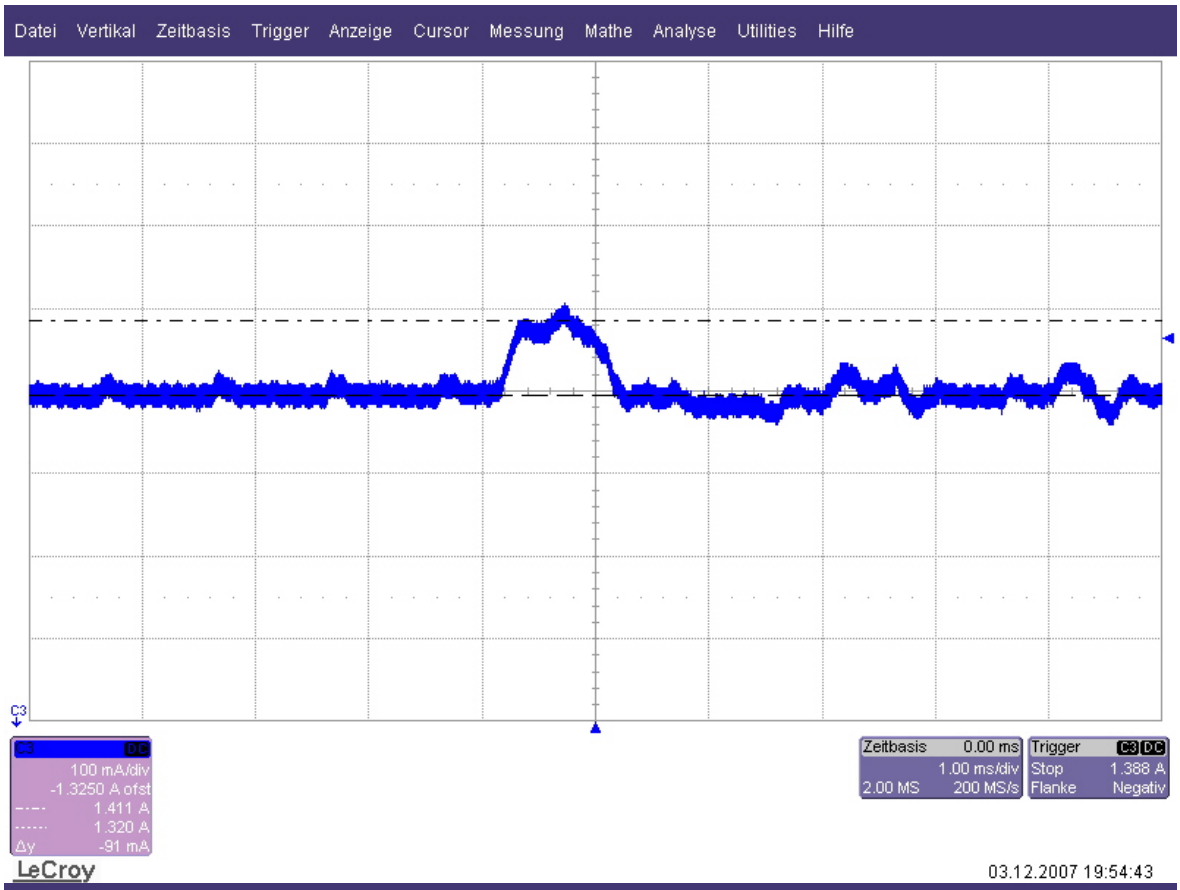


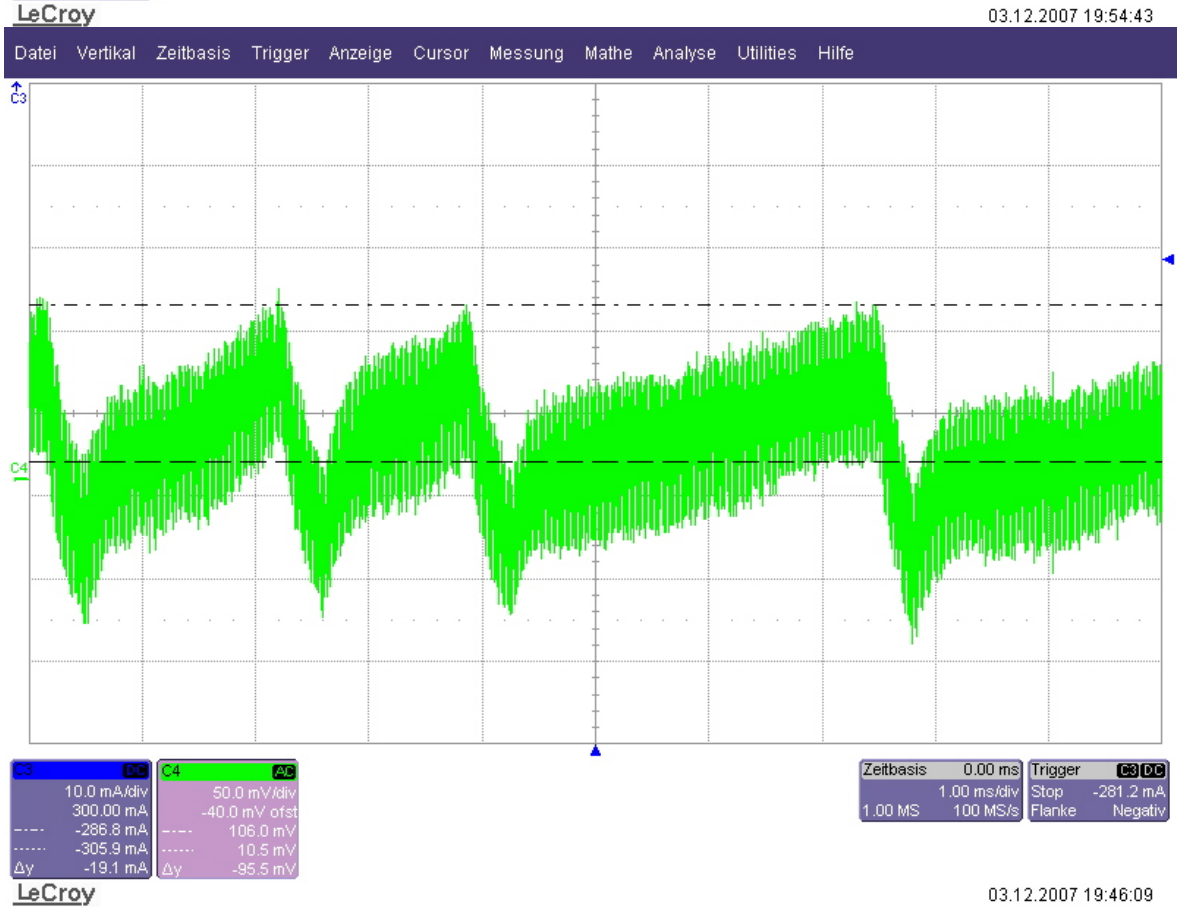
PLOT NO: 105; PROJECT: Herschel Conducted; OP MODE: noisiest; COMMENT: DUT,CM; DATE: 07/12/03

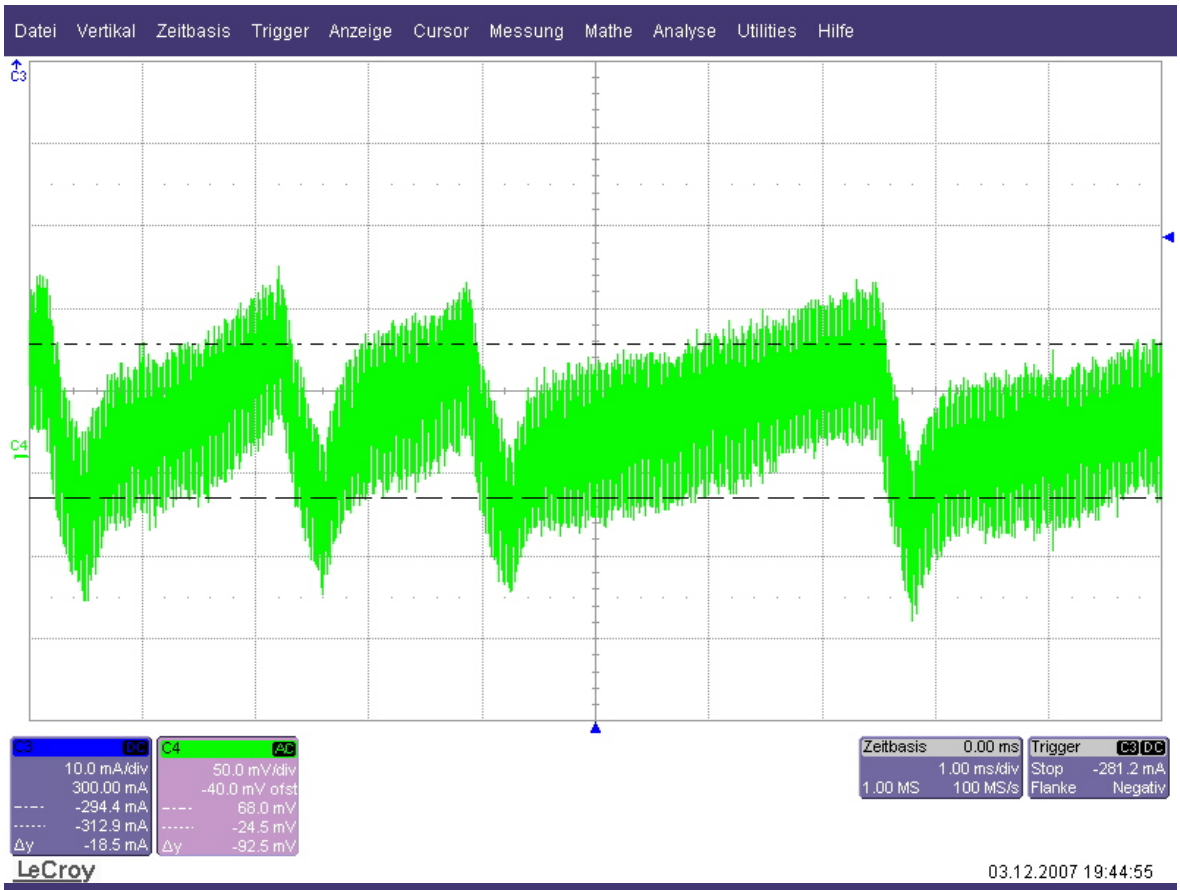


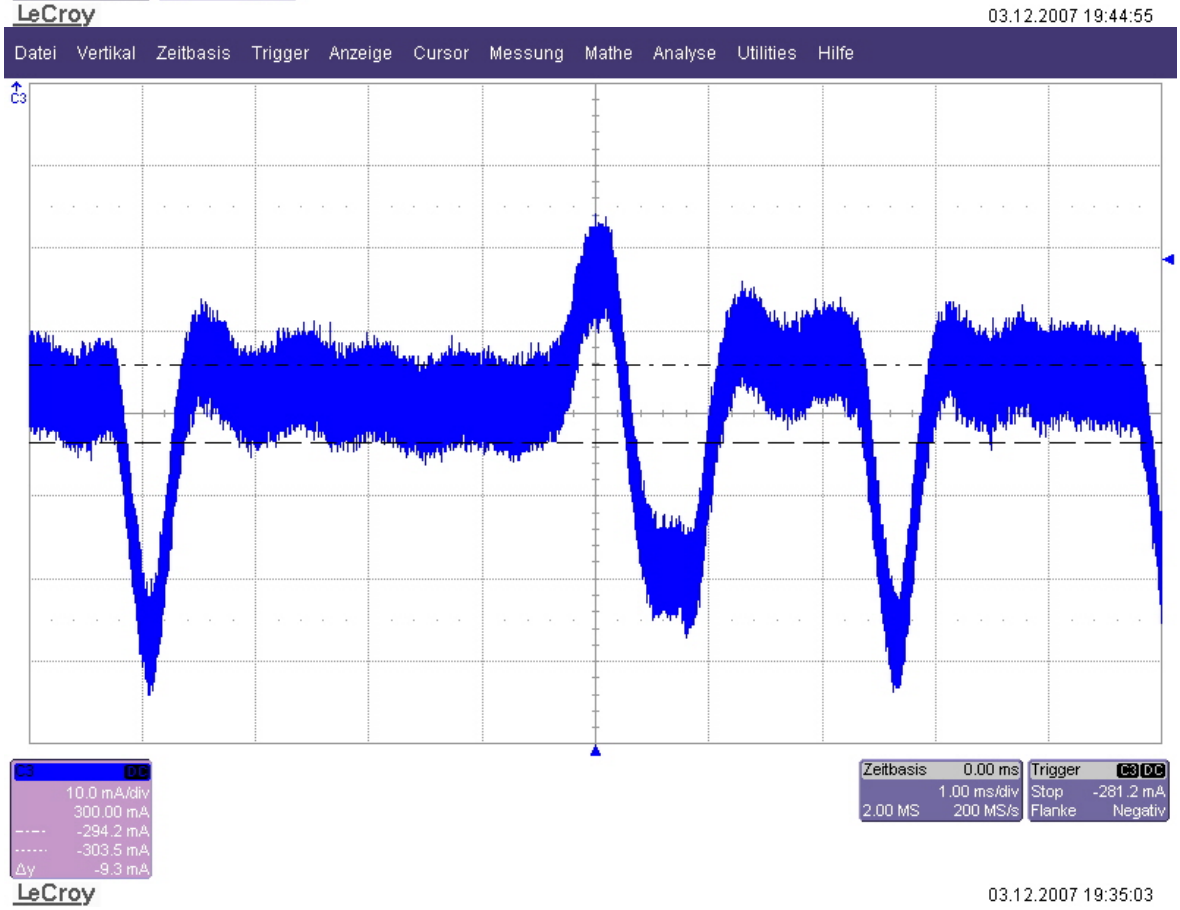

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
C4 AC
50.0 mV/div
-40.0 mV ofst
103.5 mV
-59.5 mV
Δy -163.0 mV
Zeitbasis 0.00 µs
2.00 µs/div
40.0 kS 2.0 GS/s
Trigger C3 DC
Stop 1.388 A
Flanke Negativ
LeCroy
03.12.2007 19:56:57


Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
C3 DC
100 mA/div
-1.3250 A ofst
1.330 A
1.308 A
Δy -22 mA
Zeitbasis 0.00 ms
1.00 ms/div
2.00 MS 200 MS/s
Trigger C3 DC
Stop 1.388 A
Flanke Negativ
LeCroy
03.12.2007 19:54:57

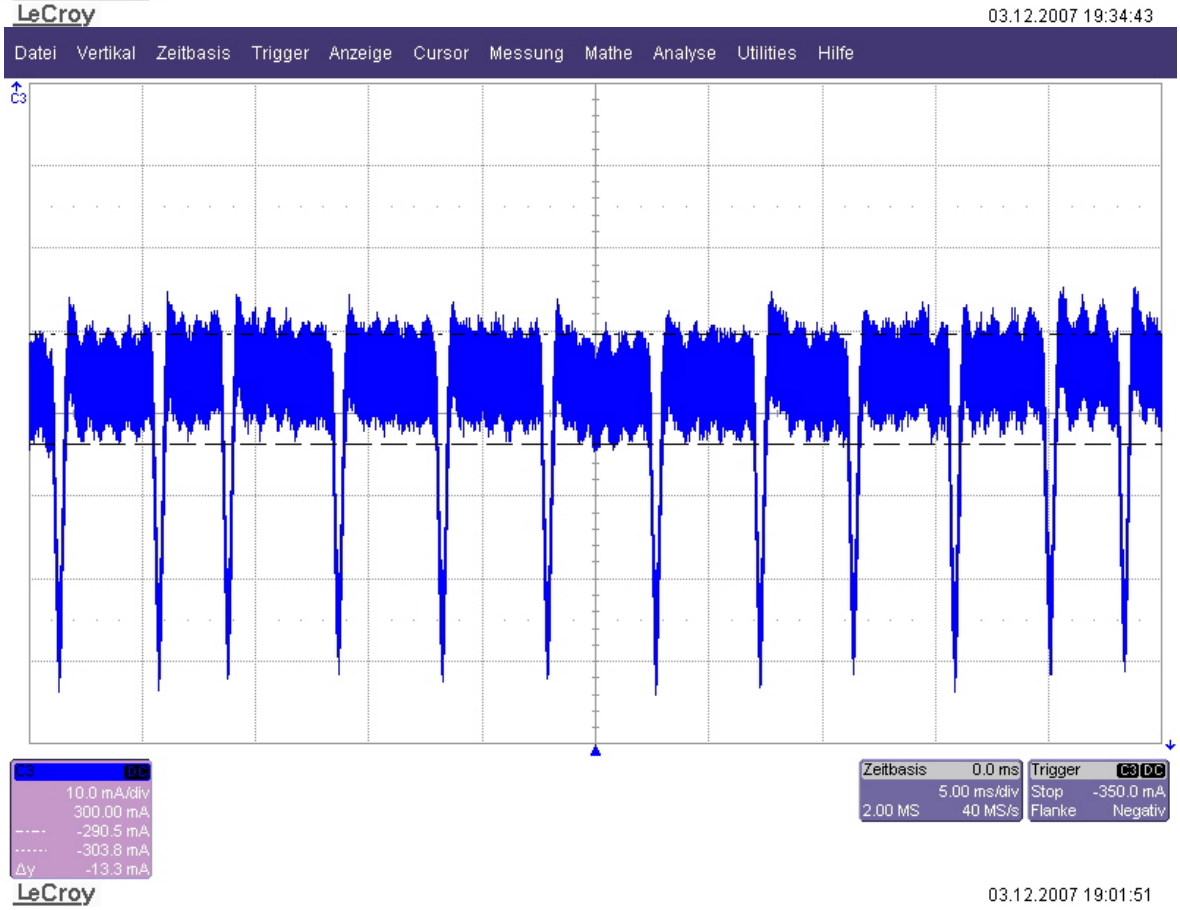

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
C3 DC
100 mA/div
-1.3250 A ofst
1.411 A
1.320 A
Δy -91 mA
Zeitbasis 0.00 ms
1.00 ms/div
2.00 MS 200 MS/s
Trigger C3 DC
Stop 1.388 A
Flanke Negativ
LeCroy
03.12.2007 19:54:43

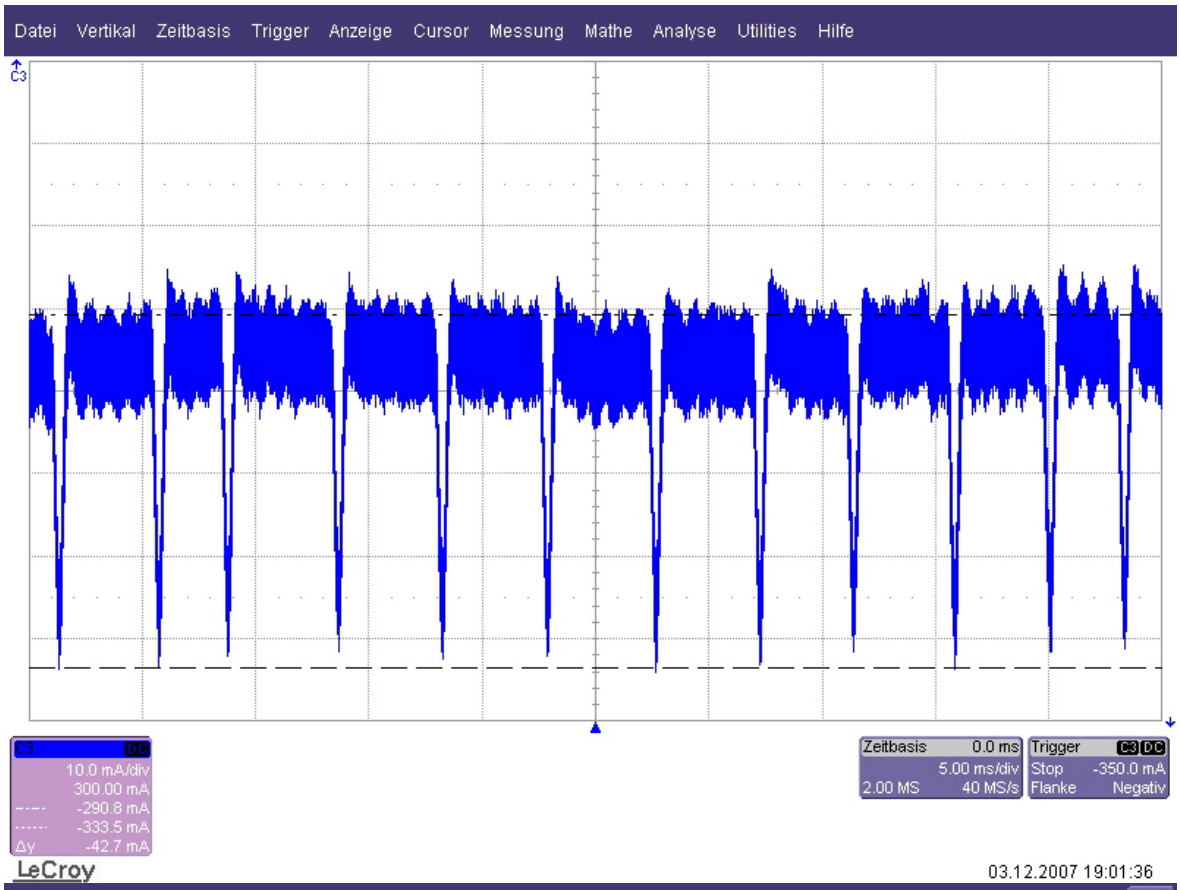

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
C4
C3 DC
10.0 mA/div
300.00 mA
-286.8 mA
-305.9 mA
Δy -19.1 mA
C4 AC
50.0 mV/div
-40.0 mV ofst
106.0 mV
10.5 mV
Δy -95.5 mV
Zeitbasis 0.00 ms
1.00 ms/div
1.00 MS 100 MS/s
Trigger C3 DC
Stop -281.2 mA
Flanke Negativ
LeCroy
03.12.2007 19:46:09

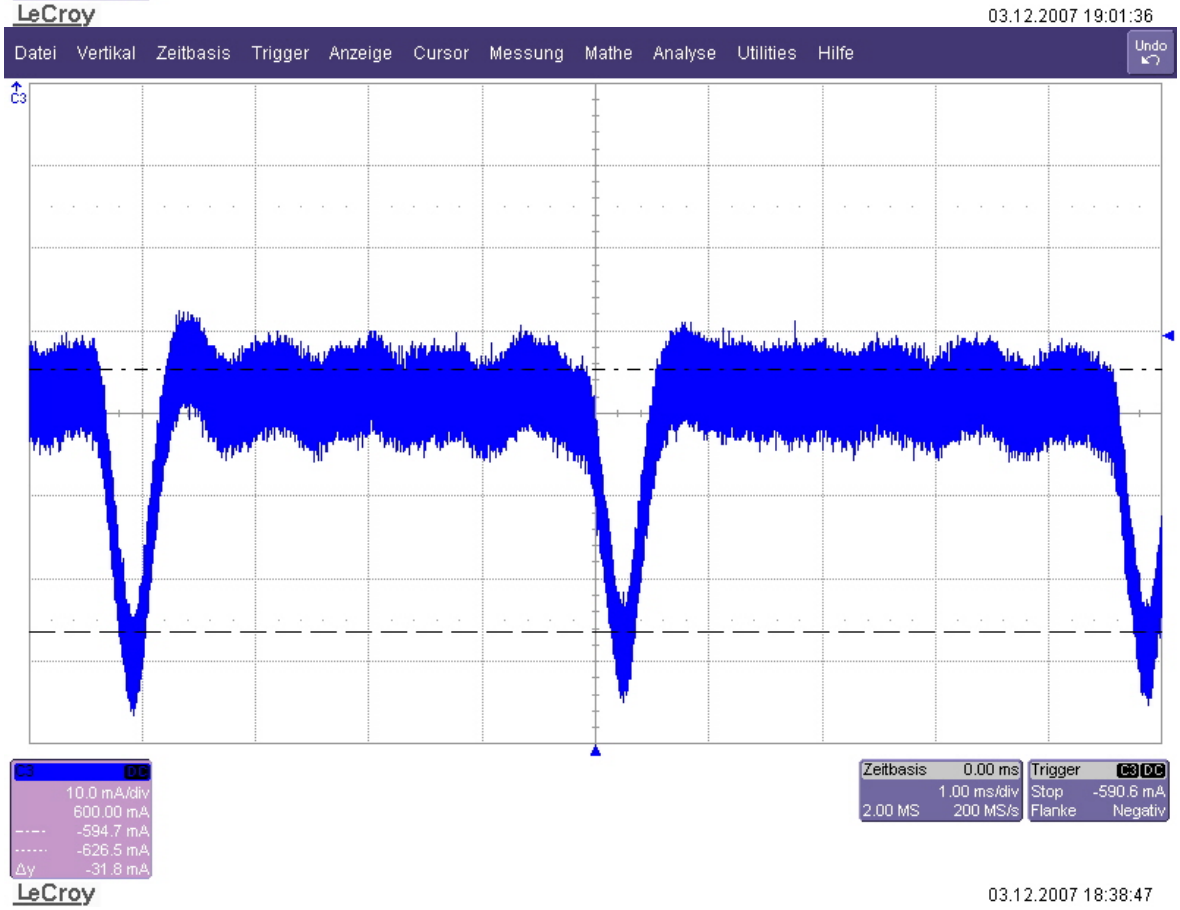

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
C4
C3 DC
10.0 mA/div
300.00 mA
-294.4 mA
-312.9 mA
Δy -18.5 mA
C4 AC
50.0 mV/div
-40.0 mV ofst
68.0 mV
-24.5 mV
Δy -92.5 mV
Zeitbasis 0.00 ms
1.00 ms/div
1.00 MS 100 MS/s
Trigger C3 DC
Stop -281.2 mA
Flanke Negativ
LeCroy
03.12.2007 19:44:55

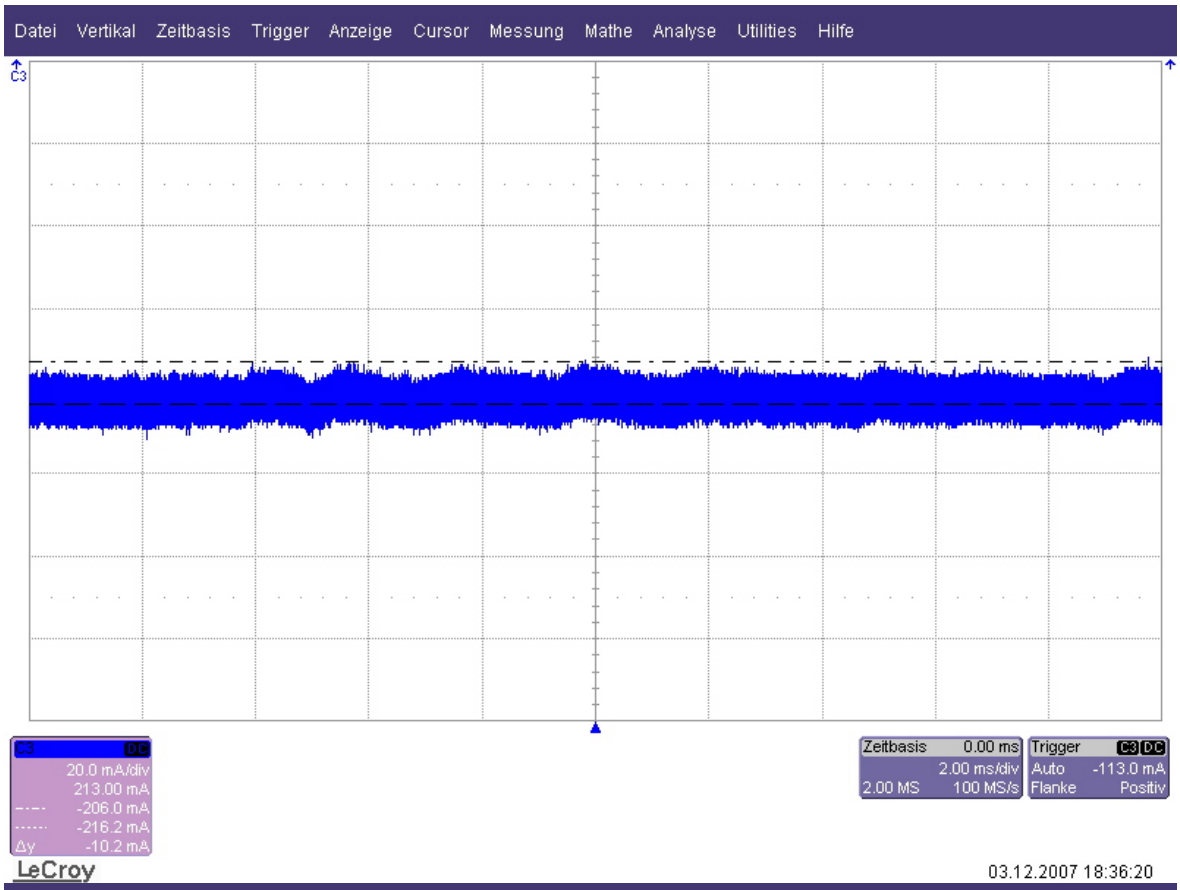

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
C3 DC
10.0 mA/div
300.00 mA
-294.2 mA
-303.5 mA
Δy -9.3 mA
Zeitbasis 0.00 ms
1.00 ms/div
2.00 MS 200 MS/s
Trigger C3 DC
Stop -281.2 mA
Flanke Negativ
LeCroy
03.12.2007 19:35:03

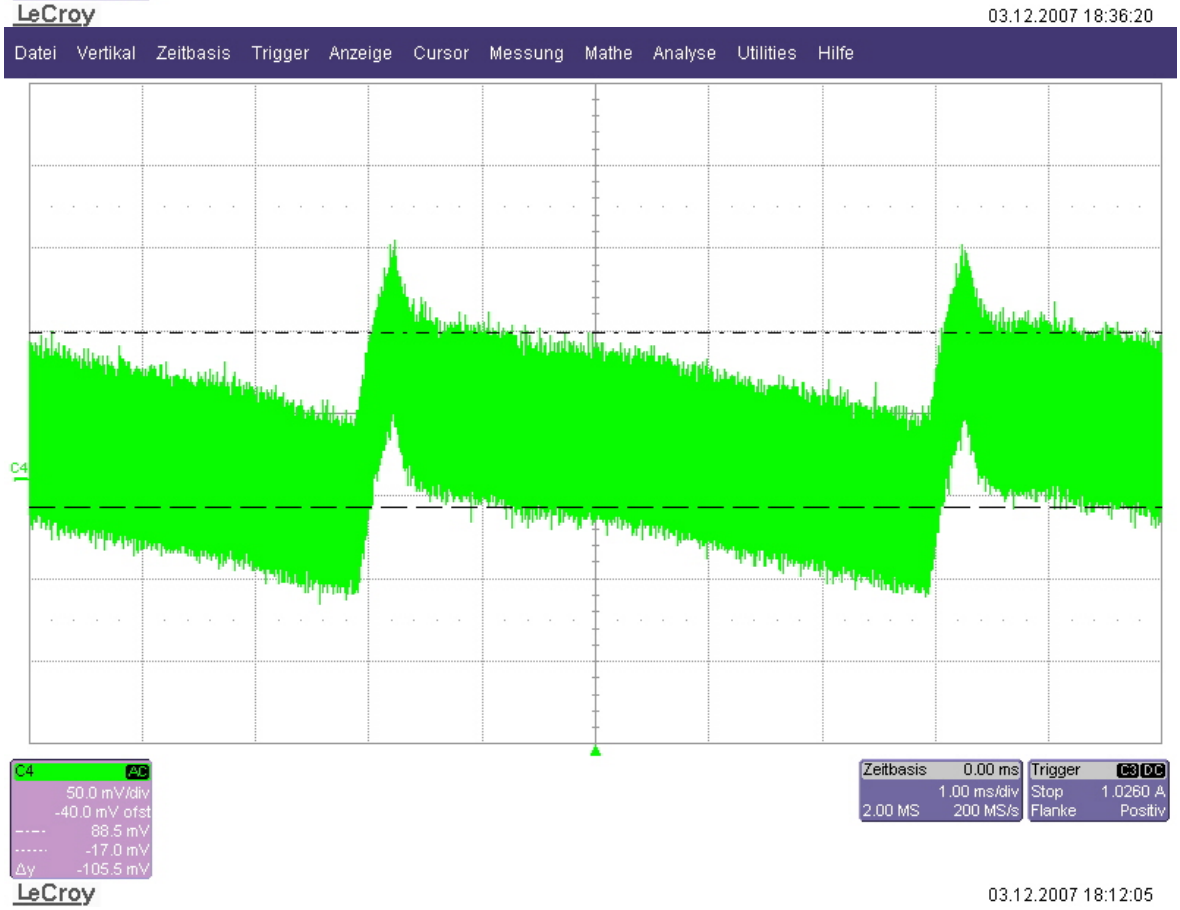

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
10.0 mA/div
300.00 mA
-284.7 mA
-325.7 mA
Δy -41.0 mA
Zeitbasis 0.00 ms
1.00 ms/div
2.00 MS 200 MS/s
Trigger C3 DC
Stop -281.2 mA
Flanke Negativ
LeCroy
03.12.2007 19:34:43

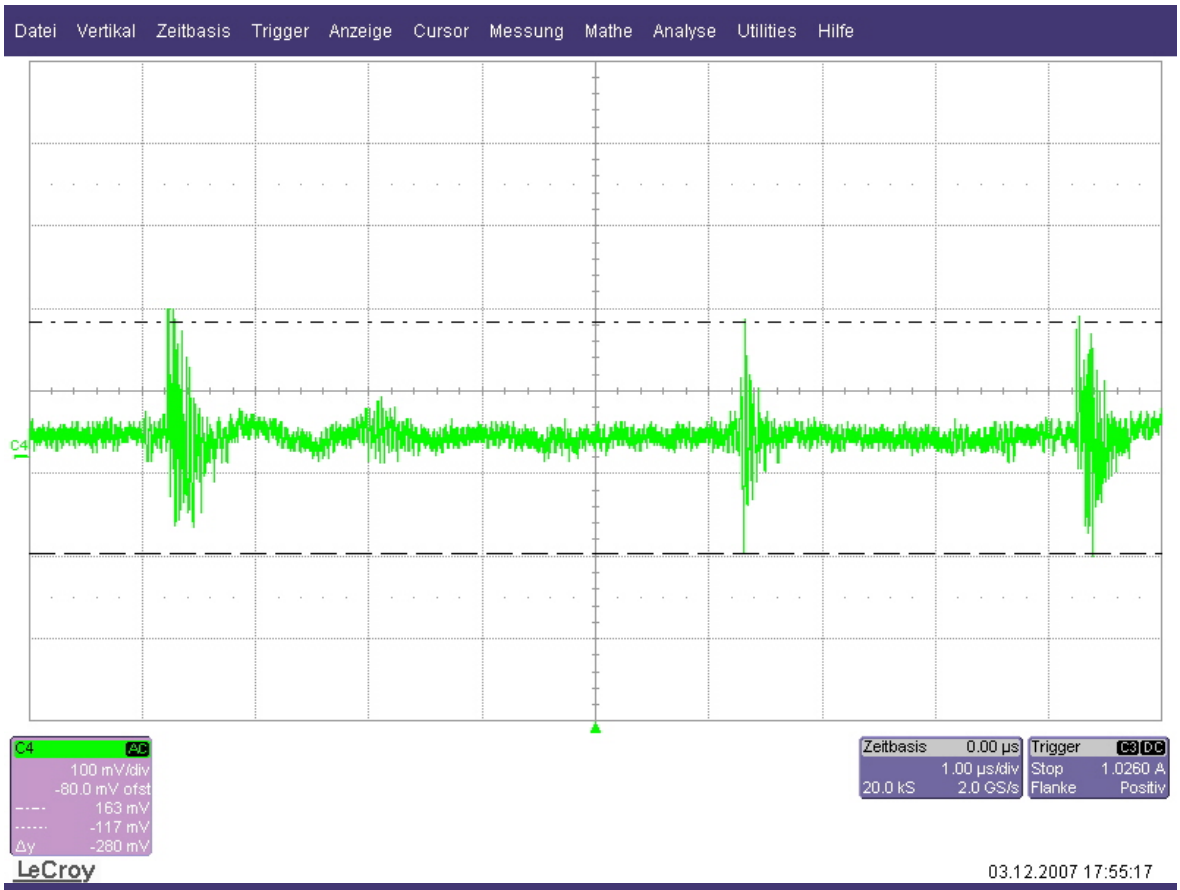

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
10.0 mA/div
300.00 mA
-290.5 mA
-303.8 mA
Δy -13.3 mA
Zeitbasis 0.0 ms
5.00 ms/div
2.00 MS 40 MS/s
Trigger C3 DC
Stop -350.0 mA
Flanke Negativ
LeCroy
03.12.2007 19:01:51

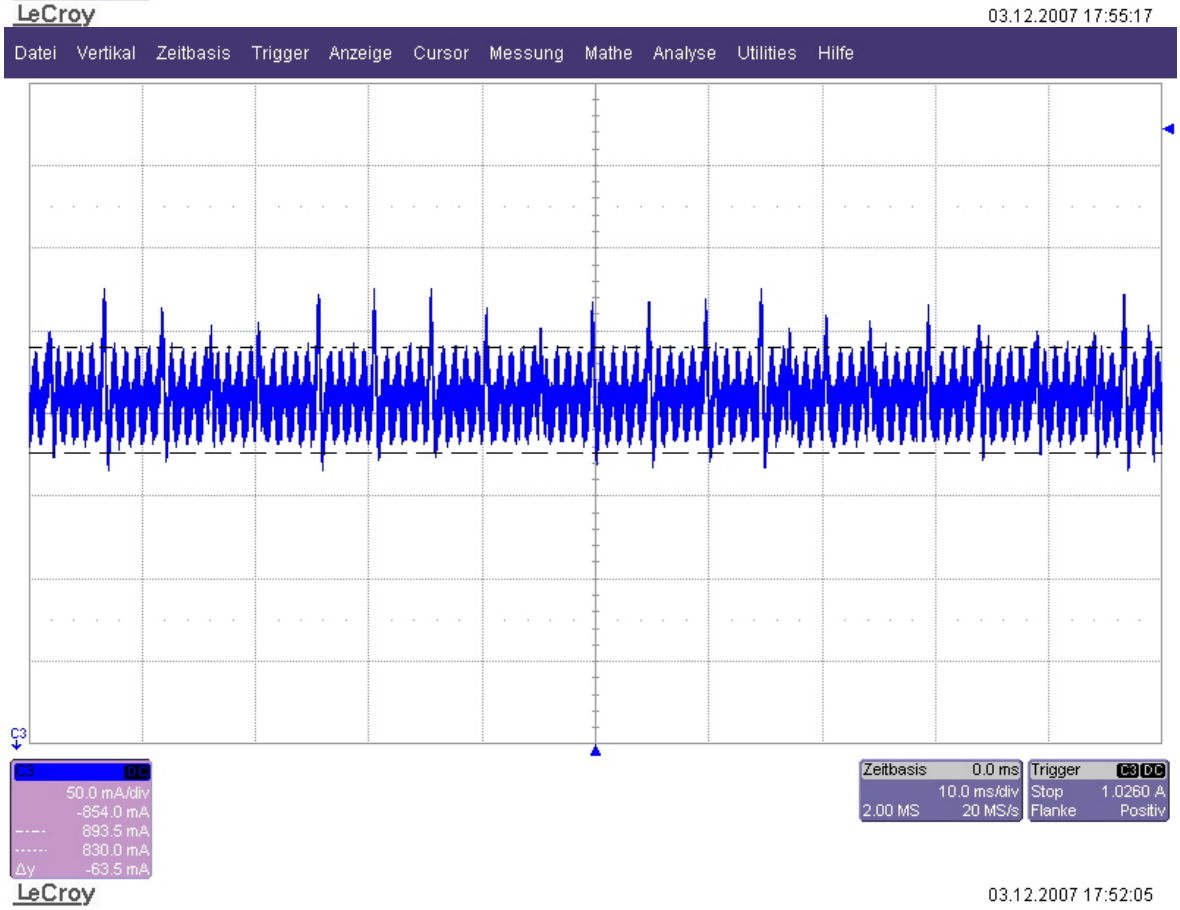

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
C3 DC
10.0 mA/div
300.00 mA
-290.8 mA
-333.5 mA
Δy -42.7 mA
Zeitbasis 0.0 ms
5.00 ms/div
2.00 MS 40 MS/s
Trigger C3 DC
Stop -350.0 mA
Flanke Negativ
LeCroy
03.12.2007 19:01:36

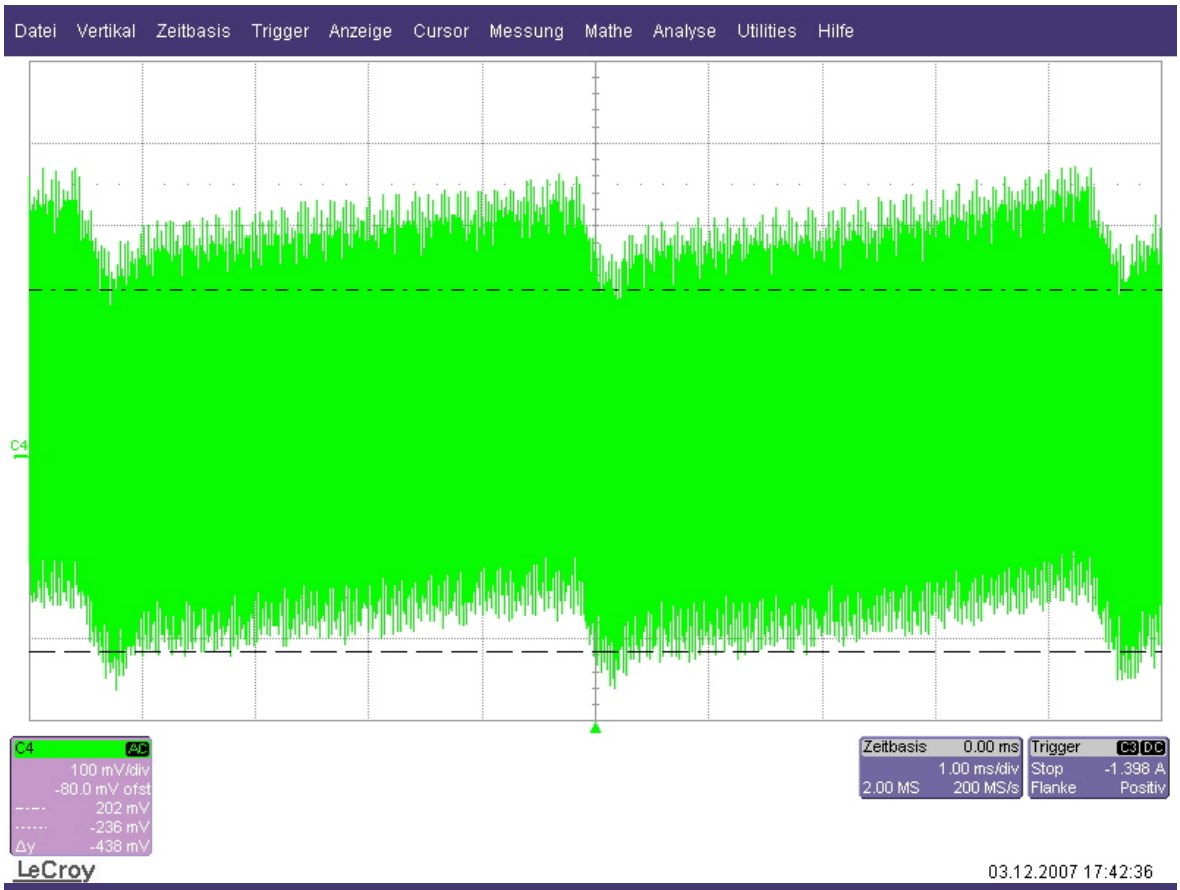

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
Undo
C3
C3 DC
10.0 mA/div
600.00 mA
-594.7 mA
-626.5 mA
Δy -31.8 mA
Zeitbasis 0.00 ms
1.00 ms/div
2.00 MS 200 MS/s
Trigger C3 DC
Stop -590.6 mA
Flanke Negativ
LeCroy
03.12.2007 18:38:47


Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
C3 DC
20.0 mA/div
213.00 mA
-206.0 mA
-216.2 mA
Δy -10.2 mA
Zeitbasis 0.00 ms
2.00 ms/div
2.00 MS 100 MS/s
Trigger C3 DC
Auto -113.0 mA
Flanke Positiv
LeCroy
03.12.2007 18:36:20

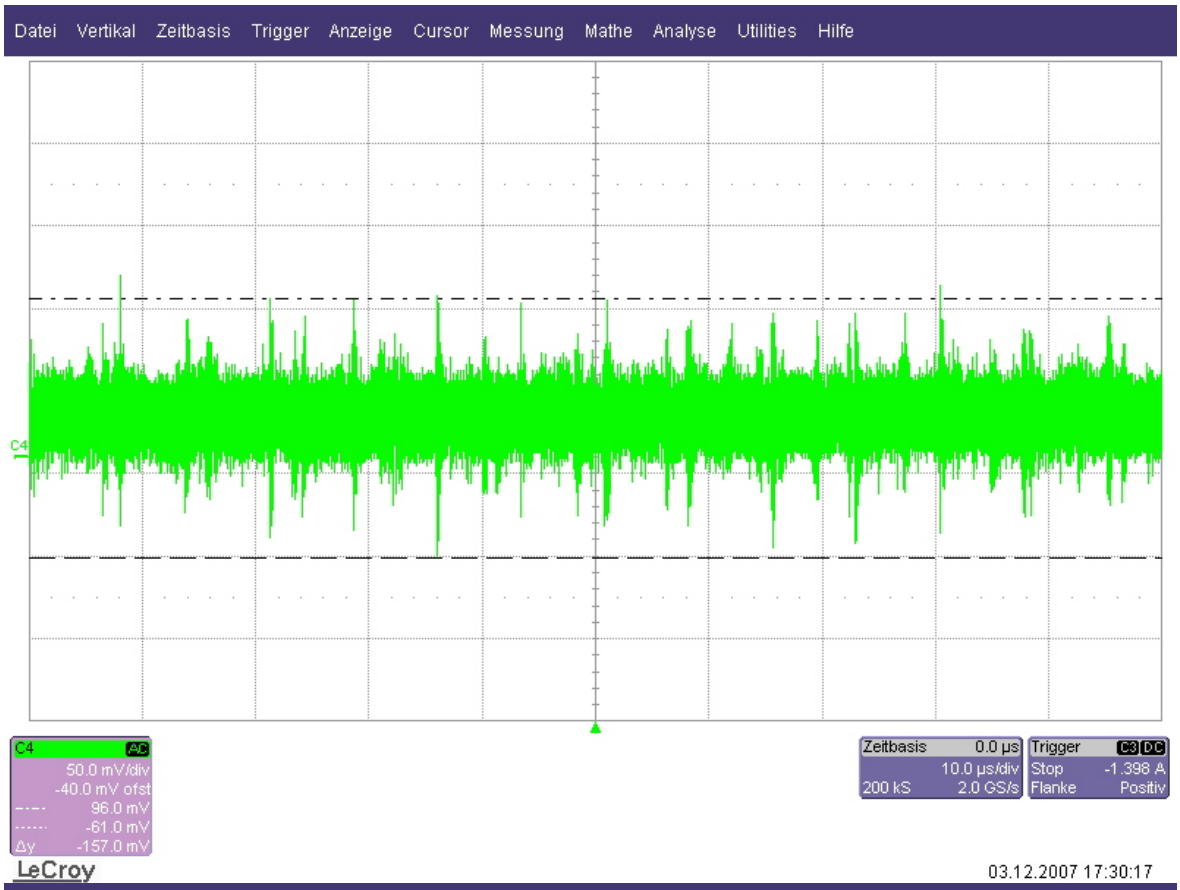

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
C4 AC
50.0 mV/div
-40.0 mV ofst
88.5 mV
-17.0 mV
Δy -105.5 mV
Zeitbasis 0.00 ms
1.00 ms/div
2.00 MS 200 MS/s
Trigger C3 DC
Stop 1.0260 A
Flanke Positiv
LeCroy
03.12.2007 18:12:05

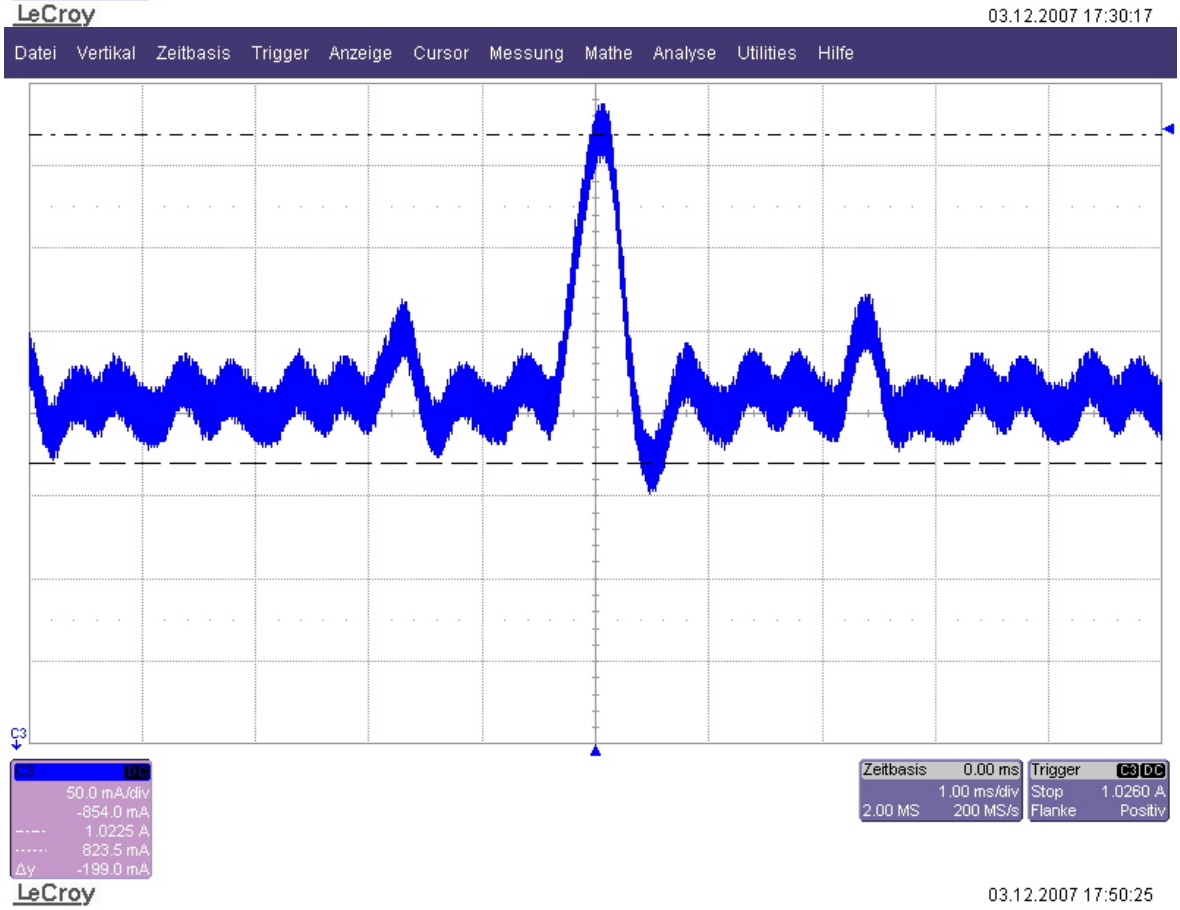

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
C4 AC
100 mV/div
-80.0 mV ofst
163 mV
-117 mV
Δy -280 mV
Zeitbasis 0.00 µs
1.00 µs/div
20.0 kS 2.0 GS/s
Trigger C3 DC
Stop 1.0260 A
Flanke Positiv
LeCroy
03.12.2007 17:55:17

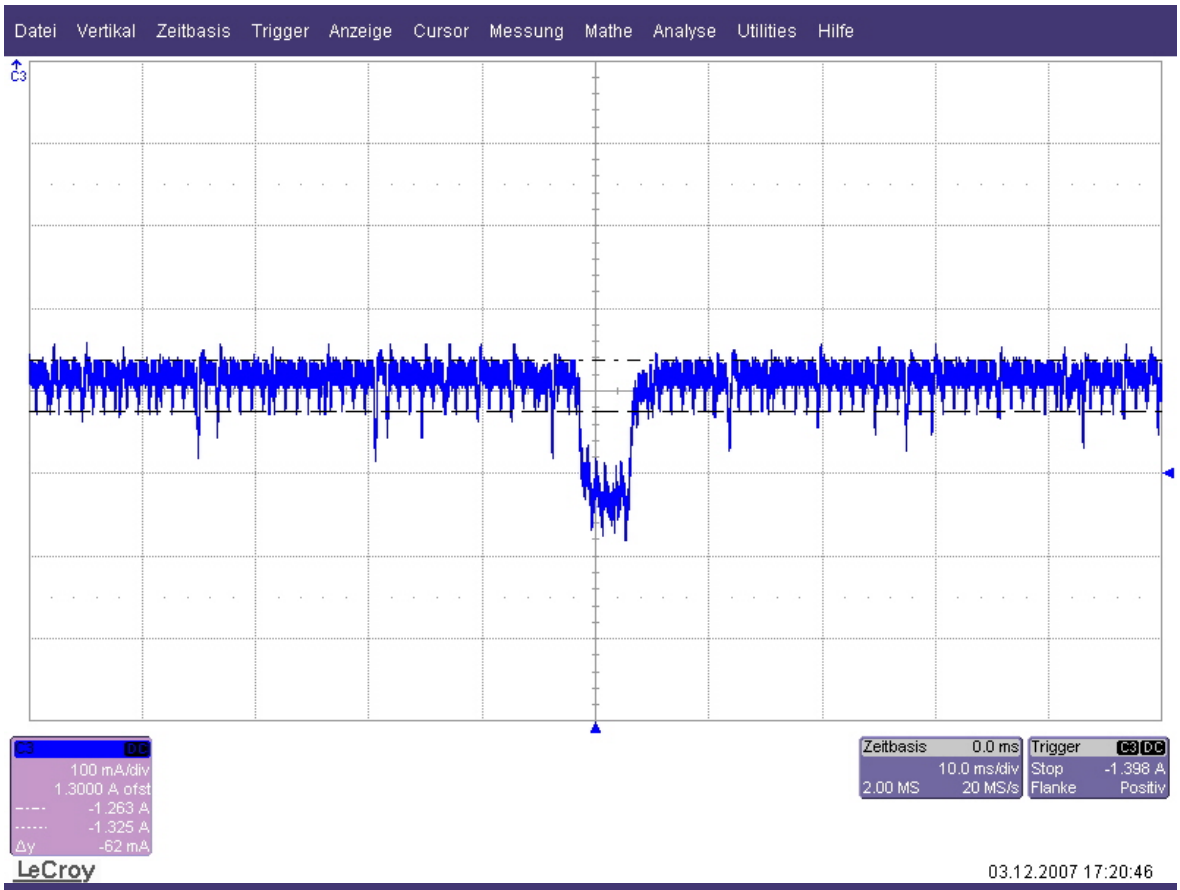

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
C3 DC
50.0 mA/div
-854.0 mA
893.5 mA
830.0 mA
Δy -63.5 mA
Zeitbasis 0.0 ms
10.0 ms/div
2.00 MS 20 MS/s
Trigger C3 DC
Stop 1.0260 A
Flanke Positiv
LeCroy
03.12.2007 17:52:05

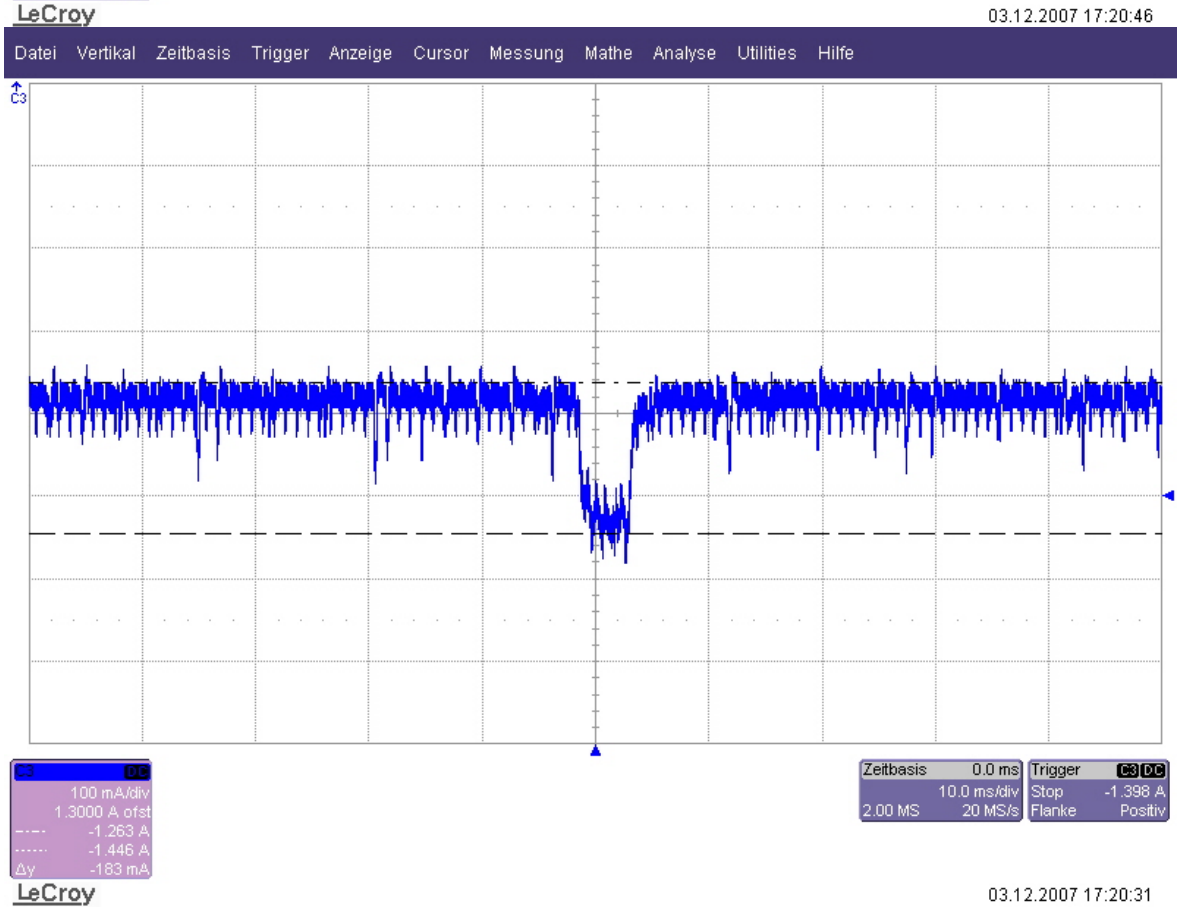

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
C4 AC
100 mV/div
-80.0 mV ofst
202 mV
-236 mV
Δy -438 mV
Zeitbasis 0.00 ms
1.00 ms/div
2.00 MS 200 MS/s
Trigger C3 DC
Stop -1.398 A
Flanke Positiv
LeCroy
03.12.2007 17:42:36

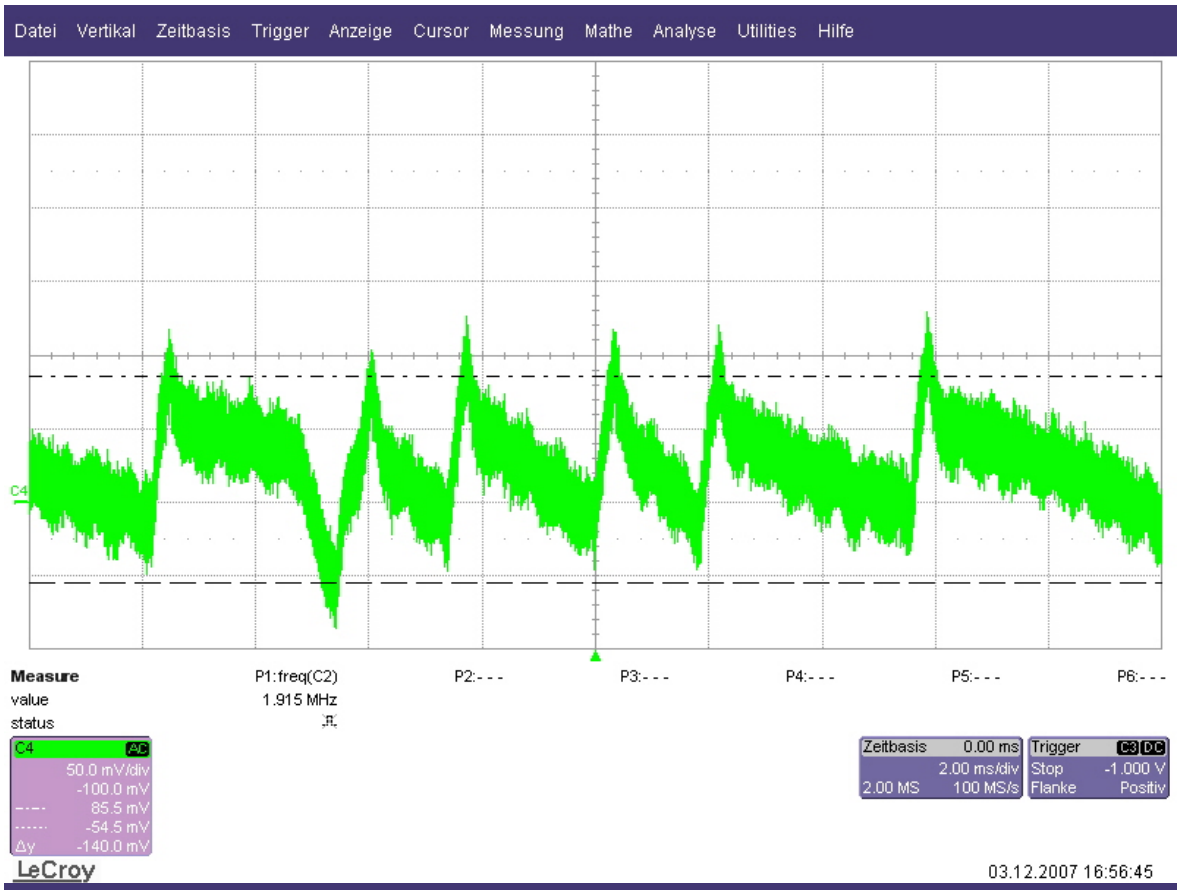

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
C4 AC
50.0 mV/div
-40.0 mV ofst
52.5 mV
-11.0 mV
Δy -63.5 mV
Zeitbasis 0.0 µs
10.0 µs/div
200 kS 2.0 GS/s
Trigger C3 DC
Stop -1.398 A
Flanke Positiv
LeCroy
03.12.2007 17:30:35


Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
C4 AC
50.0 mV/div
-40.0 mV ofst
96.0 mV
-61.0 mV
Δy -157.0 mV
Zeitbasis 0.0 µs
10.0 µs/div
200 kS 2.0 GS/s
Trigger C3 DC
Stop -1.398 A
Flanke Positiv
LeCroy
03.12.2007 17:30:17


Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
C3 DC
50.0 mA/div
-854.0 mA
1.0225 A
823.5 mA
Δy -199.0 mA
Zeitbasis 0.00 ns
1.00 ms/div
2.00 MS 200 MS/s
Trigger C3 DC
Stop 1.0260 A
Flanke Positiv
LeCroy
03.12.2007 17:50:25

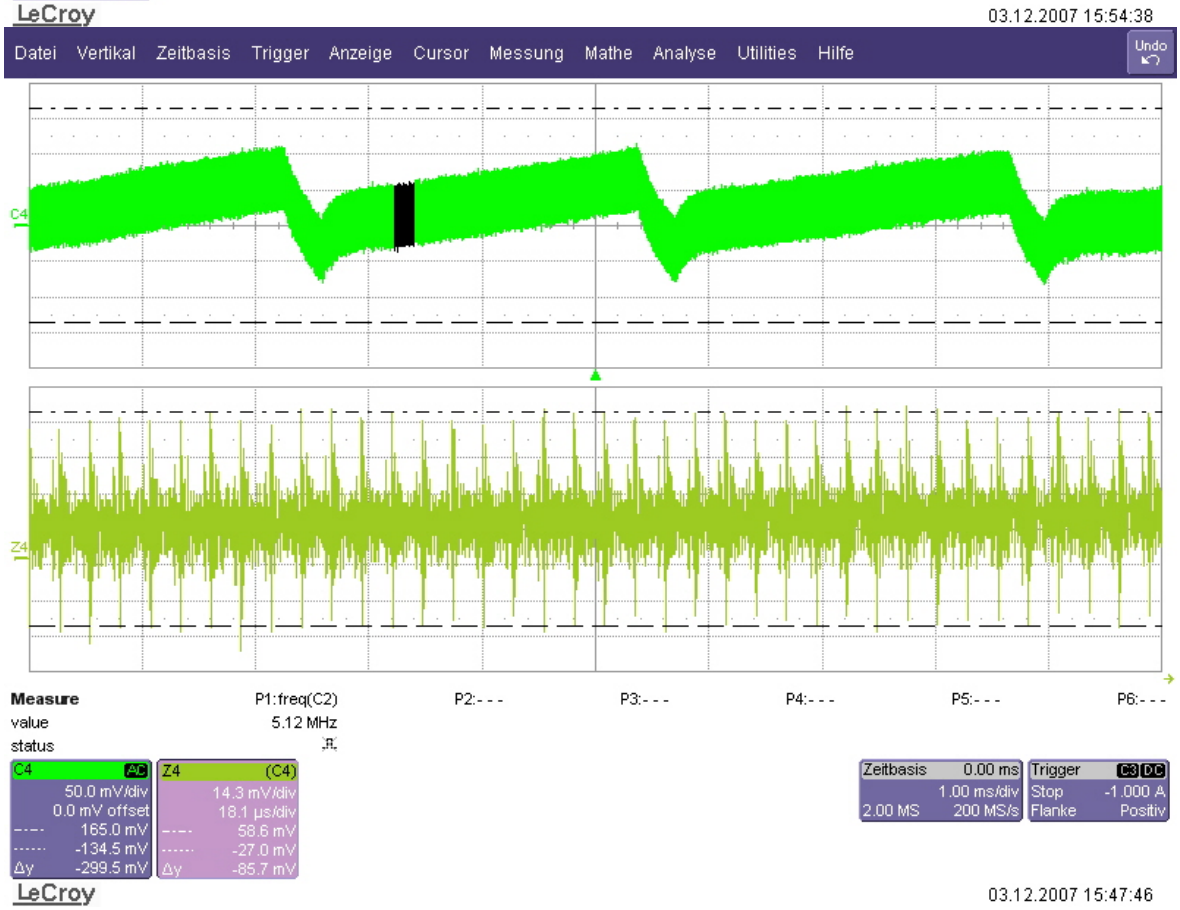

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
C3 DC
100 mA/div
1.3000 A ofst
-1.263 A
-1.325 A
Δy -62 mA
Zeitbasis 0.0 ms
10.0 ms/div
2.00 MS 20 MS/s
Trigger C3 DC
Stop -1.398 A
Flanke Positiv
LeCroy
03.12.2007 17:20:46

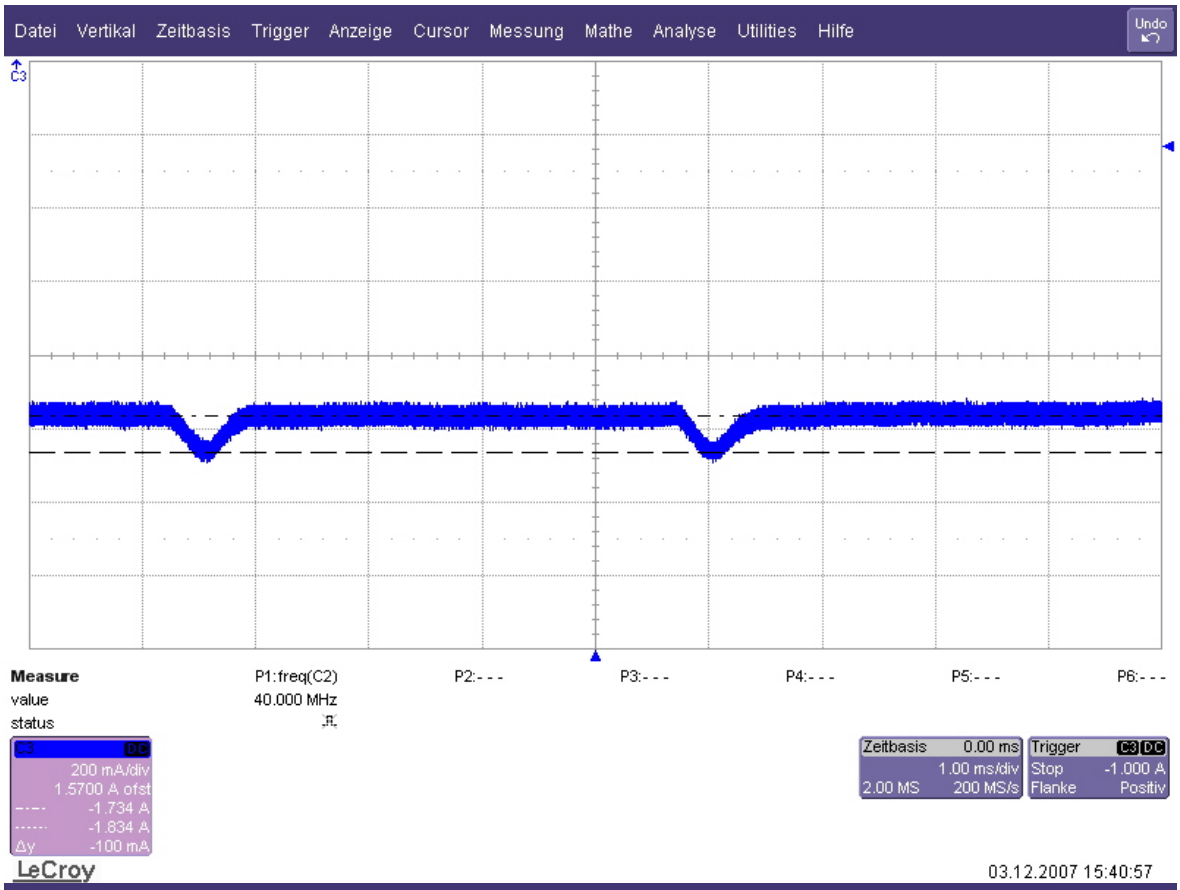

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
C3 DC
100 mA/div
1.3000 A ofst
-1.263 A
-1.446 A
Δy -183 mA
Zeitbasis 0.0 ms
10.0 ms/div
2.00 MS 20 MS/s
Trigger C3 DC
Stop -1.398 A
Flanke Positiv
LeCroy
03.12.2007 17:20:31

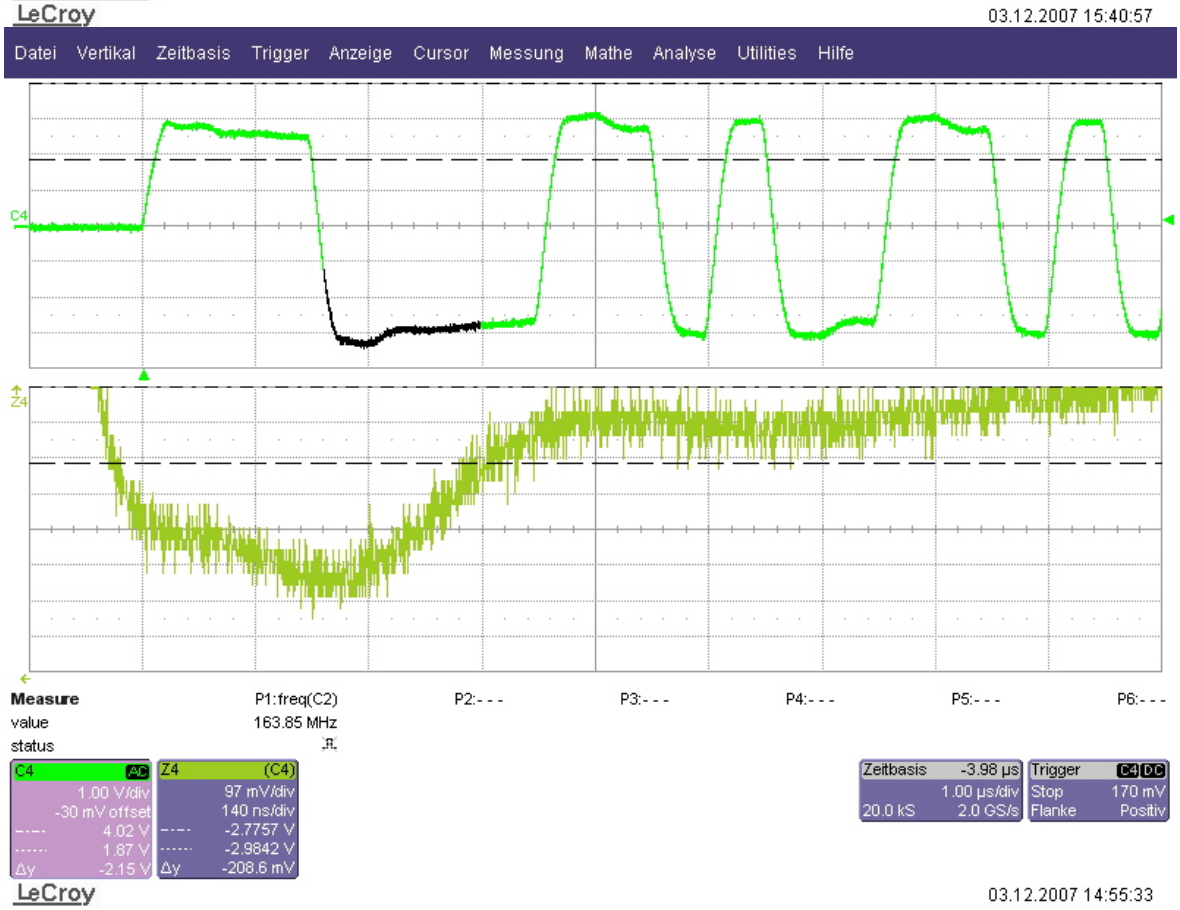

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 1.915 MHz
status
C4 AC
50.0 mV/div
-100.0 mV
85.5 mV
-54.5 mV
Δy -140.0 mV
Zeitbasis 0.00 ms
2.00 ms/div
2.00 MS 100 MS/s
Trigger C3 DC
Stop -1.000 V
Flanke Positiv
LeCroy
03.12.2007 16:56:45

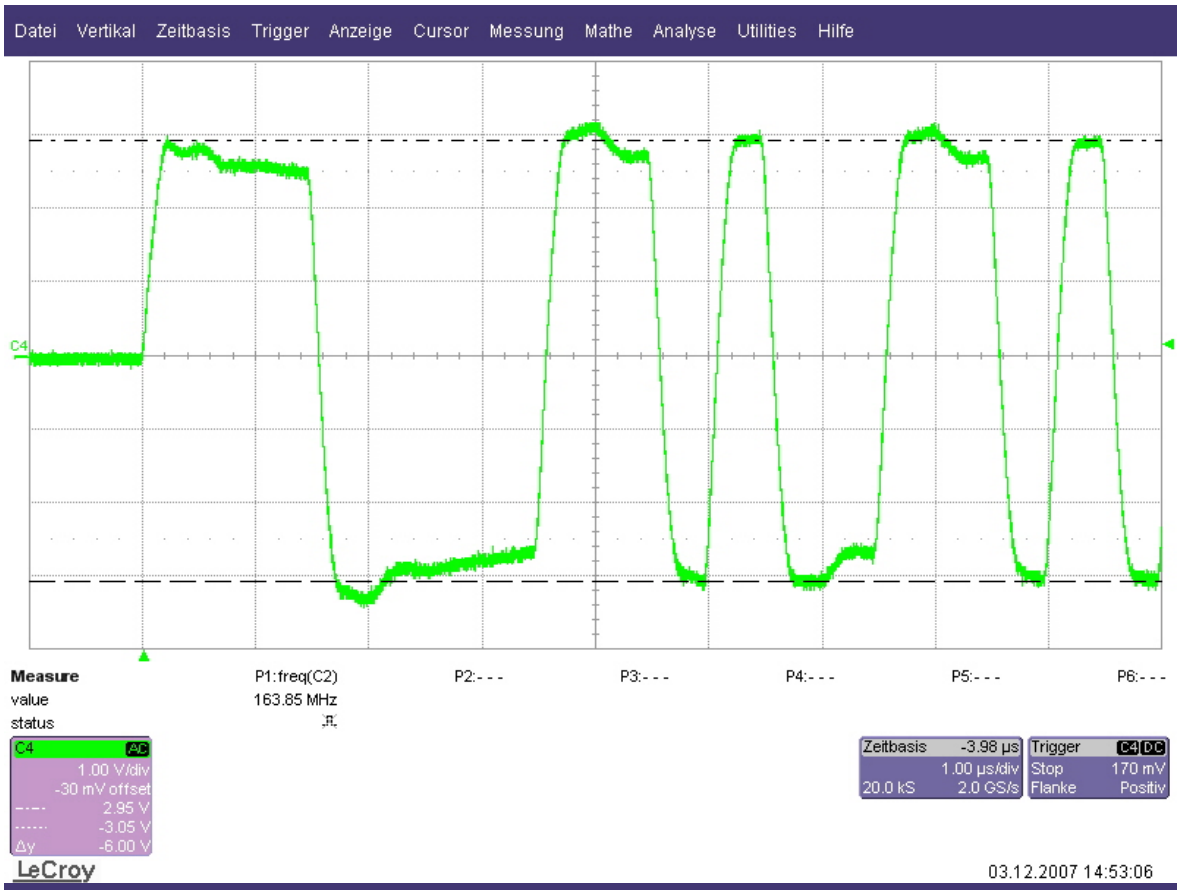

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 125.19 MHz
status
C4 AC
20.0 mV/div
-40.00 mV
45.8 mV
9.6 mV
Δy -36.2 mV
Zeitbasis 0.00 µs
500 ns/div
10.0 kS 2.0 GS/s
Trigger C3 DC
Stop -1.000 V
Flanke Positiv
LeCroy
03.12.2007 15:55:28

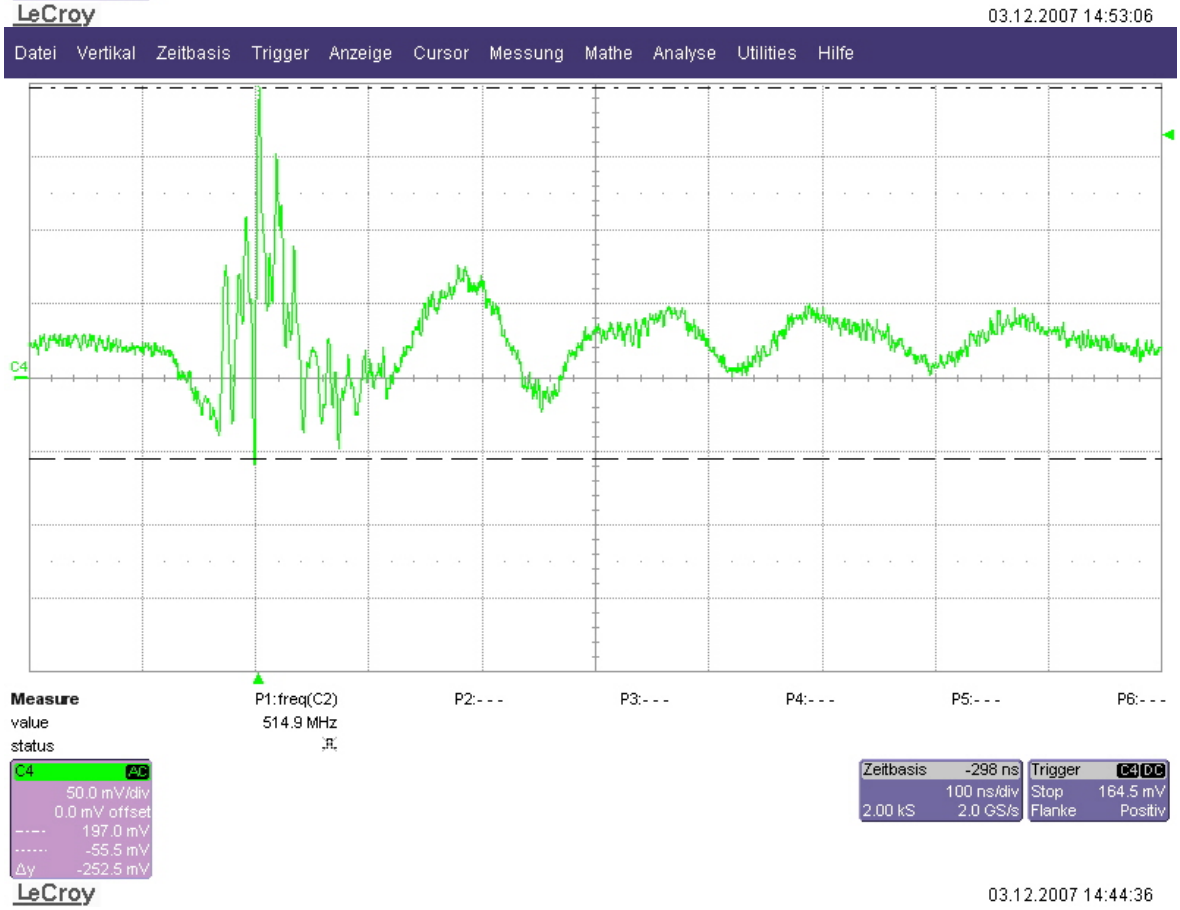

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 336.87 MHz
status
C4 AC
20.0 mV/div
-40.00 mV
78.4 mV
-15.4 mV
Δy -93.8 mV
Zeitbasis 0.00 µs
2.00 µs/div
40.0 kS 2.0 GS/s
Trigger C3 DC
Stop -1.000 V
Flanke Positiv
LeCroy
03.12.2007 15:54:38

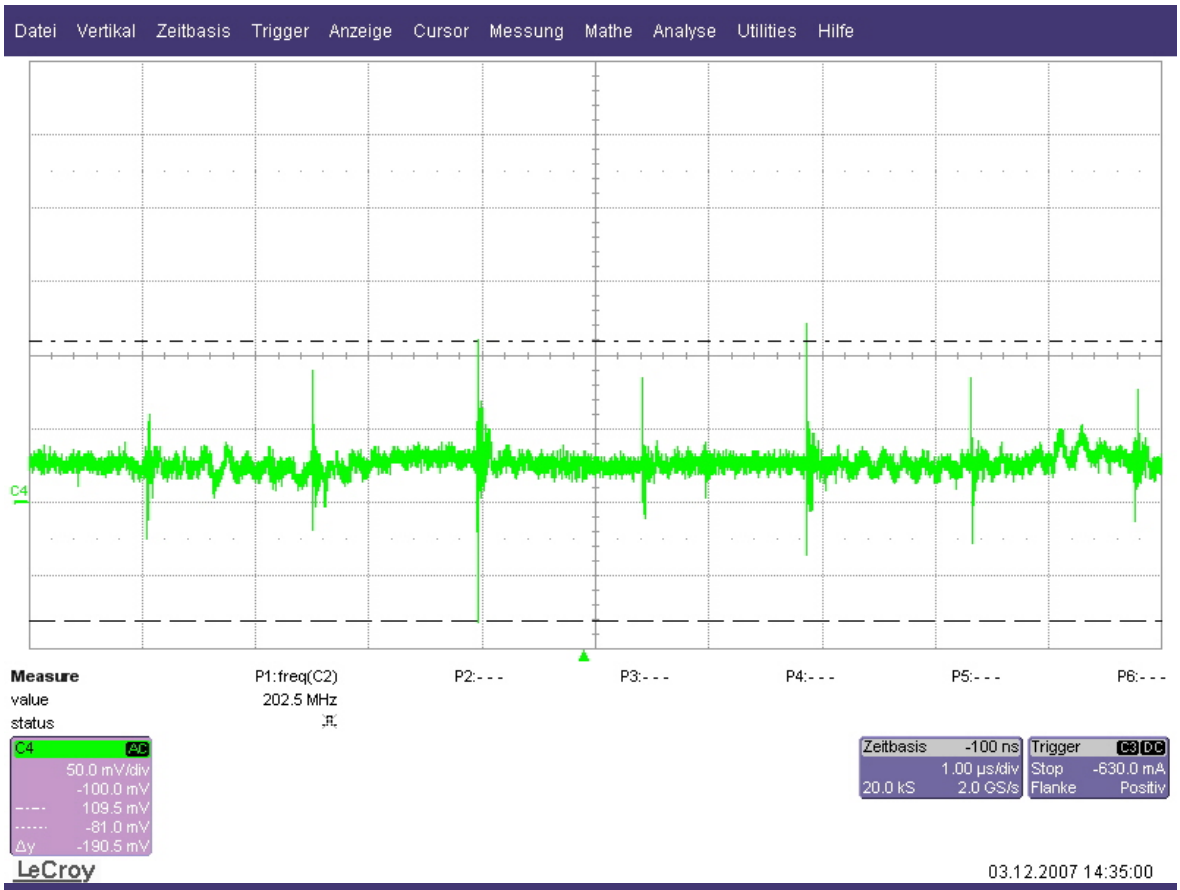

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
Undo
C4
Z4
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 5.12 MHz
status
C4 AC
50.0 mV/div
0.0 mV offset
165.0 mV
-134.5 mV
Δy -299.5 mV
Z4 (C4)
14.3 mV/div
18.1 µs/div
58.6 mV
-27.0 mV
Δy -85.7 mV
Zeitbasis 0.00 ns
1.00 ms/div
2.00 MS 200 MS/s
Trigger C3 DC
Stop -1.000 A
Flanke Positiv
LeCroy
03.12.2007 15:47:46

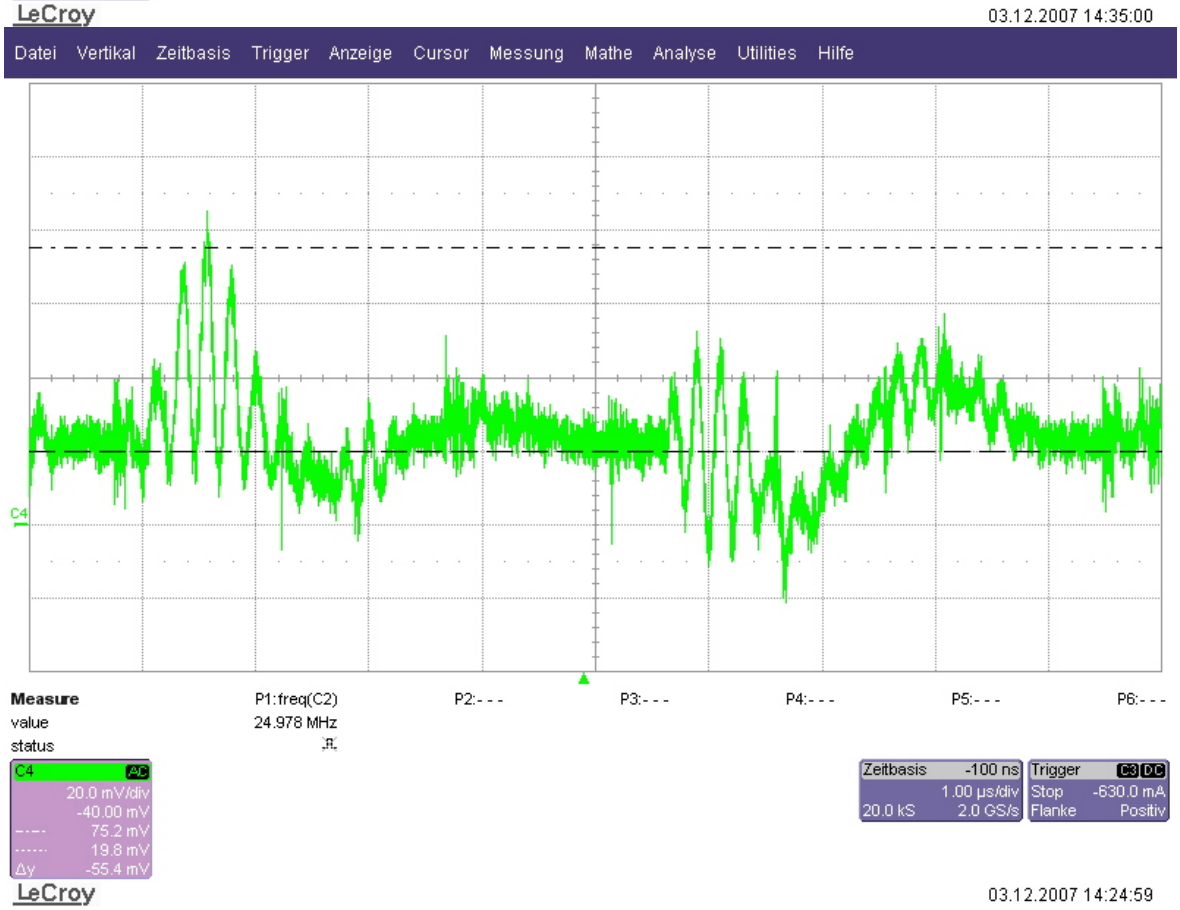

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
Undo
C3
Measure
value
status
P1:freq(C2)
40.000 MHz
P2:- - -
P3:- - -
P4:- - -
P5:- - -
P6:- - -
C3 DC
200 mA/div
1.5700 A ofst
-1.734 A
-1.834 A
Δy -100 mA
Zeitbasis 0.00 ms
1.00 ms/div
2.00 MS 200 MS/s
Trigger C3 DC
Stop -1.000 A
Flanke Positiv
LeCroy
03.12.2007 15:40:57

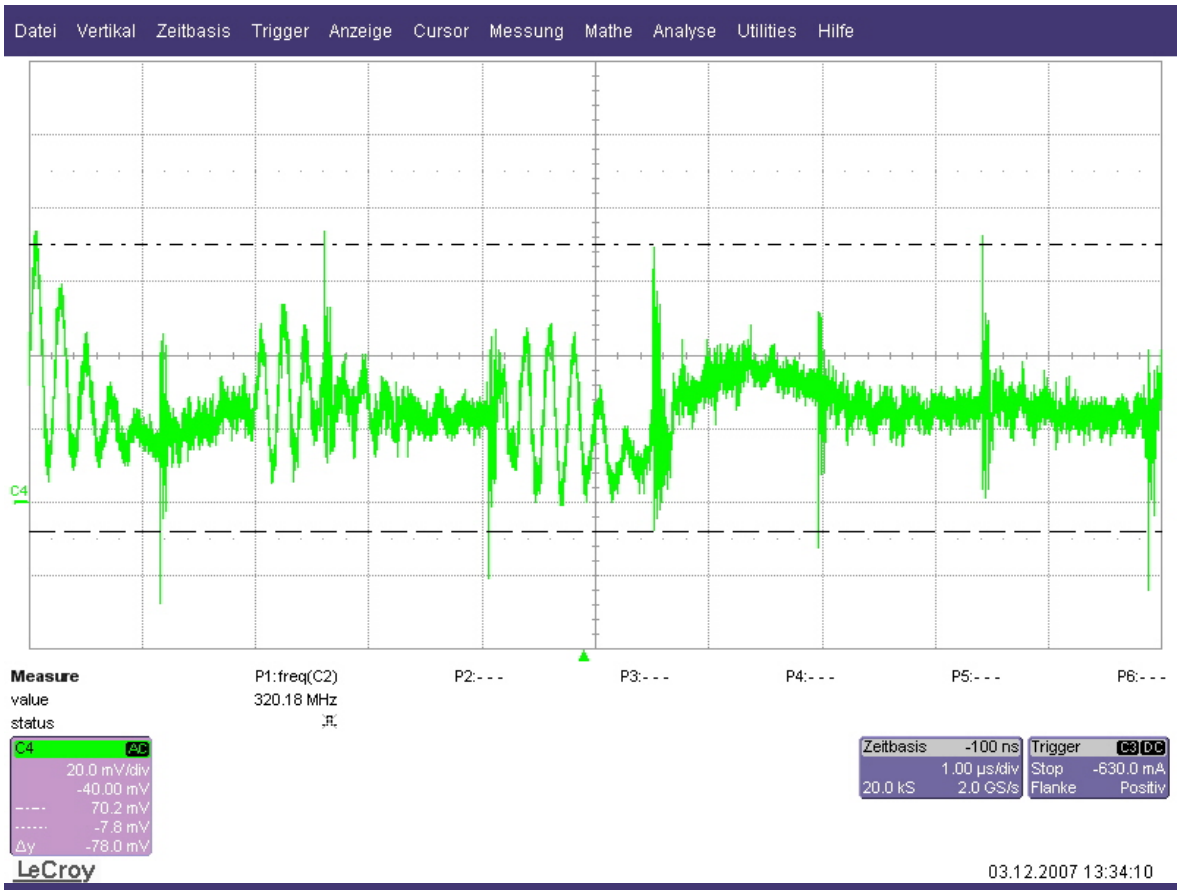

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Z4
Measure
value
status
P1:freq(C2)
163.85 MHz
P2:- - -
P3:- - -
P4:- - -
P5:- - -
P6:- - -
C4 AC
1.00 V/div
-30 mV offset
4.02 V
1.87 V
Δy -2.15 V
Z4 (C4)
97 mV/div
140 ns/div
-2.7757 V
-2.9842 V
Δy -208.6 mV
Zeitbasis -3.98 µs
1.00 µs/div
20.0 kS 2.0 GS/s
Trigger C4 DC
Stop 170 mV
Flanke Positiv
LeCroy
03.12.2007 14:55:33


Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 163.85 MHz
status
C4 AC
1.00 V/div
-30 mV offset
2.95 V
-3.05 V
Δy -6.00 V
Zeitbasis -3.98 µs
1.00 µs/div
20.0 kS 2.0 GS/s
Trigger C4 DC
Stop 170 mV
Flanke Positiv
LeCroy
03.12.2007 14:53:06


Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 514.9 MHz
status
C4 AC
50.0 mV/div
0.0 mV offset
197.0 mV
-55.5 mV
Δy -252.5 mV
Zeitbasis -298 ns
100 ns/div
2.00 kS 2.0 GS/s
Trigger C4 DC
Stop 164.5 mV
Flanke Positiv
LeCroy
03.12.2007 14:44:36

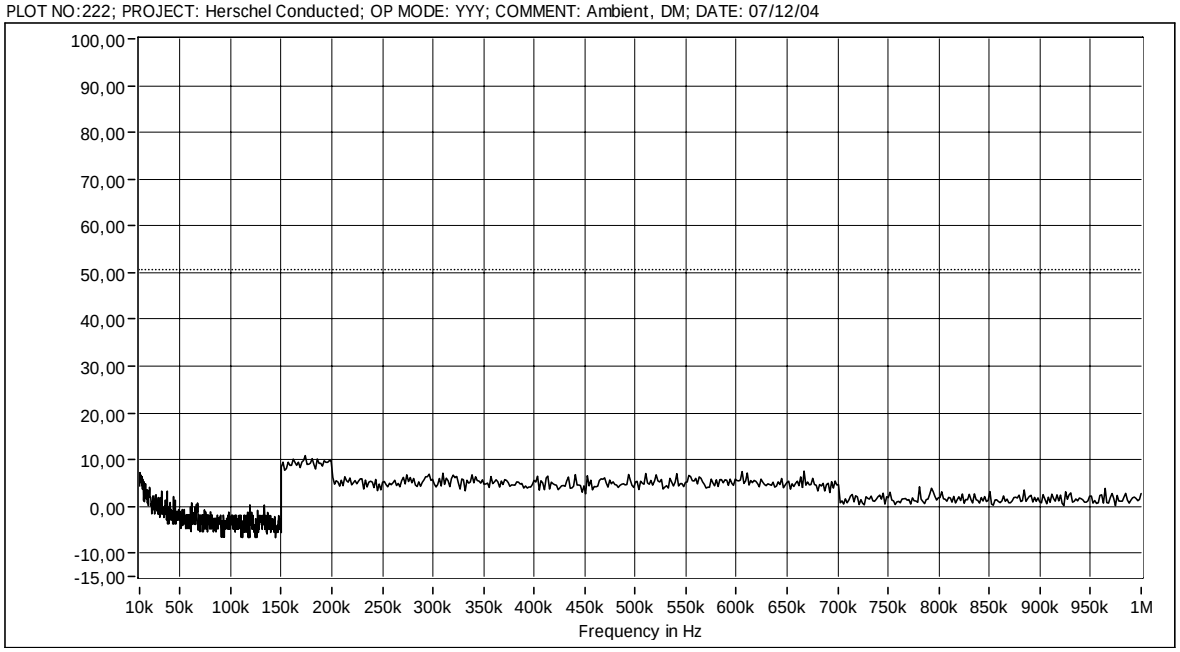

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 202.5 MHz
status
C4 AC
50.0 mV/div
-100.0 mV
109.5 mV
-81.0 mV
Δy -190.5 mV
Zeitbasis -100 ns
1.00 µs/div
20.0 kS 2.0 GS/s
Trigger C3 DC
Stop -630.0 mA
Flanke Positiv
LeCroy
03.12.2007 14:35:00


Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 24.978 MHz
status
C4 AC
20.0 mV/div
-40.00 mV
75.2 mV
19.8 mV
Δy -55.4 mV
Zeitbasis -100 ns
1.00 µs/div
20.0 kS 2.0 GS/s
Trigger C3 DC
Stop -630.0 mA
Flanke Positiv
LeCroy
03.12.2007 14:24:59

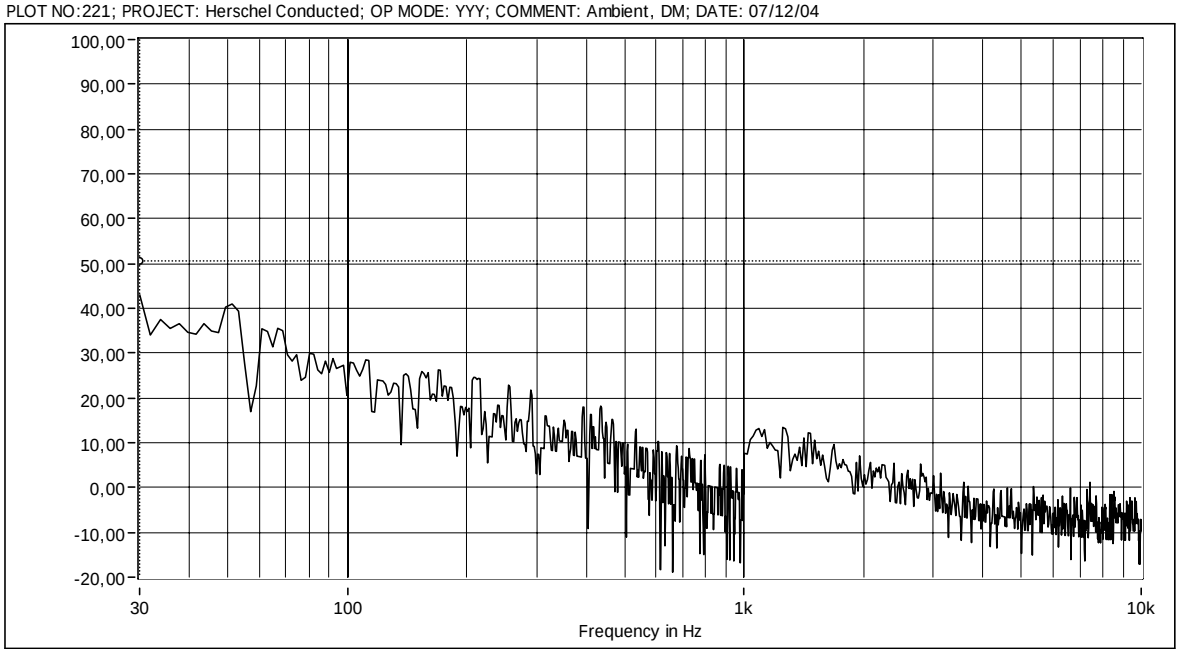

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 320.18 MHz
status
C4 AC
20.0 mV/div
-40.00 mV
70.2 mV
-7.8 mV
Δy -78.0 mV
Zeitbasis -100 ns
1.00 µs/div
20.0 kS 2.0 GS/s
Trigger C3 DC
Stop -630.0 mA
Flanke Positiv
LeCroy
03.12.2007 13:34:10


Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
Measure P1:freq(C2) P2:- - - P3:- - - P4:- - - P5:- - - P6:- - -
value 64.73 MHz
status
C4 AC
20.0 mV/div
-40.00 mV
78.0 mV
4.2 mV
Δy -73.8 mV
Zeitbasis -100 ns
1.00 µs/div
20.0 kS 2.0 GS/s
Trigger C3 DC
Stop -630.0 mA
Flanke Positiv
LeCroy
03.12.2007 14:22:28

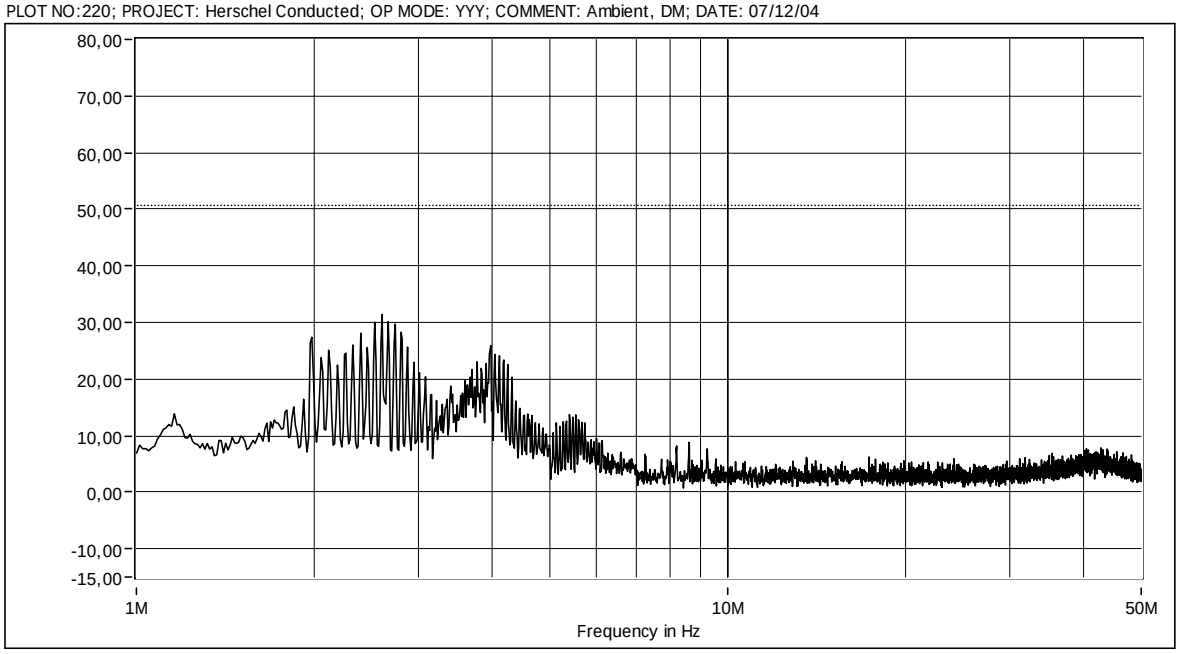
PLOT NO:222; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, DM; DATE: 07/12/04



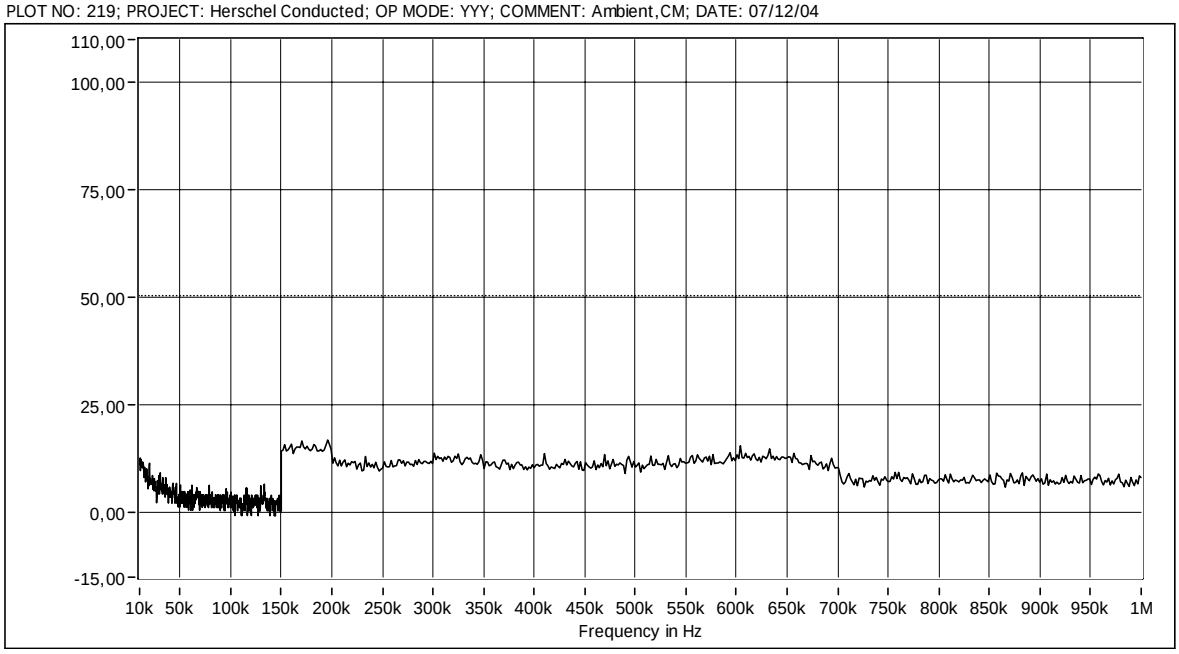
PLOT NO:221; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, DM; DATE: 07/12/04



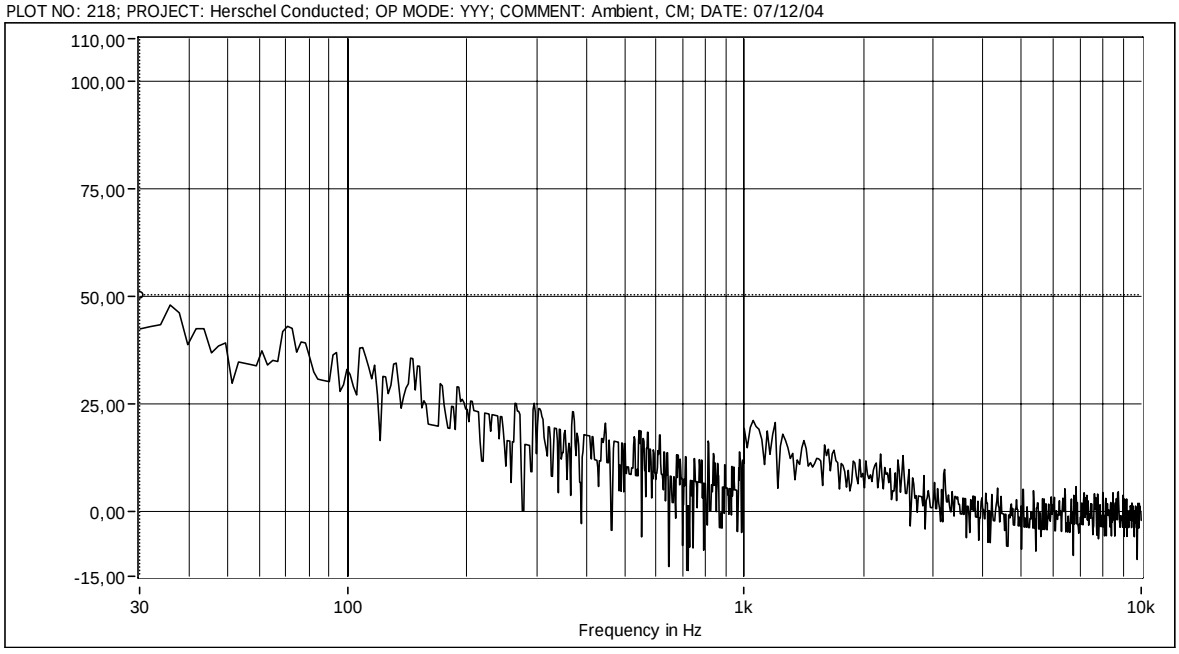
PLOT NO:220; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, DM; DATE: 07/12/04



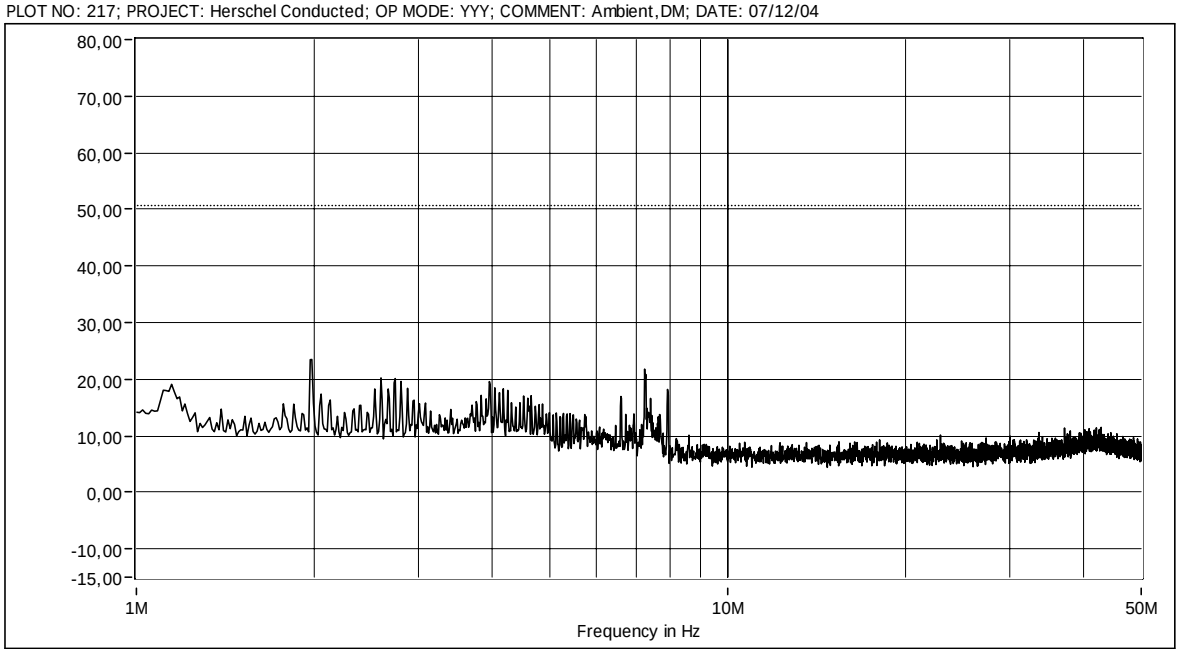
PLOT NO: 219; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient,CM; DATE: 07/12/04



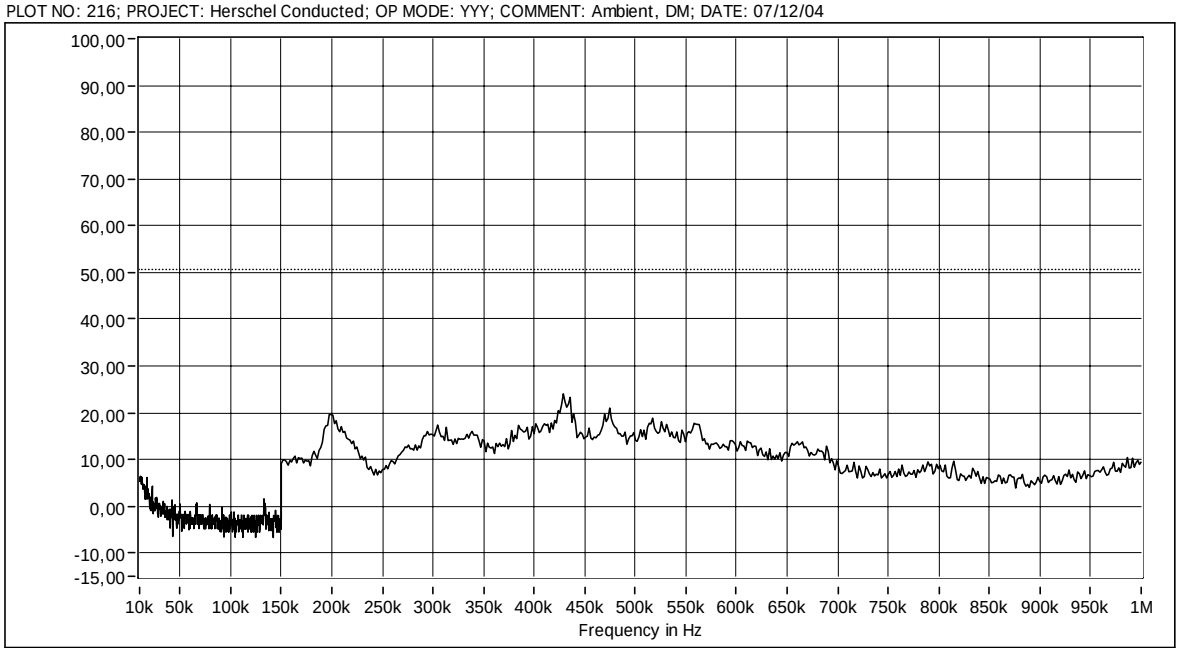
PLOT NO: 218; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, CM; DATE: 07/12/04



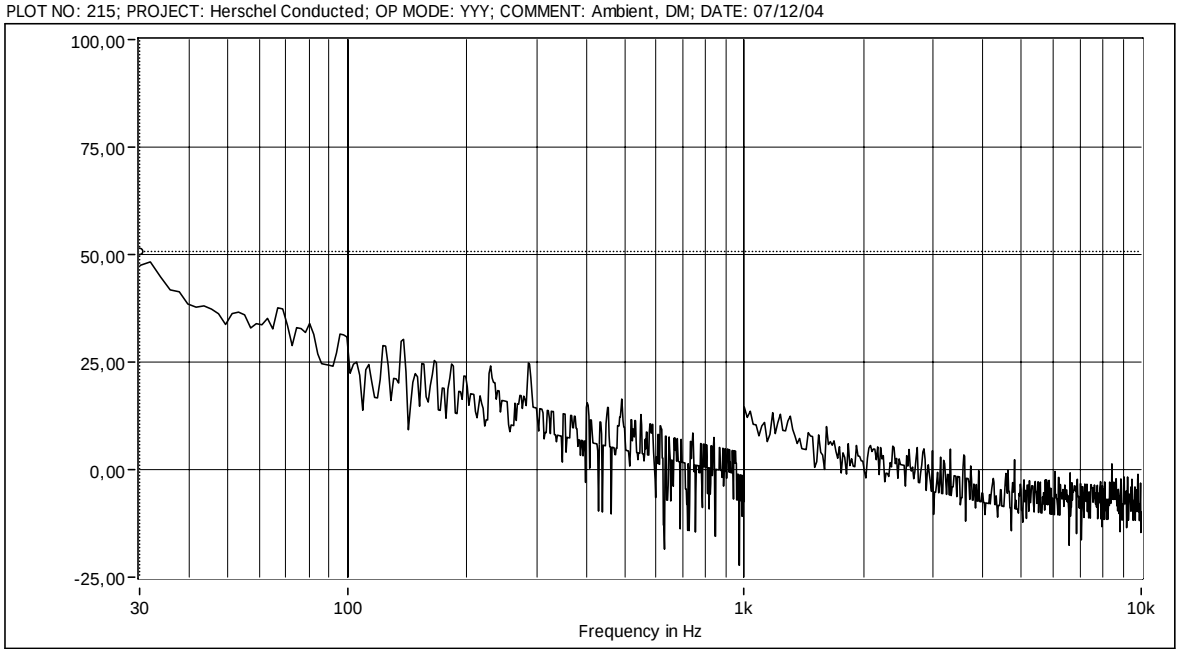
PLOT NO: 217; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient,DM; DATE: 07/12/04



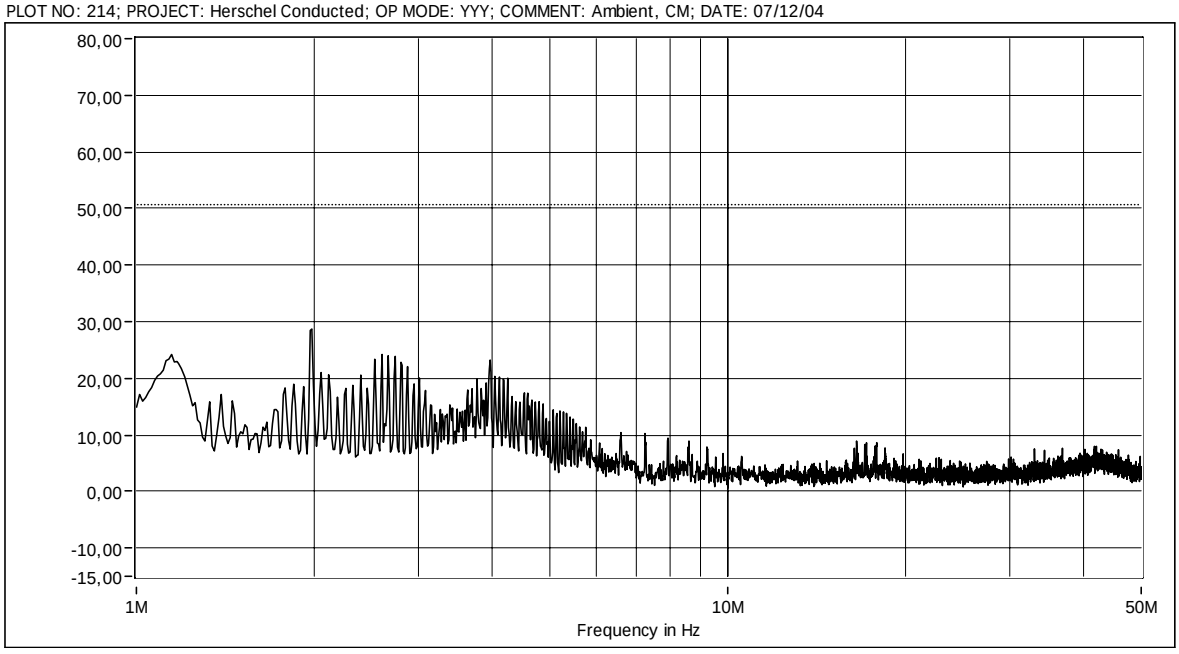
PLOT NO: 216; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, DM; DATE: 07/12/04



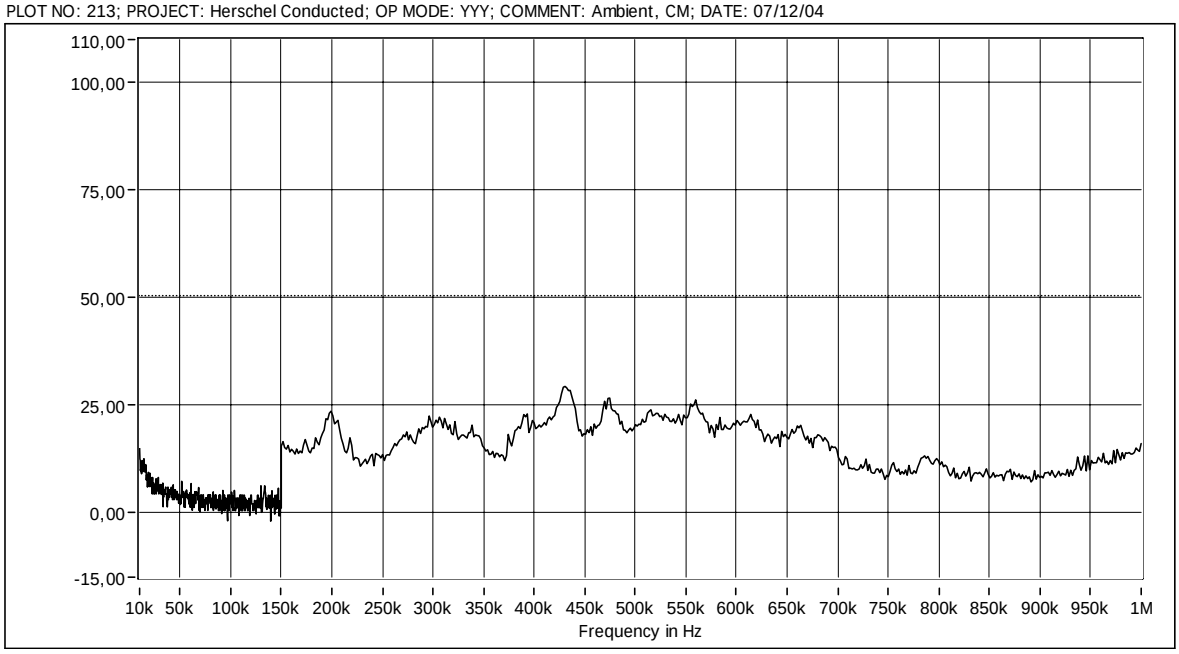
PLOT NO: 215; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, DM; DATE: 07/12/04



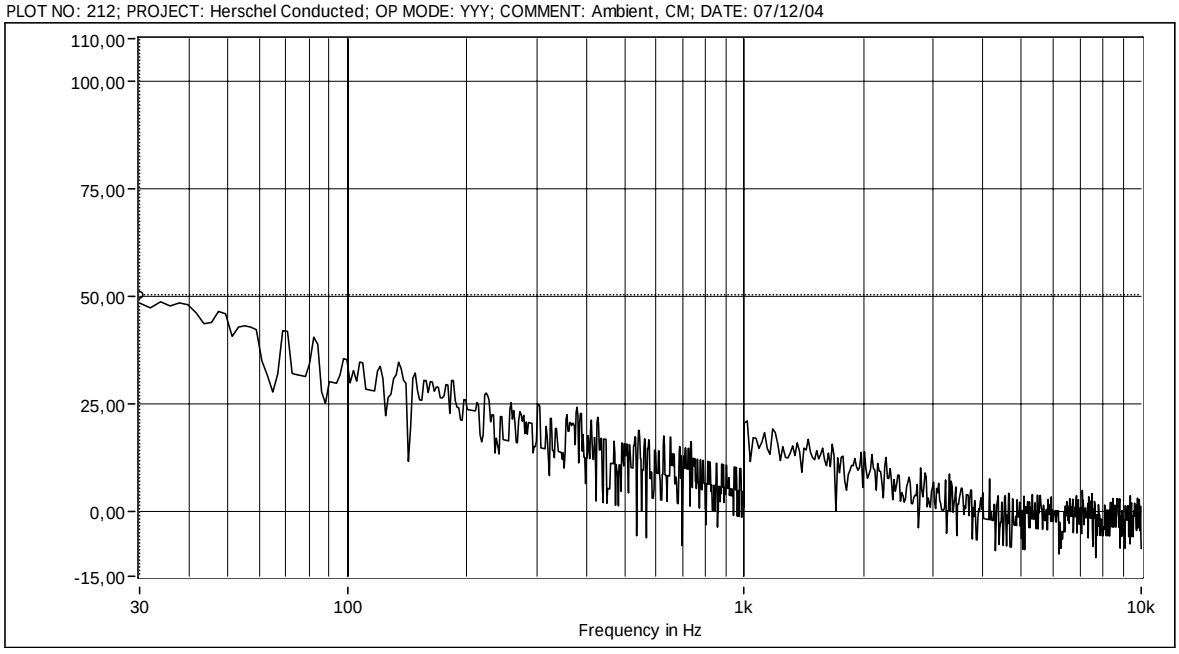
PLOT NO: 214; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, CM; DATE: 07/12/04



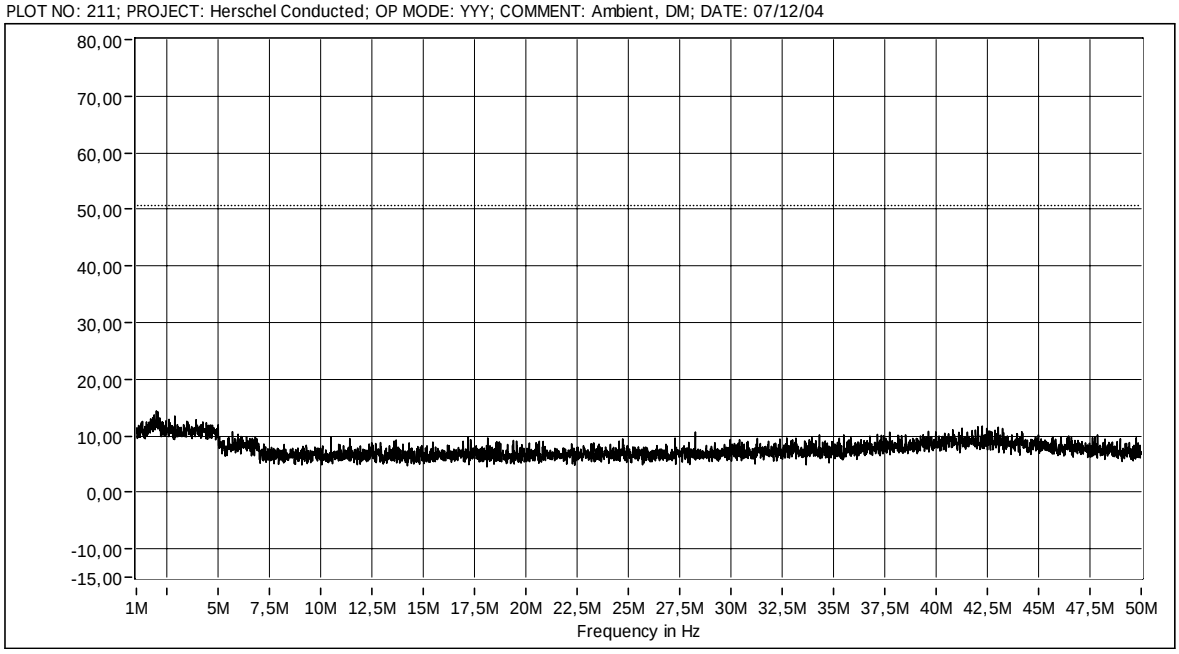
PLOT NO: 213; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, CM; DATE: 07/12/04



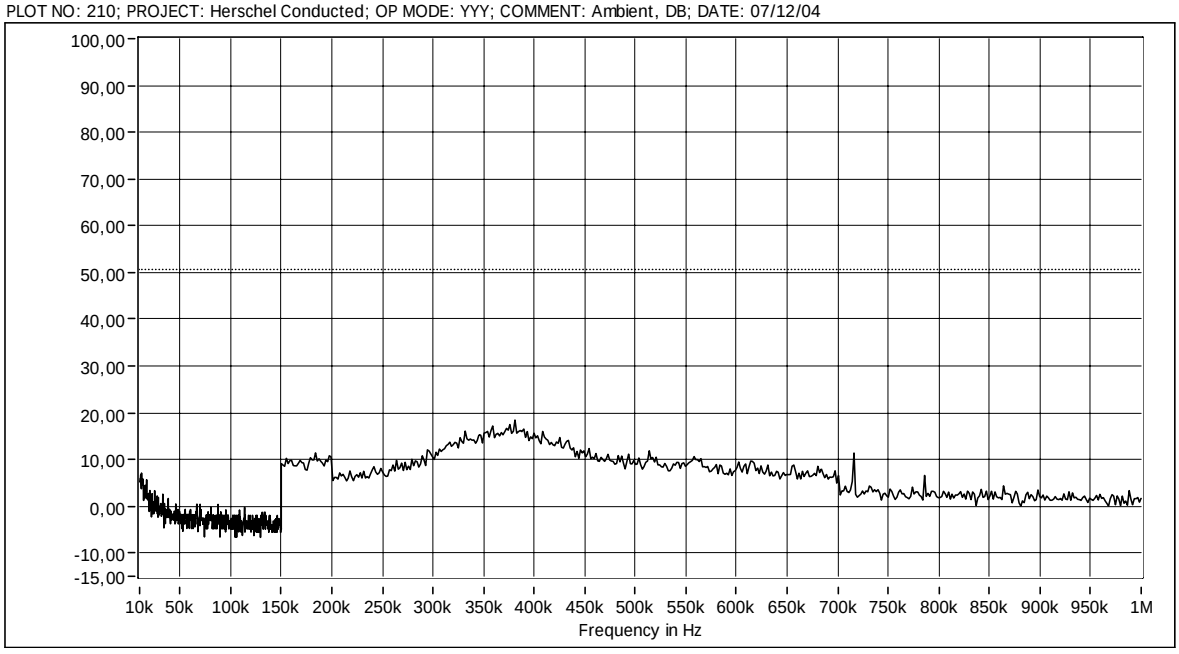
PLOT NO: 212; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, CM; DATE: 07/12/04



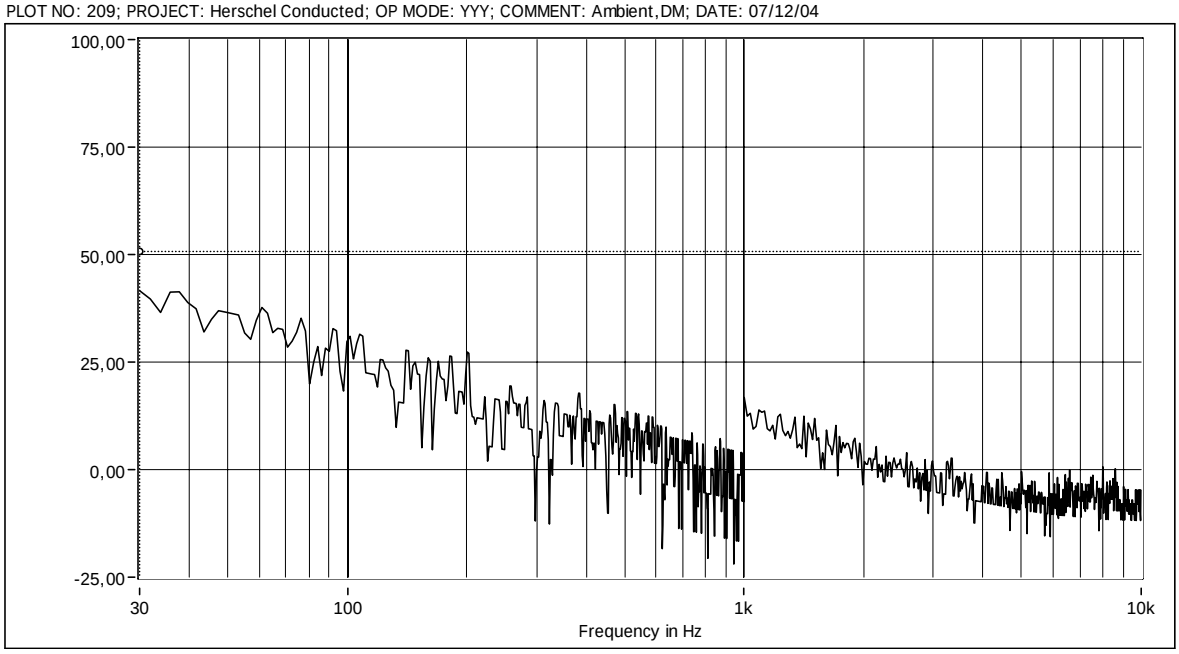
PLOT NO: 211; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, DM; DATE: 07/12/04



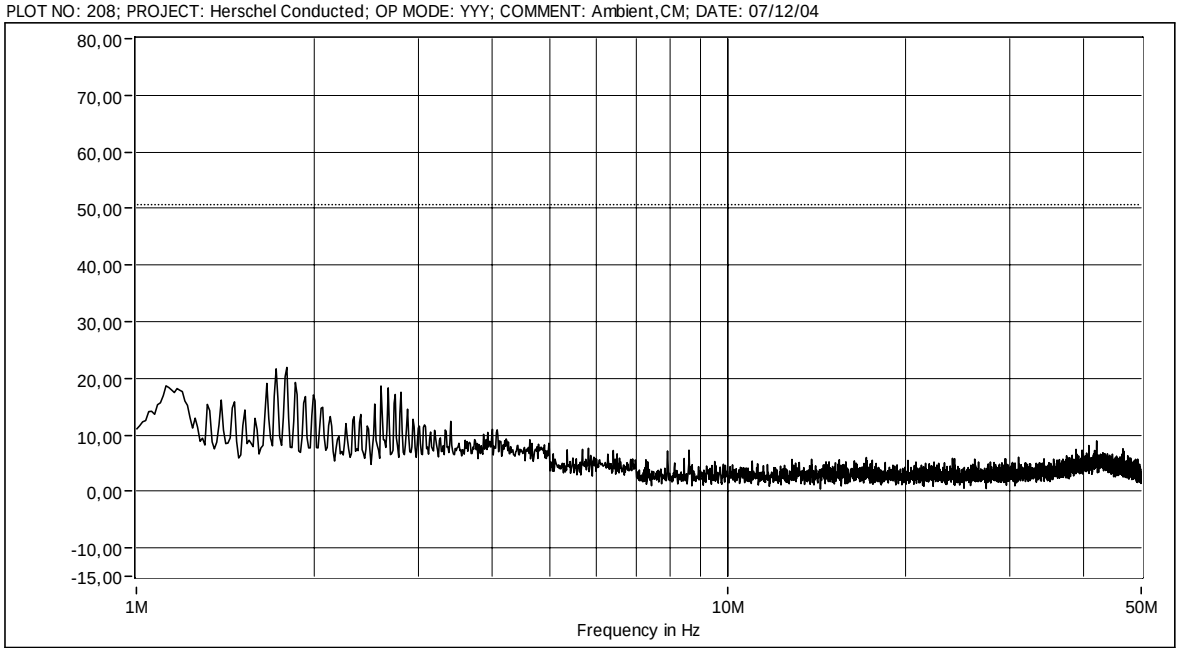
PLOT NO: 210; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, DB; DATE: 07/12/04



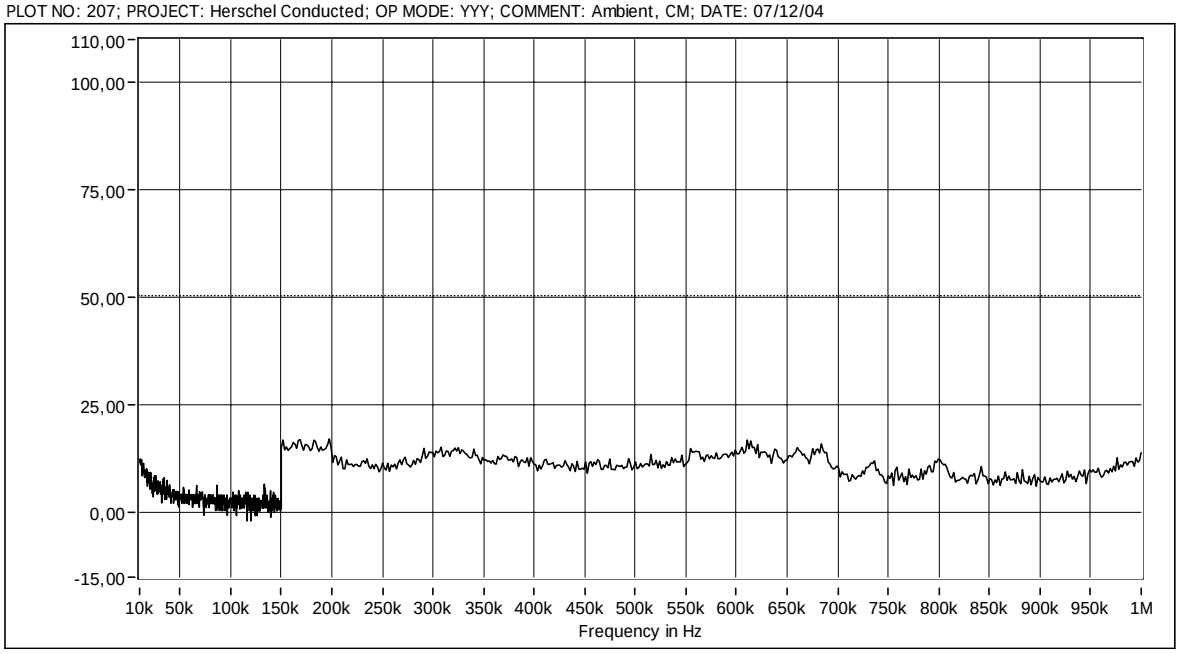
PLOT NO: 209; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient,DM; DATE: 07/12/04



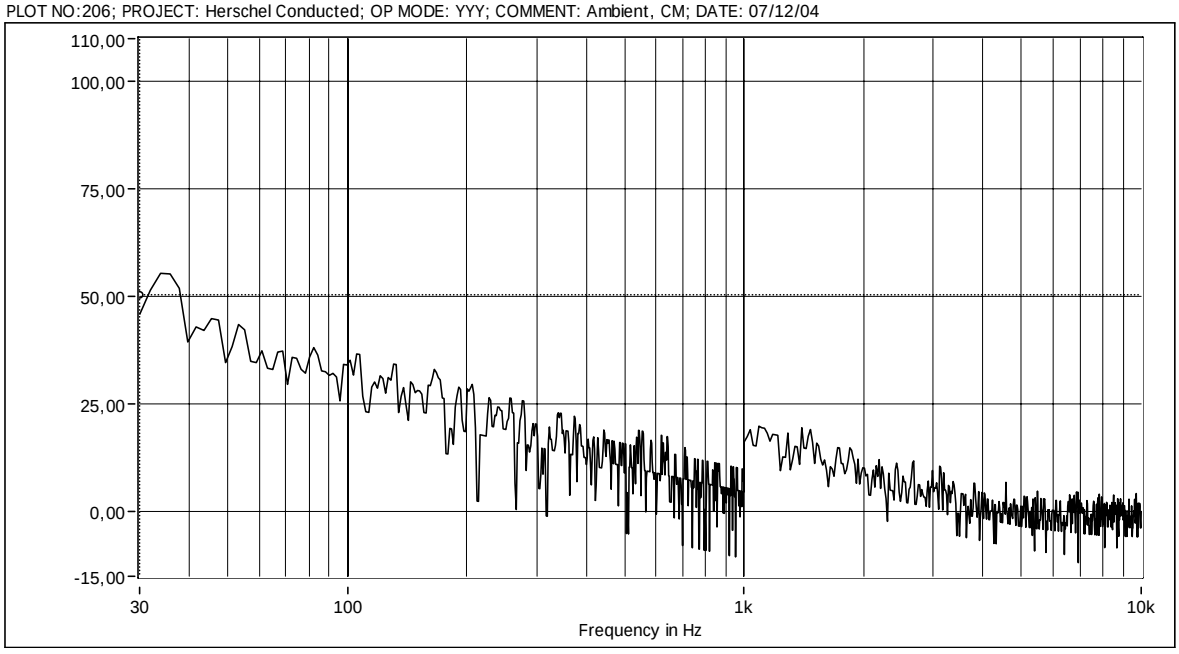
PLOT NO: 208; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient,CM; DATE: 07/12/04



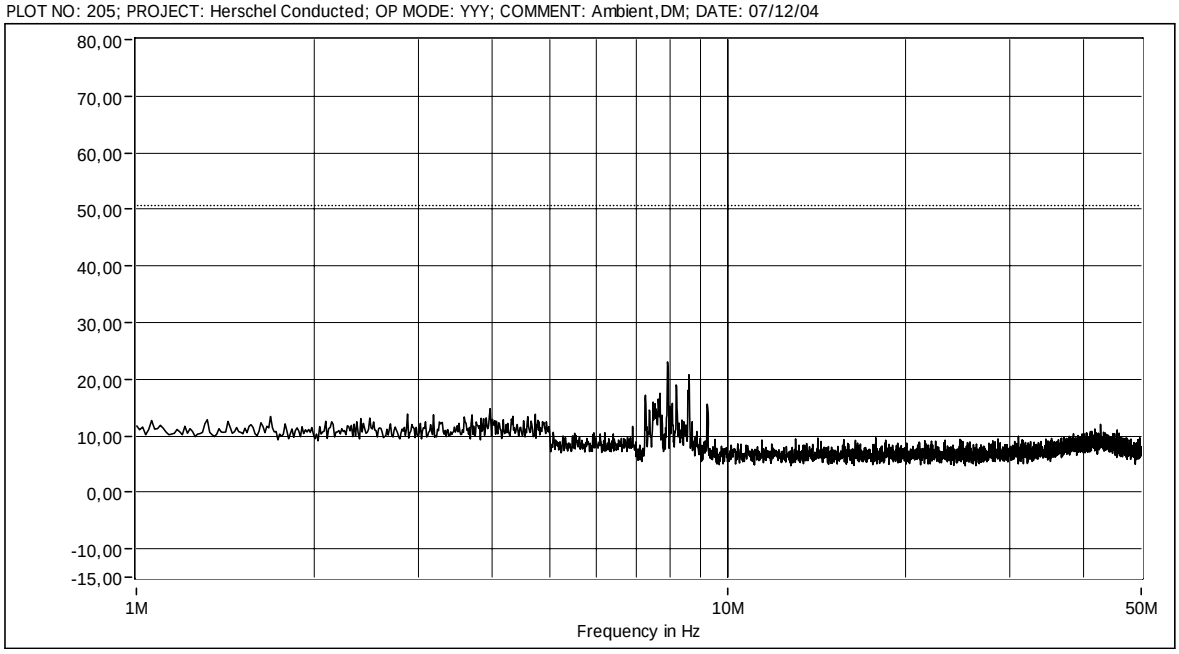
PLOT NO: 207; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, CM; DATE: 07/12/04



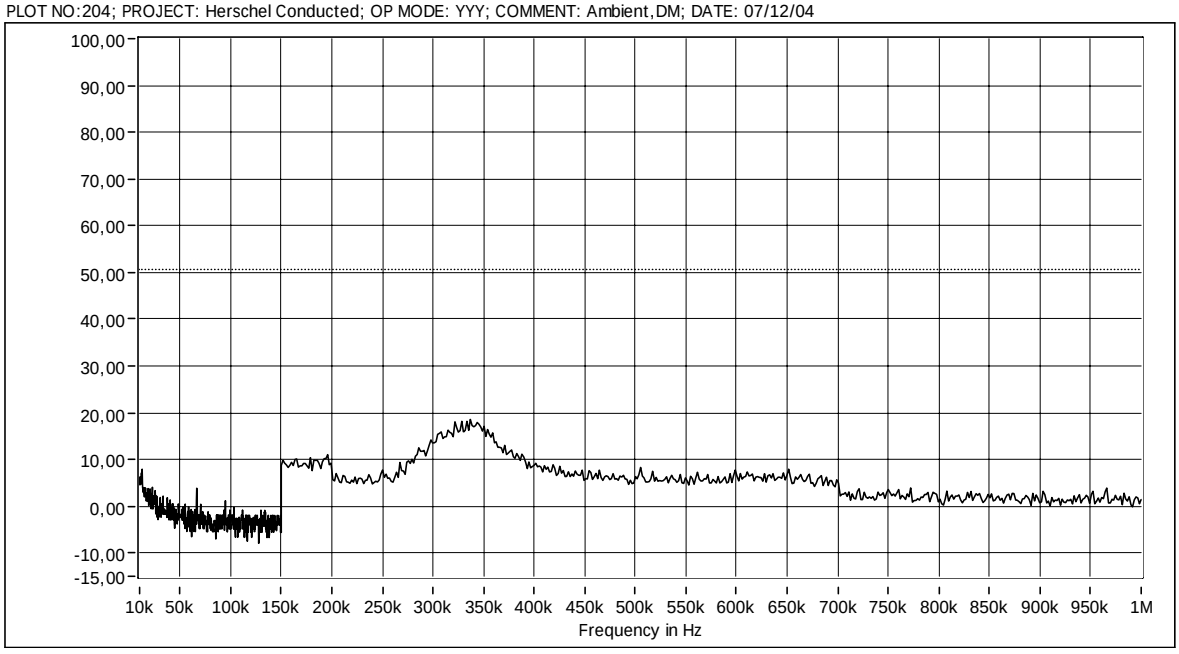
PLOT NO:206; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, CM; DATE: 07/12/04



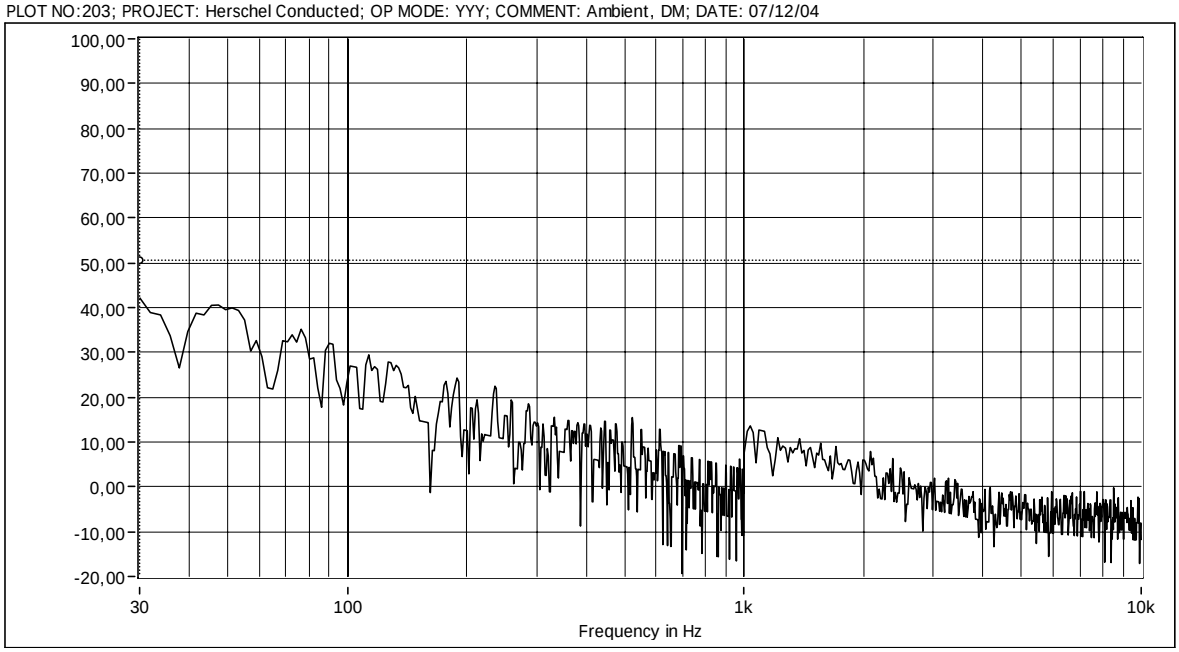
PLOT NO: 205; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient,DM; DATE: 07/12/04



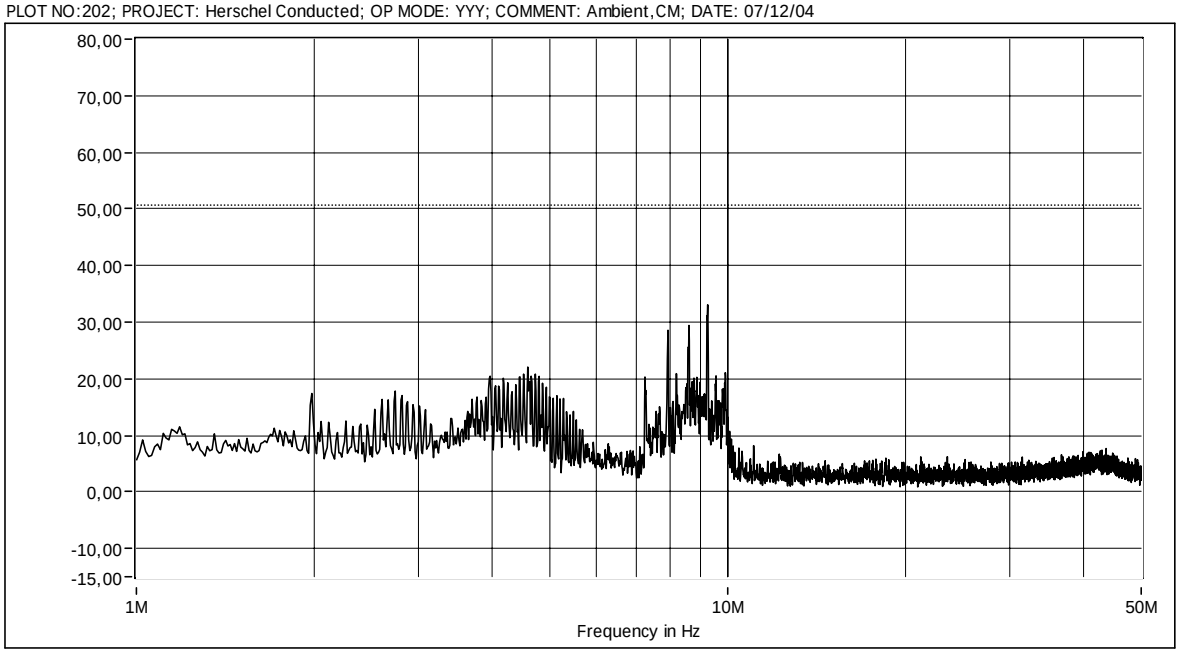
PLOT NO:204; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient,DM; DATE: 07/12/04



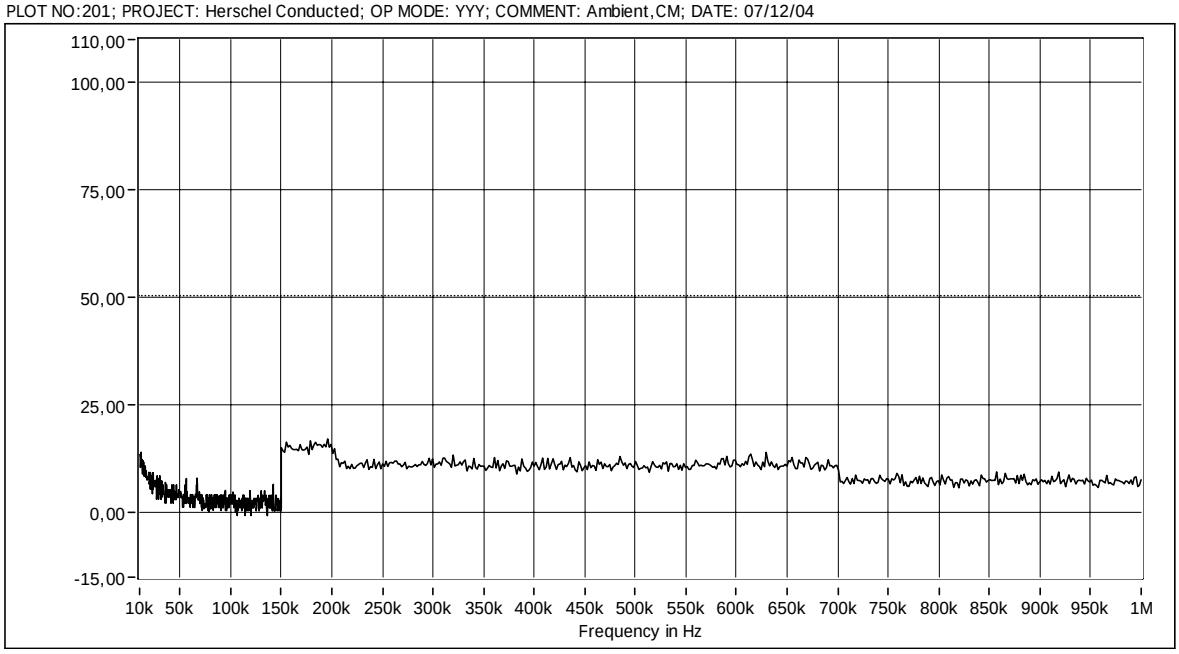
PLOT NO:203; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, DM; DATE: 07/12/04



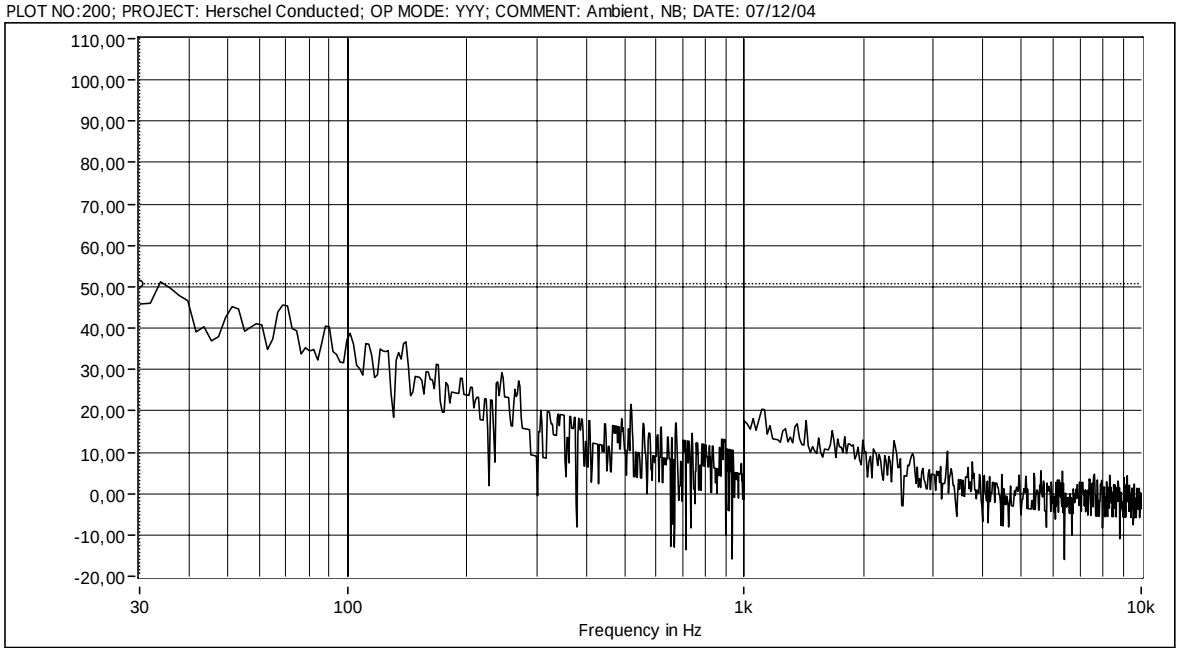
PLOT NO:202; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient,CM; DATE: 07/12/04



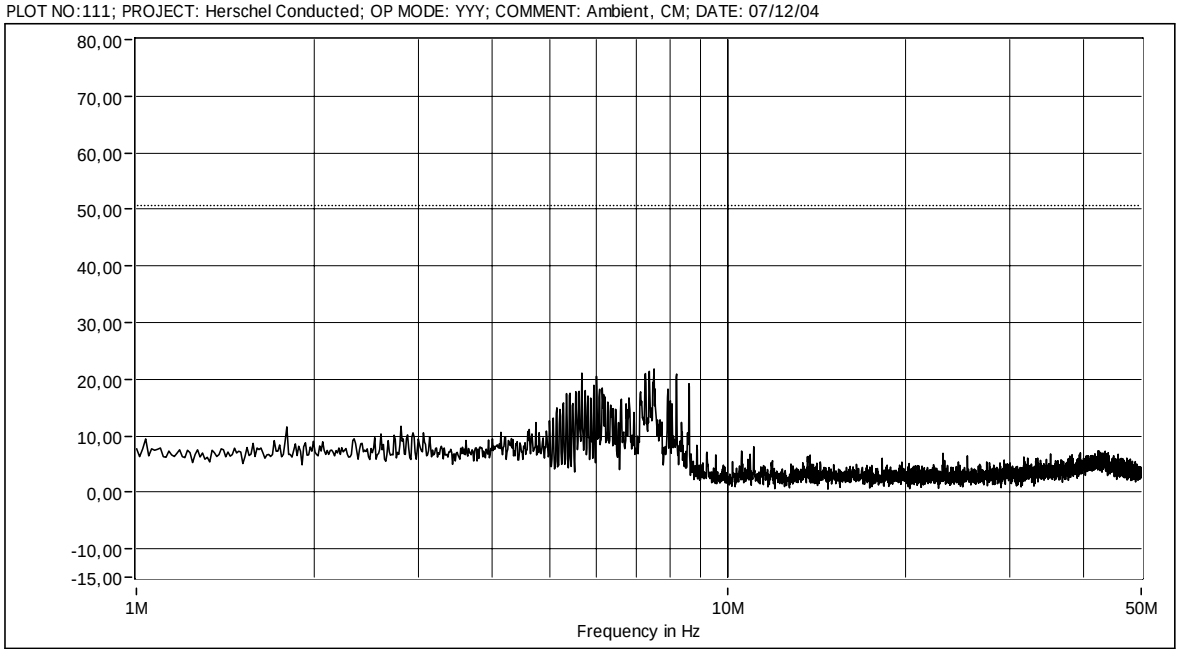
PLOT NO:201; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient,CM; DATE: 07/12/04



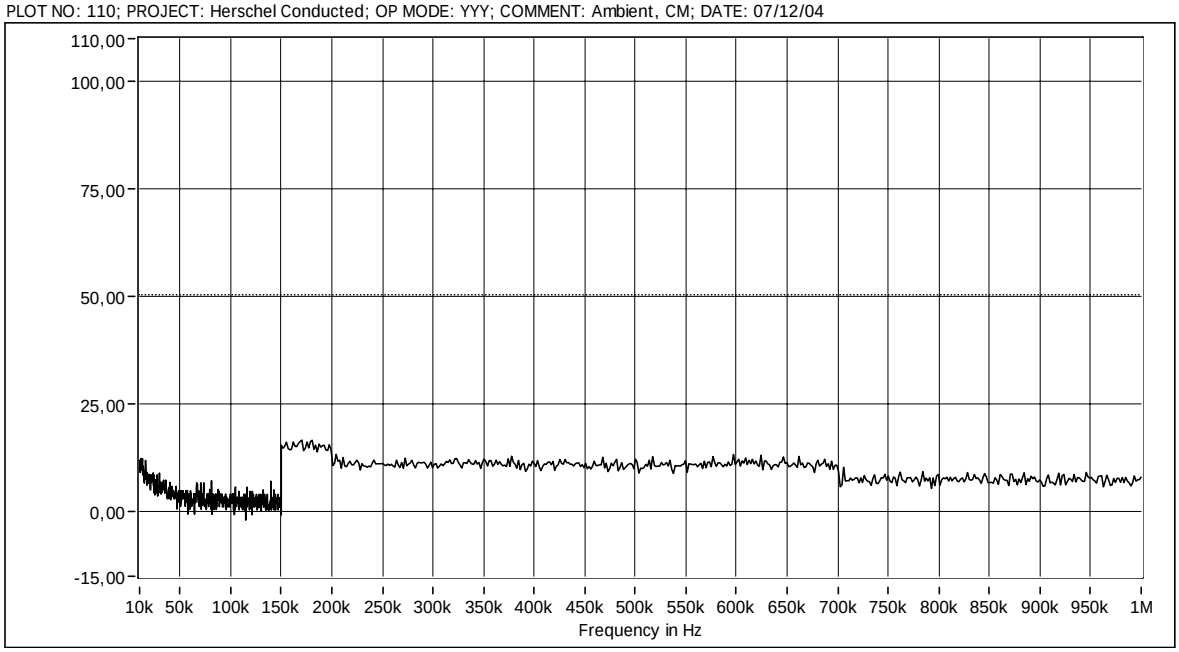
PLOT NO:200; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, NB; DATE: 07/12/04



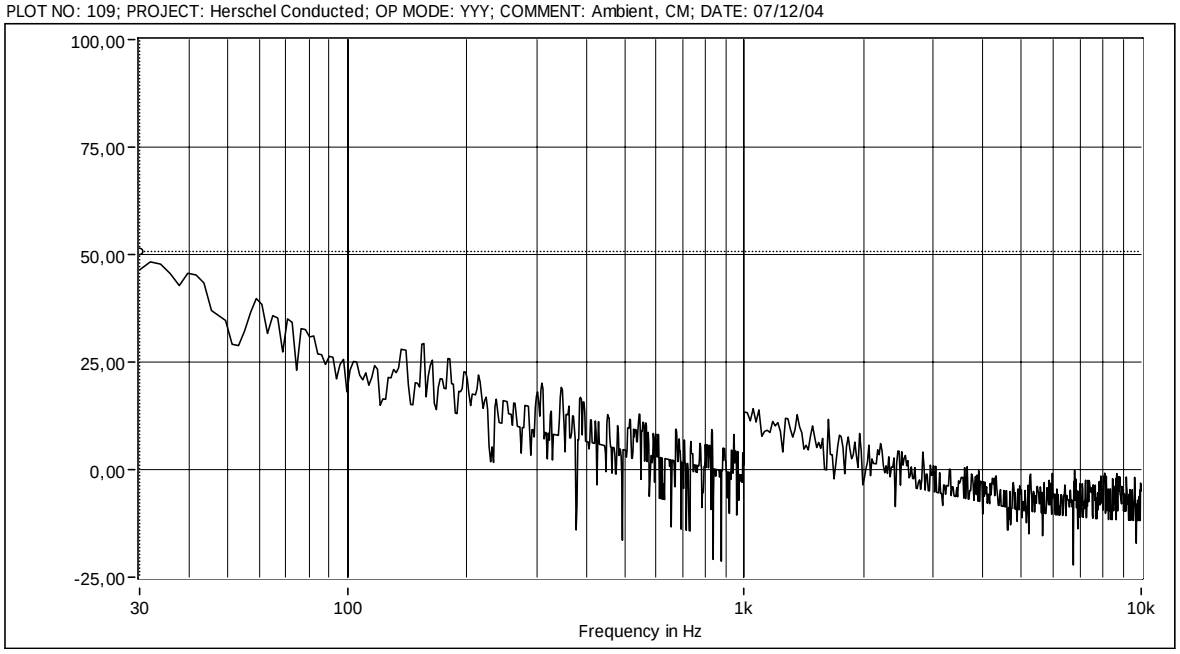
PLOT NO:111; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, CM; DATE: 07/12/04



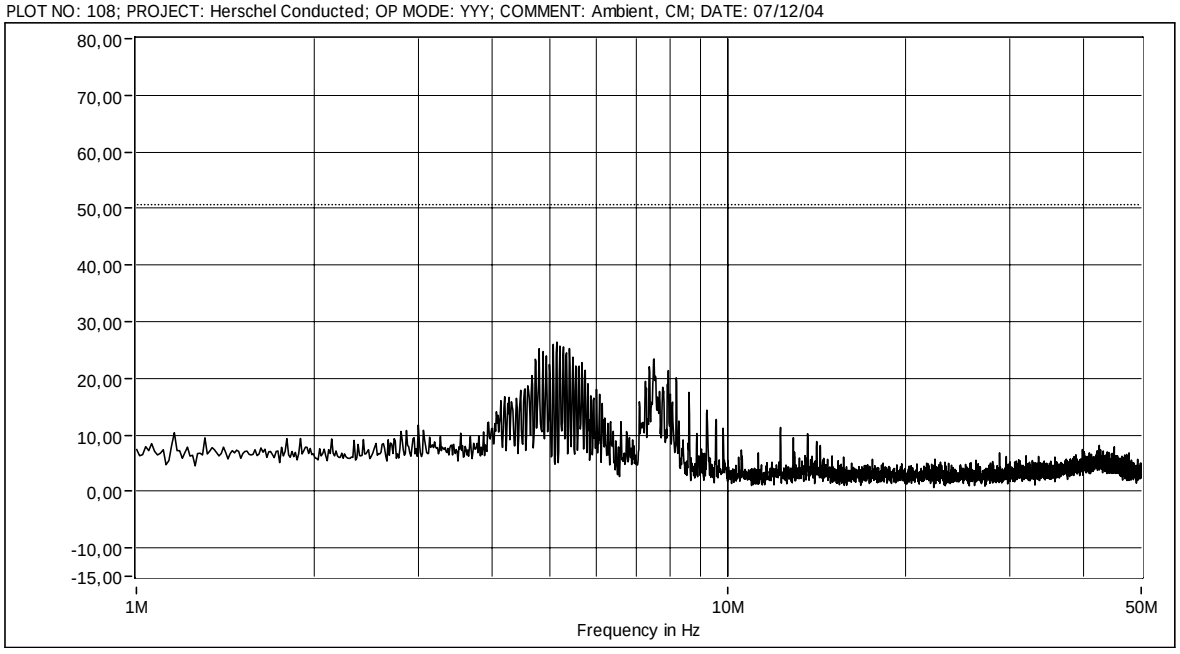
PLOT NO: 110; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, CM; DATE: 07/12/04



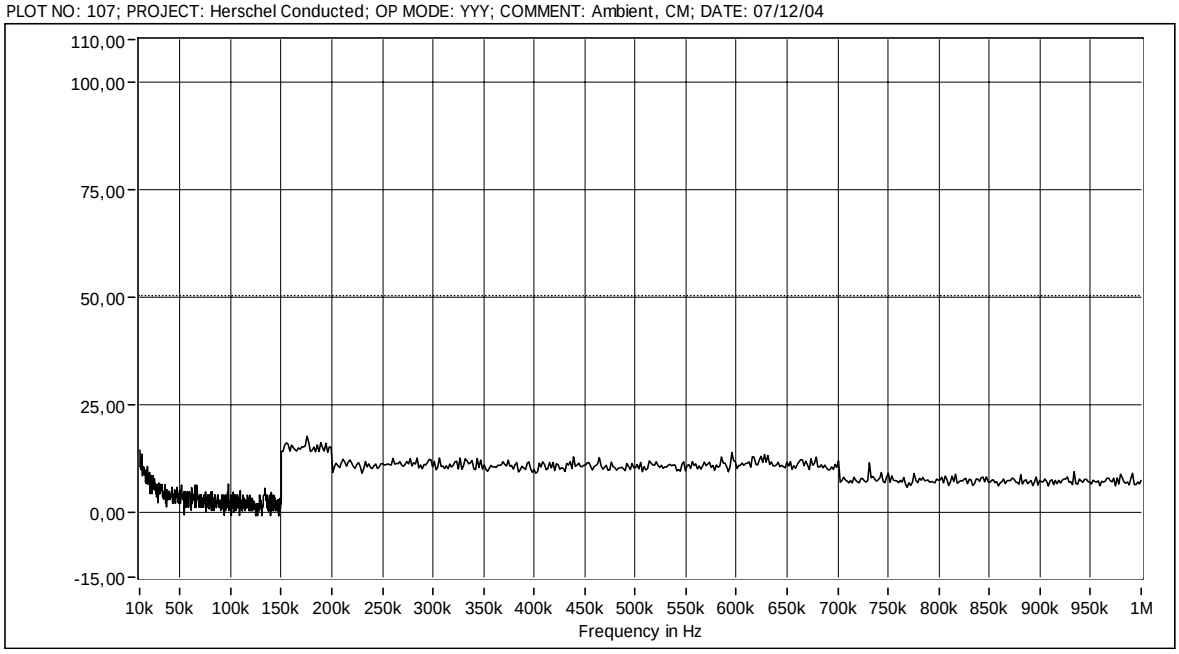
PLOT NO: 109; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, CM; DATE: 07/12/04



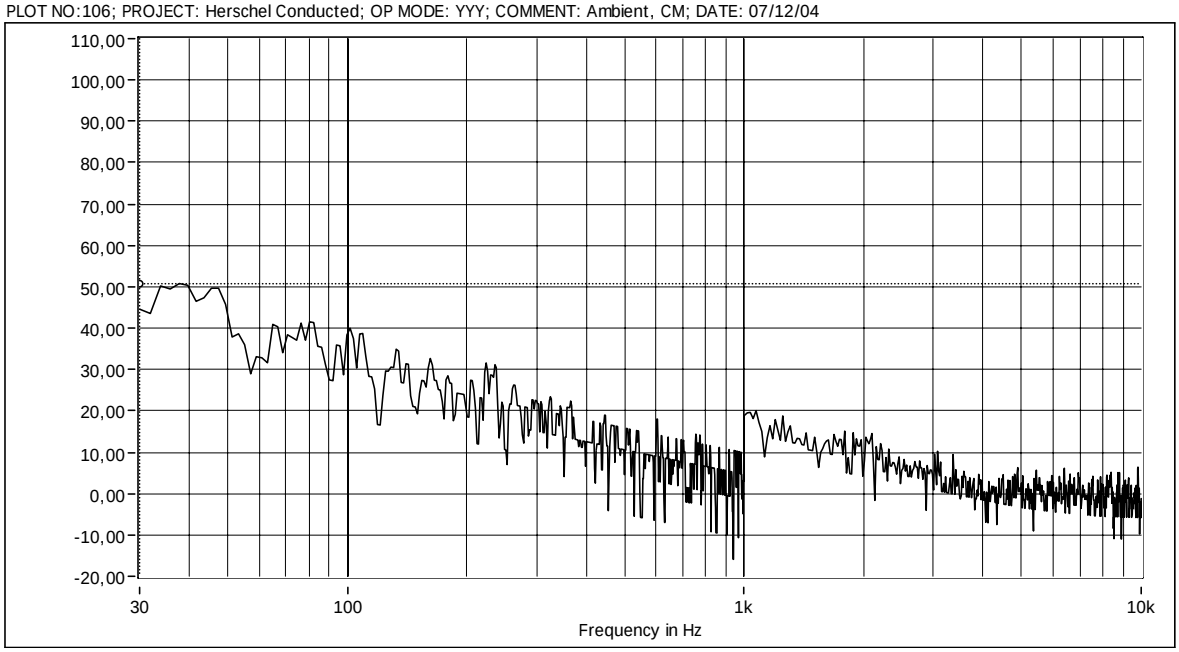
PLOT NO: 108; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, CM; DATE: 07/12/04



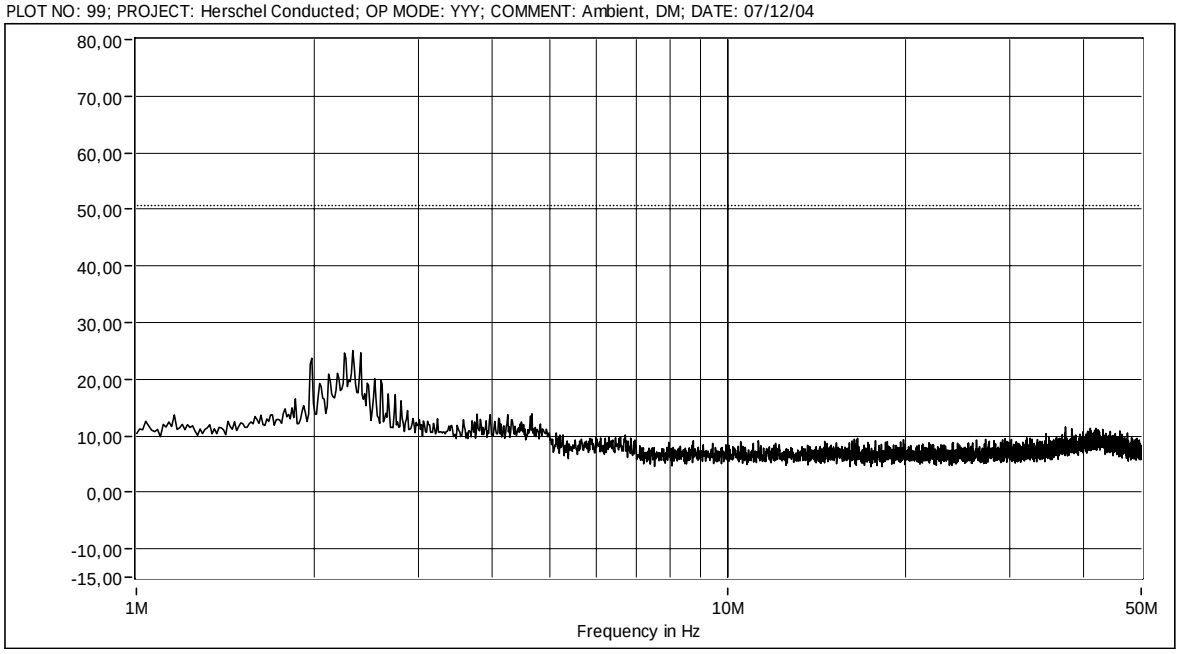
PLOT NO: 107; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, CM; DATE: 07/12/04



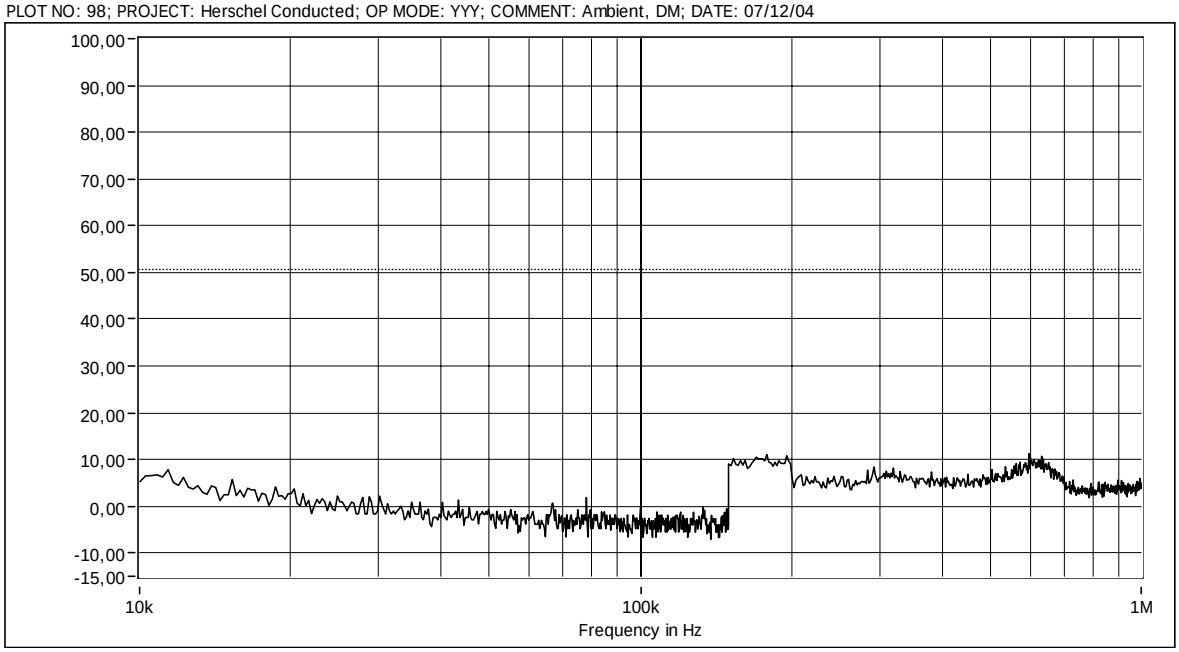
PLOT NO:106; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, CM; DATE: 07/12/04



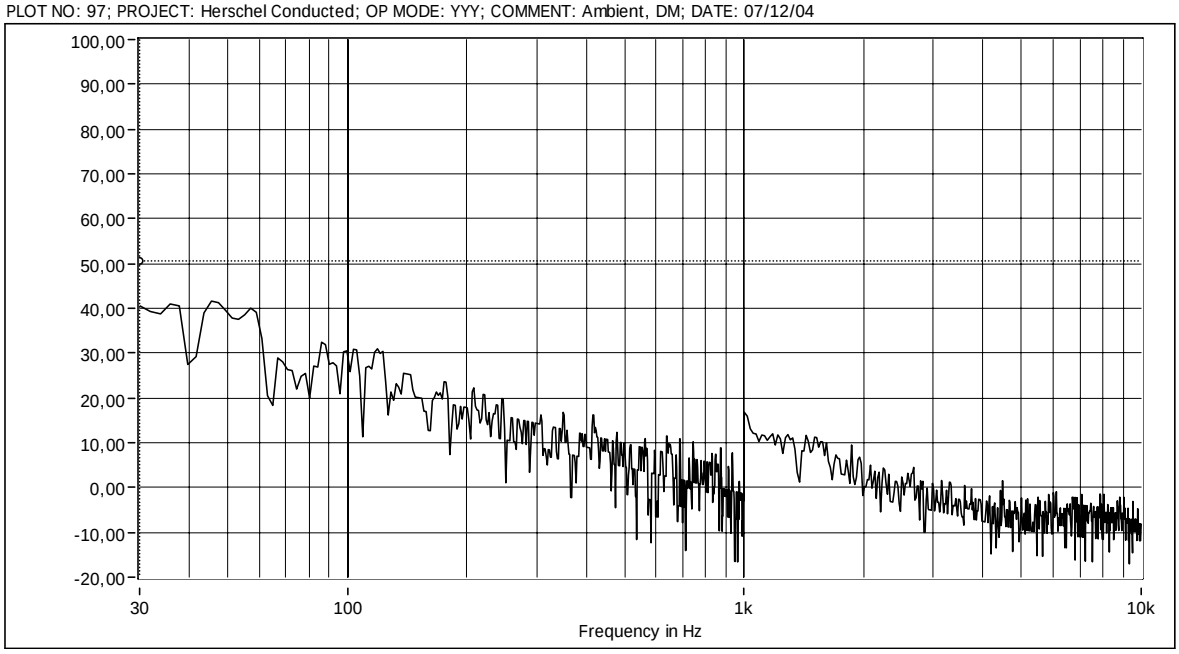
PLOT NO: 99; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, DM; DATE: 07/12/04



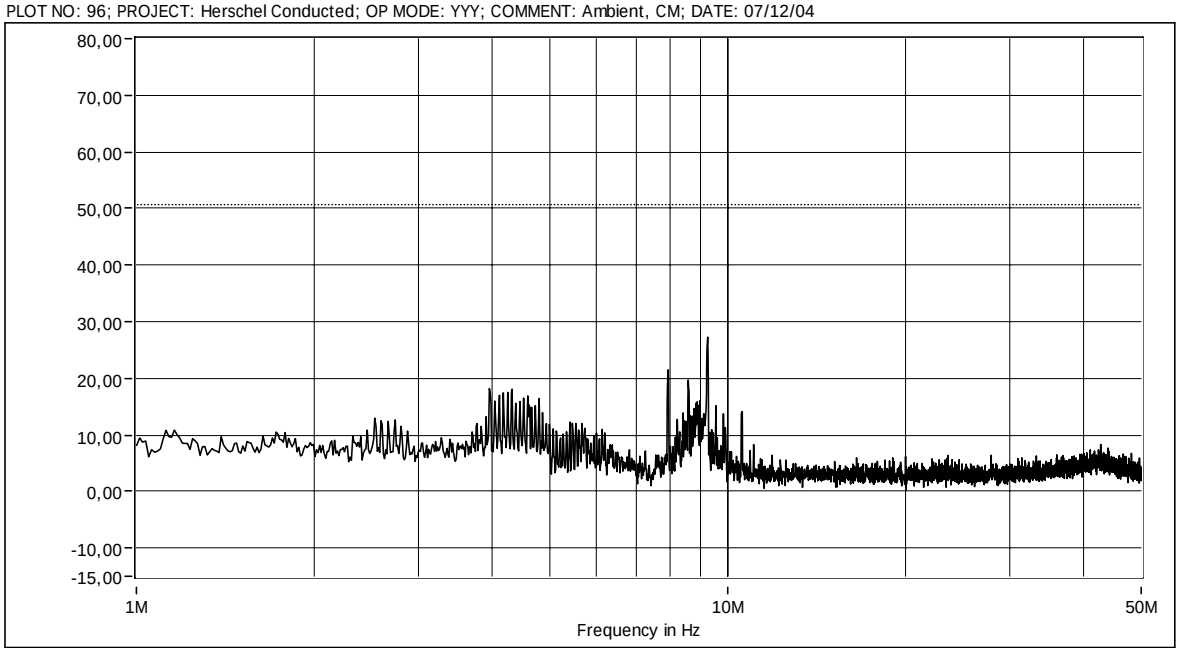
PLOT NO: 98; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, DM; DATE: 07/12/04



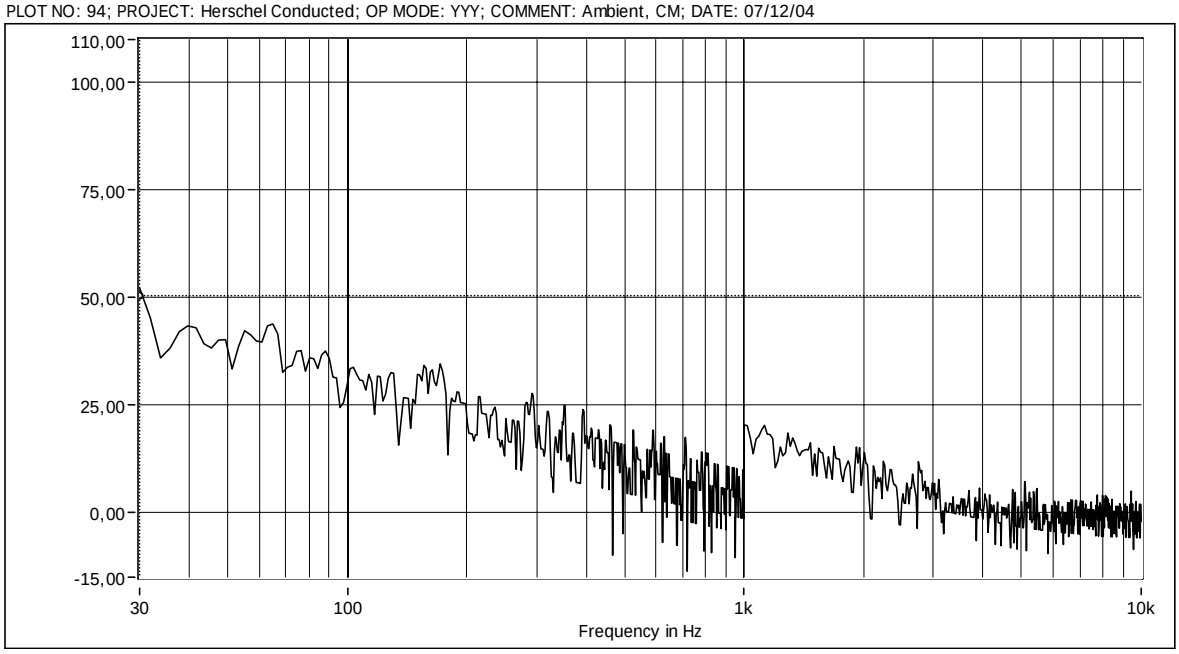
PLOT NO: 97; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, DM; DATE: 07/12/04



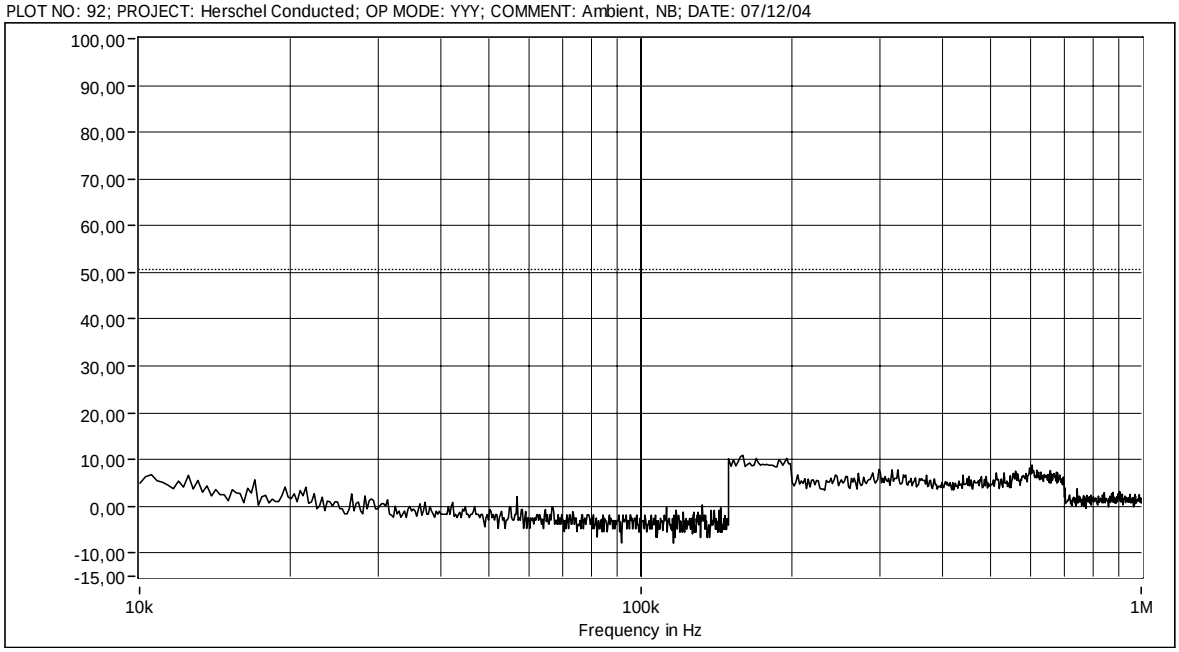
PLOT NO: 96; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, CM; DATE: 07/12/04



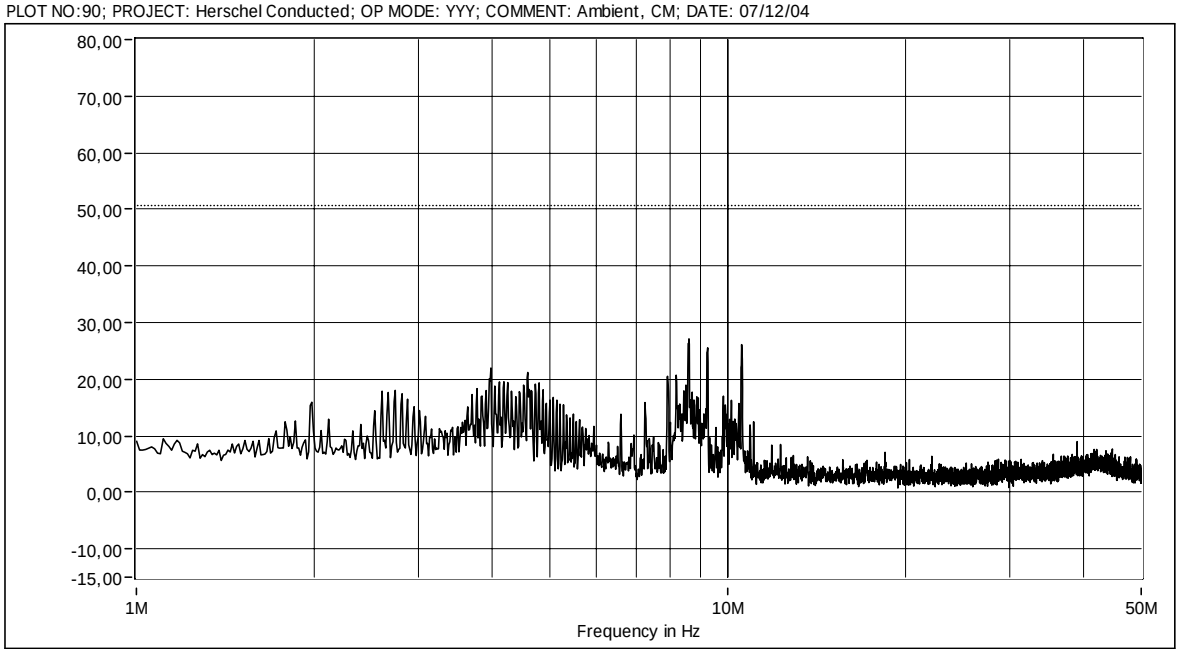
PLOT NO: 94; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, CM; DATE: 07/12/04



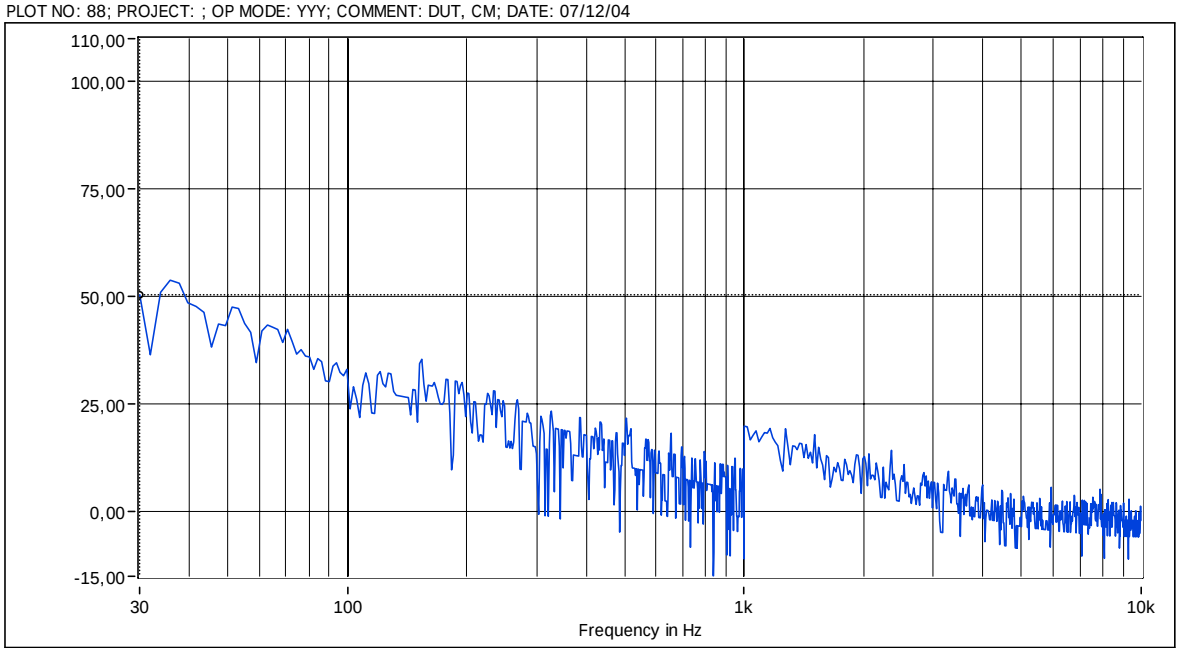
PLOT NO: 92; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, NB; DATE: 07/12/04

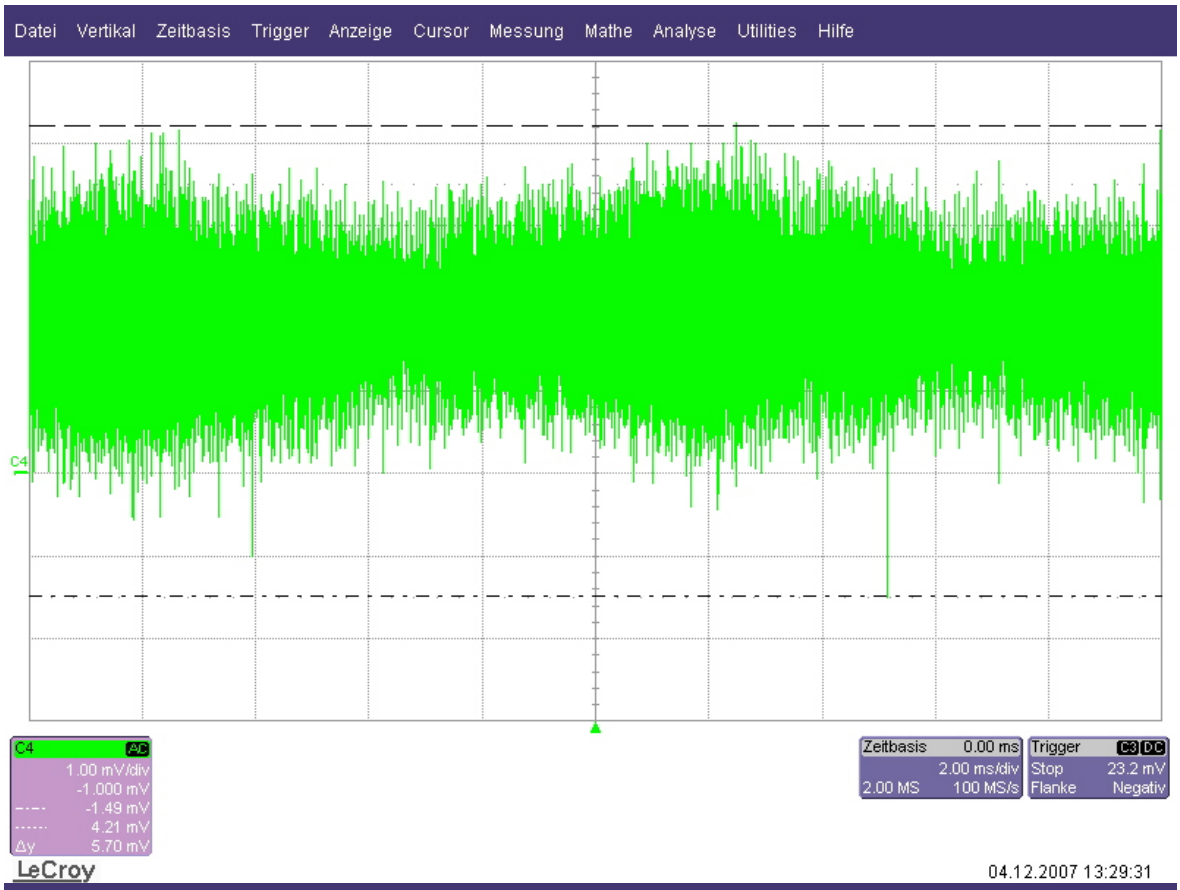


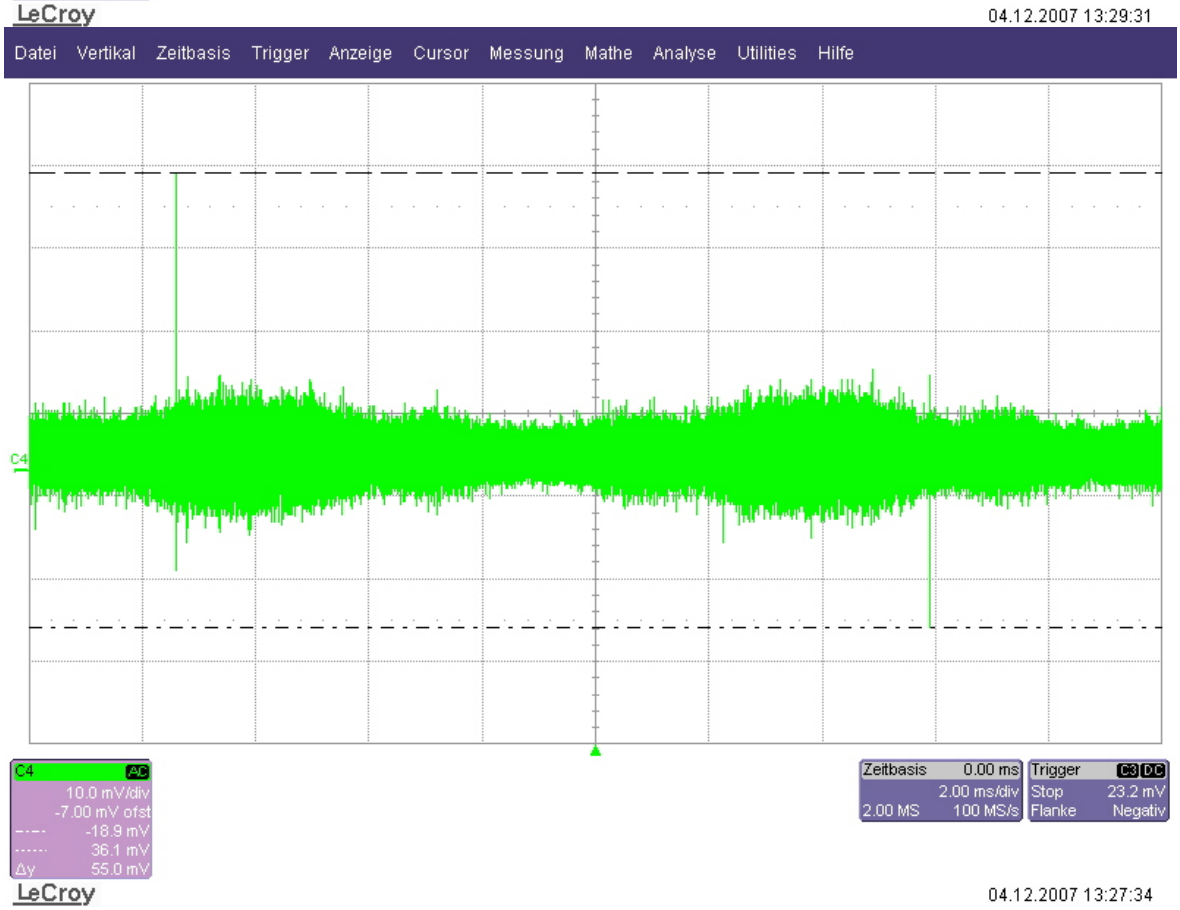
PLOT NO:90; PROJECT: Herschel Conducted; OP MODE: YYY; COMMENT: Ambient, CM; DATE: 07/12/04

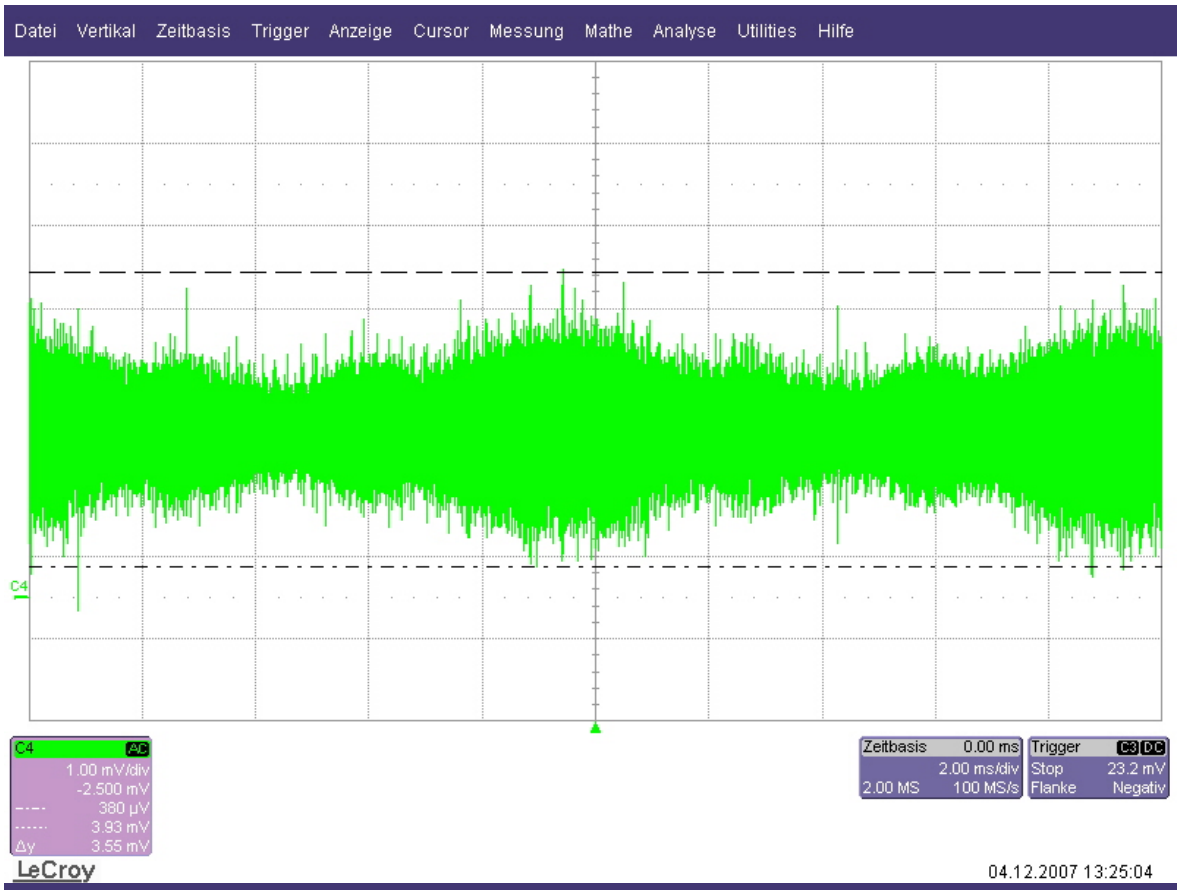


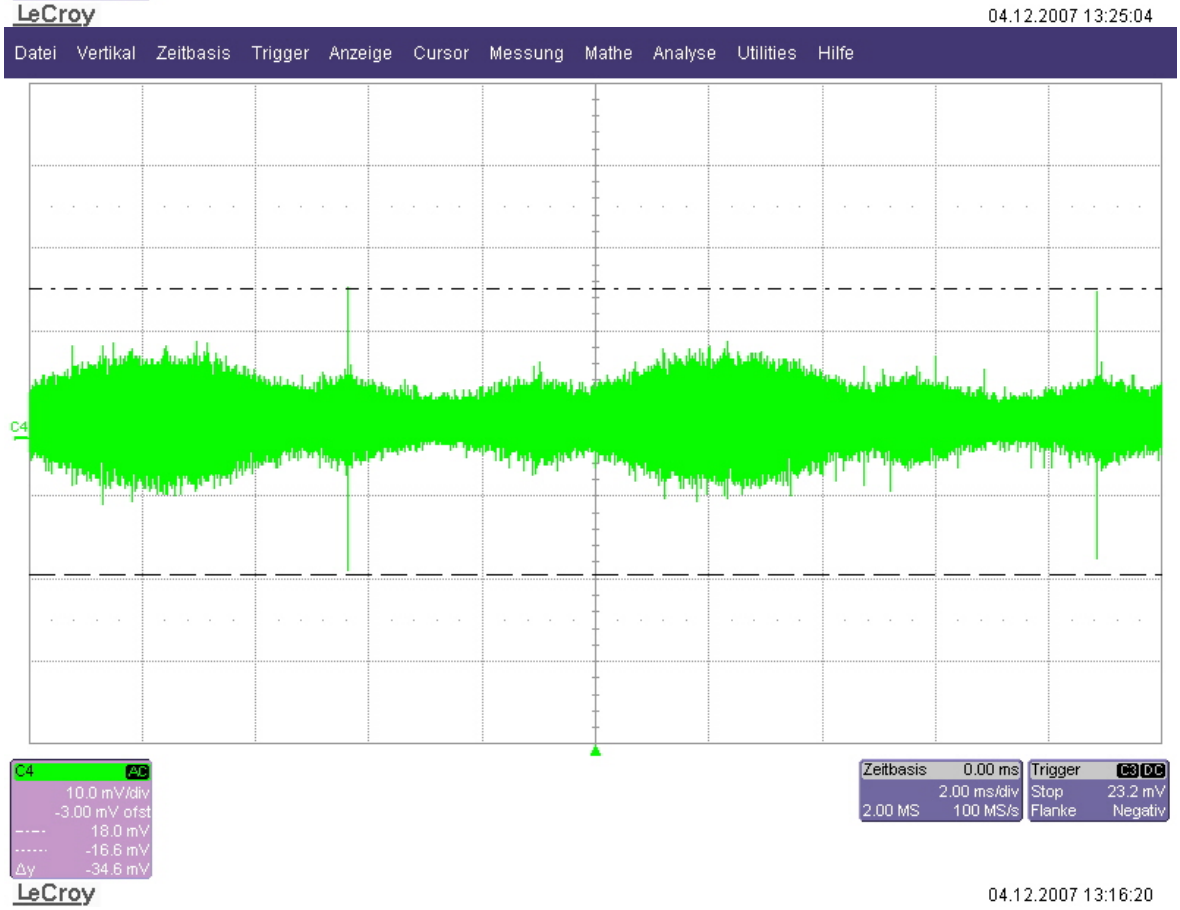
PLOT NO: 88; PROJECT: ; OP MODE: YYY; COMMENT: DUT, CM; DATE: 07/12/04

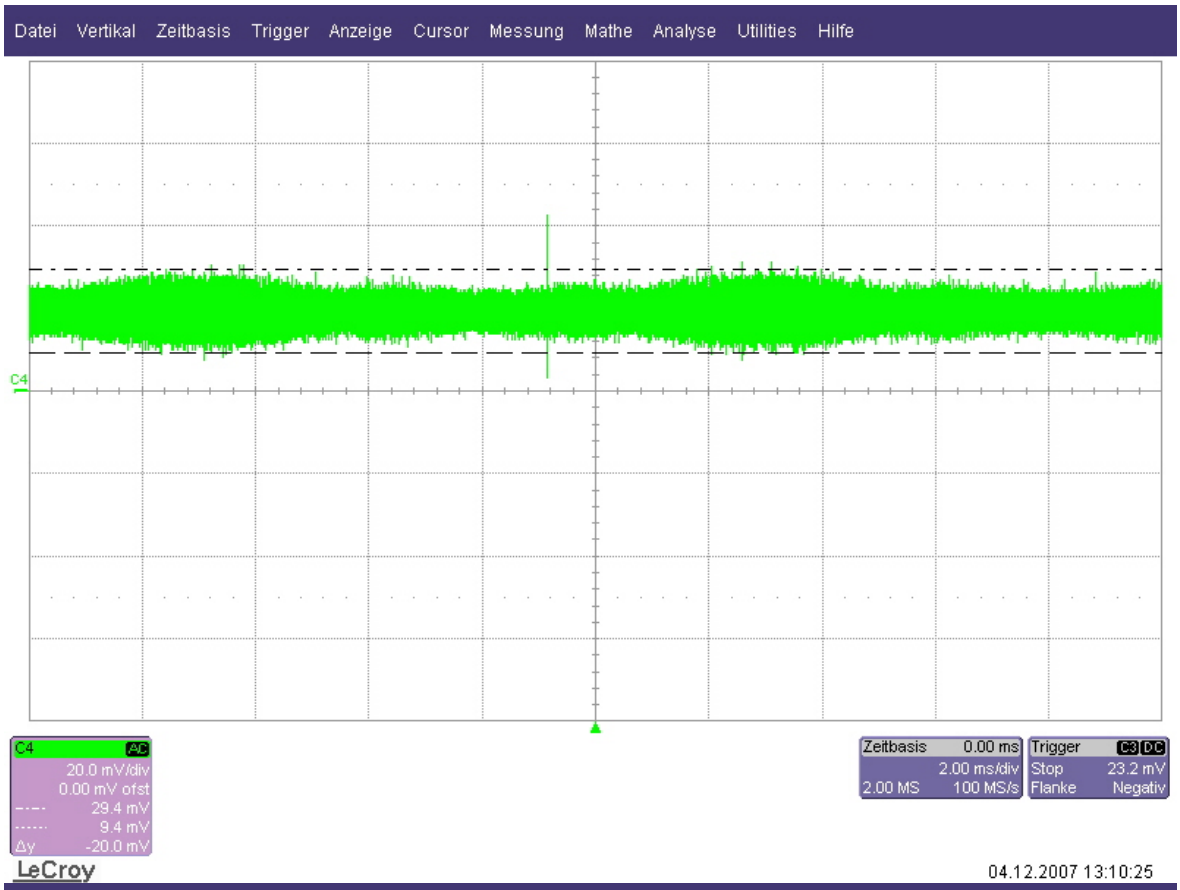


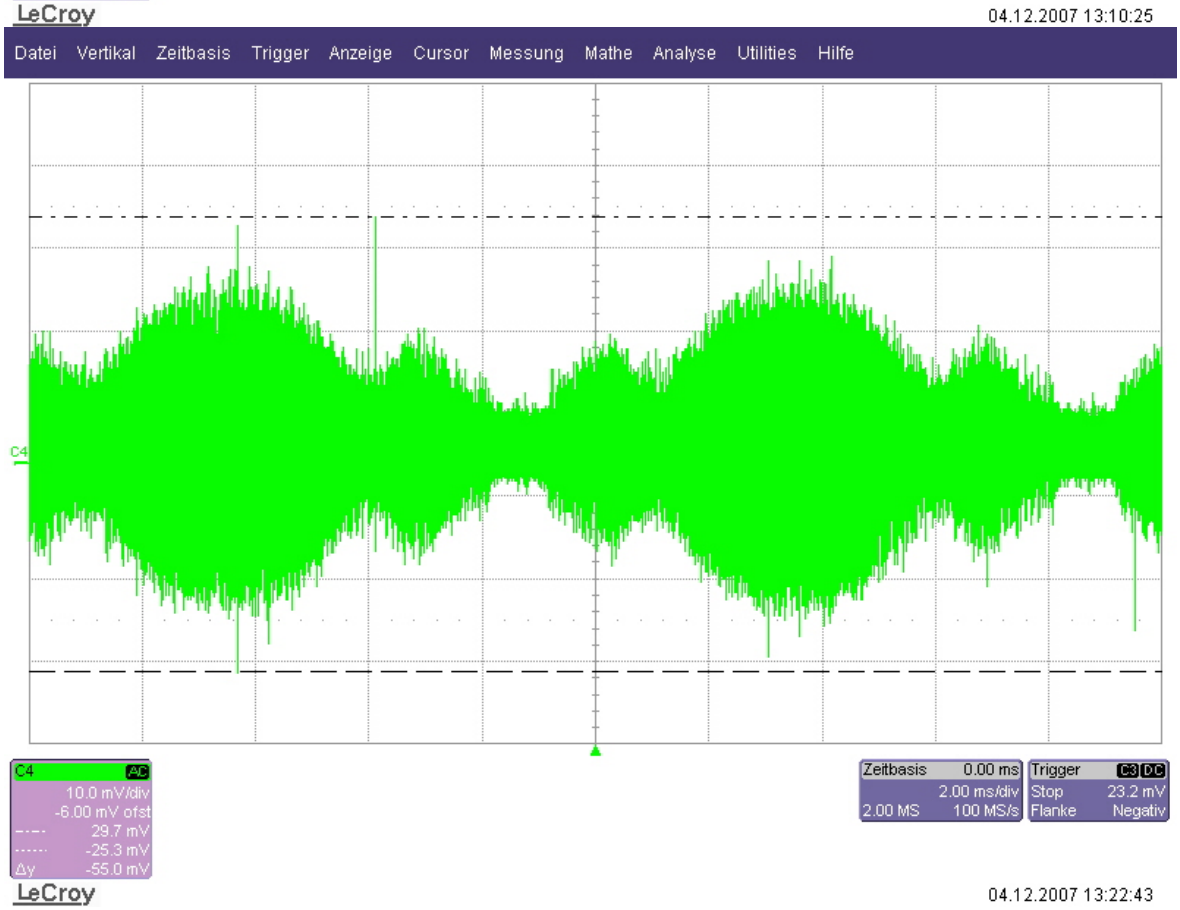

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
C4 AC
1.00 mV/div
-1.000 mV
-1.49 mV
4.21 mV
Δy 5.70 mV
Zeitbasis 0.00 ms
2.00 ms/div
2.00 MS 100 MS/s
Trigger C3 DC
Stop 23.2 mV
Flanke Negativ
LeCroy
04.12.2007 13:29:31

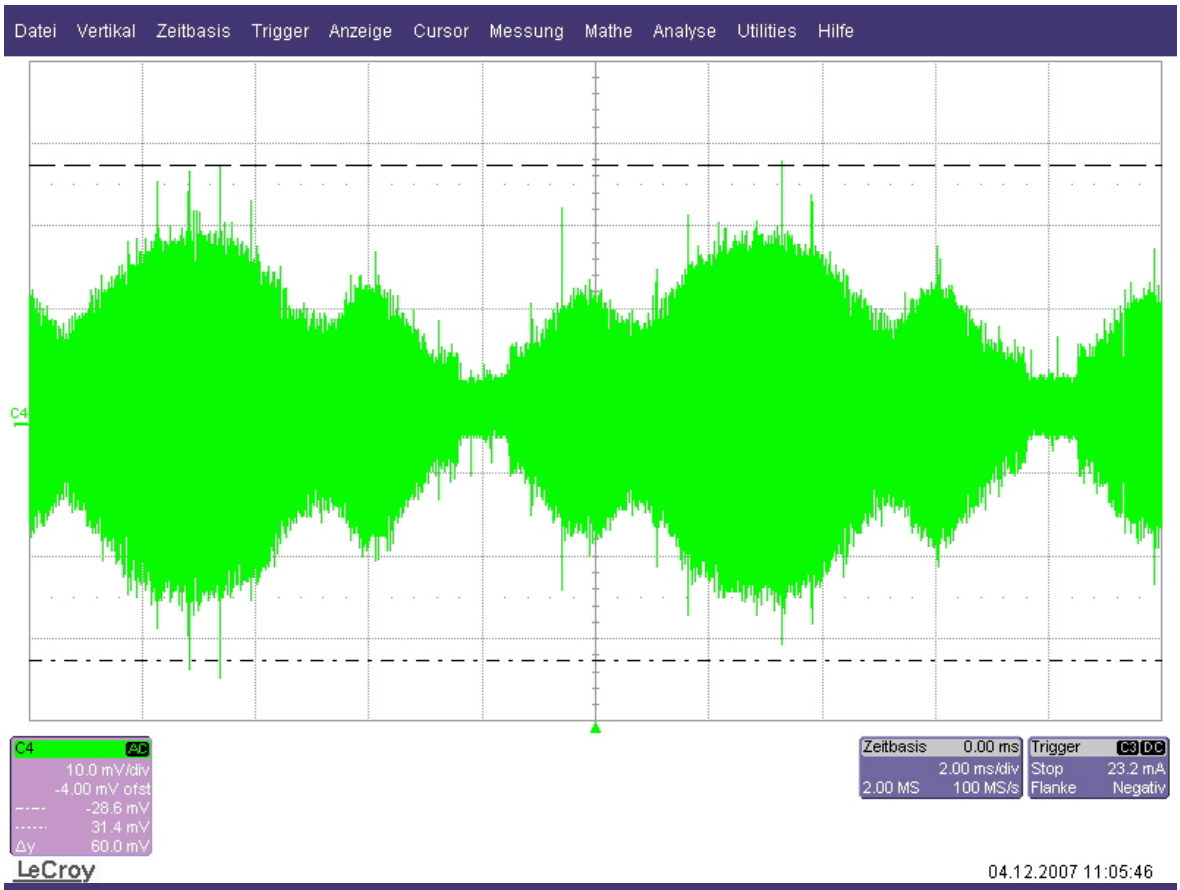

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
C4 AC
10.0 mV/div
-7.00 mV ofst
-18.9 mV
36.1 mV
Δy 55.0 mV
Zeitbasis 0.00 ms
2.00 ms/div
2.00 MS 100 MS/s
Trigger C3 DC
Stop 23.2 mV
Flanke Negativ
LeCroy
04.12.2007 13:27:34

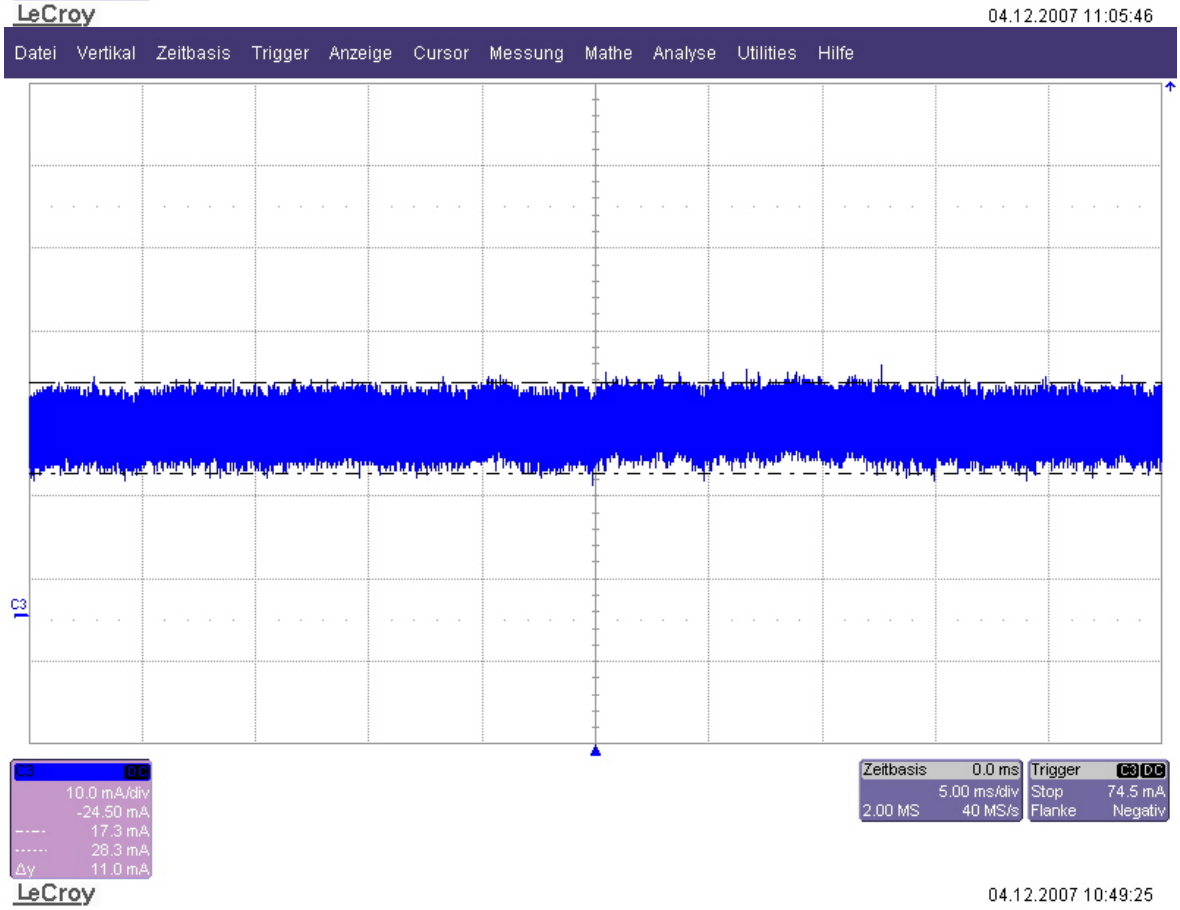

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
C4 AC
1.00 mV/div
-2.500 mV
360 µV
3.93 mV
Δy 3.55 mV
Zeitbasis 0.00 ms
2.00 ms/div
2.00 MS 100 MS/s
Trigger C3 DC
Stop 23.2 mV
Flanke Negativ
LeCroy
04.12.2007 13:25:04

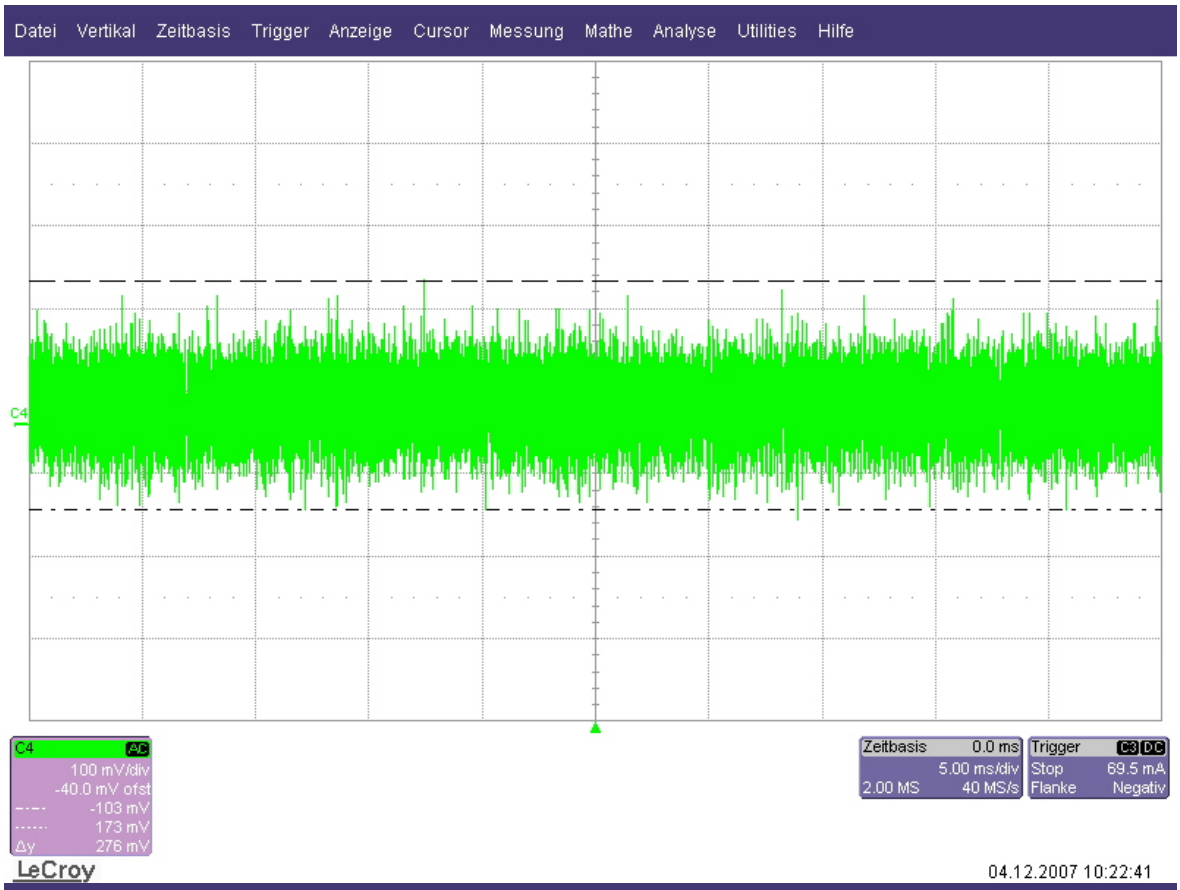

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
C4 AC
10.0 mV/div
-3.00 mV ofst
18.0 mV
-16.6 mV
Δy -34.6 mV
Zeitbasis 0.00 ms
2.00 ms/div
2.00 MS 100 MS/s
Trigger C3 DC
Stop 23.2 mV
Flanke Negativ
LeCroy
04.12.2007 13:16:20

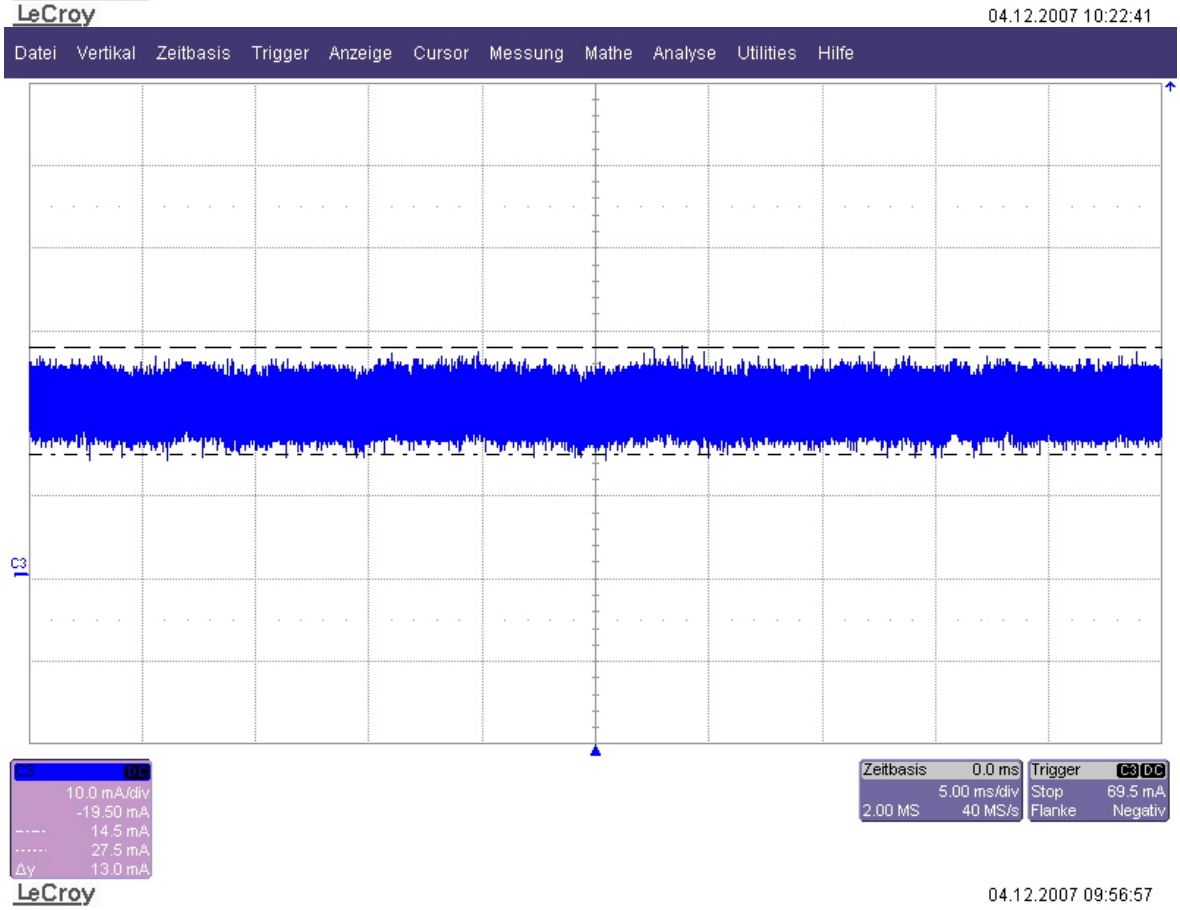

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
C4 AC
20.0 mV/div
0.00 mV ofst
29.4 mV
9.4 mV
Δy -20.0 mV
Zeitbasis 0.00 ms
2.00 ms/div
2.00 MS 100 MS/s
Trigger C3 DC
Stop 23.2 mV
Flanke Negativ
LeCroy
04.12.2007 13:10:25

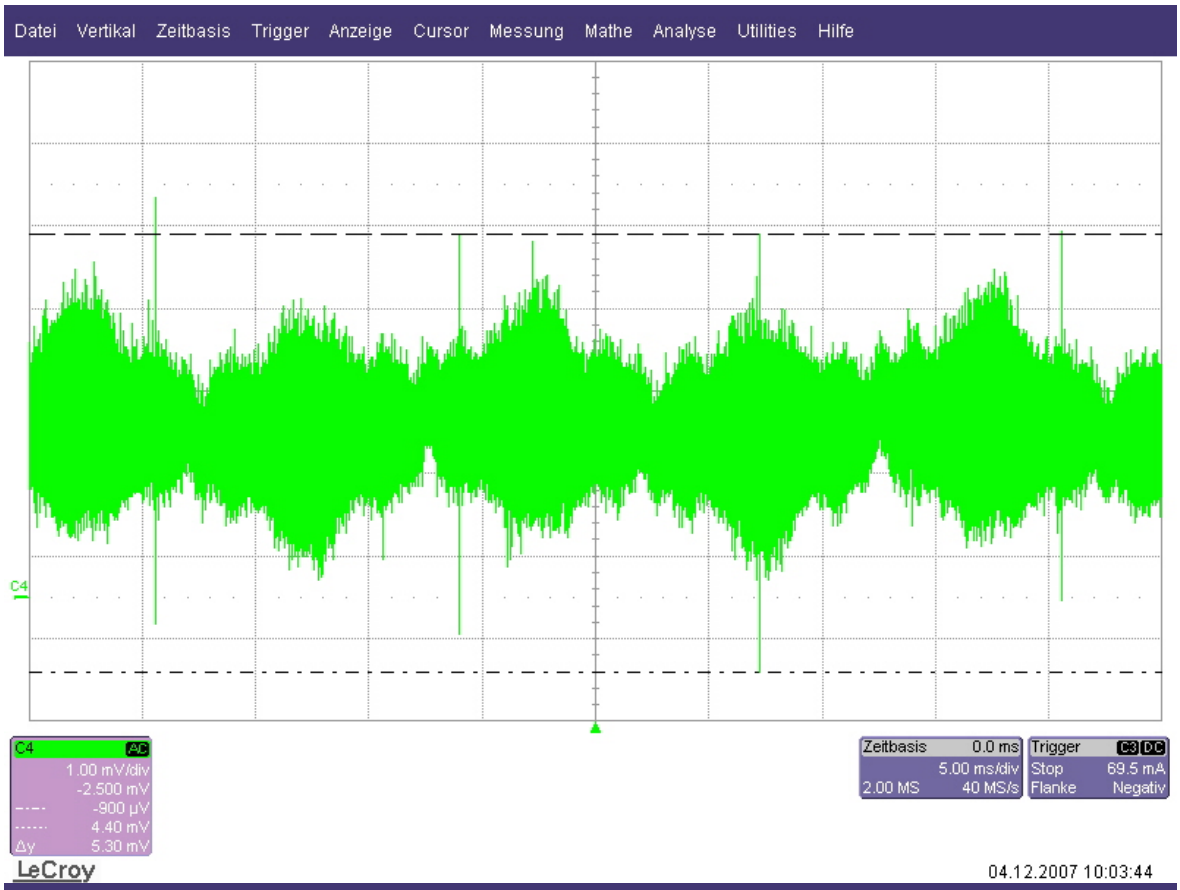

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
C4 AC
10.0 mV/div
-6.00 mV ofst
29.7 mV
-25.3 mV
Δy -55.0 mV
Zeitbasis 0.00 ms
2.00 ms/div
2.00 MS 100 MS/s
Trigger C3 DC
Stop 23.2 mV
Flanke Negativ
LeCroy
04.12.2007 13:22:43

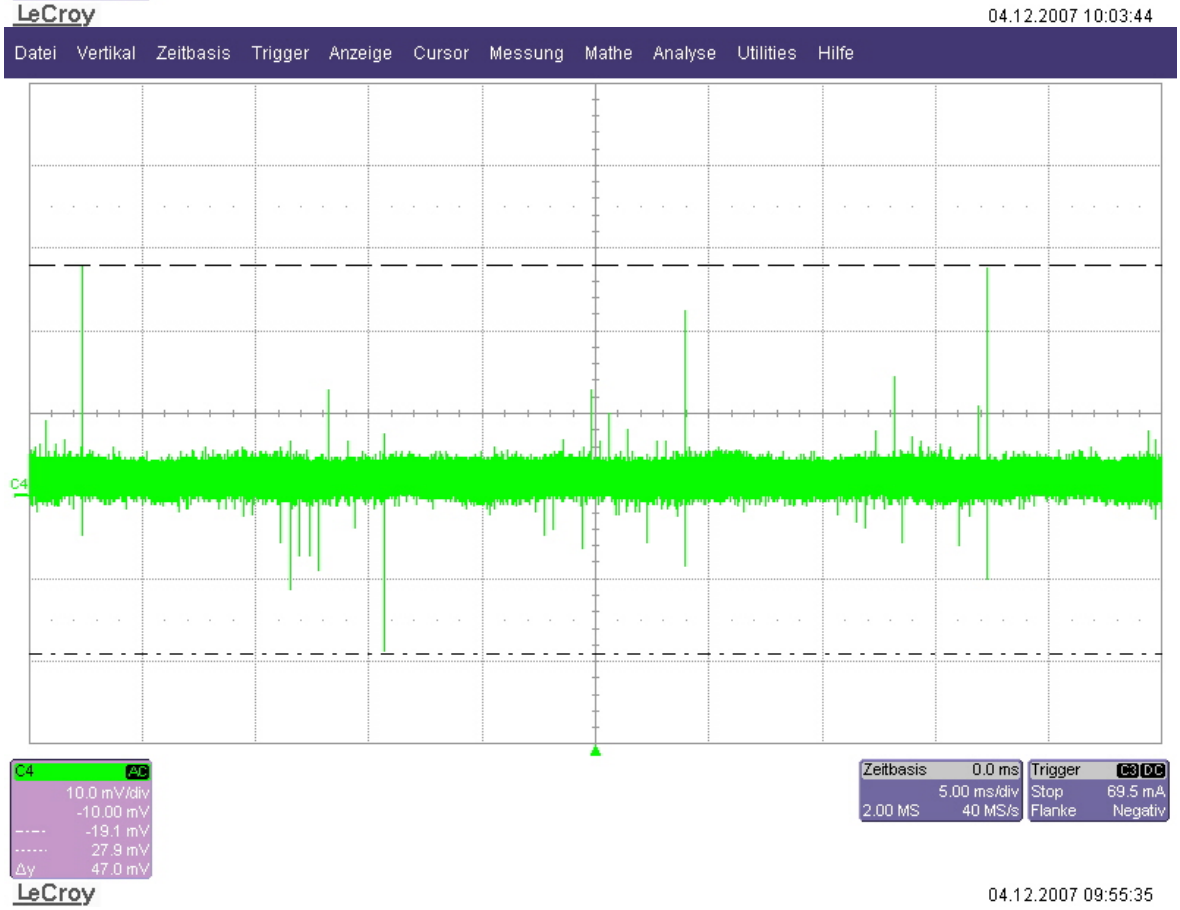

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
C4 AC
10.0 mV/div
-4.00 mV ofst
-28.6 mV
31.4 mV
Δy 60.0 mV
Zeitbasis 0.00 ms
2.00 ms/div
2.00 MS 100 MS/s
Trigger C3 DC
Stop 23.2 mA
Flanke Negativ
LeCroy
04.12.2007 11:05:46

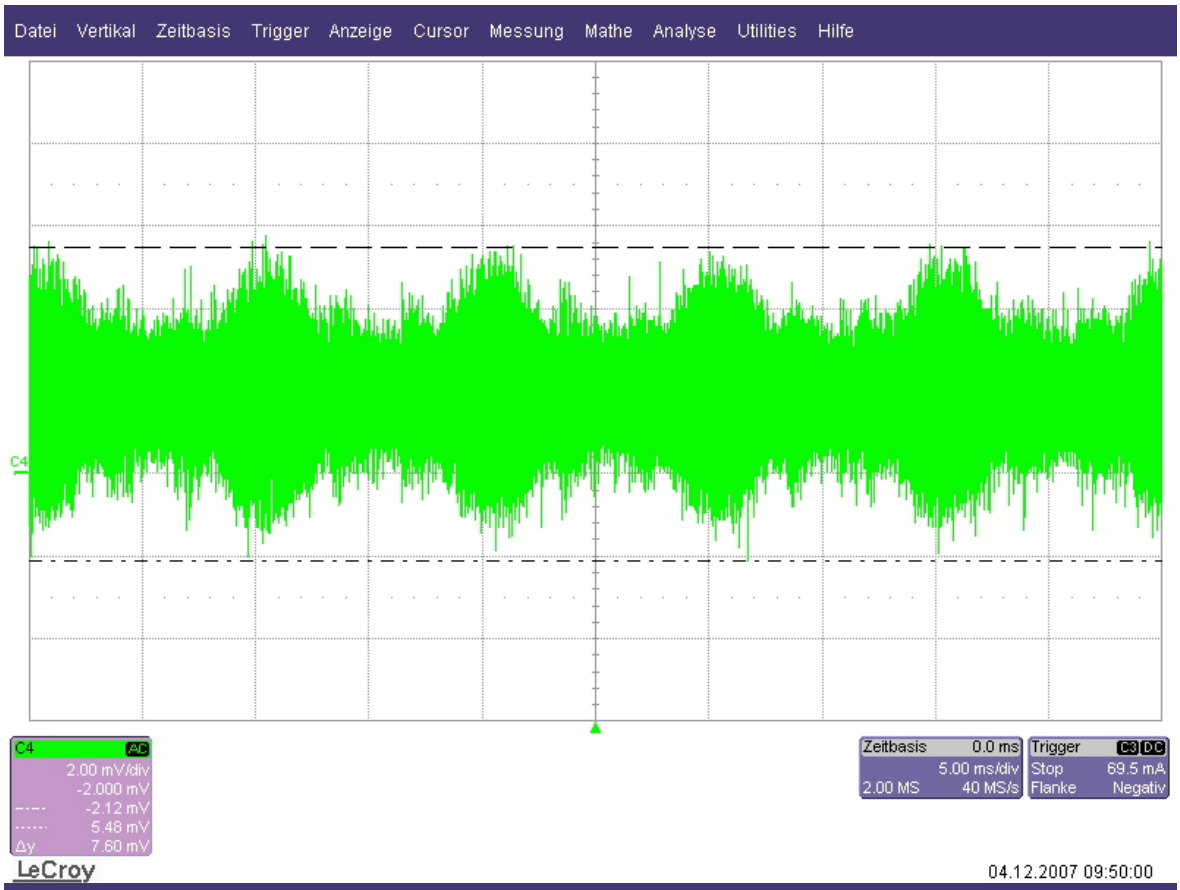

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
C3 DC
10.0 mA/div
-24.50 mA
17.3 mA
28.3 mA
Δy 11.0 mA
Zeitbasis 0.0 ms
5.00 ms/div
2.00 MS 40 MS/s
Trigger C3 DC
Stop 74.5 mA
Flanke Negativ
LeCroy
04.12.2007 10:49:25

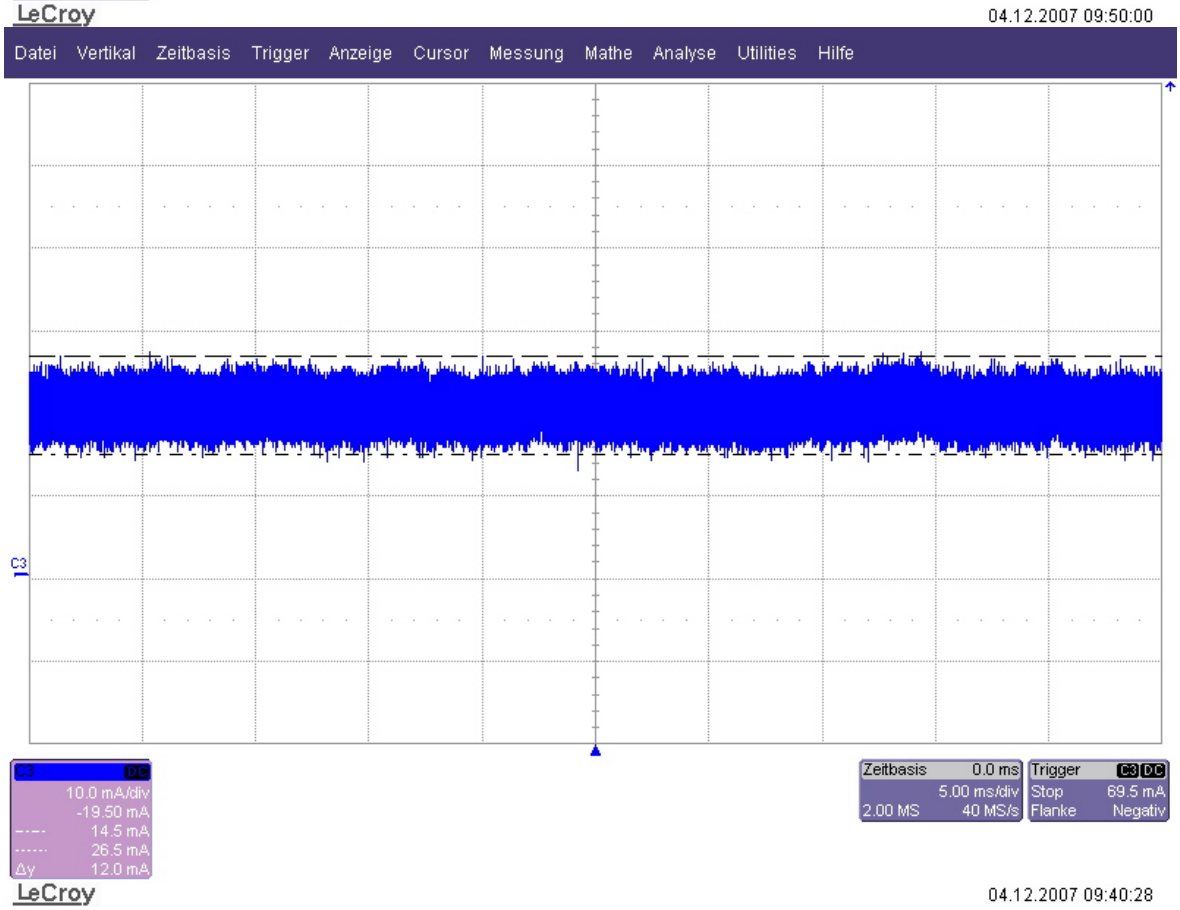

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
C4 AC
100 mV/div
-40.0 mV ofst
-103 mV
173 mV
Δy 276 mV
Zeitbasis 0.0 ms
5.00 ms/div
2.00 MS 40 MS/s
Trigger C3 DC
Stop 69.5 mA
Flanke Negativ
LeCroy
04.12.2007 10:22:41

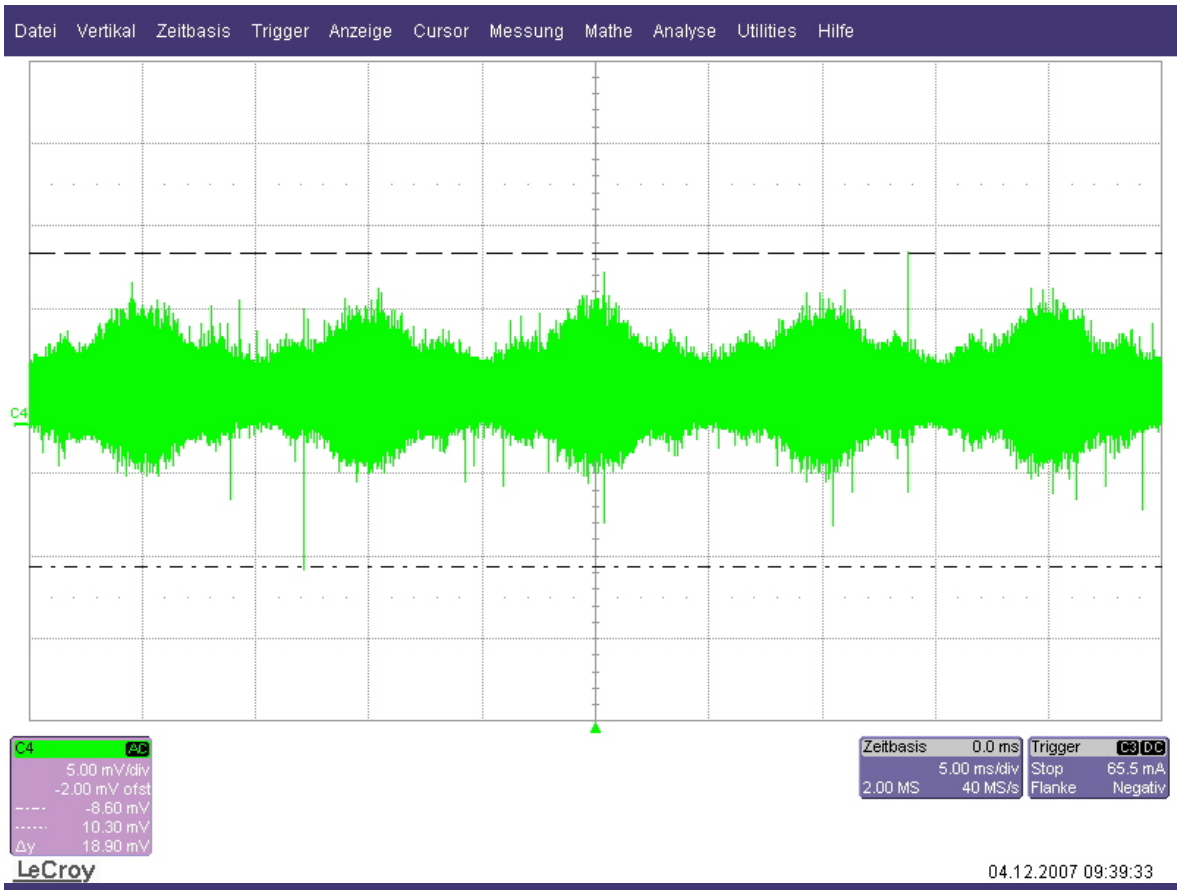

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
C3 DC
10.0 mA/div
-19.50 mA
14.5 mA
27.5 mA
Δy 13.0 mA
Zeitbasis 0.0 ms
5.00 ms/div
2.00 MS 40 MS/s
Trigger C3 DC
Stop 69.5 mA
Flanke Negativ
LeCroy
04.12.2007 09:56:57


Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
C4 AC
1.00 mV/div
-2.500 mV
-900 µV
4.40 mV
Δy 5.30 mV
Zeitbasis 0.0 ms
5.00 ms/div
2.00 MS 40 MS/s
Trigger C3 DC
Stop 69.5 mA
Flanke Negativ
LeCroy
04.12.2007 10:03:44

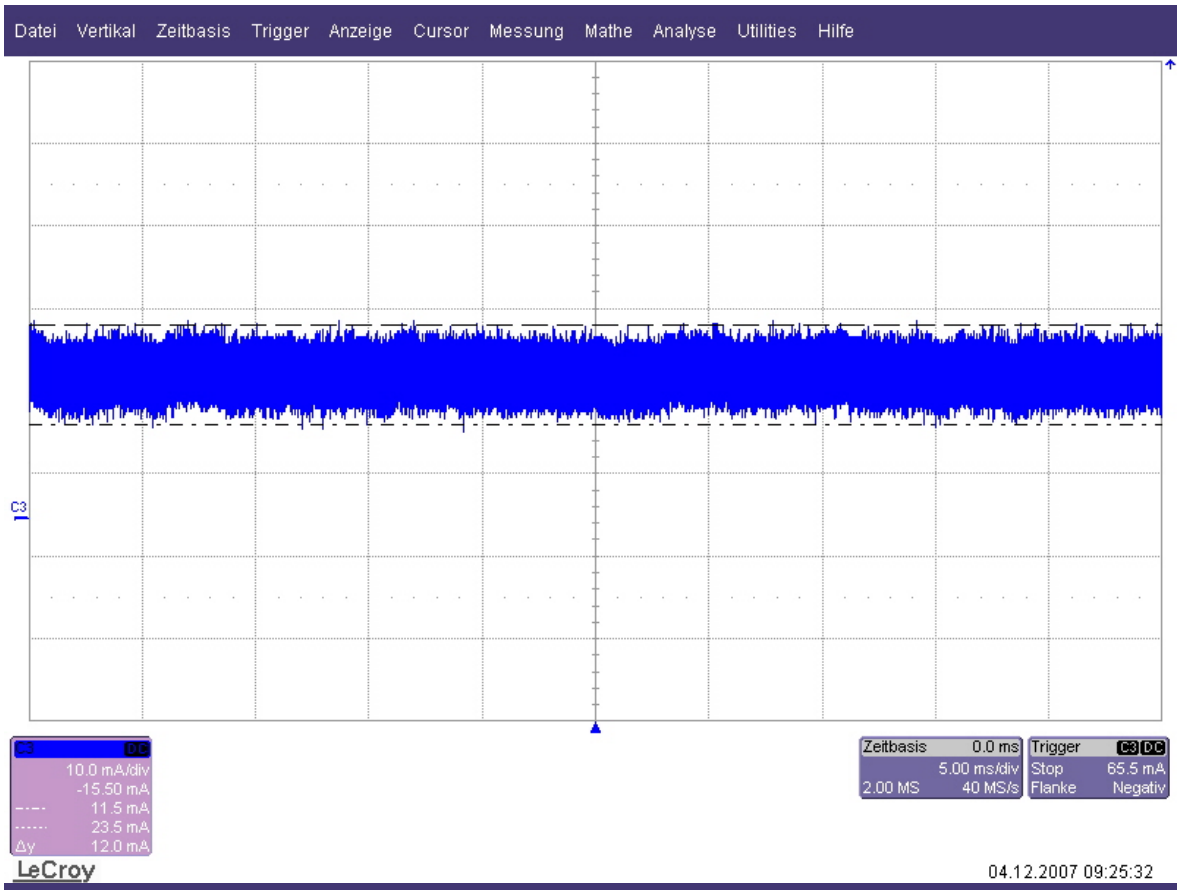

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
C4 AC
10.0 mV/div
-10.00 mV
-19.1 mV
27.9 mV
Δy 47.0 mV
Zeitbasis 0.0 ms
5.00 ms/div
2.00 MS 40 MS/s
Trigger C3 DC
Stop 69.5 mA
Flanke Negativ
LeCroy
04.12.2007 09:55:35

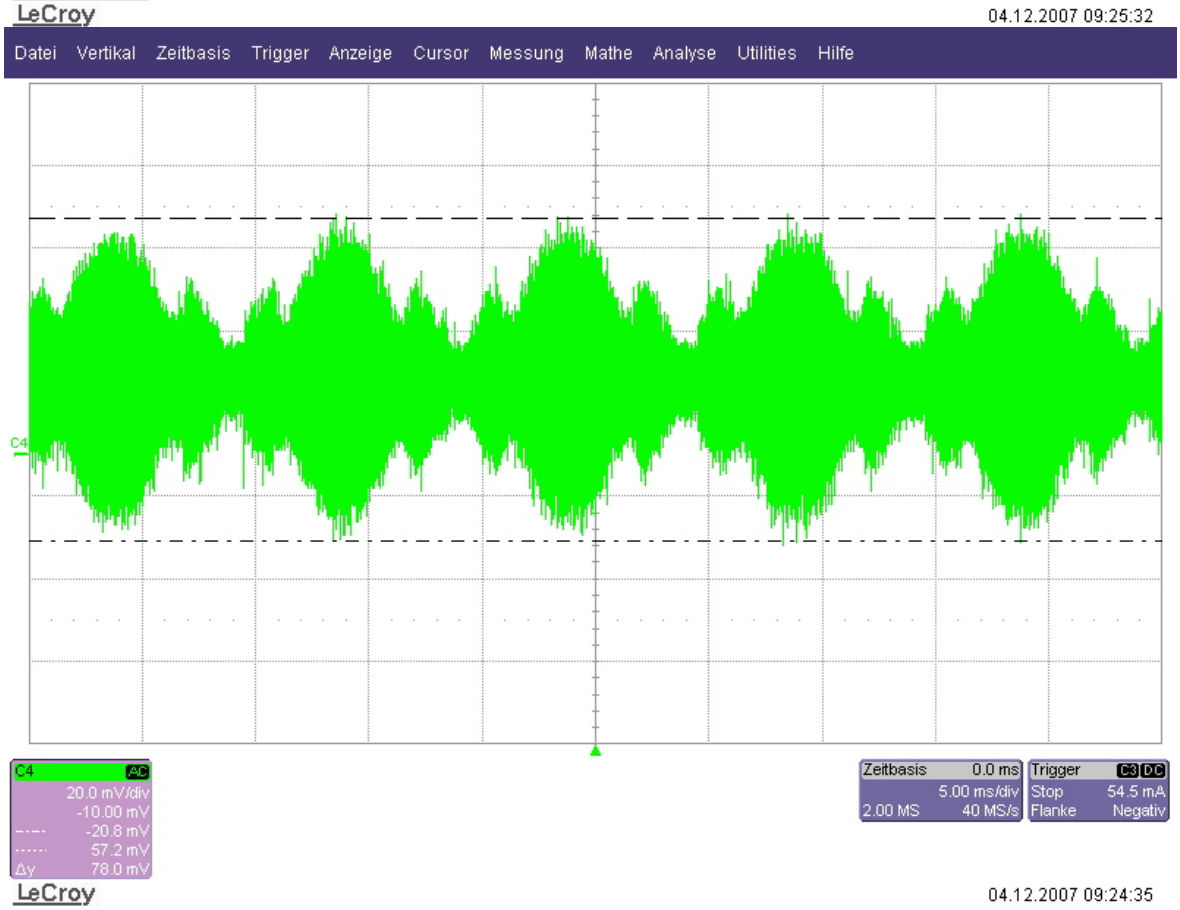

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
C4 AC
2.00 mV/div
-2.000 mV
-2.12 mV
5.48 mV
Δy 7.60 mV
Zeitbasis 0.0 ms
5.00 ms/div
2.00 MS 40 MS/s
Trigger C3 DC
Stop 69.5 mA
Flanke Negativ
LeCroy
04.12.2007 09:50:00

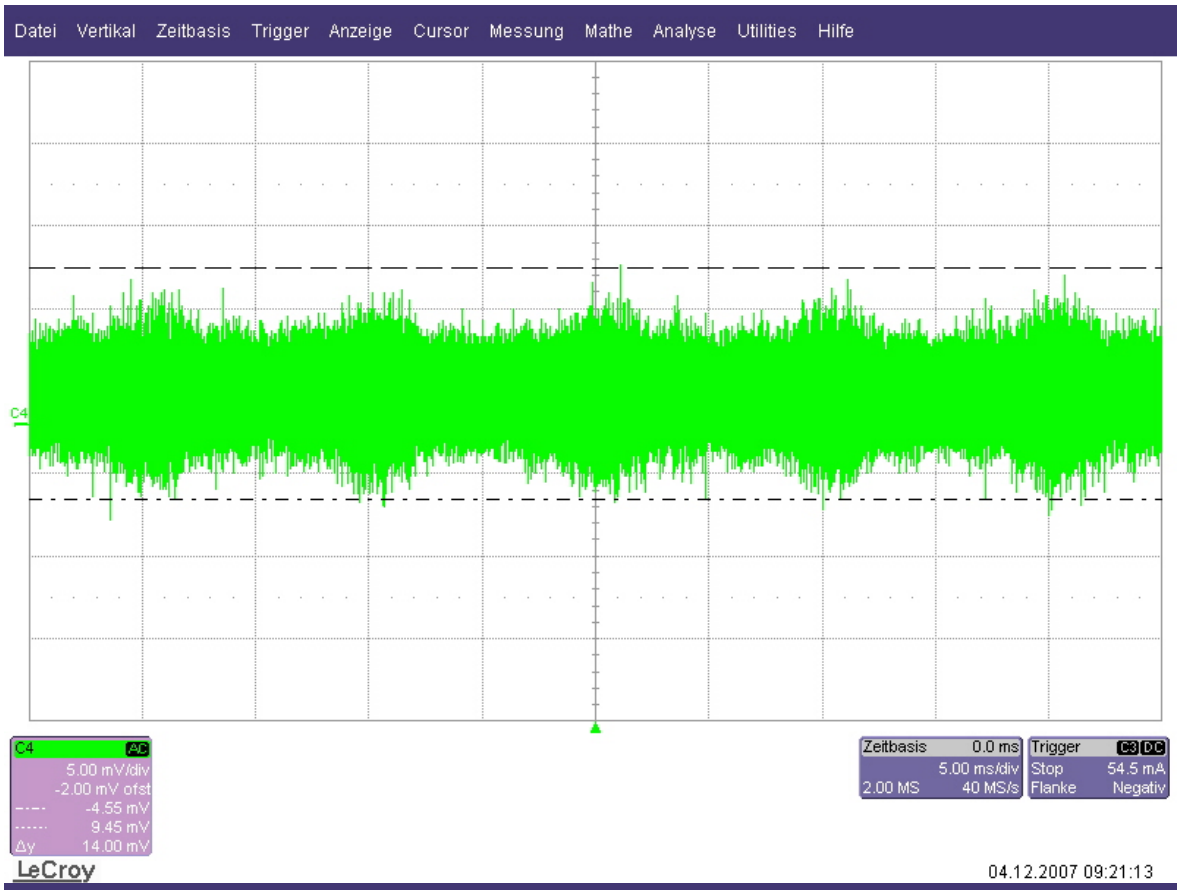

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
C3 DC
10.0 mA/div
-19.50 mA
14.5 mA
26.5 mA
Δy 12.0 mA
Zeitbasis 0.0 ms
5.00 ms/div
2.00 MS 40 MS/s
Trigger C3 DC
Stop 69.5 mA
Flanke Negativ
LeCroy
04.12.2007 09:40:28

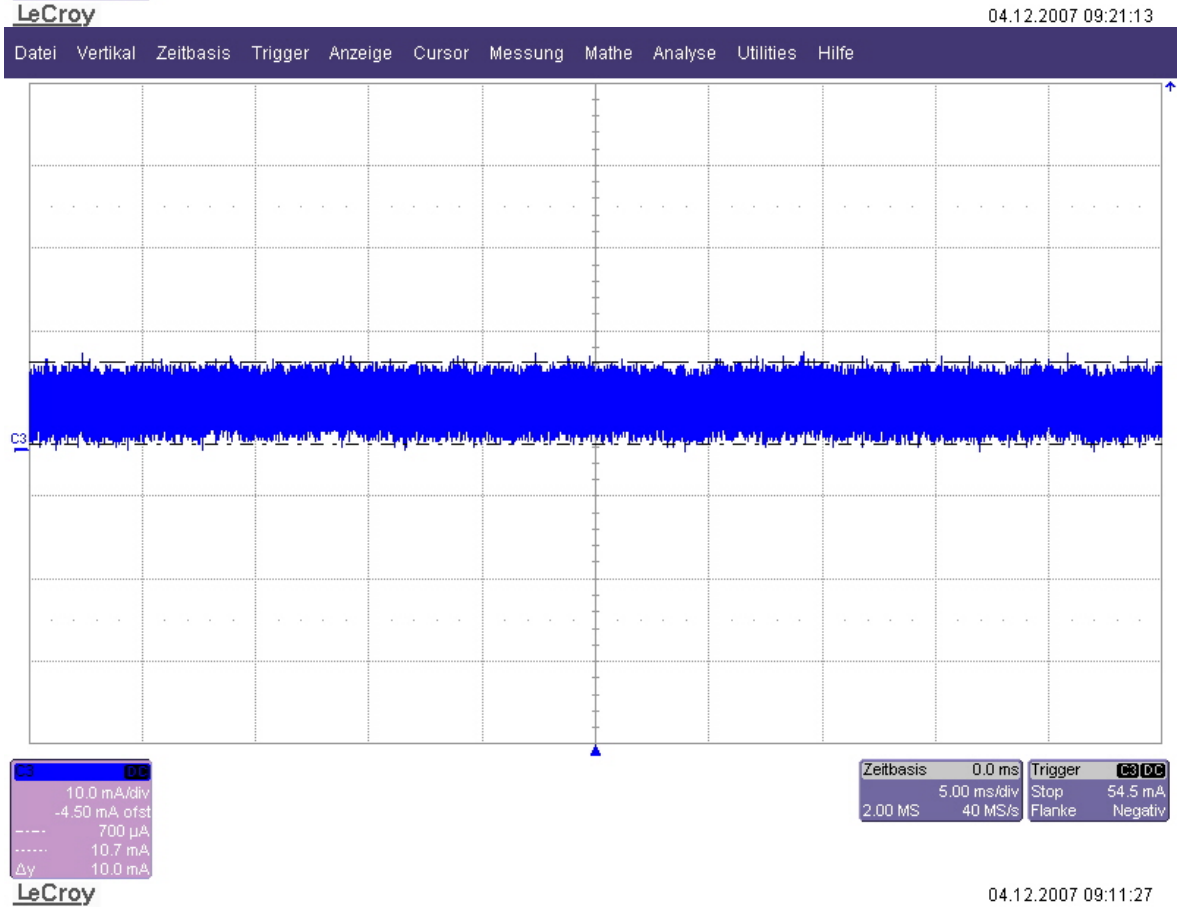

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
C4 AC
5.00 mV/div
-2.00 mV ofst
-8.60 mV
10.30 mV
Δy 18.90 mV
Zeitbasis 0.0 ms
5.00 ms/div
2.00 MS 40 MS/s
Trigger C3 DC
Stop 65.5 mA
Flanke Negativ
LeCroy
04.12.2007 09:39:33

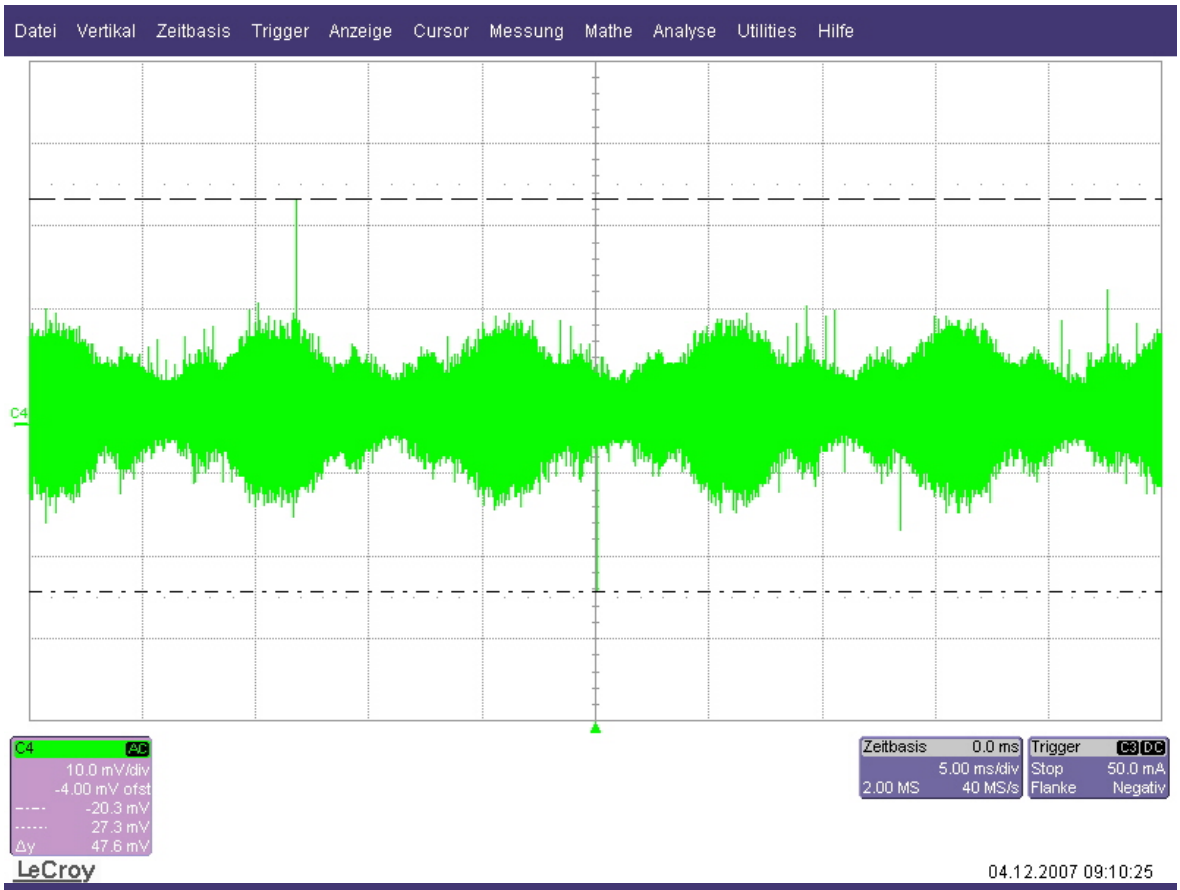

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
C4 AC
1.00 mV/div
-2.000 mV
390 µV
3.29 mV
Δy 2.90 mV
Zeitbasis 0.0 ms
5.00 ms/div
2.00 MS 40 MS/s
Trigger C3 DC
Stop 65.5 mA
Flanke Negativ
LeCroy
04.12.2007 09:35:16

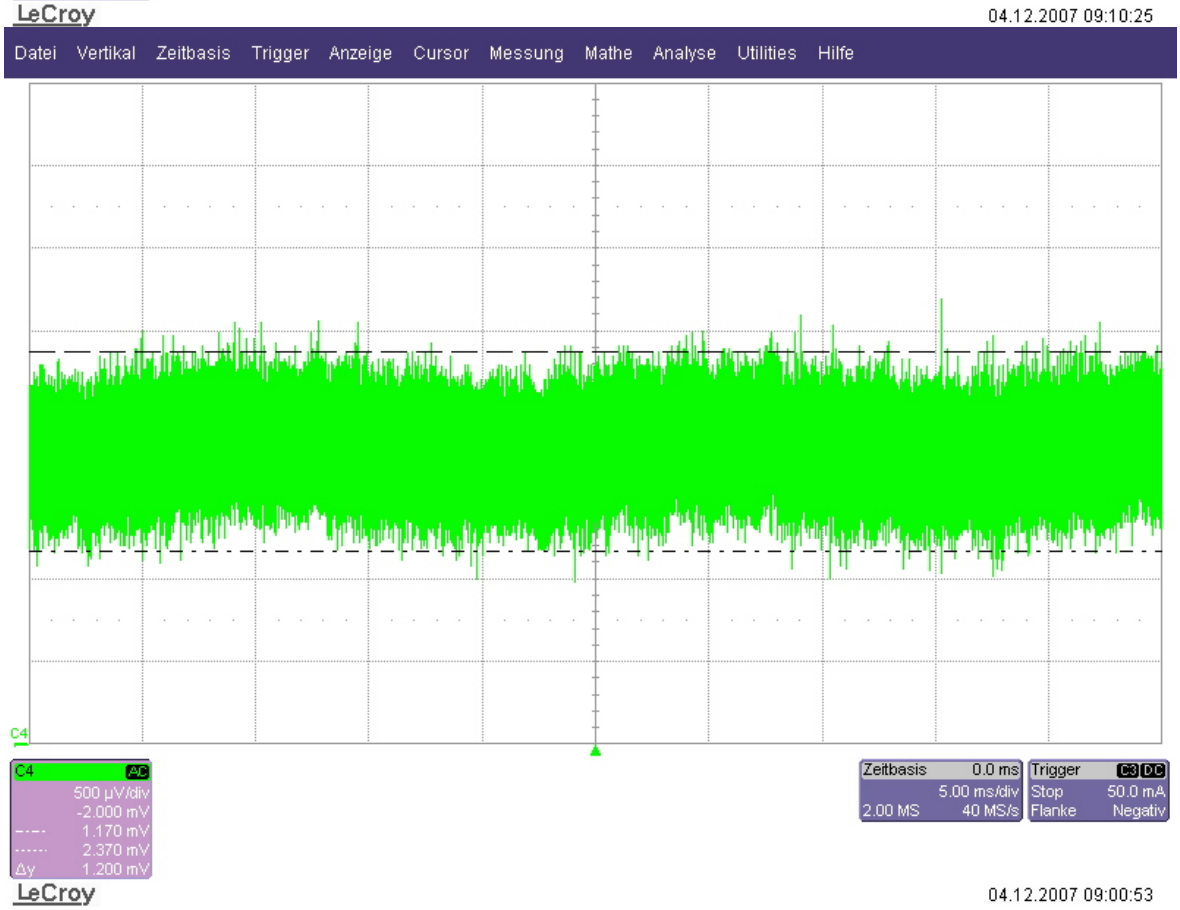

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
C3 DC
10.0 mA/div
-15.50 mA
11.5 mA
23.5 mA
Δy 12.0 mA
Zeitbasis 0.0 ms
5.00 ms/div
2.00 MS 40 MS/s
Trigger C3 DC
Stop 65.5 mA
Flanke Negativ
LeCroy
04.12.2007 09:25:32

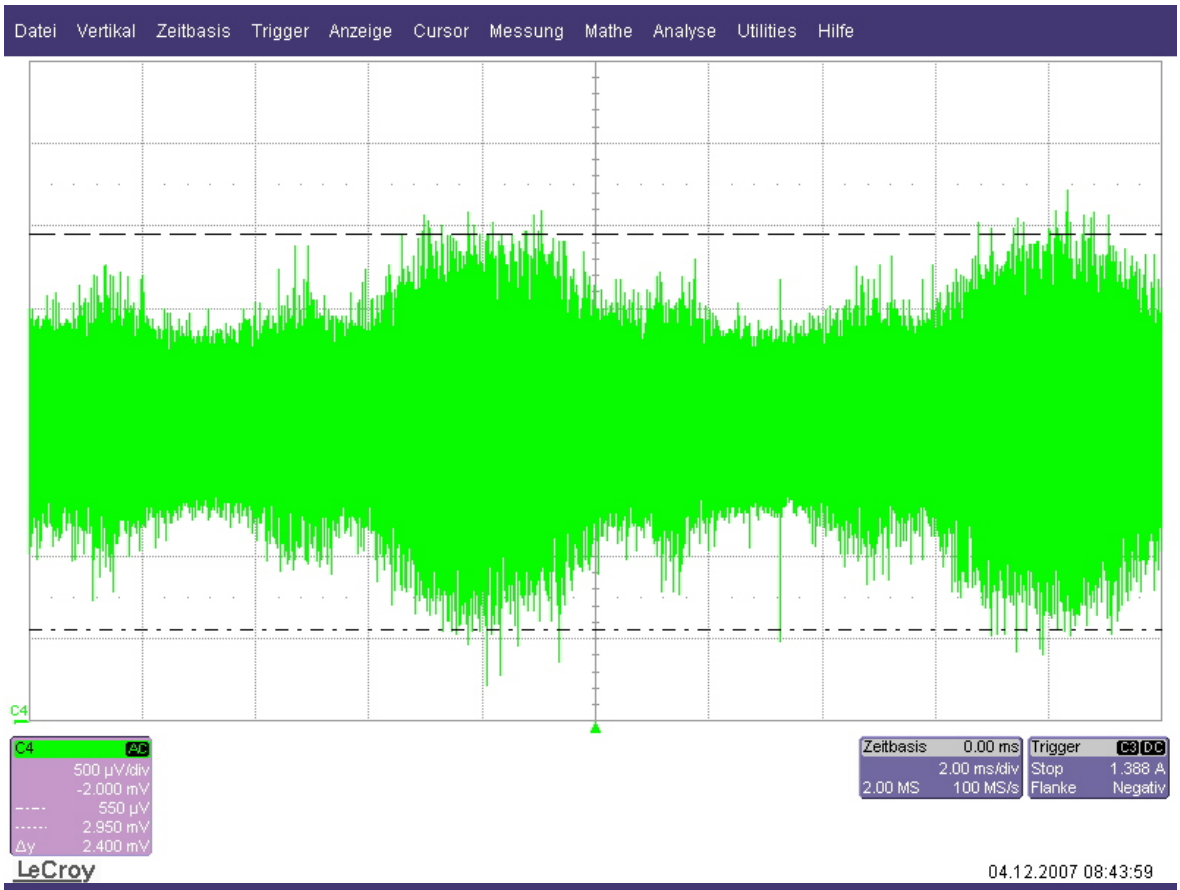

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
C4 AC
20.0 mV/div
-10.00 mV
-20.8 mV
57.2 mV
Δy 78.0 mV
Zeitbasis 0.0 ms
5.00 ms/div
2.00 MS 40 MS/s
Trigger C3 DC
Stop 54.5 mA
Flanke Negativ
LeCroy
04.12.2007 09:24:35

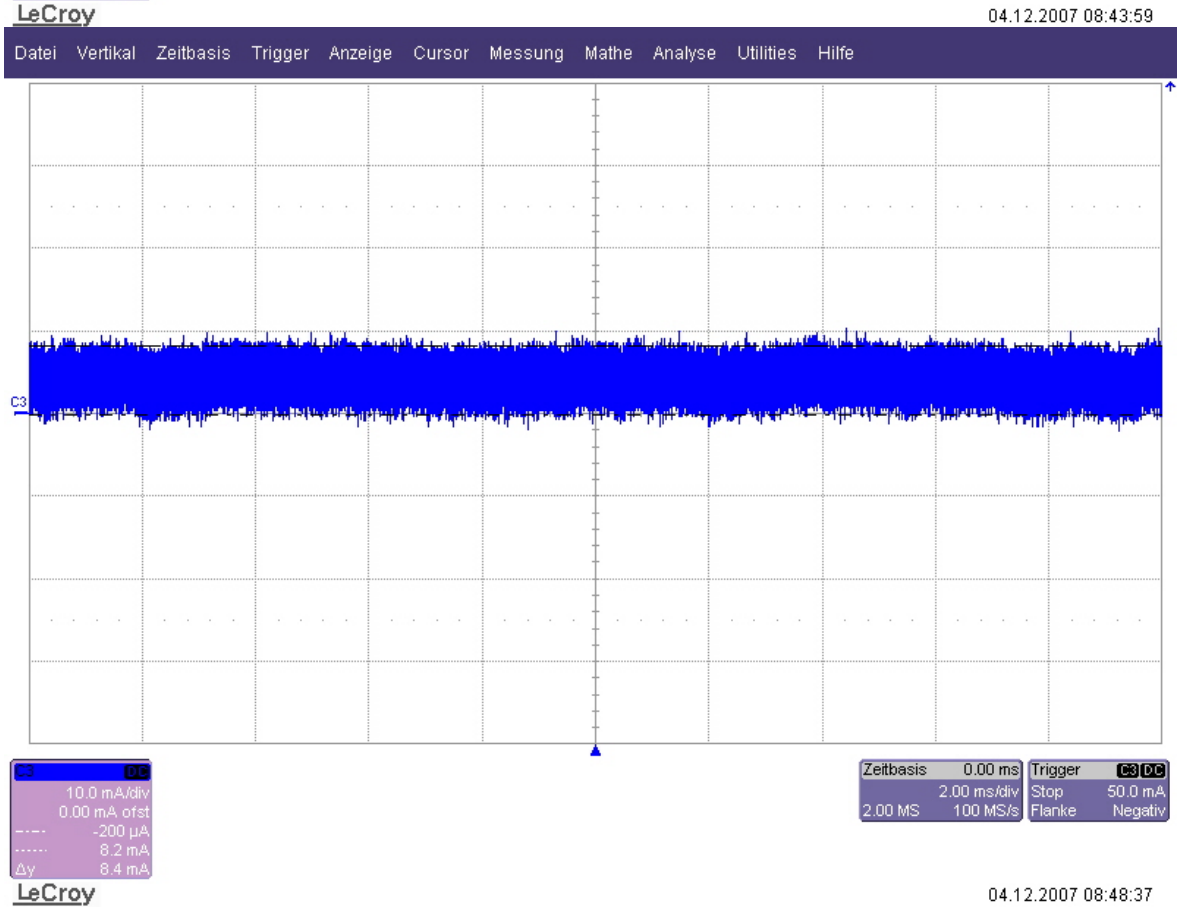

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
C4 AC
5.00 mV/div
-2.00 mV ofst
-4.55 mV
9.45 mV
Δy 14.00 mV
Zeitbasis 0.0 ms
5.00 ms/div
2.00 MS 40 MS/s
Trigger C3 DC
Stop 54.5 mA
Flanke Negativ
LeCroy
04.12.2007 09:21:13

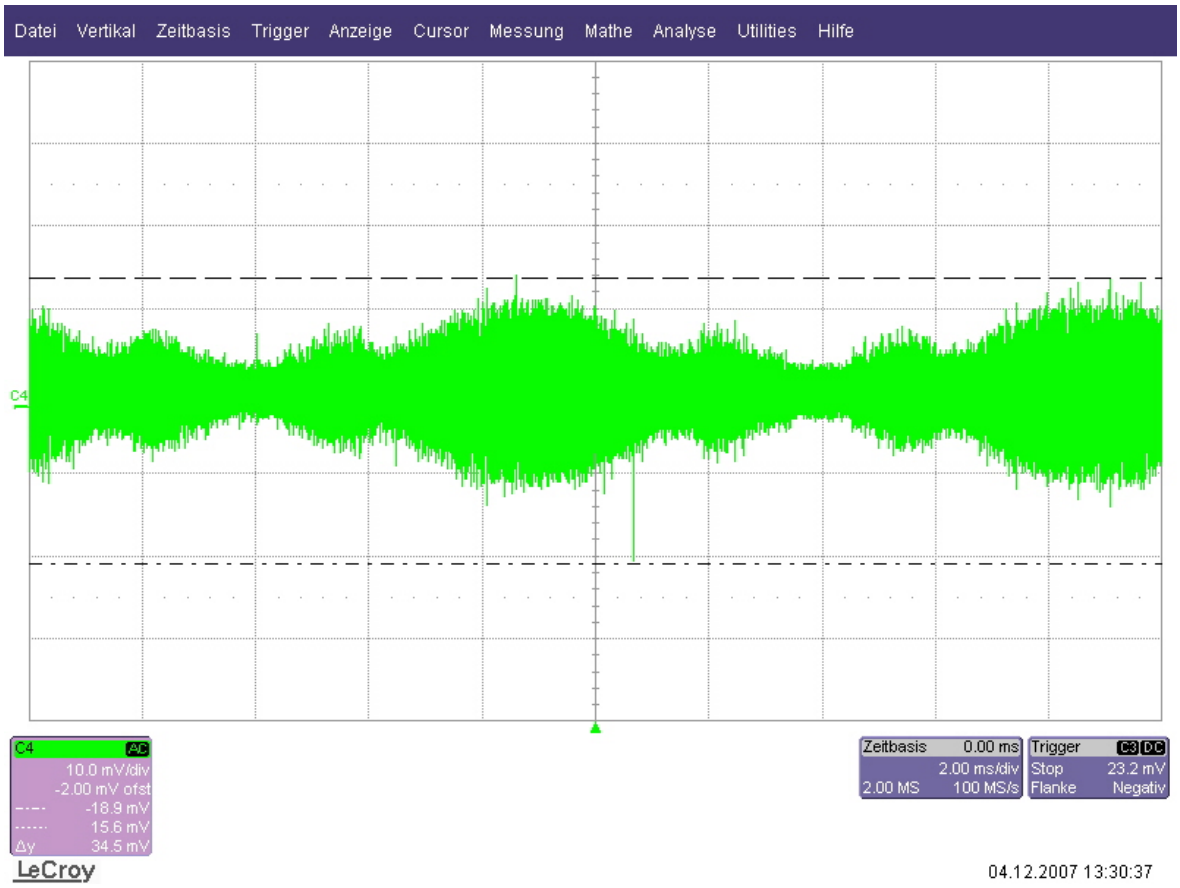

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
C3 DC
10.0 mA/div
-4.50 mA ofst
700 µA
10.7 mA
Δy 10.0 mA
Zeitbasis 0.0 ms
5.00 ms/div
2.00 MS 40 MS/s
Trigger C3 DC
Stop 54.5 mA
Flanke Negativ
LeCroy
04.12.2007 09:11:27


Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
C4 AC
10.0 mV/div
-4.00 mV ofst
-20.3 mV
27.3 mV
Δy 47.6 mV
Zeitbasis 0.0 ms
5.00 ms/div
2.00 MS 40 MS/s
Trigger C3 DC
Stop 50.0 mA
Flanke Negativ
LeCroy
04.12.2007 09:10:25

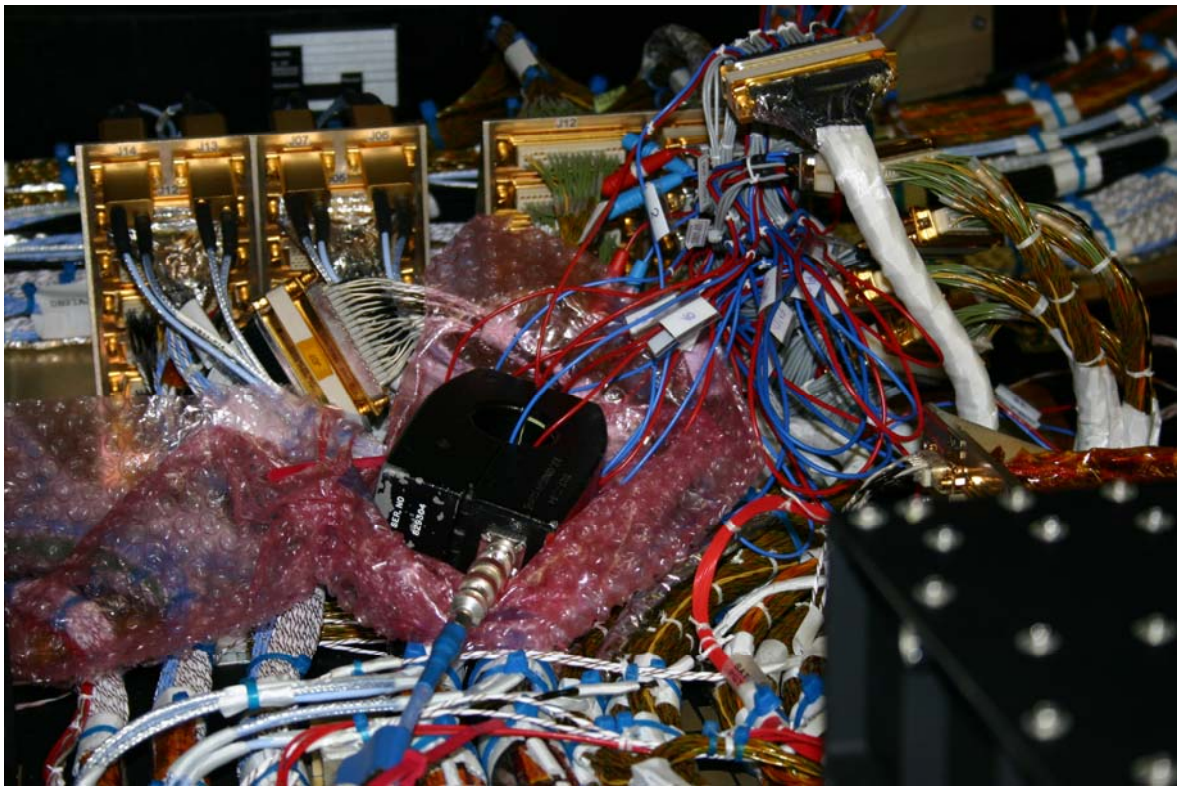

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
C4 AC
500 µV/div
-2.000 mV
1.170 mV
2.370 mV
Δy 1.200 mV
Zeitbasis 0.0 ms
5.00 ms/div
2.00 MS 40 MS/s
Trigger C3 DC
Stop 50.0 mA
Flanke Negativ
LeCroy
04.12.2007 09:00:53


Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
C4 AC
500 µV/div
-2.000 mV
550 µV
2.950 mV
Δy 2.400 mV
Zeitbasis 0.00 ms
2.00 ms/div
2.00 MS 100 MS/s
Trigger C3 DC
Stop 1.388 A
Flanke Negativ
LeCroy
04.12.2007 08:43:59


Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C3
C3 DC
10.0 mA/div
0.00 mA ofst
-200 µA
8.2 mA
Δy 8.4 mA
Zeitbasis 0.00 ms
2.00 ms/div
2.00 MS 100 MS/s
Trigger C3 DC
Stop 50.0 mA
Flanke Negativ
LeCroy
04.12.2007 08:48:37

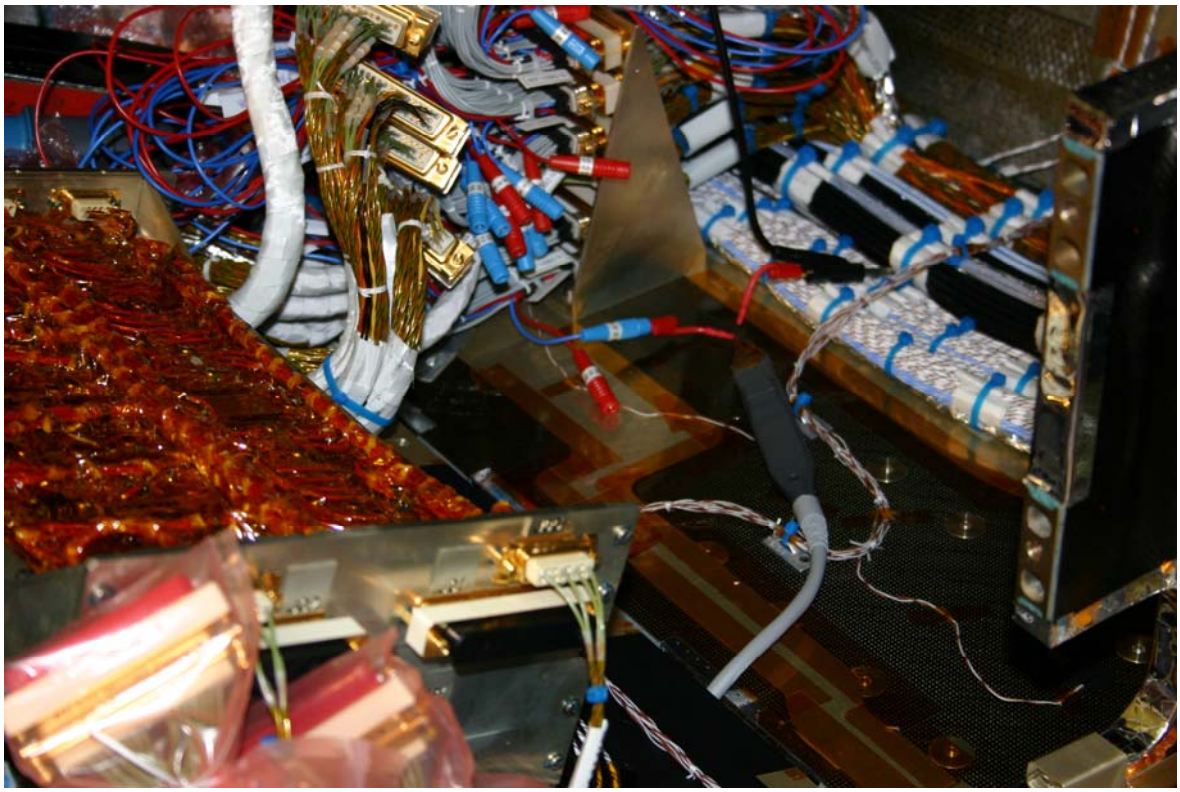

Datei Vertikal Zeitbasis Trigger Anzeige Cursor Messung Mathe Analyse Utilities Hilfe
C4
C4 AC
10.0 mV/div
-2.00 mV ofst
-18.9 mV
15.6 mV
Δy 34.5 mV
Zeitbasis 0.00 ms
2.00 ms/div
2.00 MS 100 MS/s
Trigger C3 DC
Stop 23.2 mV
Flanke Negativ
LeCroy
04.12.2007 13:30:37

Photos:



picture 1 CM Current measurement FD on power lines

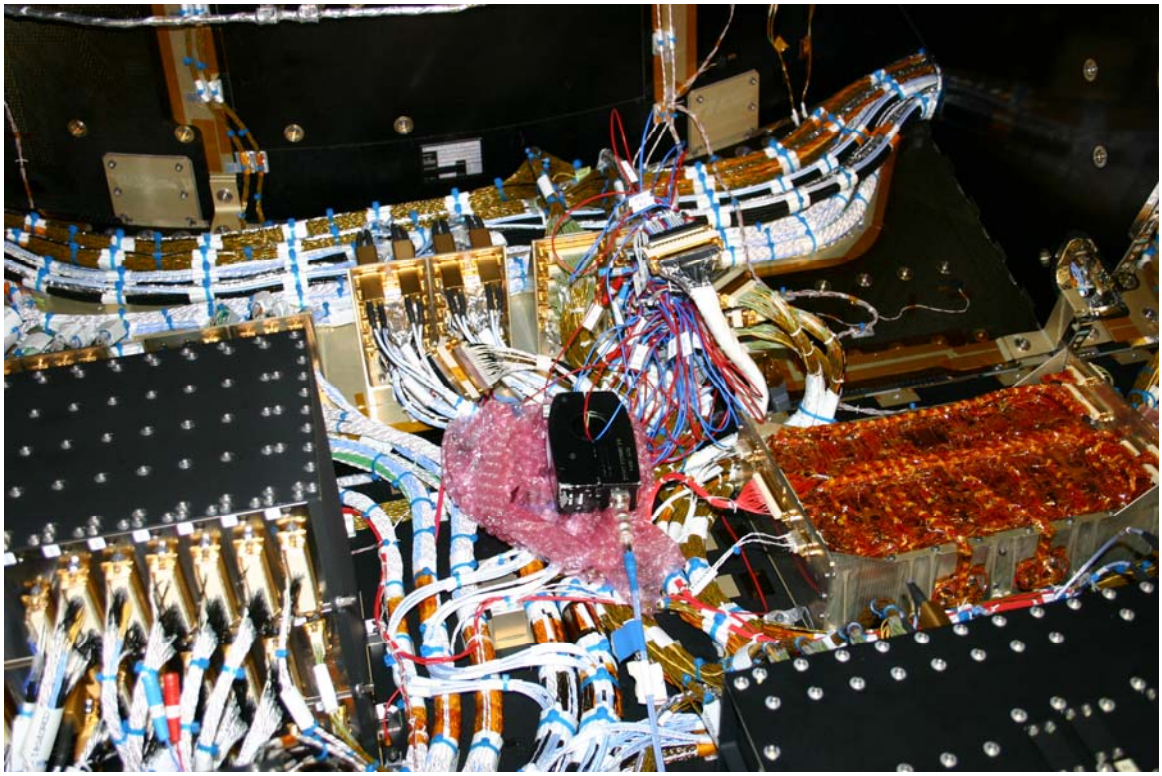
picture 2 CM Current measurement FD on power lines



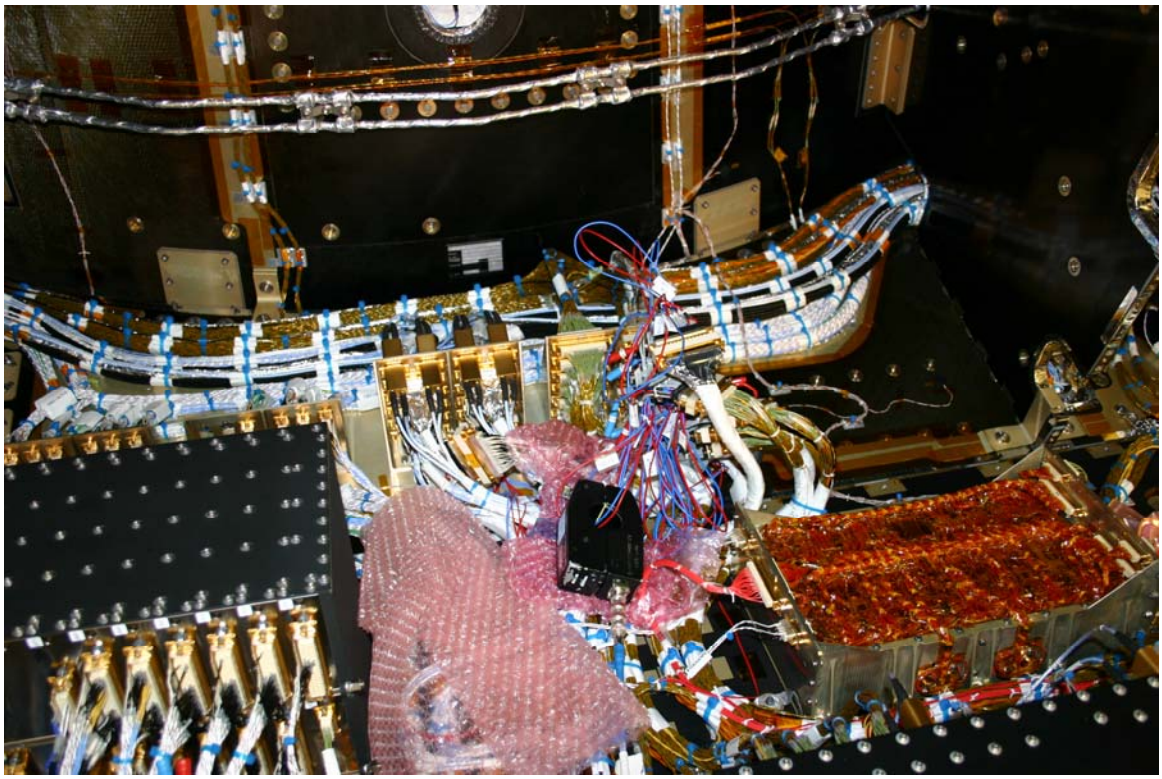
picture 3 CM Voltage measurement TD

picture 4 CM Current measurement TD on power lines

- Test 1.12.07



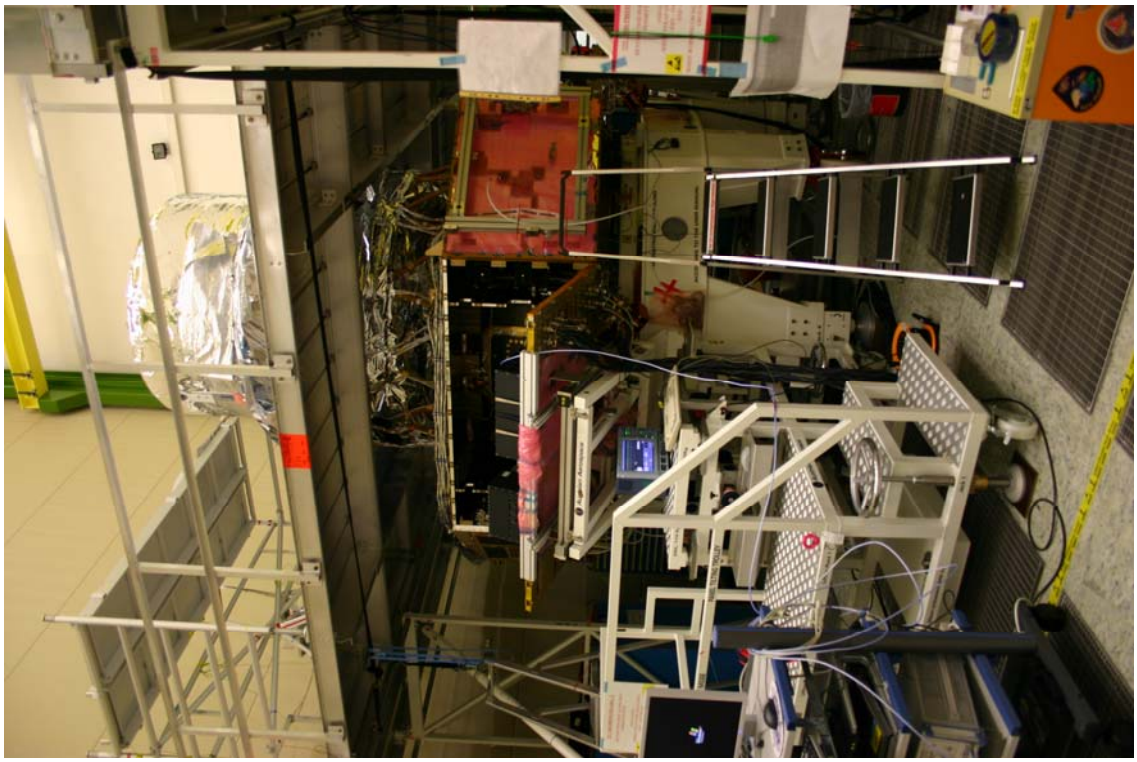
picture 5 CM Current measurement FD



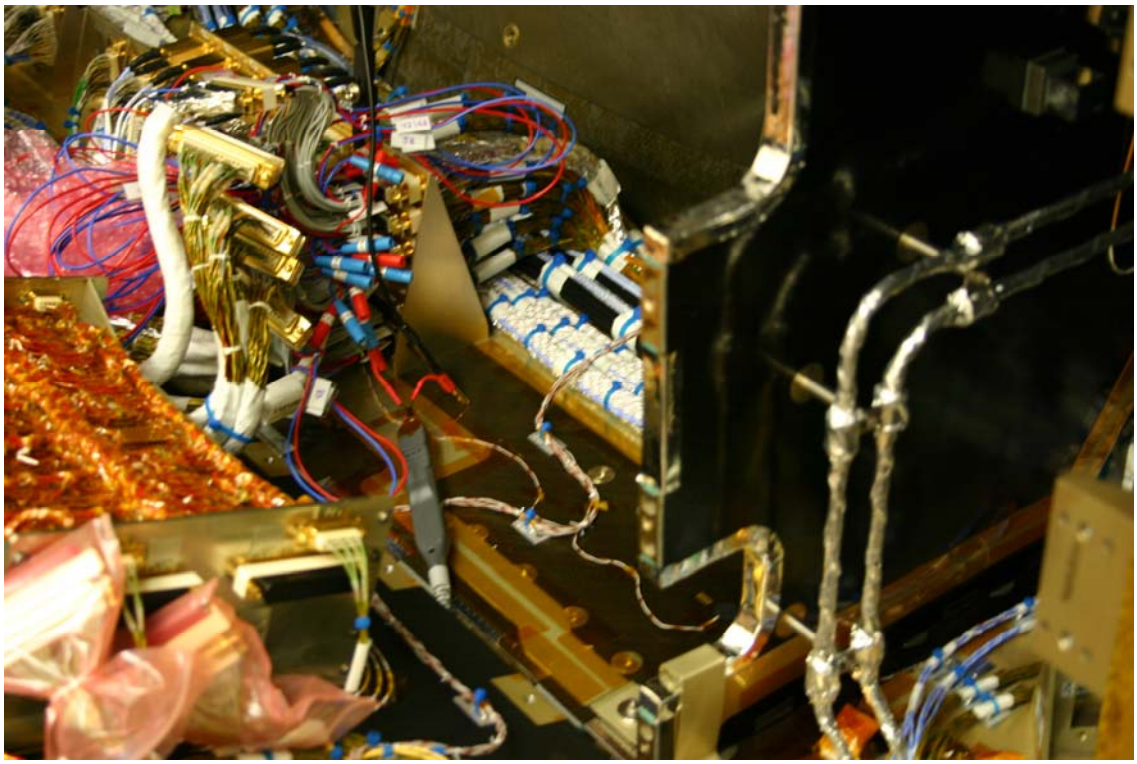
picture 6 CM Current measurement FD

- Test 3.12.07

picture 7 Current measurement TD on ground line

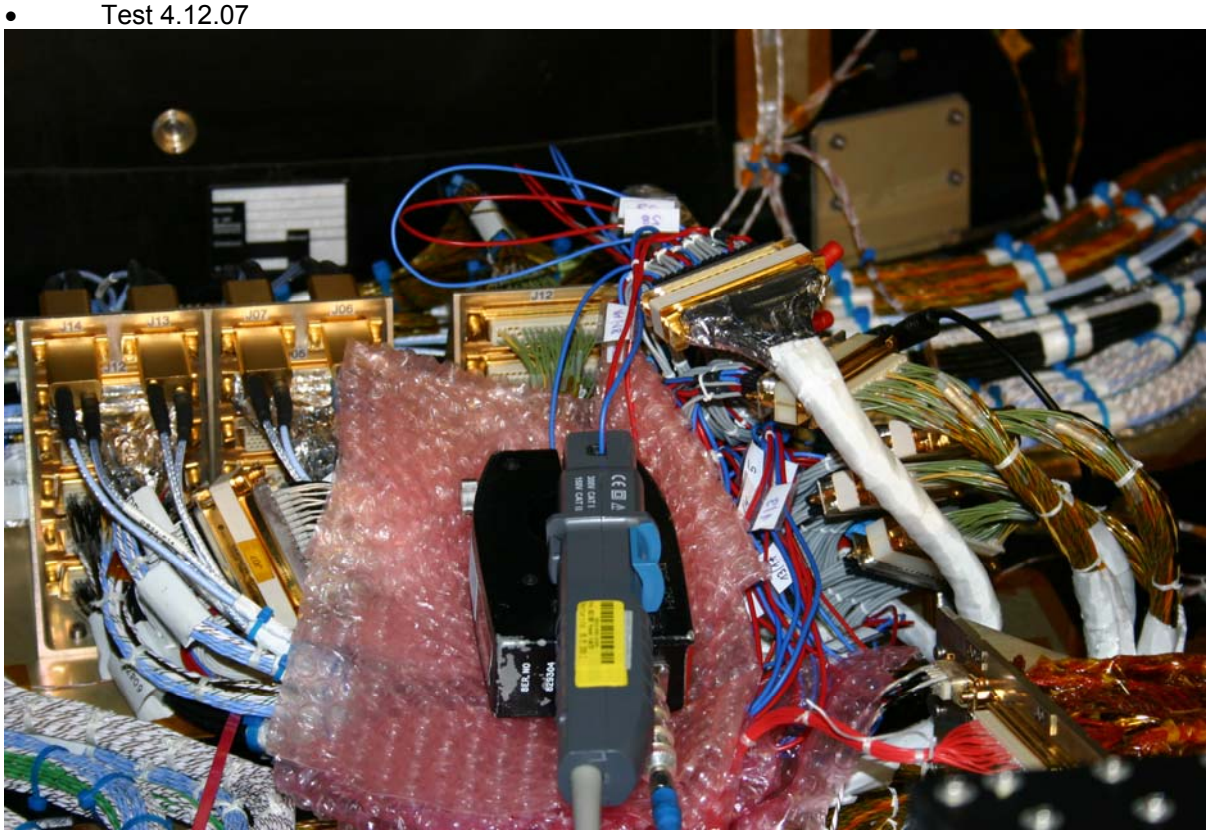


picture 8 measurement setup overview



picture 9 Voltage measurement TD on signal line

- Test 4.12.07



picture 10 Current measurement TD and FD

Annex 3

Functional AIT 'AS-RUN' Test Report

Content:

1. Filled-in 'HERSCHEL EGSE, Satellite & Instrument Procedure for the SAT EMC CE Test in Warm Conditions'; HP-2-ASED-PR-0100, Issue 1
2. Session Record
3. Start Configuration
4. End Configuration
5. Difference Configuration Report
6. Functional AIT Log Book

212 pages

Functional AIT Test Report

Table of Content

1. AS RUN Herschel EGSE, Satellite & Instrument Procedure for the SAT EMC CE Test in Warm Conditions

AS RUN

01/12/07 + 03/12/07 + 04/12/07

OK R. Vasco HO
script corrig. added

Title: **Herschel EGSE, Satellite & Instrument Procedure for the SAT EMC CE Test in Warm Conditions**

CI-No:

Prepared by:	Functional AIT team	Date: 27.11.2007
Checked by:	Koelle, Hohn	29.11.07
Product Assurance:	for Stritter	30.11.07
Configuration Control:	for Wietbrock	30.11.07.
Project Management:	Dr. Fricke	30/11/2007

Distribution: See Distribution List (last page)

Test Director TAS-F B GOBILLOT 30/11/2007

Issue	Date	Sheet	Description of Change	Release
Issue 1.0	27.11.2007		Initial version	

Table of Content

Table of Content

1 Scope

1.1 Objective

This Procedure details the EGSE and Satellite activities to be performed during the Herschel S/C level Conducted EMC Tests (reference, noisiest, sensitive).

1.2 Operational Flow

Chapter 8 provides the detailed step-by-step test procedure.

2 Documents/Drawings

This document incorporates, by dated or undated references, provisions from other publications. These normative references are cited at appropriate places in the text and publications are listed hereafter. For dated references, subsequent amendments to or revisions of any of these apply to this document only when incorporated into it by amendment or revision. For undated references, the latest edition of the publication referred to apply.

2.1 Applicable Documents

AD-1	Herschel S/C level Conducted EMC Test Procedure	H-P-2-ASED-TP-0155
AD-2	Herschel SAT Emergency Switch Off Procedure	H-P-2-ASED-PR-071

2.2 Reference Documents

RD-1	Herschel PCDU & CDMS nominal switch on / off procedure	HP-2-ASED-PR-0070
RD-2	PACS EMC Test Procedure on Integrated System Level, Issue 1.5	PACS-ME-TP-032
RD-3	HIFI conducted emissivity procedures for IST tests, Issue 1.5.3	SRON-G/HIFI/PR/2007-019
RD-4	SPIRE Warm Units EMC Conductive Emissions Procedures for IST, Issue 1.1 Redlined	SPIRE-RAL-PRC-002946
RD-5	PACS I-EGSE User Manual, Issue 1, 19-Jul-2007	PICC-ME-MN-010
RD-6	HIFI IEGSE setup procedure	SRON-U/HIFI/PR/2007-005
RD-7	SPIRE I-EGSE Set-Up, Issue 2.2	SPIRE-RAL-DOC-002841
RD-8	FIRST/PLANCK Instrument Interface Document part A	PT-IID-A-04624
RD-9	FIRST/PLANCK Instrument Interface Document part B (HIFI)	PT-IIDB/HIFI-02125
RD-10	FIRST/PLANCK Instrument Interface Document part B (PACS)	PT-IIDB/PACS-02126
RD-11	FIRST/PLANCK Instrument Interface Document part B (SPIRE)	PT-IIDB/SPIRE-02124

RD12	LO SFT Procedure using LO Dummy, Issue 1.01	MPIfR/HIFI/PR/2006-565
RD13	HIFI switch on procedure	SRON-G/HIFI/PR/200707-1.5.3
RD14	Herschel PCDU & CDMS nominal switch on / off procedure	HP-2-ASED-PR-0070

2.3 Other Documents

N/A

2.4 Acronyms

3 Requirements to be verified

N/A

4 Configuration

4.1 Herschel S/C Configuration

4.1.1 Hardware Configuration

See EMC TRR MOM H-P-TASF-MN- ,28.11.07

4.1.2 Software Configuration

The EGSE, SVM & Instruments switch on / off will be run with the following on-board software configuration:

- CDMS OBSW: 3.1.0.2
- ACMS OBSW: 3.7

4.1.3 Test Configuration

Config Control by using the tag "EMC

4.1.3.1 SVM

The tables AD-1, table 3-1, ..., 3-6 states for the different EMC test modes, REFERENCE, NOISIEST and SENSITIVE the EGSE and SVM configuration. In OFF mode the EGSE and SVM are switched OFF.

4.1.3.2 HIFI

See EMC TRR MOM H-P-TASF-MN- ,28.11.07

4.1.3.3 PACS

See EMC TRR MOM H-P-TASF-MN- ,28.11.07

4.1.3.4 SPIRE

See EMC TRR MOM H-P-TASF-MN- ,28.11.07

4.1.4 Simulated Equipments

N/A

5 Conditions

5.1 Personnel

See AD-1 chapter 4.7

5.2 Environmental

See AD-1 chapter 4.2

5.3 General Precautions and Safety

5.3.1 General Safety Requirements, Precautions

- For HIFI, Handling precautions according to RD-8 and RD-9 are applicable.
- For PACS, Handling precautions according to RD-8 and RD-10 are applicable.
- For SPIRE, Handling precautions according to RD-8 and RD-11 are applicable.

5.3.2 Special condition and hazards

The following Operational restrictions shall be carefully taken into account:

- In case of any failure, the activities shall be stopped until troubleshooting plan is generated and approved.

5.3.2.1 HIFI

Activity	Confirmed Completed	PA Sign Off	Date
Waveguide outputs must be covered with kapton tape (to prevent from dust contamination)			
For ultimate personnel safety 1 could stay 1 m away from the waveguides once the LSU output power is on.			

waveguides outputs covered with copper tape to prevent RF Leakage.

5.3.2.2 PACS

Please note, that the DEC must not be switched on !!!

5.3.2.3 SPIRE

5.3.3 ESD constraints

See AD-1, chapter 4.4.2 and

- For HIFI, ESD precautions according to RD-8 and RD-9 are applicable.
- For SPIRE
- For PACS according to nominal ESD protection

5.3.4 Special QA Requirements

N/A

5.4 GSE

5.4.1 MGSE

N/A

5.4.2 CVSE

N/A

5.4.3 EGSE

5.4.3.1 EGSE Hardware Configuration

S/S	Unit	Configuration			SCOE simulated eqpts	Remarks
		Herschel				
EGSE	CCS	1				
	TM/TC DFE	1				
	POWER SCOE	1				
	ACMS SCOE	1				

5.4.3.2 EGSE User Software

Item	Version
CCS	HPCCS 2.0-1166.
HPSDB	*HPSDB v 3.3.1.24* File: R_TM_HERSCH_FM9_711071940 with patches

5.4.3.3 Grounding Configuration

N/A

5.4.3.4 Test Equipment

N/A

5.4.3.5 Data Acquisition System

N/A

5.4.4 OGSE

N/A

5.4.5 Special Equipment

N/A

6 Verification Requirements and Test Criteria

PASS/FAIL CRITERIA

At each test stage completion, the test success is determined comparing the results obtained against the expected values.

If the compliance between obtained and expected values has been met, and authorization to proceed with the next stage of the test is given, then the actual test stage must be considered satisfactory completed.

The success of the overall testing activities is determined from the satisfactory completion of all test stages.

Successful criteria to be satisfied in each test stage shall be:

See AD-1 chapter 4.5.3

Verification that the TM(5,2), TM(5,4) and TM(1,8) received event reports are only those ones expected to fulfill the pass test criteria.

7 General Test Flow

The general test flow is defined in AD-1 table 1-1 and AD-1 chapter 7.1 & 7.2

8 Test Execution Step-by-Step Procedure

3.12.07

8.1 A Reference, Noisiest and Sensitive mode

8.1.1 A.1 Switch satellite EGSE into the REFERENCE mode Configuration according to the AD-1 configuration table in chapter 3.1 & 3.4

1/12/07

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N	
1	Login to CCS and apply a session name for EMC debugging				EMC_ref	✓		✓
2	Open proper CCS windows					✓		✓
3	Unless already on, switch on Power SCOE in cleanroom (with default settings) according RD14, chapter 7.3 and 7.4					✓		✓
4	Start the master test script **Z010999MCVT200_EMC** User info: Choose Yes or No Click in window the button YES to proceed				04.12.07 → ✓	✓		✓
5	During **Z010999MCVT200_EMC** Configuration for EMC Reference mode Click in window the button Confirm to proceed				04.12.07 → ✓	✓		✓
6	During **Y102989EPVT001_PWR_SCOE_ON_EMC** Click in window the button "End TS" to proceed					✓		

not on script

C. Mach

8.1.2 A.2 Confirm that all satellite equipment is OFF (unpowered)

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N	
7	Verify SCOE lamp = OFF				04.12.07 → LV	✓		✓
8	Verify no TM except system packet (SCOE TM)				04.12.07 → LV	✓		✓

8.1.3 A.3 Confirm that HIFI, PACS and SPIRE are OFF (unpowered)

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N	
9	Verify SCOE lamp OFF				04.12.07 → LV	✓		✓
10	Verify no TM except system packet (SCOE TM)				04.12.07 → LV	✓		✓

4/12/07 3/12/07

C. Mud

1. Dec. 2007 5:30

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N	
	During **Z010999MCVT200_EMC**							
	If verifications steps 7-10 are successful, then confirm to the EMC test conductor, that we now are in REFERENCE mode.							
11	Wait for finalization of EMC measurements.	ABORT						
	Click ABORT to end the REFERENCE mode or				4.12.07			
	Click CONTINUE to proceed with the NOISIEST and SENSITIVE mode	CONT			→ ✓			

END OF 4/12/07

8.2 B Noisiest mode

8.2.1 B.1 & B.2.1 Switch satellite EGSE + SVM into the NOISIEST mode Configuration according to the AD-1 configuration table in chapter 3.2 & 3.5

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N	
10	*During* ***Z010999MCVT200_EMC*** *Make sure, that the TMTC DFE, RF SCOE and ACMS SCOE are switched on.* *For TMTC DFE: follow RD14, chapter 7.2* *Click in window the button CONFIRM to proceed*					✓		✓
20	*During* ***Z010999MCVT200_EMC*** *Switch on S/C and SSMM in default configuration (Start noisiest Mode)* *Click in window the button CONFIRM to proceed*					✓		✓
30	At the bottom of the window, the IST_START configuration panel displays all parameters applied during the IST_START. Confirm Configuration as defined in step 40 *Click in window the button "Continue" to proceed*					✓		✓

not in script

C. Mael

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N	
40	During Z010999MCVT003_IST_START Configuration of EMC test **POWER** Battery SCOE: Simulated PCDU: A HPS: A **CCU** CCU: A & B Mode: 8s (Mode 2) **CDMS** TM OBT: A BUS: A PM: A2 PapCcs: PMAnominal **Survival Register** Bus: B Launch Straps: Not Separated PCDU: B TTR: B Tx Chain: B RFDN Switches Position: ABBB **Rx and Tx Chain** Tx Chain (Xpnd,...): A TC decoder: A TM Rate: Medium (150 kbps) RFDN Switches in use: 1&3 **SSMM** Mass Memory: A0 and B0							

Step-No.	IST_START-Step-Description	Nominal Value	Tolerance	Actual Value	*Remarks*	P	N	
50	During **Z010999MCVT003_IST_START** Check the configuration diagram and the listed parameters **"START Satellite HERSCHEL "IST_START""** ⇨ Choose "Yes"					✓		✓
60	During **Z010999MCVT097_ASDGEN_CRIT_PARS_CHECK** This script will run during the whole session to monitor critical parameters. As soon as wrong value will be detected. A popup window will occur taking the operator aware of the actual situation ⇨ Minimise this window by clicking the corresponding button (on corner top right, first button from left)					✓		✓
70	During **Z010999MCVT003_IST_START** Reply to the prompt: **"SPACECRAFT POWER_ON"** ⇨ Click in window the button "Confirm" to proceed					✓		✓

C. Muth

Step-No.	IST_START-Step-Description	Nominal Value	Tolerance	Actual Value	*Remarks*	P	N	
80	During **Z010999MCVT001_POWER_ON_HER_IST** **Set Battery** **Simulated** **Set TCDecoder to** **A** **Set PM_SW** **A2** Do you want to continue with the upper configuration: ⇨ click the button "OK" to proceed					✓		✓
90	During **Z010999MCVT001_POWER_ON_HER_IST** A Popup window occurs asking to verify on TM/TC Data Frontend: in window "System Status" ->TM chain -> TM Acquisition status is synchronised and locked -> View -> TM Transfer Frame Monitor- > arriving of TM frame data ⇨ click in window the button "OK" to proceed					✓		✓

C. Mueller

Step-No.	IST_START-Step-Description	Nominal Value	Tolerance	Actual Value	*Remarks*	P	N	
100	During **Z010999MCVT001_POWER_ON_HER_IST** A Popup Window occurs asking to start a new acquisition in Bus Monitor with name IST on the CDMU SCOE: - start a new acquisition by clicking "Menu Mode/Start new Acquisition" If an acquisition is already started, please stop and restart ⇨ click in window the button "OK" to proceed After few minutes Data transfer should be visible on the Bus Monitor,					✓		✓
110	During **D102159SCVT007PM_RESET** ⇨ Click the button "End TS!" to proceed					✓		✓
120	During **D102159SCVT032TIMESYNCRO** ⇨ Click in window the button "End TS!" to proceed					✓		✓
130	During **Z010999MCVT001_POWER_ON_HER_IST** ⇨ Click vthe button "End TS!" to proceed					✓		✓

C. Muck

Step-No.	IST_START-Step-Description	Nominal Value	Tolerance	Actual Value	*Remarks*	P	N	
140	During **Z010999MCVT003_IST_START** Reply to the prompt: **"CDMS Configuration: "** **"CROME settings PMAnominal"** If the CROME settings is in accordance with the CROME PAP/CCS of IST Configuration Table (Page), ⇨ Click in window the button "Confirm" to proceed				To be skipped for this EMC test	V		V
150	During **Z010999MCVT003_IST_START** Reply to the prompt: **"CDMS Configuration:"** **"Set configuration"** **"Bus A PCDU A HPS A TxChain A RFDN 1&3"** **"TM-OBT A TMrate Medium (150Kbps)"** ⇨ Click the button "Confirm" to proceed				To be skipped for this EMC test	V		V
160	During **Z010999MCVT003_IST_START** Reply to the prompt: **"SSMM Configuration A0 and B0"** ⇨ Click in window the button "Confirm" to proceed					V		V

C. Muel

step missing ll confirm ll

Step-No.	IST_START-Step-Description	Nominal Value	Tolerance	Actual Value	*Remarks*	P	N
170	During **D102159SCVT186_IST_SSMM_ON** Check the SSMM configuration and ⇨ Click in window the button "Continue" to proceed					✓	
180	During **Z010999MCVT003_IST_START** Reply to the prompt: **ACMS SCOE Configuration – ACMS Power ON** ⇨ Click in window the button "Confirm" to proceed					✓	
190	During **D102159SCVT186_IST_SSMM_ON** Do you want to continue with such CONFIGURATION or repeat with the definition of a new one ? ⇨ Click in window the button "Continue" to proceed					✓	
200	During **Z010999MCVT003_IST_START** Reply to the prompt: **"EAT UPLOADING"** ⇨ Click in window the button "Confirm" to proceed"				[redacted]		

not performed

c. Mach

Step-No.	IST_START-Step-Description	Nominal Value	Tolerance	Actual Value	*Remarks*	P	N
210	During **D102159SCVT192_GET_EAT_REPORT** Check that every initial entries of the Event Action Table are correctly set ⇨ Click in window the button "End TS!" to proceed				To be skipped for this EMC test		
220	During **D102159SCVT192_GET_EAT_REPORT** Check that every uploaded entries of the Event Action Table are correctly set ⇨ Click in window the button "End TS!" to proceed				To be skipped for this EMC test		
240	During **D102159SCVT192_IST_UPLOAD_EAT** ⇨ Click in window the button "End TS!" to proceed				To be skipped for this EMC test		
250	**Open the ACMS_H_BLOCK MIM Display** to verify the telemetry status updating. Configure a "Telemetry Packet History" window set with filter APID = 512					✓	

Not Asked Due to previous Skip.

c. Pluch

Step-No.	IST_START-Step-Description	Nominal Value	Tolerance	Actual Value	*Remarks*	P	N	
260	During **A102109SPVT003_ACMS_CONFIG_25** At the prompt "Enter your choice", enter **"1"** to select **"Select/Load ACMS_CONFIG Input File"** ⇨ Click in window the button "OK" to proceed					V		✓
270	During **A102109SPVT003_ACMS_CONFIG_25** Click in window the button "CONTINUE" to proceed					V		✓
280	During **A102109SPVT003_ACMS_CONFIG_25** At the prompt, **"Enter your choice: IST_EMC** ⇨ Click in window the button "OK" to proceed					V		✓
290	During **A102109SPVT003_ACMS_CONFIG_25** At the prompt "Enter your choice", enter **"6"** to select **"ACMS SCOE Configuration".** ⇨ Click in window the button "OK" to proceed					V		✓
300	During **A102109SPVT003_ACMS_CONFIG_25** ⇨ Click in window the button "Continue" to proceed					V		✓

C. Ahrd

Step-No.	IST_START-Step-Description	Nominal Value	Tolerance	Actual Value	*Remarks*	P	N	
310	During **A102109SPVT017_ACMS_CRS BACKGROUND** ⇨ Minimise this window by clicking the corresponding button (on corner top right, first button from left)					✓		✓
320	During **A102109SPVT003_ACMS_CONFIG_25** At end of **A..._ACMS_CONFIG1** verify on AND YA001939 AMCS SCOE - AS_PSEUDO 1 of 1 the parameters YMACT939 (ACMS SCOE state) YMASE939 (Simulator state) YMAMS939 (MILFE state) YMAUS939 (UIFE state)	 executing executing executing executing				✓		✓
330	During **A102109SPVT003_ACMS_CONFIG_25** At the prompt "Enter your choice",enter **"4"** to select **"ACMS Power ON (in Pre-Sep configuration)"** ⇨ click in window the button "OK" to proceed					✓		✓
340	During **A102109SPVT003_ACMS_CONFIG_25** ⇨ Click in window the button "Continue" to proceed					✓		✓

C. Muel

Step-No.	IST_START-Step-Description	Nominal Value	Tolerance	Actual Value	*Remarks*	P	N	
350	During **Z010999MCVT003_IST_START** Because the step 5-20 of the ACMS config is not performed, the ACMS will remain for this test in stand by mode At the end of **A102109SPVT011_ACMS_ON** ⇨ Click in window the button "OK" to proceed					✓		✓
360	During **Z010999MCVT003_IST_START** Ckeck that ACC is running by setting TM Packet history with filter on APID 512 and checking packets reception.					✓		✓
370	During **Z010999MCVT003_IST_START** Reply to the prompt: **"SWITCH ON CCU A&B and"** **"START MONITORING in MODE 2"** ⇨ Click in window the button "Confirm" to proceed					✓		✓
380	During **D102159SCVT186_IST_SSMM_ON** ⇨ Click in window the button "End TS!" to proceed					✓		✓

leave the window like it is 1.12.07 C. Mud

Warning: CCU DO NOT Switch ON before ACMS Press OK 01.12.07 C. Mud

C. Mud

1/12/07 3/12/07

Step-No.	IST_START-Step-Description	Nominal Value	Tolerance	Actual Value	*Remarks*	P	N	
390	During **Z010999MCVT005_IST_START_SSMM** Reply to the prompt: **"OBCP UPLOADING"** ⇨ Click in window the button "Confirm" to proceed **Let run in parallel the sequence** **D102159SCVT193_IST_UPLOAD_OBCP** and continue with next step "Packet Store Definition"				To be skipped for this EM test			✓
400	During **Z010999MCVT005_IST_START_SSMM** Reply to the prompt: **"Definition of the Packet Store"** ⇨ Click in window the button "Confirm" to proceed					✓		✓
410	If only 1 Bank (bank 0, 1, 2 or 3) is initialised on each SSMM During **D102159SCVT185_IST_PKT_STORE_DEF** When the requested SSMM bank are initialised ⇨ Click in window the button "Yes" to proceed					✓		✓
420	Only 1 Bank is initialised on SSMM A & B for this test During **D102159SCVT185_IST_PKT_STORE_DEF** ⇨ Click in window the button "End TS!" to proceed				Script failure in StorageSetDefReport Is Try to return → 2 TCs Allocate PktStore were rejected ⇒ STOP 01.12.2007			✓

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Step-No.	IST_START-Step-Description	Nominal Value	Tolerance	Actual Value	*Remarks*	P	N	
430	During **Z010999MCVT005_IST_START_SSMM** Reply to the prompt: **"Initialise of MTL Service Buffers"** ⇨ Click in window the button "Confirm" to proceed					✓		✓
440	At end of **D102159SCVT209_START_ON_BOARD_SCHEDULE** ⇨ Click in window the button "End TS" to proceed					✓		✓
450	At end of **Z010999MCVT005_IST_START_SSMM** ⇨ Click in window the button "End TS" to proceed					✓		✓
460	During **Z010999MCVT003_IST_START** Ensure that no telecommands are issued until next step. Reply to the prompt: **"CDMS CONFIGURATION:"** **"SURVIVAL REGISTER SETTING"** **"(Bus B, PCDU B, RFDN BBBB, TxChain B, TTR B, Sep Strap not Separated)"** Click in window the button "Confirm" to proceed					✓		✓

C. Ulrich

Step-No.	IST_START-Step-Description	Nominal Value	Tolerance	Actual Value	*Remarks*	P	N	
470	During **D102159SCVT175_SET_SURV_REG** ⇨ Click in window the button "End TS!" to proceed					✓		✓
480	During **Z010999MCVT003_IST_START** Reply to the prompt: **"CDMS CEL A&B DOWNLINK"** ⇨ Click in window the button "Confirm" to proceed					✓		✓
490	During **D102159SCVT080_CEL_DOWNLINK** ⇨ Click in window the button "End TS!" to proceed					✓		✓
500	During **Z010999MCVT003_IST_START** ⇨ Click in window the button "End TS!" to proceed					✓		✓
510	During **Z010999MCVT200_EMC** SAS Configuration: Transition from 1.5 A to 2 A for all section. ⇨ Click in window the button "Confirm" to proceed					✓		✓

C. Mud

Step-No.	IST_START-Step-Description	Nominal Value	Tolerance	Actual Value	*Remarks*	P	N	
520	During **Z010999MCVT200_EMC** CDMS Setting For Transition to Sunacquisiton ⇨ Click in window the button "Confirm" to proceed					✓		✓
530	During **D102159SCVT212_EMC_LAUNCH_SUNACQ** ⇨ Click in window the button "End TS!" to proceed				TWTA1 is OFF as expected ✓	✓		✓
540	During **Z010999MCVT200_EMC** Transition to Nominal ⇨ Click in window the button "Confirm" to proceed					✓		✓
550	During **D102159SCVT137_IST_SUNACQ_NOM** ⇨ Click in window the button "End TS!" to proceed					✓		✓
560	During **Z010999MCVT200_EMC** Turn on LCL 45 THR A (Verify …) ⇨ Click in window the button " Confirm!" to proceed					✓		✓

C. Mueh

Step-No.	IST_START-Step-Description	Nominal Value	Tolerance	Actual Value	*Remarks*	P	N	
570	During **Z010999MCVT200_EMC** Switch on SREM and Start ACQUISITION SERVICE Click in window the button Confirm to proceed					✓		✓
580	During **Z102999MCVT003_SREM_ACQ_START** Click in window the button "End TS" to proceed					✓		✓
590	Execute **A102109SPVT206_ACMS_RWL_EMC_SETUP** click in window the button confirm					✓		✓
600	**Select from the menu: 4 (Enable RWLs DTM)**					✓		✓
610	**N/A**				[redacted]			
620	**Select from the menu: 11 (RWL 1 switch on)**					✓		✓
630	**Select from the menu: 21 (RWL 2 switch on)**					✓		✓
640	**Select from the menu: 31 (RWL 3 switch on)**					✓		✓

C. Muel

Step-No.	IST_START-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N	
650	Select from the menu: 41 (RWL 4 switch on)					V		✓
660	Execute A102109SPVT207_ACMS_STR_EMC_SETUP STR Management click in window the button confirm					V		✓
670	Select from the menu: ..11 – STR1 Switch On					V		✓ 8:11
680	Select from the menu: ..12 – STR1 Switch Off				skipped			
690	Select from the menu: ..13 – STR1 CCD dump				"			
700	Select from the menu: ..21 – STR2 Switch On					V		✓
710	Select from the menu: ..22 – STR2 Switch Off				skipped			
720	Select from the menu: ..23 – STR2 CCD dump				"			
730	Select from the menu: ..99 – EXIT					V		✓ 8:19
740	During Z010999MCVZ154_EMC_STATUS status STR2 should be on Select NOISIEST – INST OFF status CYR2 CCL closed -> OK no current. When all OK click "End Ts" to proceed status CCL latch value wrong					V		✓ 8:29

Missing "select from menu: 99 exit"

END TS 1.12.07 C. Mück called within 8.2.7

Missing → END TS

check EMC status "confirm" 1.12.07 C. Mück

C. Mück

Step-No.	IST_START-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N
750	During **Z010999MCVT200_EMC** NOISIEST MODE for EGSE & SVM is now reached Click in window the button "OK" to proceed					V	

V 8:29

C. Muel

8.2.2 B.2.5 *Confirm that SVM & CCU equipment is operating correctly in NOISIEST mode and instruments powered in STANDBY*

Step-No.	*Test-Step-Description*	*Nominal Value*	*Tolerance*	*Actual Value*	*Remarks*	*P*	*N*	
1	*Execute **Z010999MCVZ154_EMC_STATUS*** *Click on NOISIEST*							
2	*During **Z010999MCVZ154_EMC_STATUS*** *Click on End TS to proceed*							
3.	*Inform EMC personnel that SVM & CCU is operating correctly in NOISIEST mode and instruments are powered in STANDBY*							not performed

8.2.3 *PACS Instrument Procedures*

8.2.3.1 PACS OFF to Standby (SAFE)

The following will switch ON and configure PACS Prime instrument in SAFE mode. HKTM packets will be generated on APIDs 1152 dec and 1154 decimal (these can be observed using TMPH with corresponding filter – note however a limited number of TMPHs should be running at one time).

Step-No.	*Test-Step-Description*	*Nominal Value*	*Tolerance*	*Actual Value*	*Remarks*	*P*	*N*	
1.	**Deleted** Not required for PACS in SAFE mode							
2.	If not already on, Switch on & configure PACS I-EGSE i.a.w. **RD-5**	OK				✓		✓
3.	From HPCCS Test Conductor console issue command to connect to PACS I-EGSE **connect HPACSEGSE**	CONNECTED			AND: SYS_PARS Parameter V7528940	✓		✓
4.	**Deleted** Not required for PACS in SAFE mode							

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N	
5.	From the HPCCS test conductor console start the test script: **P102999SCVT905_ASDISTPACS_PWR_ON_N** *NB: This script will power on all PACS warm units, force boot the DPU ASW and configure the instrument to SAFE (~~Standby mode~~) as per PACS test script: PACS_Switch_On_CCS_Nominal.tcl*	OK		This comment is not related to the PACS_PWR_ON_N should be deleted.	[redacted]	✓		9:00 ✓ 12.41
6.	On HPCCS when prompted: "FM PACS Switch ON in Warm or Cold conditions, FPU connected - Select NO to abort TS if not correct" Select **YES**	YES		YES		✓		✓
7.	On HPCCS when prompted: "Set Bus Profile Back to Original Setting?" Select **NO**	NO		NO		✓		✓
8.	At prompt: "Bus Profile left unchanged" Select OK to continue	OK				✓		✓
9.	Request IEGSE Operator to confirm that PACS is in SAFE mode: DP_SPS_LINK = "ON" DP_SPL_LINK = "ON" DP_DMC_LINK = "ON" DP_SPUS_CMD = "SS ENABLED" DP_SPUL_CMD = "SS ENABLED" DP_DMC_CMD = "SS ENABLED" DP_SPUS_HK = "NEW HK"	OK		ON ON ON SS ENABLED.	AND: PA000380 003390 ~~M206400~~ → ANDPA006400 019420 001380			

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N	
	DP_SPUL_HK = "NEW HK" DP_DMC_HK = "NEW HK" SPL_DMC_LINK = "LINK ON" SPS_DMC_LINK = "LINK ON" DM_BOL_REC_PAC = incrementing - Counters for TM(1,2), TM(1,8) and NACKs shall be 0 DP_1_8_REJECTED = 0 DP_1_2_REJECTED = 0 DP_COM_DMC_NACK = 0 DP_COM_SPL_NACK = 0 DP_COM_SPS_NACK = 0 - HK parameter DP_UNIT indicates "NOMINAL DPU"			NEW HK. LINK ON OK # 13802 0 0 NOMINAL.	01.12.07 → AND: PACQ 4 400	✓	✓	9:10 12.47
10.	**PACS in SAFE mode.** **Return to EMC Procedure AD-1**							

8.2.3.2 Transition from Standby to PACS Noisiest Mode

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N	
1.	***DELETED***							
2.	***DELETED***							
3.	***DELETED***							
4.	***DELETED***							

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N
5.	***DELETED***						
6.	***DELETED***						
7.	***DELETED***						
8.	***DELETED***						
9.	**Notify EMC Test Conductor that PACS is configured in its noisiest mode for test. Return to EMC procedure AD-1**	OK					

8.2.3.3 Transition from PACS Noisiest Mode to Standby

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N
1.	*DELETED*						
2.	*DELETED*						
3.	**PACS in SAFE mode Return to EMC procedure AD-1**						

8.2.3.4 PACS Standby (SAFE) to OFF

Step-No.	*Test-Step-Description*	*Nominal Value*	*Tolerance*	*Actual Value*	*Remarks*	*P*	*N*
1.	From the HPCCS test conductor console start the test script: **P102999SCVT906_ASDISTPACS_PWR_OFF_N** *NB: This script will power off all PACS warm units as per PACS test script: PACS_Switch_Off_CCS_Nominal.tcl*	OK			Please note, DEC was not switched on	✓	
2.	On HPCCS when prompted: " `FM PACS Switch OFF in Warm or Cold conditions, FPU connected - Select NO to abort TS if not correct`" Select **YES**	YES				✓	
3.	Note: During switch off of PACS (5,2) are expected					✓	
4.	From HPCCS Test Conductor console issue command to disconnect PACS I-EGSE **disconnect HPACSEGSE**	YES 2 8910 0 DISCONNECTED			**AND: SYS_PARS**	✓	
5.	**PACS OFF.** **Return to main EMC procedure AD-1**	OK				✓	

19:19

19:29

8.2.4 SPIRE Instrument Procedures

8.2.4.1 SPIRE OFF to Standby (REDY)

SPIRE I-EGSE Switch ON

Step-No.	*Test-Step-Description*	*Nominal Value*	*Tolerance*	*Actual Value*	*Remarks*	*P* (1/12/07)	*N*	(3/12/07)
1.	Confirm that I-EGSE is connected to the HPCCS					✓		✓
2.	If not already on, switch on HPCCS, SCOEs and Satellite / SVM and configure into Basic Test Mode					✓		✓
3.	Record Test Session Name:					✓		✓
4.	Confirm that EGSE and Satellite are in the correct configuration					✓		✓
5.	If not already selected, from HPCCS command CDMU to use SPIRE Bus Profile (Profile 3): **DC819160** SelectActiveSCBP: **DH049160=3**					✓		✓
6.	Verify correct bus profile selected: **DEF5F160**					✓		✓
7.	Confirm SPIRE I-EGSE is in correct configuration					✓		✓
8.	From HPCCS Test Conductor console issue command to connect to SPIRE-IEGSE **Connect HSPIREEGSE**				che ck Y2S2 9 9 4C	✓		✓
9.	Confirm from HPCCS and SPIRE I-EGSE that the connected has been established					✓		✓
10.	On HPCCS start the following test script: **ALL_SubscribeParams.tcl**					✓		✓

The following will switch ON and configure SPIRE Prime instrument in REDY (Standby) mode. HKTM packets will be generated on APIDs 1280 dec and 1282 decimal (these can be observed using TMPH with corresponding filter – note however a limited number of TMPHs should be running at one time).

Step-No.	*Test-Step-Description*	*Nominal Value*	*Tolerance*	*Actual Value*	*Remarks*	*P*	*N*
1	**Deleted**						
2	If not already on, Switch on & configure SPIRE I-EGSE i.a.w. **RD-7** (should be identical to I-EGSE switch on, see steps 1-10 above)	OK				✓	✓
3	From HPCCS Test Conductor console Issue command to connect to SPIRE I-EGSE Connect HSPIREEGSE ⇨ connected ⇨ YZES29940 AND SYS_PARS					✓	✓
4	Verify HPCCS-IEGSE connection by sending the following test command from manual command stack (repeater value 0) and verify received OK on IEGSE: **YC00X966**	OK replaced by check of "gateway" status on IEGSE.	deleted, compliant with HCS .. SD-0214	confirmed from Sunil 28.Nov.2007			
new 4	check on SPIRE IEGSE screen that in the "Gateaways" window both "ROUTER" and "PIPE" indications are "green"					✓	✓

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N
5	From the HPCCS test conductor console start the test script: **S102999SCVT901_ASDDBGSPIR_PWR_ON_P** *NB: This script will power on all SPIRE warm units, force boot the DPU ASW and configure the instrument to Standby including load of VM tables*	OK				✓	
6	On HPCCS when prompted: "SPIRE Switch ON for IST Debug only in warm conditions - Select NO to abort TS if not correct" Select **YES**	YES	Out of limits are observed after FORCE_BOOT_SECONDARY. 03.12	TM Failure for SM_OV520 (SCUPSV)		✓	
7	On HPCCS when prompted: "Set Bus Profile Back to Original Setting?" Select **NO**	NO	01.12	No TM Packet received when TAP display correct value with time update		✓	
8	At prompt: "Bus Profile left unchanged" Select OK to continue	OK				✓	
9	Request IEGSE Operator to confirm that:				**AND: SA_1_559**	✓	
	THSK (SM00T500) parameter refreshing @ 0.25 Hz	OK				✓	
	TM1N and TM2N parameters are incrementing as indicated: TM1N (SMT0N500) by 2 every 4 secs TM2N (SMT1N500) by 1 every 4 secs	OK				✓	

✓
10:02
8.38
Out of Limits are observed after Boot Secondary
TM failure SCUPSV
✓
✓
✓
✓
✓

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N
	MODE parameter is set to "REDY" mode (RAW value 0x0200)	SM00M500 = 0x0200 (REDY)				√	
10	**SPIRE powered and in Standby mode Return to EMC procedure AD-1**					√	

√ 8.55 10:16

8.2.4.2 Transition from Standby to SPIRE Noisiest Mode

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N
1.	If SPIRE Prime Bus Profile not selected then send the following command from manual command stack: ***DC819160*** ***DH049160=3***	**DEF5F160=3** OK			**AND: ZAD07999**	√	
2.	From the HPCCS test conductor console start the test script: **SPIRE-IST-WU-EMC-CE-STBY2PHOTOPS**	OK				√	
3.	Request IEGSE Operator to confirm that:	OK			**ANDs: SA_1_559, SA_6_559**	√	
	THSK parameter is refreshing every second:	OK				√	

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Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N
	TM1N and TM2N parameters are incrementing as indicated: TM1N @ 0.5Hz TM2N @ 1Hz	OK				√	
	TM3N & TM5N are incrementing as indicated: TM3N @ ~18-20 Hz	OK				√	
	TM5N Incrementing by ~4-5 every 2 seconds	OK				√	
	DCUFRAMECNT (SMD1N510) and MCUFRAMECNT (SMD0N515) on the FUNCTIONAL TEST PARAMETERS AND are incrementing as indicated: DCUFRAMECNT ~18-20 Hz MCUFRAMECNT Incrementing by ~96-100 every 2 seconds	OK				√	
	Check that the MODE parameter is set to RAW value 0xFFCE for the "PHOTOPS" mode *Note that "PHOTOPS" is a dummy value for the EMC CE activities – no converted value is defined.*	SM00M500 = 0xFFCE 65486				√	
4.	Request IEGSE operator to confirm that Science data is being generated	OK	Verify at CC that: 1280 1282			√	

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N
5.	**Notify EMC Test Conductor that SPIRE is configured in its noisiest mode for test. Return to EMC procedure AD-1**	OK				✓	

8.2.4.3 Transition from SPIRE Noisiest Mode to Standby

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N
1.	From the HPCCS test conductor console start the test script: **SPIRE-IST-WU-EMC-CE-PHOTOPS2STBY**	OK					
2.	Request IEGSE Operator to confirm that:	OK			**ANDs: SA_1_559, SA_6_559**		
	that THSK parameter is refreshing every second: THSK Refreshing @ 0.25Hz	OK					
	Check that TM1N and TM2N parameters are incrementing as indicated: TM1N (SMT0N500) by 2 every 4 secs TM2N (SMT1N500) by 1 every 4 secs	OK					
	Check that TM3N (SMT2N500) & TM5N (SMT4N500) have stopped incrementing	OK					
	Check that DCUFRAMECNT (SMD1N510) and MCUFRAMECNT (SMD0N515) on the FUNCTIONAL TEST PARAMETERS AND have stopped incrementing	OK					
	Check that the MODE parameter is set to "REDY" mode (RAW value 0x0200)	SM00M500 = 0x0200 (REDY)	DEC: 512				

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N
3.	**SPIRE in Standby (REDY) mode.** **Return to EMC procedure AD-1**						

8.2.4.4 SPIRE Standby (REDY) to OFF

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N
1.	From the HPCCS test conductor console start the test script: **S102999SCVT903_ASDDBGSPIR_PWR_OFF_P** *NB: This script will power off SPIRE*	OK				√	
2.	On HPCCS when prompted: "`SPIRE Switch OFF for IST Debug only in warm conditions - Select NO to abort TS if not correct`" Select **YES**	YES				√	
3.	During Switch OFF of SPIRE the following (5,1) and (5,4) event messages on APID 1280 are expected and do not indicate a problem: a) EVID 1313 No_MCU_Response_Error b) EVID 21773 ALARM_LSMCU_DEAD					√	

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N
4.	On HPCCS when prompted: "Set Bus Profile Back to Original Setting?" Select **NO**	NO				√	
5.	At prompt: "Bus Profile left unchanged" Select OK to continue	OK					
6.	From HPCCS Test Conductor console issue command to connect to SPIRE I-EGSE **disconnect HSPIREEGSE**	Y2529&0= DISCONNECTED Y			**AND: SYS_PARS**	√	
7.	**SPIRE OFF. Return to EMC procedure AD-1**					√	

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SPIRE I-EGSE Switch OFF

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N
1	From HPCCS Test conductor console issue command to disconnect from SPIRE I-EGSE **Disconnect HSPIREEGSE**						
2	Confirm from HPCCS and SPIRE I-EGSE that the disconnection was successful						
3	**Switch off I-EGSE**						

8.2.5 *HIFI Instrument Procedures*

8.2.5.1 HIFI OFF to Standby

The following will switch ON and configure HIFI Prime instrument in Standby mode. HKTM packets will be generated on APIDs 1024 dec and 1026 decimal (these can be observed using TMPH with corresponding filter – note however a limited number of TMPHs should be running at one time).

NB: The WBS laser temperature (HM023193 HWH_Laser_T and HWV_Laser_T) may rise above a red limit in the MIB. If this occurs the test can continue. Logging is not required since the lasers will not be switched on in the CE test

Step-No.	*Test-Step-Description*	*Nominal Value*	*Tolerance*	*Actual Value*	*Remarks*	*P*	*N*
1.	Confirm LOU dummy connected to flight harness connector and configured for band 1a, and that drain resistances D1 & D2 on LOA dummy have been set correctly plus that LSU waveguide 1a is terminated with matched load as per section 4.1.3.2. Confirm that safety precautions have been applied as per section 5.3.2.1	Confirmed				V	✓
2.	If not already running from the HPCCS test conductor console execute the test script: **ALL_SubscribeParams**	OK				V	✓

03.12

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N
3.	If not already on, Switch on & configure HIFI I-EGSE i.a.w. **RD-6** Ensure HIFI I-EGSE up and running and configured according to RD-6. As part of the configuration in section 4.1, step 3 the configuration to be selected is "**Prime**" and "**FM FPU and dummy LOU**"	OK				V	
4.	From HPCCS Test Conductor console issue command to connect to HIFI I-EGSE **connect HHIFIEGSE**	Y25 27 960 CONNECTED			**AND:** **SYS_PARS**	V	
5.	Verify HPCCS-IEGSE connection by sending the following test command from manual command stack (repeater value 0) and verify received OK on IEGSE: **YC00X962**	OK				V	
6.	Patch HIFI synthetic parameters for warm conditions by executing the following scripts: **HIFIST_ASED_PatchPtvChecksum** **HIFIST_ASED_PatchTempLimits** *Note these scripts replace* HIFIST_CCS_conf_ptv_checksum due to NCR-3652	OK				V	

01.12 03.12 9.12 ✓ ✓ ✓ ✓

01.12 03.12

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N
7.	From the HPCCS test conductor console start the test script: **H102999SCVT005_ASDGENHIFI_PWR_ON_P** *NB: This script will power on all HIFI warm units, force boot the ICU ASW and configure the instrument to Standby*	OK			ANDs HA000289 HA004289	V	V V
8.	On HPCCS when prompted: "FM HIFI Switch ON for Functional Tests only in warm conditions with LOU or dummy - Select NO to abort TS if not correct" Select **YES**	YES		YES		V	V V

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N
	See Remarks !! On HPCCS when prompted: "Press OK when IEGSE confirms LCU status OK" Request I_EGSE operator to run the command 'LCUtable/verifyreadback' using the OBSID retrieved in the previous step. If the word PASS does not appear on the screen at the end, this is a nogo on this test procedure. If OK respond to prompt accordingly, otherwise contact SRON to investigate and resolve before continuing At prompt to record OBS_ID_per_hk during execution of following script HIFIST_Startup_LCU_table_read record value if HM003190 = 900002A5 hex (Note: at start & end value is 90000000 hex)	OK	Good → 0x 900000BE	OK.	The HIFi instrument support responsible shall be connected remotely to observe the status of the HIFI. **So he should be contacted before this test step**	✓	✓
10.	On HPCCS when prompted: "Set Bus Profile Back to Original Setting?" Select **NO**	NO	read → 0x 900000BF	900000BF NO		✓	✓
11.	At prompt: "Bus Profile left unchanged" Select OK to continue	OK				✓	✓ 10:09
12.	**HIFI powered and in Standby mode Return to EMC procedure AD-1**	OK				✓	✓ 12.38

To observe this Step nothing will be display on Windows Log file (HIFI PWRON) should be observed and TC History

01.12.07

Msg Box LCU Status Press OK

03.12.07

01.12

02.12

performing

0x 900002D8

8.2.5.2 Transition from Standby to HIFI Noisiest Mode

01.12 03.12

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N	
1.	If HIFI Prime Bus Profile not selected then send the following command from manual command stack: ***DC819160*** ***DH049160=2***	OK			AND: ZAD07999 PAR: DEF5F160	✓		✓
2.	From the HPCCS test conductor console start the test script to set Testmode_Init band 1: **HIFIST_SFT_FCU_INIT_1**	OK				✓		✓ 11.42
3.	From the HPCCS test conductor console start the test script: **HIFIST_SFT_LO_SFT1a** *Sets Testmode_LO_SFT band 1a*	OK				✓		✓
4.	Check **HL_Channel_S** to verify correct band has been selected:	**HM003194=** 1a			AND: HA003289	✓		✓
5.	If selected band is not "1a", (HM003194 is meant here) then start the test script: **HIFIST_SFT_LCU_switch_off** and contact SRON (HIFI) to investigate error and resolve before continuing, otherwise continue with next step	OK			Band is 1a	✓		✓

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Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N
6.	From the HPCCS test conductor console start the test script: **HIFIST_SFT_WBS_Lasers_off** *Sets Testmode_WBS_config band 1a laser_H Lasers_off laser_V Lasers_off*	OK				✓	
7.	From the HPCCS test conductor console start the test script: **HIFIST_SFT_HRS_tune** *Sets Testmode_HRS_tune band 1a hrs_mode_h wb1 hrs_mode_v wb1 integ_time 4 backend hrs*	OK				✓	
8.	**Notify EMC Test Conductor that HIFI is configured in its noisest mode for test Return to EMC procedure AD-1**	OK				✓	

✓ 11.52
13:46

✓

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03.12.

8.2.5.3 Transition from HIFI Noisiest Mode to Standby

Step-No.	*Test-Step-Description*	*Nominal Value*	*Tolerance*	*Actual Value*	*Remarks*	*P*	*N*
1.	From the HPCCS test conductor console start the test script to set Testmode_LCU_switchoff: **HIFIST_SFT_LCU_switch_off**	OK				√	√
2.	From the HPCCS test conductor console start the test script: **HIFIST_Startup_FPU_standby** Sets Testmode_FPU_standby_hbb_off	OK				√	√
3.	From the HPCCS test conductor console start the test script: **HIFIST_Startup_HRS_standby** Sets Testmode_HRS_standby band 0	OK				√	√
4.	From the HPCCS test conductor console start the test script: **HIFIST_Startup_LCU_standby** Sets Testmode_LCU_standby	OK				√	√
5.	**HIFI in Standby** **Return to EMC procedure AD-1**						

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8.2.5.4 HIFI Standby to OFF

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N
1.	From the HPCCS test conductor console start the test script: **H102999SCVT006_ASDGENHIFI_PWR_OFF_P** *NB: This script will power off all HIFI warm units after first commanding to Standby*	OK				√	√
2.	On HPCCS when prompted: "FM HIFI Prime Switch OFF for Functional Tests only in warm conditions - Select NO to abort TS if not correct" Select **YES**	YES				√	
3.	On HPCCS when prompted: "Press OK when IEGSE confirms LCU status OK" respond accordingly	OK			NOT CONFIG P		
4.	On HPCCS when prompted: "Set Bus Profile Back to Original Setting?" Select **NO**	NO				√	√
5.	At prompt: "Bus Profile left unchanged" Select OK to continue	OK				√	√

19:10 Dk Dk ← Dk

Markisch

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N
6.	From HPCCS Test Conductor console issue command to connect to HIFI I-EGSE **disconnect HHIFIEGSE**	YZSZ740 DISCONNECTED			AND: SYS_PARS	✓	✓
7.	**HIFI OFF** **Return to EMC procedure AD-1**	OK				✓	✓

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8.2.6 *RWLs 1-4 in noisiest mode for ca. 20 minutes*

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N
1	**Using A102109SPVT206_ACMS_RWL_EMC_SETUP**					√	
2	**Select from the menu: 45 (Apply torque commands clockwise to all RWLs)**					√	

NOTE: Wheels will spin down to zero (with friction) after TBD minutes

8.2.7 Switch STR 1 to dumping mode (CCD) and STR 2 to Standby mode

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N
1	Execute A... STR1_DUMPING A102108SPVT207_ACTIS_STR_ETIC_SETUP					V 14:24	V 15:25
2	During A... STR1_DUMPING **Select from menu: 13 (STR1 CCD Dump)** This puts STR1 in dump mode which takes ca. 1 ½ h					V	V
3	Execute A... STR2_STANDBY						
4	**Stop CCD Dumping**					✓	✓

04.12.
03.12.
14:20
22:33
18:30
19:22
REMEMBER FROM STEP 1
(HOUSEKEEPING TM WAIT)
SEE LOG BOOK!
Marksch

8.3 C. Sensitive mode

IF EGSE & S/C is in NOISIEST Mode

8.3.1 C.2. Satellite SENSITIVE mode Follow procedure RD5 for the satellite subsystems and for CCU specifics. Confirm that all satellite equipment is operating correctly in SENSITIVE mode

Step-No.	*Test-Step-Description*	*Nominal Value*	*Tolerance*	*Actual Value*	*Remarks*	*P*	*N*
1.	*IF the wheels are spun down (see note above), then we are in SENSITIVE mode*						

not performed

8.3.2 C.1.1 Confirm that all satellite equipment is operating correctly in SENSITIVE mode

Step-No.	*Test-Step-Description*	*Nominal Value*	*Tolerance*	*Actual Value*	*Remarks*	*P*	*N*
1	*Execute script* ***Z..EMC_STATUS*** *Click on SENSITIVE*						
2.	*Inform EMC personnel that we are in SENSITIVE mode*						

Step 0: from Master ship ... EMC, click the button "OK" to proceed with AE
(when the EMC Test is prepared)

8.4 D Off mode

8.4.1 D.4 Satellite OFF

8.4.1.1 D.4.1 Follow procedure for the satellite and for the CCU

01.12. 03.12.

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N
1	*During Z010999MCVT200_EMC* *Disable monitoring and switch off CCU* *Click Confirm to proceed*					V	V
2	**Using A102109SPVT206_ACMS_RWL_EMC_SETUP**						V
3	**Ensure that all RWLs are spun down to zero**						V
4	**Select from the menu: 12 (RWL 1 switch of)**						V

Skipped

Markisch

19:27

JM JM JM

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks			P	N	
5	**Select from the menu: 22 (RWL 2 switch of)**						✓			JM
6	**Select from the menu: 32 (RWL 3 switch of)**					skipped	✓			JM
7	**Select from the menu: 42 (RWL 4 switch of)**					skipped	✓			JM
7.1	Dwg x.. RWL-ENC Ewn 99 To Terminate	+ Ewn TS!					v			JM
8	*During A.....ACMS25* *Enter 99 to terminate ACMS config*							V	✓	JM
9	*At prompt:* *"Choose one of the following options:* *- Modify current selection* *- Confirm current selection* *Press the relevant button below"* *Select **Continue***							V	✓	JM
10	*During A.....ACMS25* *Enter 1 to switch off ACMS*							V	✓	JM

Manbir d

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N	
11	*At prompt:* *"Choose one of the following options:* *- Modify current selection* *- Confirm current selection* *Press the relevant button below"* *Select* ***Continue***					√	⌣	JM
12	*During A.....ACMS25* *Enter 99 to terminate ACMS config*					√	⌣	DW
13	*At prompt:* *"Choose one of the following options:* *- Modify current selection* *- Confirm current selection* *Press the relevant button below"* *Select* ***Continue***					√	⌣	DW
14	*During Z010999MCVT200_EMC* ~~*Do you want to switch off SVM/EGSE ?*~~ *Click Yes to proceed*							DW
15	*During Z010999MCVT200_EMC* *SREM OFF* *Click Confirm to proceed*				~~Tahn on TM~~ and REF TM 160 = YES (then to no)	√	⌣	DW
16	*When prompted click on button* ***End TS*** *to proceed*					√	⌣	JM

Markus

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N
17	During Z010999MCVT200_EMC Spacecraft power off Click Confirm to proceed	click End TS!				✓	✓
18	During D102159SCVT187_IST_SSMM_OFF When prompted click on button **End TS** to proceed					✓	✓
19	During D102159SCVT001PM_SELECT When prompted click on button **End TS** to proceed					✓	✓
20	During Z010999MCVT002_POWER_OFF When prompted click on button **End TS** to proceed					✓	✓
21	During Z010999MCVT004_IST_END When prompted click on button **End TS** to proceed					✓	✓
22	During Z010999MCVT200_EMC When prompted click on button **End TS** to proceed					✓	✓
23	The background (asynchronous) sequence A102109SPVT017_ACMS_CRS_BACKGROUND should be aborted when prompted: (OS kann OS öffnen) - "Abort the TM check" - "Abort the sequence" - "Press OK to proceed"	OK					

8.4.1.2 D.4.2 Confirm that all satellite equipment is OFF (unpowered)

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N
1	Verify SCOE amber lamp (indicating if S/C powered) is OFF	OK				✓	✓
2	Verify no TM except system packet (SCOE TM)	OK				✓	✓

JM
JM

8.4.2 D.5 Switch all EGSE OFF

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N
1	Verify no TM packet from SCOE TM	OK				✓	✓

Mauldin, D

9 ANNEX

9.1 ANNEX 1

9.1.1 Script hierarchy

According to TRR MOM this shall be provided with the as run procedure

10 Summary Sheets

10.1 Procedure Variation Summary

	Test Change	Curr. No.: Date Page of	
Test designation	Test Procedure	Issue	Rev.
Test step changed	Reason for Change		
Prepared by:	Resp. Test Leader	Project Engineer	
PA/QA	Prime	Customer	

Table 10.1-1: Procedure Variation Sheet

10.2 Non Conformance Report (NCR) Summary

NCR - No.	NCR - Title	Date	Open Closed	PA sig.

Table 10.2-1: Non-Conformance Record Sheet

10.3 Sign-off Sheet

	Date	Signature
Test Manager		
Operator	5.12.	C. Much
PA Responsible		
ESA Representative		

END OF DOCUMENT

	Name	Dep./Comp.		Name	Dep./Comp.
	Alberti von Mathias Dr.	ASG22		Schweickert Gunn	ASG22
	Baldock Richard	FAE12		Sonn Nico	ASG51
	Barlage Bernhard	AED13		Steininger Eric	AED32
	Bayer Thomas	ASA42		Stritter Rene	AED11
	Brune Holger	ASA45		Suess Rudi	OTN/ASA44
	Edelhoff Dirk	AED2		Theunissen Martijn	Dutch Space
	Fehringer Alexander	ASG13		Wagner Klaus	ASG22
	Fricke Wolfgang Dr.	AED 65		Wietbrock Walter	AET12
	Geiger Hermann	ASA42		Wöhler Hans	ASG22
	Grasl Andreas	OTN/ASA44		Wössner Ulrich	ASE252
	Grasshoff Brigitte	AET12			
	Hamer Simon	Terma			
	Hendry David	Terma			
	Hengstler Reinhold	ASA42			
	Hinger Jürgen	ASG22			
	Hohn Rüdiger	AED65			
	Hölzle Edgar Dr.	AED32			
	Huber Johann	ASA42			
	Hund Walter	ASE252			
	Idler Siegmund	AED312			
	Iványi von András	FAE12			
	Jahn Gerd Dr.	ASG22			
	Kalde Clemens	ASM2			
	Kameter Rudolf	OTN/ASA42			
	Kettner Bernhard	AET42			
	Knoblauch August	AET32		Alcatel Alenia Space Cannes	AAS-F
	Koelle Markus	ASA43		Alcatel Alenia Space Torino	AAS-I
	Koppe Axel	AED312		ESA/ESTEC	ESA
	Kroeker Jürgen	AED65			
	La Gioia Valentina	Terma		**Instruments:**	
	Lang Jürgen	ASE252		MPE (PACS)	MPE
	Langenstein Rolf	AED15		RAL (SPIRE)	RAL
	Langfermann Michael	ASA41		SRON (HIFI)	SRON
	Maukisch Jan	ASA43			
	Much Christoph	ASA43			
	Müller Jörg	ASA42		**Subcontractors:**	
	Müller Martin	ASA43		Alcatel Alenia Space Antwerp	ABSP
	Peltz Heinz-Willi	ASG13		Austrian Aerospace	AAE
	Pietroboni Karin	AED65		Austrian Aerospace	AAEM
	Platzer Wilhelm	AED2		BOC Edwards	BOCE
	Reichle Konrad	ASA42		Dutch Space Solar Arrays	DSSA
	Runge Axel	OTN/ASA44		EADS Astrium Sub-Subsyst. & Equipment	ASSE
	Schink Dietmar	AED32		EADS CASA Espacio	CASA
	Schlosser Christian	OTN/ASA44		EADS CASA Espacio	ECAS
	Schmidt Rudolf	FAE12		European Test Services	ETS
	Schmidt Thomas	ASA42		Patria New Technologies Oy	PANT
	Schuler Günter	ASA42		SENER Ingenieria SA	SEN

ANNEX (17)

"Koelle, Markus" <Markus.Koelle@astrium.eads.net>
29/11/2007 14:16

To <Kevin.Goodey@esa.int>
cc <Alessandra.Gatti@esa.int>, <benoit.gobillot@thalesaleniaspace.com>, "Hopfgarten, Michael" <Michael.Hopfgarten@astrium.eads.net>,
bcc
Subject AW: HIFI LOU Read back Verification procedure.

History: This message has been replied to.

----- Message from "Koelle, Markus" <Markus.Koelle@astrium.eads.net> on Thu, 29 Nov 2007 14:16:27 +0100 -----

To: <Kevin.Goodey@esa.int>

cc: <Alessandra.Gatti@esa.int>, <benoit.gobillot@thalesaleniaspace.com>, "Hopfgarten, Michael" <Michael.Hopfgarten@astrium.eads.net>, <nicjknight@aol.com>

Subject: AW: HIFI LOU Read back Verification procedure.

Dear Kevin,

this should not be a problem to be performed by each of the team members.
The only question I have is in Step 5:
How do I get the /x/ "where /x/ is the obsid of the LCU table readback previously got?
I assume it is the decimal recorded value from step 3.

I propose to add your step by step procedure as a PVS to our ASED procedure HP-2-ASED-PR-0100.

Kind regards
Markus

Von: Kevin.Goodey@esa.int [mailto:Kevin.Goodey@esa.int]
Gesendet: Donnerstag, 29. November 2007 13:40
An: Koelle, Markus
Cc: Alessandra.Gatti@esa.int; benoit.gobillot@thalesaleniaspace.com; Hopfgarten, Michael; nicjknight@aol.com
Betreff: HIFI LOU Read back Verification procedure.
Wichtigkeit: Hoch

Markus,

In lieu of the EMC team actively investigation this procedure, May I ask that a member of your functional team attempts the attached procedure in order to report to the EMC TRR whether ASED are able to perform this task.

I suggest using the attached word document as we have improved the clarity for the operator.

Please note that in order to get a "PASS" for this test of the procedure the same OBSID needs to be used as was used for the debugging yesterday. In the event of problems call either my self or Albrecht (HIFI).

Regards,

Kevin

----- Forwarded by Kevin Goodey/estec/ESA on 29/11/2007 13:31 -----

Kevin Goodey/estec/ESA

29/11/2007 12:31

To Kevin Goodey/estec/ESA

cc Alessandra Gatti/estec/ESA@ESA, benoit.gobillot@thalesaleniaspace.com, "Hopfgarten, Michael" <Michael.Hopfgarten@astrium.eads.net>, nicjknight@aol.com

Subject Re: HIFI LOU Read back Verification procedure.Link

Please find attached a clearer step-by-step for the attached HIFI procedure. Again I would like to re-iterate the need to have this procedure attempted by ASED prior to the TRR.

In the event of problems please contact Albrecht de Jonge (HIFI) : +31 (0)653604322

Regards,

Kevin

Kevin Goodey/estec/ESA

29/11/2007 11:32

To Benoit Gobillot, nicjknight@aol.com, "Hopfgarten, Michael" <Michael.Hopfgarten@astrium.eads.net>

cc Alessandra Gatti/estec/ESA@ESA

Subject HIFI LOU Read back Verification procedure.

All,

Please find below the revised procedure for the HIFI LOU readback verification.

I would ask that the ASED functional team attempt this procedure immediately and confirm if they are able to action this check as is foreseen during the EMC test. For the TRR I would like a statement if there is a problem understanding the following procedure or if this is able to be carried out with on-call support.

Personally, reading the procedure I do not foresee that on-site support will be required for this function. TBD

8. Verify LCU table readback

This is a temporary procedure pending final installation of the pertinent java tool (AdJ 28-Nov-2007)

* Obtain the decimal obsid number for the LCU table readback to be verified.
    - The /obsid/ can be obtained as the value of telemetry parameter HM003190 OBS_ID_per_hk, read off the SCOS or CCS display HA000289 ICU_housekeeping during execution of the readback. Alternatively, you could use the value of command parameter HP000197 HIFI_OBS_ID of the telecommand HC014289 HIFI_Set_OBS_ID sent just before the series of HC018289 HIFI_non_periodic_hk_LCU telecommands that constitute the readback. The obsid must be specified in decimal.
* Make sure you're logged in as hifi on machine hhifiegse.islan
* Open terminal window
* type verifyreadback /x/ with /x/ the obsid of the LCU table readback. For example

```
/home/hifi>
/home/hifi>
/home/hifi>verifyreadback 2415919648
```

* The table readback is succesfully verified if the log file on the terminal window shows the word *PASS* in the last line, for example

```
2007.330.14.00.24.288 thread 10 herschel.hifi.misc.LcuUploadVerifier.main
INFO: PASS
/home/hifi>
```

From: Kevin.Goodey@esa.int
To: benoit.gobillot@thalesaleniaspace.com; nicjknight@aol.com; Hopfgarten, Michael <Michael.Hopfgarten@astrium.eads.net>
CC: Alessandra.Gatti@esa.int
Subject: HIFI LOU Read back Verification procedure.
Date: Thu, 29 Nov 2007 10:32 am

All,

Please find below the revised procedure for the HIFI LOU readback verification.

I would ask that the ASED functional team attempt this procedure immediately and confirm if they are able to action this check as is foreseen during the EMC test. For the TRR I would like a statement if there is a problem understanding the following procedure or if this is able to be carried out with on-call support.

Personally, reading the procedure I do not foresee that on-site support will be required for this function. TBD

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This is a temporary procedure pending final installation of the pertinent java tool (AdJ 28-Nov-2007)

- Obtain the decimal obsid number for the LCU table readback to be verified.
  - The /obsid/ can be obtained as the value of telemetry parameter HM003190 OBS_ID_per_hk, read off the SCOS or CCS display HA000289 ICU_housekeeping during execution of the readback. Alternatively, you could use the value of command parameter HP000197 HIFI_OBS_ID of the telecommand HC014289 HIFI_Set_OBS_ID sent just before the series of HC018289 HIFI_non_periodic_hk_LCU telecommands that constitute the readback. The obsid must be specified in decimal.
- Make sure you're logged in as hifi on machine hhifiegse.islan
- Open terminal window
- type verifyreadback /x/ with /x/ the obsid of the LCU table readback. For example

```
/home/hifi>
/home/hifi>
/home/hifi>verifyreadback 2415918648
```

- The table readback is succesfully verified if the log file on the terminal window shows the word *PASS* in the last line, for example

```
2007.330.14.03.34.282 thread 10 herschel.hifi.misc.LouUploadVerifier.main INFO: PASS
/home/hifi>
```

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N
1	**LCU Table Read Back**						
2	On **CCS Workstation**, during the execution of the reedback, select the Display `HA000289 ICU_housekeeping` (set the display to get decimal value)						
3	On the display `HA000289 ICU_housekeeping`, get the `"OBSID" value by recording the value of parameter:` `HM003190 OBS_ID_per_hk`	Record the value			In Decimal		
4	Log in on **IEGSE** machine **`hhifiegse.islan as hifi user.`** (username: hifi / password: SronGron)						

Step-No.	Test-Step-Description	Nominal Value	Tolerance	Actual Value	Remarks	P	N
5	Open terminal window type: verifyreadback /x/ where /x/ is the obsid of the LCU table readback previously got (For example: /home/hifi> /home/hifi> /home/hifi>verifyreadback 2415919648)						
6	*Verify that the table readback is successful by checking that the LOG FILE shows the word *PASS* in the last line* (for example 2007.330.14.00.24.288 thread 10 herschel.hifi.misc.LcuUploadVerifier.main INFO: PASS /home/hifi>) Record the logfile and status.	PASS					
7							

```
logm ============  EMC ================
logm
logm |-----> Y102989EPVT001_PWR_SCOE_ON_EMC
logm |-----> Z010999MCVT003_IST_START EMC
logm |-----|-----> async Z010999MCVT097_ASDGEN_CRIT_PARS_CHECK
logm |-----|-----|-----> D102159SCVT003DISTHERMALCONTROL
logm |-----|-----|-----> A102109SPVT061_RWL_SPINDOWN
logm |-----|-----> Z010999MCVT001_POWER_ON_HER_IST $PM $tcDec $batScoe
logm |-----|-----|-----> Y102989EPVT007_IST_PWR_SCOE_ON $configBS
logm |-----|-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----|-----|-----> D102159SCVT003DISTHERMALCONTROL
logm |-----|-----|-----> D102159SCVT007PM_RESET A
logm |-----|-----|-----|-----> D102159SCVT003DISTHERMALCONTROL
logm |-----|-----|-----> D102159SCVT007PM_RESET B
logm |-----|-----|-----|-----> D102159SCVT003DISTHERMALCONTROL
logm |-----|-----|-----> async referby timeSynchronisation
D102159SCVT032TIMESYNCRO
logm |-----|-----|-----> D102159SCVT210_CDMS_GET_ALARM_STATUS
logm |-----|-----|-----> D102159SCVT210_CDMS_GET_ALARM_STATUS
logm |-----|-----|-----> W102584EPVT007_IST_CHECK_PCDU
logm |-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----|-----|-----> R102479ECVT009_UNITS_SELECTION
logm |-----|-----> Z010999MCVT001_POWER_ON_HER_IST $PM $tcDec $batScoe
logm |-----|-----|-----> Y102989EPVT007_IST_PWR_SCOE_ON $configBS
logm |-----|-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----|-----|-----> D102159SCVT003DISTHERMALCONTROL
logm |-----|-----|-----> D102159SCVT007PM_RESET A
logm |-----|-----|-----|-----> D102159SCVT003DISTHERMALCONTROL
logm |-----|-----|-----> D102159SCVT007PM_RESET B
logm |-----|-----|-----|-----> D102159SCVT003DISTHERMALCONTROL
logm |-----|-----|-----> async referby timeSynchronisation
D102159SCVT032TIMESYNCRO
logm |-----|-----|-----> D102159SCVT210_CDMS_GET_ALARM_STATUS
logm |-----|-----|-----> D102159SCVT210_CDMS_GET_ALARM_STATUS
logm |-----|-----|-----> W102584EPVT007_IST_CHECK_PCDU
logm |-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----|-----|-----> R102479ECVT009_UNITS_SELECTION
logm |-----|-----> D102159SCVT210_GET_ALARM_STATUS
logm |-----|-----> D102159SCVT176_WRITE_CROME $papCcs 1
logm |-----|-----> D102159SCVT174_IST_REDUNDANT_CONF $bus $pcduTmTc $hps
$txChain $rfdn $tmObt $tmRate
logm |-----|-----|-----> D102159SCVT104_ENCODER_SELECT $tmObt $tm_Enc_Config
logm |-----|-----> async referby istStartSSMM Z010999MCVT005_IST_START_SSMM
$ssmm
logm |-----|-----> async A102109SPVT003_ACMS_CONFIG25
logm |-----|-----|-----> A102109SPVT004_ACMS_LOADCONFIG1
logm |-----|-----|-----> A102109SPVT003_ACMS_LAUNCHCONFIG1
logm |-----|-----|-----> ACMS_QSL_LOAD
logm |-----|-----|-----> A102109SPVT011_ACMS_ON
logm |-----|-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----|-----|-----|-----> ACMS_get_RM_status RMA
logm |-----|-----|-----|-----> ACMS_get_RM_status RMB
logm |-----|-----|-----> A102109SPVT010_ACMS_SCOE_CONFIG1
logm |-----|-----|-----|-----> async A102109SPVT017_ACMS_CRS_BACKGROUND
logm |-----|-----|-----> A102109SPVT021_ACMS_ACC_SEPARA
logm |-----|-----|-----> A102109SPVT021_ACMS_ACC_SEPARA
logm |-----|-----|-----> A102109SPVT065_ACMS_CIRSIR_04_I
logm |-----|-----|-----|-----> ACMS_event_buffer_dump sgma
logm |-----|-----|-----|-----> ACMS_event_buffer_dump sgmb
logm |-----|-----|-----|-----> ACMS_get_RM_status RMA
logm |-----|-----|-----|-----> ACMS_get_RM_status RMB
logm |-----|-----|-----> A102109SPVT069_ACMS_AV_02
logm |-----|-----|-----|-----> ACMS_get_RM_status RMA
logm |-----|-----|-----|-----> ACMS_get_RM_status RMB
logm |-----|-----|-----|-----> ACMS_event_buffer_dump sgma
logm |-----|-----|-----|-----> ACMS_event_buffer_dump sgmb
logm |-----|-----|-----> A102109SPVT070_ACMS_AV_04
logm |-----|-----|-----|-----> ACMS_event_buffer_dump sgma
```

```
logm |-----|-----|-----|-----> ACMS_event_buffer_dump sgmb
logm |-----|-----|-----|-----> ACMS_get_RM_status RMA
logm |-----|-----|-----|-----> ACMS_get_RM_status RMB
logm |-----|-----|-----> A102109SPVT020_ACMS_EE_06
logm |-----|-----|-----> shortcut_master
logm |-----|-----|-----> A102109SPVT044_RCS_MODE
logm |-----|-----|-----> A202109SPVT036_ACMS_STR_ON
logm |-----|-----|-----> A102109SPVT036_ACMS_STR_ON
logm |-----|-----|-----> A102109SPVT038_RWL_ON
logm |-----|-----|-----> ACMS_SAM_MON_P
logm |-----|-----|-----> A102109SPVT034_ACMS_SAM_MON
logm |-----|-----|-----|-----> ACMS_event_buffer_dump sgma
logm |-----|-----|-----|-----> ACMS_event_buffer_dump sgmb
logm |-----|-----|-----> A102109SPVT052_TRANSITION_TO_SCM
logm |-----|-----|-----> FOP_SAM_SCM_H
logm |-----|-----|-----> SAM_HCM_P
logm |-----|-----|-----> FOP_SAM_HCM_P
logm |-----|-----|-----> SAM_OCM_P
logm |-----|-----|-----> A102109SPVT043_TRANSITION_TO_OCM
logm |-----|-----|-----> FOP_SAM_OCM_P
logm |-----|-----|-----> FOP_SAM_OCM_H
logm |-----|-----|-----> FOP_SCM_MON_P
logm |-----|-----|-----> FOP_SCM_MON_H
logm |-----|-----|-----> A102109SPVT043_TRANSITION_TO_OCM
logm |-----|-----|-----> FOP_SCM_OCM_P
logm |-----|-----|-----> FOP_SCM_OCM_H
logm |-----|-----|-----> FOP_SAM_FALLBACK_P
logm |-----|-----|-----> A102109SPVT057_TRANSITION_TO_SAM
logm |-----|-----|-----> SCM_HCM_P
logm |-----|-----|-----> FOP_SCM_HCM_P
logm |-----|-----|-----> FOP_THRES_CHANGE_P
logm |-----|-----|-----> ACMS_HCM_MON_P
logm |-----|-----|-----> FOP_HCM_SCM_P
logm |-----|-----|-----> FOP_SAM_FALLBACK_P
logm |-----|-----|-----> A102109SPVT057_TRANSITION_TO_SAM
logm |-----|-----|-----> A102109SPVT038_RWL_ON
logm |-----|-----|-----> A102109SPVT042_RWL_SPINUP
logm |-----|-----|-----> A102109SPVT052_TRANSITION_TO_SCM
logm |-----|-----|-----> A102109SPVT061_RWL_SPINDOWN
logm |-----|-----|-----> A102109SPVT012_ACMS_OFF
logm |-----|-----|-----|-----> ACMS_get_RM_status RMA
logm |-----|-----|-----|-----> ACMS_get_RM_status RMB
logm |-----|-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----|-----|-----> A102109SPVT010_ACMS_SCOE_CONFIG1
logm |-----|-----|-----|-----> async A102109SPVT017_ACMS_CRS_BACKGROUND
logm |-----|-----|-----> A102109SPVT050_BACK_TO_PRESEP
logm |-----|-----|-----|-----> ACMS_get_RM_status RMA
logm |-----|-----|-----|-----> ACMS_get_RM_status RMB
logm |-----|-----|-----> A102109SPVT64_ACMS_RECOVERY
logm |-----|-----> Z010999MCVT153_IST_STATUS 5.8.2.4.2
logm |-----|-----|-----> ACMS_get_RM_status RMA
logm |-----|-----|-----> ACMS_get_RM_status RMB
logm |-----|-----> D102159SCVT192_IST_UPLOAD_EAT
logm |-----|-----|-----> D102159SCVT192_GET_EAT_REPORT
logm |-----|-----|-----> D102159SCVT192_GET_EAT_REPORT 1
logm |-----|-----> K102999ECVT001_ASDGENCCU_ABPWRON
logm |-----|-----|-----> K102999ECVT001_ASDGENCCU_MnDisDLC
logm |-----|-----|-----> K102999ECVT001_ASDGENCCUA_POWERON
logm |-----|-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----|-----|-----> K102999ECVT001_ASDGENCCUA_ChkEssTM
logm |-----|-----|-----> K102999ECVT001_ASDGENCCUB_POWERON
logm |-----|-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----|-----|-----> K102999ECVT001_ASDGENCCUB_ChkEssTM
logm |-----|-----> K102999ECVT001_ASDGENCCU_MnEBOTH2
logm |-----|-----> K102999ECVT001_ASDGENCCU_MnEBOTH1
logm |-----|-----> K102999ECVT001_ASDGENCCUA_POWERON
logm |-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----|-----> K102999ECVT001_ASDGENCCUA_MnEnaMd2
```

```
logm |-----|-----> K102999ECVT001_ASDGENCCUA_MnEnaMd1
logm |-----|-----> K102999ECVT001_ASDGENCCUB_POWERON
logm |-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----|-----> K102999ECVT001_ASDGENCCUB_MnEnaMd2
logm |-----|-----> K102999ECVT001_ASDGENCCUB_MnEnaMd1
logm |-----|-----> D102159SCVT175_SET_SURV_REG $busSM $pcduSM $rfdnSM $txChainSM
$ttrSM $sepStsSM
logm |-----|-----> D102159SCVT080_CEL_DOWNLINK
logm |-----> W102584SPVT102_PCDU_TRANSITION_EMC 1
logm |-----> D102159SCVT212_EMC_LAUNCH_SUNACQ
logm |-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----> D102159SCVT137_IST_SUNACQ_NOM
logm |-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----> Z102999SCVT001_SREM_ON 60
logm |-----|-----> Z102999SCVT003_SREM_ACQ_START $argv
logm |-----> Z102999SCVT011_ASDGEN_PACSPWROFF_P
logm |-----|-----> async P102999SCVT910_ASDGENPACS_PWR_OFF_N
logm |-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----> Z102999SCVT005_ASDGEN_SPIREPWROFF_P
logm |-----|-----> async S102999SCVT903_ASDDBGSPIR_PWR_OFF_P
logm |-----|-----|-----> SPIRE-IST-DBG-STBY2OFF
logm |-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----> K102999ECVT001_ASDGENCCU_MnDBOTH1
logm |-----> K102999ECVT001_ASDGENCCU_ABPWROFF
logm |-----|-----> K102999ECVT001_ASDGENCCU_MnDisDLC
logm |-----|-----> K102999ECVT001_ASDGENCCUB_POWEROFF
logm |-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----|-----> K102999ECVT001_ASDGENCCUA_POWEROFF
logm |-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----> Z102999SCVT002_SREM_OFF
logm |-----> W102584SPVT102_PCDU_TRANSITION_EMC 2
logm |-----> Z010999MCVT004_IST_END
logm |-----|-----> $swOFFsequence
logm |-----|-----> A102109SPVT061_RWL_SPINDOWN
logm |-----|-----> async referby acmsOff A102109SPVT012_ACMS_OFF
logm |-----|-----> Z102999SCVT002_SREM_OFF
logm |-----|-----> D102159SCVT174_IST_REDUNDANT_CONF A A 0 0  0 0 0
logm |-----|-----|-----> D102159SCVT104_ENCODER_SELECT $tmObt $tm_Enc_Config
logm |-----|-----> D102159SCVT175_SET_SURV_REG B B ABBB B B not
logm |-----|-----> D102159SCVT176_WRITE_CROME AB 1
logm |-----|-----> D102159SCVT181_DISABLE_PKT_STORE
logm |-----|-----> D102159SCVT187_IST_SSMM_OFF
logm |-----|-----> Y102989ETVT020_TTC_SCOE_OFF
logm |-----|-----|-----> Y102989ECVT018_TTC_TC_OP_METHOD OFFLINE
logm |-----|-----|-----|-----> Y102989ETVT017_TTC_CHECK_ROUTINE
logm |-----|-----|-----|-----> Y102989ETVT019_TTC_SCOE_ACTIVITY
logm |-----|-----> W102584SPVT101_PCDU_TRANSITION_FDIR 5
logm |-----|-----> Z010999MCVT002_POWER_OFF
logm |-----|-----|-----> D102159SCVT028SSMM_OFF
logm |-----|-----|-----> D102159SCVT001PM_SELECT B
logm |-----|-----|-----|-----> D102159SCVT003DISTHERMALCONTROL
logm |-----|-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----|-----|-----> D102159SCVT001PM_SELECT A
logm |-----|-----|-----|-----> D102159SCVT003DISTHERMALCONTROL
logm |-----|-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----|-----|-----> R102479SMXX001_XPND_HUM_TXT
logm |-----|-----|-----> Y102989EPVT002_PWR_SCOE_OFF
logm |-----|-----|-----|-----> Z010999MMXX003UNITS_CHECK_PWR_OFF
logm |-----|-----|-----|-----> Z010999MMXX003UNITS_CHECK_PWR_OFF
logm |-----|-----|-----|-----> Z010999MMXX003UNITS_CHECK_PWR_OFF
logm |-----|-----|-----> Z010999MMXX003UNITS_CHECK_PWR_OFF
logm
```

ANNEX 1 to HP-2-ASED-PR-0100 Issue 10

SCRIPT HIERARCHY FOR EMC TEST

EMC.txt

```
logm ============  EMC ================
logm
logm |-----> Y102989EPVT001_PWR_SCOE_ON_EMC
logm |-----> Z010999MCVT003_IST_START EMC
logm |-----|-----> async Z010999MCVT097_ASDGEN_CRIT_PARS_CHECK
logm |-----|-----|-----> D102159SCVT003DISTHERMALCONTROL
logm |-----|-----|-----> A102109SPVT061_RWL_SPINDOWN
logm |-----|-----> Z010999MCVT001_POWER_ON_HER_IST $PM $tcDec $batScoe
logm |-----|-----|-----> Y102989EPVT007_IST_PWR_SCOE_ON $configBS
logm |-----|-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----|-----|-----> D102159SCVT003DISTHERMALCONTROL
logm |-----|-----|-----> D102159SCVT007PM_RESET A
logm |-----|-----|-----|-----> D102159SCVT003DISTHERMALCONTROL
logm |-----|-----|-----> D102159SCVT007PM_RESET B
logm |-----|-----|-----|-----> D102159SCVT003DISTHERMALCONTROL
logm |-----|-----|-----> async referby timeSynchronisation
D102159SCVT032TIMESYNCRO
logm |-----|-----|-----> D102159SCVT210_CDMS_GET_ALARM_STATUS
logm |-----|-----|-----> D102159SCVT210_CDMS_GET_ALARM_STATUS
logm |-----|-----|-----> W102584EPVT007_IST_CHECK_PCDU
logm |-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----|-----|-----> R102479ECVT009_UNITS_SELECTION
logm |-----|-----> Z010999MCVT001_POWER_ON_HER_IST $PM $tcDec $batScoe
logm |-----|-----|-----> Y102989EPVT007_IST_PWR_SCOE_ON $configBS
logm |-----|-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----|-----|-----> D102159SCVT003DISTHERMALCONTROL
logm |-----|-----|-----> D102159SCVT007PM_RESET A
logm |-----|-----|-----|-----> D102159SCVT003DISTHERMALCONTROL
logm |-----|-----|-----> D102159SCVT007PM_RESET B
logm |-----|-----|-----|-----> D102159SCVT003DISTHERMALCONTROL
logm |-----|-----|-----> async referby timeSynchronisation
D102159SCVT032TIMESYNCRO
logm |-----|-----|-----> D102159SCVT210_CDMS_GET_ALARM_STATUS
logm |-----|-----|-----> D102159SCVT210_CDMS_GET_ALARM_STATUS
logm |-----|-----|-----> W102584EPVT007_IST_CHECK_PCDU
logm |-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----|-----|-----> R102479ECVT009_UNITS_SELECTION
logm |-----|-----> D102159SCVT210_GET_ALARM_STATUS
logm |-----|-----> D102159SCVT176_WRITE_CROME $papCcs 1
logm |-----|-----> D102159SCVT174_IST_REDUNDANT_CONF $bus $pcduTmTc $hps
$txChain $rfdn $tmObt $tmRate
logm |-----|-----|-----> D102159SCVT104_ENCODER_SELECT $tmObt $tm_Enc_Config
logm |-----|-----> async referby istStartSSMM Z010999MCVT005_IST_START_SSMM
$ssmm
logm |-----|-----> async A102109SPVT003_ACMS_CONFIG25
logm |-----|-----|-----> A102109SPVT004_ACMS_LOADCONFIG1
logm |-----|-----|-----> A102109SPVT003_ACMS_LAUNCHCONFIG1
logm |-----|-----|-----> ACMS_QSL_LOAD
logm |-----|-----|-----> A102109SPVT011_ACMS_ON
logm |-----|-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----|-----|-----|-----> ACMS_get_RM_status RMA
logm |-----|-----|-----|-----> ACMS_get_RM_status RMB
logm |-----|-----|-----> A102109SPVT010_ACMS_SCOE_CONFIG1
logm |-----|-----|-----|-----> async A102109SPVT017_ACMS_CRS_BACKGROUND
logm |-----|-----|-----> A102109SPVT021_ACMS_ACC_SEPARA
logm |-----|-----|-----> A102109SPVT021_ACMS_ACC_SEPARA
logm |-----|-----|-----> A102109SPVT065_ACMS_CIRSIR_04_I
logm |-----|-----|-----|-----> ACMS_event_buffer_dump sgma
logm |-----|-----|-----|-----> ACMS_event_buffer_dump sgmb
logm |-----|-----|-----|-----> ACMS_get_RM_status RMA
logm |-----|-----|-----|-----> ACMS_get_RM_status RMB
logm |-----|-----|-----> A102109SPVT069_ACMS_AV_02
logm |-----|-----|-----|-----> ACMS_get_RM_status RMA
logm |-----|-----|-----|-----> ACMS_get_RM_status RMB
logm |-----|-----|-----|-----> ACMS_event_buffer_dump sgma
logm |-----|-----|-----|-----> ACMS_event_buffer_dump sgmb
logm |-----|-----|-----> A102109SPVT070_ACMS_AV_04
logm |-----|-----|-----|-----> ACMS_event_buffer_dump sgma
```

```
logm |-----|-----|-----|-----> ACMS_event_buffer_dump sgmb
logm |-----|-----|-----|-----> ACMS_get_RM_status RMA
logm |-----|-----|-----|-----> ACMS_get_RM_status RMB
logm |-----|-----|-----> A102109SPVT020_ACMS_EE_06
logm |-----|-----|-----> shortcut_master
logm |-----|-----|-----> A102109SPVT044_RCS_MODE
logm |-----|-----|-----> A202109SPVT036_ACMS_STR_ON
logm |-----|-----|-----> A102109SPVT036_ACMS_STR_ON
logm |-----|-----|-----> A102109SPVT038_RWL_ON
logm |-----|-----|-----> ACMS_SAM_MON_P
logm |-----|-----|-----> A102109SPVT034_ACMS_SAM_MON
logm |-----|-----|-----|-----> ACMS_event_buffer_dump sgma
logm |-----|-----|-----|-----> ACMS_event_buffer_dump sgmb
logm |-----|-----|-----> A102109SPVT052_TRANSITION_TO_SCM
logm |-----|-----|-----> FOP_SAM_SCM_H
logm |-----|-----|-----> SAM_HCM_P
logm |-----|-----|-----> FOP_SAM_HCM_P
logm |-----|-----|-----> SAM_OCM_P
logm |-----|-----|-----> A102109SPVT043_TRANSITION_TO_OCM
logm |-----|-----|-----> FOP_SAM_OCM_P
logm |-----|-----|-----> FOP_SAM_OCM_H
logm |-----|-----|-----> FOP_SCM_MON_P
logm |-----|-----|-----> FOP_SCM_MON_H
logm |-----|-----|-----> A102109SPVT043_TRANSITION_TO_OCM
logm |-----|-----|-----> FOP_SCM_OCM_P
logm |-----|-----|-----> FOP_SCM_OCM_H
logm |-----|-----|-----> FOP_SAM_FALLBACK_P
logm |-----|-----|-----> A102109SPVT057_TRANSITION_TO_SAM
logm |-----|-----|-----> SCM_HCM_P
logm |-----|-----|-----> FOP_SCM_HCM_P
logm |-----|-----|-----> FOP_THRES_CHANGE_P
logm |-----|-----|-----> ACMS_HCM_MON_P
logm |-----|-----|-----> FOP_HCM_SCM_P
logm |-----|-----|-----> FOP_SAM_FALLBACK_P
logm |-----|-----|-----> A102109SPVT057_TRANSITION_TO_SAM
logm |-----|-----|-----> A102109SPVT038_RWL_ON
logm |-----|-----|-----> A102109SPVT042_RWL_SPINUP
logm |-----|-----|-----> A102109SPVT052_TRANSITION_TO_SCM
logm |-----|-----|-----> A102109SPVT061_RWL_SPINDOWN
logm |-----|-----|-----> A102109SPVT012_ACMS_OFF
logm |-----|-----|-----|-----> ACMS_get_RM_status RMA
logm |-----|-----|-----|-----> ACMS_get_RM_status RMB
logm |-----|-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----|-----|-----> A102109SPVT010_ACMS_SCOE_CONFIG1
logm |-----|-----|-----|-----> async A102109SPVT017_ACMS_CRS_BACKGROUND
logm |-----|-----|-----> A102109SPVT050_BACK_TO_PRESEP
logm |-----|-----|-----|-----> ACMS_get_RM_status RMA
logm |-----|-----|-----|-----> ACMS_get_RM_status RMB
logm |-----|-----|-----> A102109SPVT64_ACMS_RECOVERY
logm |-----|-----> Z010999MCVT153_IST_STATUS 5.8.2.4.2
logm |-----|-----|-----> ACMS_get_RM_status RMA
logm |-----|-----|-----> ACMS_get_RM_status RMB
logm |-----|-----> D102159SCVT192_IST_UPLOAD_EAT
logm |-----|-----|-----> D102159SCVT192_GET_EAT_REPORT
logm |-----|-----|-----> D102159SCVT192_GET_EAT_REPORT 1
logm |-----|-----> K102999ECVT001_ASDGENCCU_ABPWRON
logm |-----|-----|-----> K102999ECVT001_ASDGENCCU_MnDisDLC
logm |-----|-----|-----> K102999ECVT001_ASDGENCCUA_POWERON
logm |-----|-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----|-----|-----> K102999ECVT001_ASDGENCCUA_ChkEssTM
logm |-----|-----|-----> K102999ECVT001_ASDGENCCUB_POWERON
logm |-----|-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----|-----|-----> K102999ECVT001_ASDGENCCUB_ChkEssTM
logm |-----|-----> K102999ECVT001_ASDGENCCU_MnEBOTH2
logm |-----|-----> K102999ECVT001_ASDGENCCU_MnEBOTH1
logm |-----|-----> K102999ECVT001_ASDGENCCUA_POWERON
logm |-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----|-----> K102999ECVT001_ASDGENCCUA_MnEnaMd2
```

```
logm |-----|-----> K102999ECVT001_ASDGENCCUA_MnEnaMd1
logm |-----|-----> K102999ECVT001_ASDGENCCUB_POWERON
logm |-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----|-----> K102999ECVT001_ASDGENCCUB_MnEnaMd2
logm |-----|-----> K102999ECVT001_ASDGENCCUB_MnEnaMd1
logm |-----|-----> D102159SCVT175_SET_SURV_REG $busSM $pcduSM $rfdnSM $txChainSM
$ttrSM $sepStsSM
logm |-----|-----> D102159SCVT080_CEL_DOWNLINK
logm |-----> W102584SPVT102_PCDU_TRANSITION_EMC 1
logm |-----> D102159SCVT212_EMC_LAUNCH_SUNACQ
logm |-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----> D102159SCVT137_IST_SUNACQ_NOM
logm |-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----> Z102999SCVT001_SREM_ON 60
logm |-----|-----> Z102999SCVT003_SREM_ACQ_START $argv
logm |-----> Z102999SCVT011_ASDGEN_PACSPWROFF_P
logm |-----|-----> async P102999SCVT910_ASDGENPACS_PWR_OFF_N
logm |-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----> Z102999SCVT005_ASDGEN_SPIREPWROFF_P
logm |-----|-----> async S102999SCVT903_ASDDBGSPIR_PWR_OFF_P
logm |-----|-----|-----> SPIRE-IST-DBG-STBY2OFF
logm |-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----> K102999ECVT001_ASDGENCCU_MnDBOTH1
logm |-----> K102999ECVT001_ASDGENCCU_ABPWROFF
logm |-----|-----> K102999ECVT001_ASDGENCCU_MnDisDLC
logm |-----|-----> K102999ECVT001_ASDGENCCUB_POWEROFF
logm |-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----|-----> K102999ECVT001_ASDGENCCUA_POWEROFF
logm |-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----> Z102999SCVT002_SREM_OFF
logm |-----> W102584SPVT102_PCDU_TRANSITION_EMC 2
logm |-----> Z010999MCVT004_IST_END
logm |-----|-----> $swOFFsequence
logm |-----|-----> A102109SPVT061_RWL_SPINDOWN
logm |-----|-----> async referby acmsOff A102109SPVT012_ACMS_OFF
logm |-----|-----> Z102999SCVT002_SREM_OFF
logm |-----|-----> D102159SCVT174_IST_REDUNDANT_CONF A A 0 0  0 0 0
logm |-----|-----|-----> D102159SCVT104_ENCODER_SELECT $tmObt $tm_Enc_Config
logm |-----|-----> D102159SCVT175_SET_SURV_REG B B ABBB B B not
logm |-----|-----> D102159SCVT176_WRITE_CROME AB 1
logm |-----|-----> D102159SCVT181_DISABLE_PKT_STORE
logm |-----|-----> D102159SCVT187_IST_SSMM_OFF
logm |-----|-----> Y102989ETVT020_TTC_SCOE_OFF
logm |-----|-----|-----> Y102989ECVT018_TTC_TC_OP_METHOD OFFLINE
logm |-----|-----|-----|-----> Y102989ETVT017_TTC_CHECK_ROUTINE
logm |-----|-----|-----|-----> Y102989ETVT019_TTC_SCOE_ACTIVITY
logm |-----|-----> W102584SPVT101_PCDU_TRANSITION_FDIR 5
logm |-----|-----> Z010999MCVT002_POWER_OFF
logm |-----|-----|-----> D102159SCVT028SSMM_OFF
logm |-----|-----|-----> D102159SCVT001PM_SELECT B
logm |-----|-----|-----|-----> D102159SCVT003DISTHERMALCONTROL
logm |-----|-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----|-----|-----> D102159SCVT001PM_SELECT A
logm |-----|-----|-----|-----> D102159SCVT003DISTHERMALCONTROL
logm |-----|-----|-----|-----> Z010999MMXX002UNITS_CHECK
logm |-----|-----|-----> R102479SMXX001_XPND_HUM_TXT
logm |-----|-----|-----> Y102989EPVT002_PWR_SCOE_OFF
logm |-----|-----|-----|-----> Z010999MMXX003UNITS_CHECK_PWR_OFF
logm |-----|-----|-----|-----> Z010999MMXX003UNITS_CHECK_PWR_OFF
logm |-----|-----|-----|-----> Z010999MMXX003UNITS_CHECK_PWR_OFF
logm |-----|-----|-----> Z010999MMXX003UNITS_CHECK_PWR_OFF
logm
```

EMC CE Test - Session Record

Session ID	Start Date/Time	End Date/Time	QA Approval
2007_12_1_05_16_hercdunu_hpws23_REALTIME_EMC_mois	01.12./05:16	01.12./21:30	
2007_12_3_05_00_hercdunu_hpw23_REALTIME_EMC_mois2	03.12./05:00	03.12./20:30	
2007_12_4_06_39_hercdunu_hpws22_REALTIME_EMC_mois 3	04.12./06:39	04.12./12:45	

3. Start Configuration

Start Configuration

2007_12_01_05_16_hercdmu_hpws23_REALTIME_EMC_nois_TestEnvironment_REALTIME

Files are from /HPCCS/VARIABLE/REPOSITORIES/USER

HPCCS version is hpccs-2.0-1166

WARNING:- online patches are not reflected in this
file unless added manually to the end of the file

Contents of /HPCCS/VARIABLE/ENVIRONMENTS/EXEC/LOG

ImportMIB.log	1.1	Thu Nov 16 08:30:19 2006
consistency.log	1.1	Thu Nov 16 08:25:01 2006
sessionlog	1.14492	Sat Dec 1 04:38:35 2007

Contents of /HPCCS/VARIABLE/ENVIRONMENTS/EXEC/MISC

(empty)

Contents of /HPCCS/VARIABLE/ENVIRONMENTS/EXEC/PICT

ACMS_BLOCDIAG.ilv	1.3	Fri Feb 17 10:18:11 2006
ACMS_H_BLOC.ilv	1.26	Sat Nov 10 16:05:07 2007
ASTRIUM_TEST.ilv	1.4	Tue Mar 27 11:28:07 2007
BS_PROVA.ilv	1.1	Tue Sep 13 10:14:50 2005
CCU.ilv	1.16	Sat Nov 17 17:30:14 2007
CCU_HE_TANK.ilv	1.9	Fri Oct 19 10:09:24 2007
CCU_OPT_BENCH.ilv	1.9	Wed Oct 24 07:21:44 2007
CDMU_BLOCDIAG.ilv	1.15	Mon Aug 20 12:36:52 2007
EGSE_CCU.ilv	1.1	Thu Nov 29 14:42:56 2007
EGSE_CCU_HE_TANK.ilv	1.1	Thu Nov 29 14:43:19 2007
EGSE_CCU_OPT_BENCH.ilv	1.1	Thu Nov 29 14:43:28 2007
EGSE_CONN.ilv	1.1	Tue Sep 13 10:15:19 2005
FCL.ilv	1.1	Tue Sep 13 10:15:13 2005
GEN_POWER.ilv	1.7	Mon Aug 20 12:36:33 2007
HEATERS.ilv	1.3	Wed Apr 18 09:43:15 2007
INSTRUMENTS_H.ilv	1.12	Thu Aug 30 05:10:25 2007
LCL.ilv	1.1	Tue Sep 13 10:15:07 2005
LCL_HERSCHEL.ilv	1.5	Tue Apr 10 16:00:12 2007
LCL_PLANCK.ilv	1.1	Tue Sep 13 10:15:34 2005
RCS.ilv	1.3	Mon Aug 20 12:37:10 2007
RFDN.ilv	1.1	Tue Sep 13 10:15:39 2005
SAT.ilv	1.48	Sat Nov 17 17:29:25 2007
SINOTTICO.ilv	1.1	Tue Sep 13 10:15:02 2005
TT&C_H-P.ilv	1.3	Thu Nov 23 11:46:21 2006
TT&C_HER.ilv	1.6	Tue Jan 24 11:16:44 2006
TT&C_HERSCHEL.ilv	1.2	Thu Jan 12 08:51:35 2006
TT&C_HER_2.ilv	1.9	Mon Aug 4 08:08:14 2008
TTC_H_P.ilv	1.5	Mon Sep 24 12:23:08 2007
TWTA_1.ilv	1.1	Tue Sep 13 10:15:24 2005
TWTA_2.ilv	1.1	Tue Sep 13 10:15:45 2005
XPOND1.ilv	1.1	Tue Sep 13 10:16:29 2005
XPOND2.ilv	1.1	Tue Sep 13 10:14:37 2005

Contents of /HPCCS/VARIABLE/ENVIRONMENTS/EXEC/TSEQ

A101109SPVT201_ACMS_STATUS.tcl	1.1	Tue Nov 6 15:51:59 2007
A102109ETVT000_ACMS_PROCESS.tcl	1.72	Sat Nov 3 13:37:59 2007

2007_12_01_05_16_hercdmu_hpws23_REALTIME_EMC_nois_TestEnvironment_REALTIME

A102109SPVT003_ACMS_CONFIG25.tcl	1.20	Mon Jul 10 11:18:26 2006
A102109SPVT004_ACMS_LOADCONFIG1.tcl	1.3	Mon May 15 13:43:03 2006
A102109SPVT005_ACMS_EE_04.tcl	1.10	Wed Feb 22 19:23:18 2006
A102109SPVT006_ACMS_EE_05.tcl	1.7	Wed Feb 22 19:23:04 2006
A102109SPVT007_ACMS_PT_01.tcl	1.5	Fri Jul 21 13:26:38 2006
A102109SPVT008_ACMS_HC_04.tcl	1.12	Mon Mar 13 13:49:55 2006
A102109SPVT009_ACMS_HC_05.tcl	1.8	Wed Mar 8 11:09:33 2006
A102109SPVT010_ACMS_SCOE_CONFIG1.tcl	1.44	Fri Oct 26 09:47:29 2007
A102109SPVT011_ACMS_ON.tcl	1.36	Sat Sep 29 13:51:37 2007
A102109SPVT012_ACMS_OFF.tcl	1.35	Sat Nov 10 17:07:01 2007
A102109SPVT012_EMERGENCY_ACMS_OFF.tcl	1.2	Mon Dec 4 16:56:34 2006
A102109SPVT013_ACMS_EE_01.tcl	1.11	Tue Nov 27 04:22:27 2007
A102109SPVT014_ACMS_STR_Test.tcl	1.7	Mon Jun 5 07:08:43 2006
A102109SPVT015_ACMS_HC_01.tcl	1.12	Wed Oct 31 15:45:55 2007
A102109SPVT016_ACMS_EE_02.tcl	1.6	Sat Mar 4 13:43:54 2006
A102109SPVT017_ACMS_CRS_BACKGROUND.tcl	1.6	Thu Mar 22 11:42:10 2007
A102109SPVT018_ACMS_Units_Stim.tcl	1.5	Mon Jun 5 07:09:55 2006
A102109SPVT019_ACMS_HC_02.tcl	1.9	Sat Oct 14 15:08:31 2006
A102109SPVT020_ACMS_EE_06.tcl	1.4	Wed May 10 16:51:09 2006
A102109SPVT021_ACMS_ACC_SEPARA.tcl	1.29	Fri May 11 11:58:05 2007
A102109SPVT022_ACMS_SEP_DELAY.tcl	1.3	Mon May 15 13:44:52 2006
A102109SPVT028_ACMS_GYR_Test.tcl	1.2	Tue Jun 19 07:35:45 2007
A102109SPVT029_ACMS_HC_07.tcl	1.12	Tue Mar 6 17:07:57 2007
A102109SPVT030_ACMS_HC_03.tcl	1.20	Tue Dec 5 23:17:58 2006
A102109SPVT031_ACMS_EE_03.tcl	1.6	Fri Oct 27 16:11:41 2006
A102109SPVT032_DEFAULT_CONF_SEP.tcl	1.24	Tue Nov 6 12:55:34 2007
A102109SPVT034_ACMS_SAM_MON.tcl	1.12	Thu Oct 19 11:27:46 2006
A102109SPVT035_ACMS_GYRO_UFT.tcl	1.2	Mon Jun 5 07:11:38 2006
A102109SPVT035_ACMS_SEP_DELAY.tcl	1.4	Mon May 15 13:08:41 2006
A102109SPVT036_ACMS_STR_ON.tcl	1.6	Mon Aug 28 14:45:37 2006
A102109SPVT037_SUN_INER.tcl	1.5	Tue Feb 27 16:26:09 2007
A102109SPVT038_RWL_ON.tcl	1.10	Mon May 21 13:45:35 2007
A102109SPVT039_STR_ON.tcl	1.3	Wed Mar 15 17:22:54 2006
A102109SPVT040_STR_OFF.tcl	1.2	Mon May 15 13:46:25 2006
A102109SPVT041_RWL_CONF.tcl	1.4	Mon May 15 13:46:43 2006
A102109SPVT042_RWL_SPINUP.tcl	1.6	Mon Oct 2 14:56:38 2006
A102109SPVT043_TRANSITION_TO_OCM.tcl	1.20	Thu Oct 11 12:50:04 2007
A102109SPVT044_RCS_MODE.tcl	1.4	Sat Sep 23 12:25:07 2006
A102109SPVT045_SCM_RASTER.tcl	1.2	Thu Aug 23 11:55:24 2007
A102109SPVT046_ROT_QUAT.tcl	1.2	Mon May 15 13:47:38 2006
A102109SPVT047_RM_DUMP.tcl	1.2	Mon May 15 13:37:36 2006
A102109SPVT048_TEST_DW_ENA.tcl	1.1	Mon May 15 13:08:59 2006
A102109SPVT049_ACMS_HC_09.tcl	1.14	Wed Dec 6 11:22:50 2006
A102109SPVT050_BACK_TO_PRESEP.tcl	1.6	Wed Jul 25 08:45:06 2007
A102109SPVT051_ACMS_OCM_01.tcl	1.6	Wed Apr 18 09:22:19 2007
A102109SPVT052_TRANSITION_TO_SCM.tcl	1.22	Sat Nov 3 13:39:16 2007
A102109SPVT053_ACMS_SAM_01.tcl	1.10	Tue Nov 27 23:55:36 2007
A102109SPVT056_ACMS_HC_06.tcl	1.3	Wed Apr 18 09:22:21 2007
A102109SPVT057_TRANSITION_TO_SAM.tcl	1.2	Mon Oct 2 14:57:09 2006
A102109SPVT058_ACMS_SCM_02.tcl	1.16	Wed Jul 25 04:33:47 2007
A102109SPVT059_ACC_TIMESYNC.tcl	1.1	Mon May 15 13:10:02 2006
A102109SPVT060_ACMS_SCM_05.tcl	1.12	Tue Oct 2 14:12:36 2007
A102109SPVT061_RWL_SPINDOWN.tcl	1.3	Tue Sep 26 19:48:34 2006
A102109SPVT062_ACMS_SCM_01.tcl	1.10	Fri Jul 20 14:06:48 2007
A102109SPVT063_MODIFY_CROME_REGISTER.tcl	1.1	Mon May 22 08:28:27 2006
A102109SPVT064_ACMS_CIRSIR_03.tcl	1.3	Thu Jun 8 09:58:33 2006
A102109SPVT065_ACMS_CIRSIR_04_I.tcl	1.3	Thu Jun 8 09:59:06 2006
A102109SPVT066_ACMS_CIRSIR_04_II.tcl	1.3	Thu Jun 8 09:59:46 2006
A102109SPVT067_ACMS_SCM_04.tcl	1.12	Wed Jul 25 04:45:20 2007
A102109SPVT069_ACMS_AV_02.tcl	1.3	Thu Jun 8 10:02:23 2006
A102109SPVT070_ACMS_AV_04.tcl	1.4	Thu Jun 15 06:53:41 2006
A102109SPVT071_ACMS_RECOVERY.tcl	1.2	Thu Oct 5 10:59:51 2006
A102109SPVT072_ACMS_GYRCHK_01.tcl	1.3	Wed Apr 18 09:22:22 2007
A102109SPVT073_ACMS_PT_03.tcl	1.3	Tue Jul 25 15:46:54 2006
A102109SPVT074_ACMS_PT_06.tcl	1.4	Wed Apr 18 09:22:23 2007
A102109SPVT075_ACMS_PT_07.tcl	1.3	Wed Apr 18 09:22:24 2007
A102109SPVT076_ACMS_PT_08.tcl	1.3	Wed Apr 18 09:22:25 2007
A102109SPVT077_ACMS_PT_09.tcl	1.3	Wed Apr 18 09:22:26 2007

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A102109SPVT078_ACMS_PT_04.tcl                1.2          Thu Oct 12 13:48:06 2006
A102109SPVT079_ACMS_PT_05.tcl                1.3          Fri Oct 13 11:02:31 2006
A102109SPVT081_ACMS_ARAD_01.tcl              1.5          Thu Nov 30 09:59:57 2006
A102109SPVT082_ACMS_ARAD_05.tcl              1.6          Fri Nov 10 15:21:41 2006
A102109SPVT083_ACMS_FDIR_01.tcl              1.4          Mon Nov 27 14:15:17 2006
A102109SPVT084_ACMS_FDIR_02.tcl              1.3          Thu Nov 30 09:54:44 2006
A102109SPVT085_ACMS_PT_02.tcl                1.4          Mon Dec  4 20:01:01 2006
A102109SPVT086_ACMS_TIME_SYNCH.tcl           1.1          Tue Dec  5 17:49:39 2006
A102109SPVT087_ACMS_IST_FN.tcl               1.19         Mon Nov  5 06:02:45 2007
A102109SPVT088_ACMS_IST_UNIT_CHECKOUT.tcl 1.6              Sat Dec  1 04:53:19 2007
A102109SPVT089_ACMS_IST_ACC_HEALTH.tcl    1.8          Thu Nov 29 15:54:05 2007
A102109SPVT090_ACMS_IST_TANGO.tcl            1.6          Wed Jul 11 08:24:45 2007
A102109SPVT091_ACMS_IST_RWL_HEALTH.tcl    1.5          Fri Jul 13 13:58:58 2007
A102109SPVT092_ACMS_GC_03.tcl                1.2          Wed Nov 28 21:47:58 2007
A102109SPVT0XX_ACMS_RWL_UFT.tcl              1.4          Thu Sep 28 08:48:29 2006
A102109SPVT0xx_ACMS_RWL_Test_V2.tcl          1.1          Thu Jan 19 07:30:22 2006
A102109SPVT100_ACMS_OPS.tcl                  1.42         Wed Nov 28 21:36:28 2007
A102109SPVT200_ACMS_DELTA_V_IST.tcl          1.3          Thu Aug 30 08:13:36 2007
A102109SPVT201_ACMS_STATUS.tcl               1.12         Tue Nov 21 07:25:29 2006
A102109SPVT202_ACMS_STATUS_H.tcl             1.7          Tue Nov  6 16:34:27 2007
A102109SPVT203_RWL_SPINUP_IST.tcl            1.1          Fri Nov  3 10:43:13 2006
A102109SPVT204_ACMS_IST_FDIR.tcl             1.12         Thu Aug 30 08:14:06 2007
A102109SPVT204_ACMS_TRANSIT_SCM_OCM_SCM.tcl 1.3              Tue Nov 21 07:32:07
2006
A102109SPVT205_ACMS_IST_RCS_HC.tcl           1.2          Wed Apr 18 09:22:27 2007
A102109SPVT205_ACMS_IST_RCS_HC_PART1.tcl  1.3          Tue Dec  5 15:58:25 2006
A102109SPVT205_ACMS_IST_RCS_HC_PART2.tcl  1.4          Wed Dec  6 11:27:11 2006
A102109SPVT206_ACMS_RWL_EMC_SETUP.tcl     1.5          Wed Nov 28 09:18:14 2007
A102109SPVT207_ACMS_STR_EMC_SETUP.tcl     1.3          Wed Nov 28 09:22:00 2007
ACMS_ACC_CLOSE_STRAPS.tcl                    1.1          Mon May 15 13:11:18 2006
ACMS_ACC_SEPARA.tcl                          1.1          Mon May 15 13:11:28 2006
ACMS_Error_Inj_AAD_curr.tcl                  1.1          Mon May 15 13:11:40 2006
ACMS_FOP_RM_DISABLE.tcl                      1.1          Mon May 15 13:11:51 2006
ACMS_FOP_RM_ENABLE.tcl                       1.1          Mon May 15 13:12:02 2006
ACMS_FOP_STR_OFF.tcl                         1.1          Mon May 15 13:12:18 2006
ACMS_FOP_STR_ON.tcl                          1.1          Mon May 15 13:11:08 2006
ACMS_MON.tcl                                 1.1          Mon May 15 13:10:57 2006
ACMS_QSL.tcl                                 1.2          Mon May 15 13:48:23 2006
ACMS_RECOVERY.tcl                            1.2          Wed Apr 18 09:22:28 2007
ACMS_RECOVERY_from_AutoPeriod.tcl            1.2          Mon May 19 06:33:34 2008
ACMS_RWL_RUN_IN.tcl                          1.2          Wed Apr 18 09:22:29 2007
ACMS_RWL_RUN_IN_PASSIVE.tcl                  1.3          Tue Jun  5 07:56:32 2007
ACMS_SAM_MON_P.tcl                           1.1          Mon May 15 13:10:47 2006
ACMS_SCOE_checks.tcl                         1.4          Fri Nov 18 18:48:04 2005
ACMS_SEP_DELAY.tcl                           1.1          Mon May 15 13:10:38 2006
ACMS_STAR_VECTOR_TO_CCD.tcl                  1.2          Wed Apr 18 09:22:30 2007
ACMS_STR_CHECK.tcl                           1.1          Tue Jul 17 15:41:47 2007
ACMS_SVT0.tcl                                1.4          Wed Apr 18 09:25:41 2007
ACMS_event_buffer_dump.tcl                   1.8          Tue Jul 24 05:33:34 2007
ACMS_get_RM_status.tcl                       1.9          Wed Oct 11 07:26:48 2006
ALL_SubscribeParams.tcl                      1.2          Thu Apr 19 19:02:09 2007
ApidTracer.tcl                               1.3          Tue Sep 25 17:52:30 2007
BARBARA_TEMPO.tcl                            1.1          Tue Oct 25 09:08:46 2005
BOLO_cooler_OBS_Shell.tcl                    1.1          Thu Oct 18 15:48:09 2007
BS_SCOE_checks.tcl                           1.1          Wed Jul 20 10:35:01 2005
Background_Adjustment_01_Shell.tcl           1.1          Thu Oct 18 15:48:09 2007
Background_Adjustment_02_Shell.tcl           1.1          Thu Oct 18 15:48:09 2007
Background_Adjustment_03_Shell.tcl           1.1          Thu Oct 18 15:48:09 2007
Background_Adjustment_04_Shell.tcl           1.1          Thu Oct 18 15:48:09 2007
Background_Adjustment_05_Shell.tcl           1.1          Thu Oct 18 15:48:10 2007
Background_Adjustment_06_Shell.tcl           1.1          Thu Oct 18 15:48:10 2007
CALIBRATION_FOR_GYRO.tcl                     1.6          Thu Mar  8 17:44:26 2007
CCS-IEGSE-IFTest-HIFI_102cmds.tcl            1.1          Thu Apr 19 18:42:54 2007
CCS-IEGSE-IFTest-HIFI_200cmds.tcl            1.1          Thu Apr 19 18:43:02 2007
CCS-IEGSE-IFTest-HIFI_20cmds.tcl             1.1          Thu Apr 19 18:42:43 2007
CCS-IEGSE-IFTest-PACS_102cmds.tcl            1.1          Thu Apr 19 18:43:09 2007
CCS-IEGSE-IFTest-PACS_200cmds.tcl            1.1          Thu Apr 19 18:43:17 2007
CCS-IEGSE-IFTest-PACS_20cmds.tcl             1.1          Thu Apr 19 18:43:25 2007
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CCS-IEGSE-IFTest-SPIRE_102cmds.tcl          1.1          Thu Apr 19 18:43:32 2007
CCS-IEGSE-IFTest-SPIRE_200cmds.tcl          1.1          Thu Apr 19 18:43:39 2007
CCS-IEGSE-IFTest-SPIRE_20cmds.tcl           1.1          Thu Apr 19 18:44:01 2007
CDMU_SCOE_checks.tcl                        1.2          Fri Aug 26 10:23:33 2005
CONF_chopper_ast_OBS_Shell.tcl              1.1          Thu Oct 18 15:48:10 2007
CONF_grating_IST_OBS_Shell.tcl              1.1          Thu Oct 18 15:48:10 2007
CONF_grating_OBS_Shell.tcl                  1.1          Thu Oct 18 15:48:10 2007
CONF_grating_SFTHeII_OBS_Shell.tcl          1.1          Thu Oct 18 15:48:10 2007
CONF_grating_SFTHeII_R_OBS_Shell.tcl        1.1          Thu Oct 18 15:48:10 2007
CONF_phot_fltw_OBS_Shell.tcl                1.1          Thu Oct 18 15:48:10 2007
CONF_phot_fltw_R_OBS_Shell.tcl              1.1          Thu Oct 18 15:48:10 2007
CONF_phot_fltw_R_Warm_OBS_Shell.tcl         1.1          Thu Oct 18 15:48:10 2007
CONF_phot_fltw_Warm_OBS_Shell.tcl           1.1          Thu Oct 18 15:48:10 2007
CONF_spec_fltw_OBS_Shell.tcl                1.1          Thu Oct 18 15:48:10 2007
CONF_spec_fltw_R_OBS_Shell.tcl              1.1          Thu Oct 18 15:48:10 2007
CONF_spec_fltw_R_Warm_OBS_Shell.tcl         1.1          Thu Oct 18 15:48:10 2007
CONF_spec_fltw_Warm_OBS_Shell.tcl           1.1          Thu Oct 18 15:48:10 2007
CcsHanderTest.tcl                           1.1          Thu Mar  8 18:04:22 2007
CcsHanderTest200.tcl                        1.2          Wed Apr 18 09:33:02 2007
Check_PCDU_mini_IST.tcl                     1.1          Tue Oct 25 23:21:04 2005
Chop_mov_abs_OBS_Shell.tcl                  1.1          Thu Oct 18 15:48:10 2007
D101159SCVT003DISTHERMALCONTROL.tcl         1.1          Thu Jun 28 12:49:05 2007
D101159SCVT008MEM_MAN_RD_WR_CHK.tcl         1.1          Sat Oct 29 09:55:16 2005
D101159SCVT028SSMM_OFF.tcl                  1.1          Fri Mar 10 17:14:17 2006
D102159SCVT000SUBSEQ.tcl                    1.2          Mon Nov  7 20:47:54 2005
D102159SCVT000SUBSEQDOWN1.tcl               1.14         Wed Nov  2 10:36:30 2005
D102159SCVT000_SYSTEMINIT.tcl               1.2          Thu Sep 15 12:42:21 2005
D102159SCVT001PM_SELECT.tcl                 1.19         Thu Nov 15 12:14:21 2007
D102159SCVT002TC_VERIF_SERVICE.tcl          1.3          Fri Oct 28 11:15:48 2005
D102159SCVT003DISTHERMALCONTROL.tcl         1.7          Mon Oct 29 15:39:11 2007
D102159SCVT003_EMERGENCY_DISTHERMALCONTROL.tcl 1.2          Wed Apr 18 08:46:13
2007
D102159SCVT004DEVICE_COMMAND_DISTR.tcl      1.5          Fri Nov 11 08:21:13 2005
D102159SCVT005HK_DATA_REPORTING.tcl         1.2          Fri Oct 28 10:19:12 2005
D102159SCVT006TIMEMANAGEMENT.tcl            1.3          Tue Oct 25 17:59:31 2005
D102159SCVT007PM_RESET.tcl                  1.19         Mon Oct 29 16:46:49 2007
D102159SCVT007PM_RESET_IT.tcl               1.2          Mon Jun 11 14:03:15 2007
D102159SCVT008MEM_MAN_RD_WR_CHK.tcl         1.3          Sat Oct 29 10:03:55 2005
D102159SCVT009MEMORYMANAGEMENT.tcl          1.3          Sat Oct 29 09:54:54 2005
D102159SCVT010HK_DATA_REPORTING.tcl         1.1          Fri Oct 28 10:22:00 2005
D102159SCVT011MEM_MAN_COPY_MEM.tcl          1.4          Sat Oct 29 09:55:59 2005
D102159SCVT012_INITCOMPL_BOOTEVENT.tcl      1.3          Mon Oct 29 16:08:07 2007
D102159SCVT012_INITCOMPL_BOOTEVENT_IM2.tcl  1.2          Fri Oct 26 09:27:01 2007
D102159SCVT013_CDMSINTHKCHECK.tcl           1.3          Wed Jan 11 09:59:28 2006
D102159SCVT013_CDMSINTHKCHECK_PMB.tcl       1.4          Tue May 16 15:36:26 2006
D102159SCVT014CONNECT_TEST_SERVICE.tcl      1.2          Fri Oct 28 10:17:23 2005
D102159SCVT015_PACKET_TRASM_CONTR.tcl       1.4          Thu Nov 10 22:18:01 2005
D102159SCVT015_PACKET_TRASM_CTR_BA.tcl      1.1          Tue Nov  8 13:42:30 2005
D102159SCVT016HK_DATA_REPORTING.tcl         1.2          Fri Oct 28 10:20:24 2005
D102159SCVT017HK_DATA_REPORTING.tcl         1.2          Fri Oct 28 10:20:55 2005
D102159SCVT018HK_DATA_REPORTING.tcl         1.2          Fri Oct 28 10:21:15 2005
D102159SCVT019HK_DATA_REPORTING.tcl         1.2          Fri Oct 28 10:21:34 2005
D102159SCVT020ASW_PCDU_MANAGEMENT.tcl       1.4          Fri Nov  3 07:34:45 2006
D102159SCVT021_EVENTREPORTING.tcl           1.2          Sat Oct 29 09:45:57 2005
D102159SCVT022ASW_TT_C_MANAGEMENT.tcl       1.7          Tue Nov  6 20:48:11 2007
D102159SCVT023_TC_MODEVERIFICATION.tcl      1.3          Thu Oct 27 19:22:29 2005
D102159SCVT023_TC_MODE_VERIF_DECB.tcl       1.3          Thu Oct 27 19:22:41 2005
D102159SCVT024_THERM_CTRL_MANAG.tcl         1.5          Tue Nov  6 17:40:00 2007
D102159SCVT025ON_BOARD_MONITORING.tcl       1.4          Thu Nov  2 09:36:28 2006
D102159SCVT026BUS_COMM.tcl                  1.2          Tue Oct 25 20:06:15 2005
D102159SCVT027BUS_PROF_MAN.tcl              1.5          Mon Oct 24 18:05:46 2005
D102159SCVT028SSMM_OFF.tcl                  1.7          Sat Oct 27 10:50:38 2007
D102159SCVT028SSMM_OFF_IM2.tcl              1.1          Fri Oct 26 07:50:14 2007
D102159SCVT028SSMM_ON.tcl                   1.8          Sat Oct 27 10:50:51 2007
D102159SCVT028SSMM_ON_IM2.tcl               1.1          Fri Oct 26 06:44:51 2007
D102159SCVT028_SSMM_MANAGEMENT.tcl          1.4          Sat Oct 29 12:31:09 2005
D102159SCVT029_ON_BOARD_STOR_RETR.tcl       1.15         Mon Nov 14 16:15:08 2005
D102159SCVT029_ON_BOARD_STOR_RETR1.tcl      1.8          Mon Nov 14 14:03:10 2005
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D102159SCVT029_ON_BOARD_STOR_RETR2.tcl	1.1	Tue Nov 8 11:54:45 2005
D102159SCVT029_SEL_&_CEL.tcl	1.1	Wed Nov 9 14:25:55 2005
D102159SCVT029_SEL_AND_CEL.tcl	1.11	Tue Nov 15 14:26:33 2005
D102159SCVT029_STORING_DOWNLOAD.tcl	1.5	Wed Oct 26 07:21:59 2005
D102159SCVT029_STORING_DOWNLOAD1.tcl	1.4	Mon Nov 14 15:42:27 2005
D102159SCVT029_STORING_DOWNLOAD2.tcl	1.5	Mon Nov 14 08:49:05 2005
D102159SCVT030_DLL1_ERR_BUSAB_HEAL.tcl	1.2	Wed Oct 26 12:18:40 2005
D102159SCVT030_DLL2_CORE.tcl	1.1	Tue Sep 5 07:32:49 2006
D102159SCVT030_DLL2_ERRA_BUSB_UNHE.tcl	1.2	Thu Nov 2 06:12:57 2006
D102159SCVT030_DLL2_ERR_A_B_UNHEAL.tcl	1.1	Tue Sep 5 07:33:09 2006
D102159SCVT030_DLL3_1_NN_VITAL_RT.tcl	1.2	Wed Oct 26 19:22:33 2005
D102159SCVT030_DLL4_VITAL_RT.tcl	1.2	Thu Nov 2 06:12:32 2006
D102159SCVT030_DLL5_NON_VITAL_RT_FAIL.tcl	1.3	Thu Nov 2 06:11:44 2006
D102159SCVT031OBCP_MANAGEMENT.tcl	1.7	Thu Nov 2 14:38:44 2006
D102159SCVT032EnNomTCSLoops.tcl	1.8	Tue Nov 6 15:59:57 2007
D102159SCVT032TIMESYNCRO.tcl	1.7	Fri Oct 26 07:35:53 2007
D102159SCVT033_TCS_END_TO_END_NR.tcl	1.1	Wed Sep 20 15:51:59 2006
D102159SCVT034_TTR_MANAGEMENT.tcl	1.13	Mon Oct 29 18:39:30 2007
D102159SCVT035_LOW_RATE_PACKET.tcl	1.4	Thu May 3 14:27:19 2007
D102159SCVT035_PAYLOAD_MANAGEMENT.tcl	1.14	Tue Oct 30 12:19:28 2007
D102159SCVT036_ASW_VAL_TEST46.tcl	1.8	Fri Mar 3 06:38:26 2006
D102159SCVT037_ASW_VAL_TEST47.tcl	1.6	Thu Feb 23 11:17:20 2006
D102159SCVT038_DECONT_HEAT_MANAG.tcl	1.2	Wed Feb 15 19:43:48 2006
D102159SCVT039_EVENT_ACTION_MANAG.tcl	1.5	Thu Nov 2 09:25:07 2006
D102159SCVT040_ASW_VAL_TEST43.tcl	1.3	Sat Feb 18 11:56:22 2006
D102159SCVT040_FAILTC_READ_BOTH_MM.tcl	1.6	Wed Dec 21 14:56:42 2005
D102159SCVT041_ASW_VAL_TEST41.tcl	1.2	Tue Feb 21 16:21:57 2006
D102159SCVT042_ASW_VAL_TEST42.tcl	1.2	Tue Feb 21 14:57:07 2006
D102159SCVT043_MODE_MANAGEMENT.tcl	1.1	Tue Feb 28 06:50:55 2006
D102159SCVT044_ASW_VAL_TEST44.tcl	1.3	Mon Feb 20 06:01:24 2006
D102159SCVT045_LAUNCH_LAUNCH.tcl	1.10	Thu Nov 2 09:57:22 2006
D102159SCVT046_LAUNCH_SUNACQ.tcl	1.5	Tue Nov 6 19:01:15 2007
D102159SCVT047_SUNACQ_SUNACQ.tcl	1.4	Tue May 20 01:35:42 2008
D102159SCVT048_SUNACQ_NOMINAL.tcl	1.6	Tue Nov 6 20:38:54 2007
D102159SCVT049_NOMINAL_NOMINAL.tcl	1.2	Tue May 20 01:35:19 2008
D102159SCVT050_NOMINAL_EARTH.tcl	1.3	Tue Nov 6 18:55:09 2007
D102159SCVT051_EARTH_EARTH.tcl	1.2	Tue May 20 01:33:19 2008
D102159SCVT052_EARTH_NOMINAL.tcl	1.2	Tue Nov 6 20:55:26 2007
D102159SCVT053_NOMINAL_SUNACQ.tcl	1.2	Tue Nov 6 21:05:19 2007
D102159SCVT054_SUNACQ_SURV.tcl	1.7	Mon Nov 6 19:48:11 2006
D102159SCVT055_SURV_SURV.tcl	1.1	Wed Jun 27 19:30:44 2007
D102159SCVT057_NOMINAL_SURV.tcl	1.1	Tue Nov 6 21:15:00 2007
D102159SCVT059_EARTH_SURV.tcl	1.1	Wed Jun 27 19:31:47 2007
D102159SCVT060_ASW_VAL_TEST45.tcl	1.7	Fri Feb 17 16:32:37 2006
D102159SCVT062_DLL2_ERRA_BUSB_UNHE.tcl	1.3	Fri Feb 17 15:01:57 2006
D102159SCVT067_NOMINAL_SURV_FDIR4.tcl	1.2	Mon Jul 31 08:16:45 2006
D102159SCVT070_CDMS_RECONF_ANALYSIS.tcl	1.2	Wed Feb 22 10:30:51 2006
D102159SCVT071_ONBOARD_SCHEDULING.tcl	1.2	Wed Nov 8 20:58:34 2006
D102159SCVT072_SHUT_DOWN_LCL_XPND1.tcl	1.3	Tue Nov 6 20:59:45 2007
D102159SCVT073_OBCP_MAST_INST_LOAD.tcl	1.3	Tue Nov 6 20:48:50 2007
D102159SCVT076_LOAD_SHORT_MTL.tcl	1.1	Fri Jun 30 08:41:44 2006
D102159SCVT080_CEL_DOWNLINK.tcl	1.5	Tue Oct 9 08:29:34 2007
D102159SCVT081DIS_EATTABLE_ENTRIES.tcl	1.1	Sat Jul 29 13:58:13 2006
D102159SCVT081_PAP4_PM_A_NOM.tcl	1.2	Tue Apr 24 12:40:43 2007
D102159SCVT082_PAP3_PM_A_NOM.tcl	1.2	Tue Apr 24 12:42:00 2007
D102159SCVT082_SVT0_6BK_PSTORE_DEF.tcl	1.2	Wed Aug 2 16:34:09 2006
D102159SCVT083_PAP4_PM_B_NOM.tcl	1.3	Tue Apr 24 12:42:25 2007
D102159SCVT083_SVT0_2BK_PSTORE_DEF.tcl	1.2	Wed Aug 2 16:34:20 2006
D102159SCVT084_PAP3_PM_B_NOM.tcl	1.4	Tue Apr 24 12:42:56 2007
D102159SCVT084_TTEC_CHAIN2_NOMINAL.tcl	1.1	Sat Jul 29 14:46:45 2006
D102159SCVT085_FDIR_LEVEL2_AIR.tcl	1.3	Thu Nov 9 23:43:32 2006
D102159SCVT085_PAP0_PM_A_NOM.tcl	1.1	Fri Jun 29 13:01:37 2007
D102159SCVT085_PAP0_PM_B_ONLY.tcl	1.2	Tue Apr 24 12:43:29 2007
D102159SCVT086_LAUNCH_LAUNCH_IST.tcl	1.4	Tue May 22 07:04:59 2007
D102159SCVT086_PAP5_PM_B_NOM.tcl	1.2	Tue Apr 24 12:44:16 2007
D102159SCVT087_PAP1_PM_B_ONLY.tcl	1.2	Tue Apr 24 12:44:42 2007
D102159SCVT088_PAP1_PM_A_ONLY.tcl	1.2	Tue Apr 24 12:41:25 2007
D102159SCVT089_PAP2_PM_B_NOM.tcl	1.1	Fri Jun 29 13:12:02 2007

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D102159SCVT090_PAP0_PM_A_NOM.tcl          1.1          Fri Jun 29 13:09:19 2007
D102159SCVT095_ASW_VAL_TEST48.tcl         1.1          Wed Jul  4 07:04:09 2007
D102159SCVT100_CHKTM_UNITHEALTH.tcl       1.1          Fri May 12 14:53:16 2006
D102159SCVT101_CDMS_RED_CONFIG.tcl        1.2          Fri May 12 14:41:02 2006
D102159SCVT103_CDMS_NOM_CONFIG.tcl        1.1          Fri Mar 10 15:57:38 2006
D102159SCVT104_ENCODER_SELECT.tcl         1.7          Thu Jul  5 13:40:13 2007
D102159SCVT105_IST_NOMLNCH_LNCH_SAM.tcl   1.3          Mon Sep  3 09:08:05 2007
D102159SCVT105_LAUNCH_SUNACQ.tcl          1.7          Thu Aug 30 06:51:30 2007
D102159SCVT107_PACKET_STORE_DEF.tcl       1.6          Fri May 11 10:16:36 2007
D102159SCVT108_SUNACQ_NOMINAL.tcl         1.7          Mon May 14 16:11:25 2007
D102159SCVT110_MTL_RASTER_POINTING.tcl    1.8          Tue Nov 21 08:26:42 2006
D102159SCVT111_MTL_LINE_SCANNING.tcl      1.6          Tue Nov 21 08:29:40 2006
D102159SCVT112_MTL_PACS_BURST_CHECK.tcl   1.3          Fri Nov 17 15:56:53 2006
D102159SCVT113_3A_NOMINAL_PMA.tcl         1.8          Mon Nov 13 17:10:34 2006
D102159SCVT114_3B_SAM_PMA.tcl             1.6          Mon Nov 13 17:37:22 2006
D102159SCVT115_CHECK_HCS_OFF.tcl          1.3          Sun May 18 06:25:01 2008
D102159SCVT116_IST_16_OBCP_LOADING.tcl    1.1          Tue Sep 26 15:31:12 2006
D102159SCVT117_IST_OBCP_FULL_LOAD.tcl     1.1          Tue Sep 26 15:31:54 2006
D102159SCVT118_FDIR4_EARTHSURV_PMB.tcl    1.6          Wed Nov 22 10:52:32 2006
D102159SCVT119_MTL_DUMMY.tcl              1.1          Mon Oct 16 16:24:29 2006
D102159SCVT120_TM_LINK_5_KBPS.tcl         1.1          Mon Oct 16 16:22:27 2006
D102159SCVT121_P_L_SIMULATION.tcl         1.3          Fri Nov 10 14:50:21 2006
D102159SCVT122_P_L_SIMULATION_OFF.tcl     1.2          Fri Nov 10 14:50:36 2006
D102159SCVT123_TM_LINK_150_KBPS.tcl       1.2          Tue May 20 01:29:23 2008
D102159SCVT124__IST_TM_LINK_150_KBPS.tcl  1.6          Sat Nov 11 10:04:06 2006
D102159SCVT125_IST_CDMS_REDCON_PMB.tcl    1.2          Sat Nov 11 11:23:33 2006
D102159SCVT126_LCL_OFF_BEF_SC_OFF.tcl     1.3          Wed Feb 21 15:29:14 2007
D102159SCVT127_FDIR4_NOMSURV_PMA_ONLY.tcl 1.7          Thu Apr  5 09:45:21 2007
D102159SCVT128_RESTORE_FROM_SURV.tcl      1.4          Wed Jun 27 19:28:54 2007
D102159SCVT129_LANCH_SACQ_FDIR_SEP.tcl    1.2          Fri Nov 10 14:37:05 2006
D102159SCVT130_3A_NOMINAL_PMB.tcl         1.3          Wed Nov 22 10:54:00 2006
D102159SCVT131_DLL2_CORE_BUSB.tcl         1.3          Wed Nov 22 10:54:37 2006
D102159SCVT132_FDIR4_SAMSURV_PMA.tcl      1.3          Wed Nov 22 10:55:15 2006
D102159SCVT133_1553_BUS_SWITCH_OVER.tcl   1.1          Fri Nov 17 11:00:24 2006
D102159SCVT134_PCDU_1553_BUS_FAIL_RECOV.tcl 1.2          Tue Oct 30 08:00:27
2007
D102159SCVT134_RECOVERY_SGM_FAILURE.tcl   1.5          Thu Dec 21 13:41:12 2006
D102159SCVT135_AIR_DGN_DEFINITION.tcl     1.1          Wed Nov 22 18:35:18 2006
D102159SCVT135_TTC_RX_POWER_OOL.tcl       1.2          Tue Nov  6 18:10:52 2007
D102159SCVT135_TTC_XPND_INVALID_RT.tcl    1.2          Tue Nov  6 18:45:58 2007
D102159SCVT136_FUNC_FAIL_MODE_RFDN_SWITCH.tcl 1.4          Tue Nov  6 17:49:01
2007
D102159SCVT136_VERIFY_PKT_VMC.tcl         1.3          Thu Dec 21 13:42:29 2006
D102159SCVT137_IST_SUNACQ_NOM.tcl         1.5          Tue Nov 27 17:29:15 2007
D102159SCVT138_IST_LAUNCH_SUNACQ.tcl      1.4          Fri Nov  2 13:33:26 2007
D102159SCVT150_DELIBERATE_ERRORS.tcl      1.7          Sat Nov 17 06:56:57 2007
D102159SCVT151_HER_IST_PKT_STR_DEF.tcl    1.1          Wed Feb 21 07:36:55 2007
D102159SCVT152_LAUNCH_SUNACQ_IST_FDIR.tcl 1.9          Tue May 29 07:38:09 2007
D102159SCVT153_MTL_FDIR.tcl               1.11         Mon Sep  3 14:06:29 2007
D102159SCVT154_FDIR_NOM_EARTH_3a.tcl      1.10         Thu Sep 20 14:15:01 2007
D102159SCVT155_SHORT_MTL_FDIR.tcl         1.2          Fri Jun 15 07:55:04 2007
D102159SCVT156_CDMS_ANALYSIS_FDIR_IST.tcl 1.3          Thu May  3 07:52:50 2007
D102159SCVT157_FDIR_EARTH_EARTH_3b.tcl    1.15         Thu Oct  4 12:30:57 2007
D102159SCVT158_FDIR_NOMINAL_SUNACQ.tcl    1.8          Wed Sep 26 11:27:58 2007
D102159SCVT159_FDIR_CHECK_SUNACQMODE.tcl  1.4          Wed Sep 26 11:32:44 2007
D102159SCVT160_FDIR_NOM_SURV_DOD.tcl      1.5          Tue Oct  9 11:57:20 2007
D102159SCVT161_IST_MM_NOM_NOM.tcl         1.14         Fri Sep 14 12:03:34 2007
D102159SCVT162_IST_MM_SAM_SAM.tcl         1.10         Sat Nov 10 16:31:44 2007
D102159SCVT163_IST_MM_LAN_SAM.tcl         1.9          Sat Nov 10 08:21:03 2007
D102159SCVT164_IST_MM_SAM_NOM.tcl         1.9          Sat Nov 10 09:00:57 2007
D102159SCVT165_RECOVERY_PCDUA_FAST.tcl    1.3          Sat Oct 27 11:17:45 2007
D102159SCVT166_SHORT_MTL_FDIR_MM.tcl      1.6          Thu Jun 14 08:58:15 2007
D102159SCVT167_IST_MM_NOM_EAM.tcl         1.8          Fri Jun  1 13:42:08 2007
D102159SCVT168_IST_MM_EAM_EAM.tcl         1.3          Fri Apr 20 13:12:27 2007
D102159SCVT169_IST_MM_EAM_NOM.tcl         1.6          Thu May 31 16:05:56 2007
D102159SCVT170_IST_MM_NOM_SM.tcl          1.6          Fri Jun  1 08:51:09 2007
D102159SCVT171_IST_MM_SM_SM.tcl           1.5          Fri Jun  1 08:50:39 2007
D102159SCVT172_IST_MM_SM_SAM.tcl          1.3          Fri Jun  1 10:44:00 2007
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D102159SCVT173_IST_MM_EAM_SAM.tcl          1.4          Fri Jun  1 13:46:56 2007
D102159SCVT174_IST_REDUNDANT_CONF.tcl      1.19         Fri Nov  2 08:02:00 2007
D102159SCVT175_SET_SURV_REG.tcl            1.16         Sat Nov 17 16:39:19 2007
D102159SCVT176_WRITE_CROME.tcl             1.22         Fri Nov 30 17:03:21 2007
D102159SCVT177_IST_MM_NOM_SAM.tcl          1.3          Fri Jun  1 14:09:55 2007
D102159SCVT178_RMS_PKT_STORE_DEF.tcl       1.7          Fri Nov 30 17:04:24 2007
D102159SCVT179_DBG_PKT_STORE_DEF.tcl       1.11         Fri Nov 30 17:04:38 2007
D102159SCVT180_DUMP_PKT_STORE.tcl          1.2          Thu May 31 16:38:41 2007
D102159SCVT181_DISABLE_PKT_STORE.tcl       1.3          Thu May 31 16:26:37 2007
D102159SCVT182_DUMP_PKT_STORE_RMS_DTCP.tcl 1.3            Fri Aug  3 18:25:46 2007
D102159SCVT183_CEL_DOWNLINK_RMS_DTCP.tcl   1.2          Tue May 20 13:25:00 2008
D102159SCVT184_SWITCH_TO_PCDUB_BUS_B.tcl   1.4          Thu Jul 19 12:36:12 2007
D102159SCVT185_IST_PKT_STORE_DEF.tcl       1.13         Tue Nov 27 08:48:02 2007
D102159SCVT186_IST_SSMM_ON.tcl             1.21         Sat Oct 27 10:58:02 2007
D102159SCVT187_IST_SSMM_OFF.tcl            1.8          Fri Nov 30 17:03:49 2007
D102159SCVT188_IST_DUMP_PKT_STORE.tcl      1.10         Wed Nov 21 15:43:44 2007
D102159SCVT189_IST_PKT_STORE_DEF_2.tcl     1.10         Fri Nov 30 17:03:34 2007
D102159SCVT190_IST_WCS_PKTSTORE_DUMP_BREAK.tcl 1.3           Wed Nov  7 08:31:31
2007
D102159SCVT191_TM_5_KBPS_IST.tcl           1.2          Thu Jul 12 16:31:57 2007
D102159SCVT192_GET_EAT_REPORT.tcl          1.7          Fri Nov 30 17:04:01 2007
D102159SCVT192_IST_UPLOAD_EAT.tcl          1.11         Mon Nov 19 14:02:48 2007
D102159SCVT193_IST_UPLOAD_OBCP.tcl         1.12         Sat Nov 10 09:10:11 2007
D102159SCVT196_IST_ONBOARD_SCHEDULING.tcl  1.6           Mon Nov  5 15:01:57 2007
D102159SCVT197_IST_OBCP_MANAGM.tcl         1.5          Wed Oct 31 11:21:46 2007
D102159SCVT198_IST_SSMM_MANAGM.tcl         1.12         Wed Oct 31 09:46:29 2007
D102159SCVT199_IST_OBT_MANAGM.tcl          1.8          Tue Oct  9 11:56:09 2007
D102159SCVT200_IST_CDMS_MANAGM_FDIR.tcl    1.10         Thu Oct 25 13:12:43 2007
D102159SCVT201_IST_SAT_COM_CDMS.tcl        1.7          Wed Nov 28 20:25:56 2007
D102159SCVT202_IST_MTL_PING_TEST.tcl       1.5          Wed Nov 21 14:38:14 2007
D102159SCVT203_IST_MTL_ReportCat.tcl       1.1          Thu Oct 11 14:03:23 2007
D102159SCVT204_GET_MOT.tcl                 1.3          Tue Sep 11 14:32:01 2007
D102159SCVT205_SAT_COM_TCT.tcl             1.2          Mon Nov 26 19:59:13 2007
D102159SCVT206_IST_SCIENCE_DOWNLNK.tcl     1.6          Mon Nov  5 14:49:30 2007
D102159SCVT207_SAT_COM_FCCT.tcl            1.2          Fri Sep 14 13:52:17 2007
D102159SCVT209_START_ON_BOARD_SCHEDULE.tcl 1.7            Fri Nov 30 16:49:56 2007
D102159SCVT210_GET_ALARM_STATUS.tcl        1.2          Mon Nov 19 08:33:37 2007
D102159SCVT211_IST_INSTR_MTL_PING.tcl      1.2          Wed Nov  7 16:38:50 2007
D102159SCVT212_EMC_LAUNCH_SUNACQ.tcl       1.2          Wed Nov 28 13:19:04 2007
D102159SCVT213_DUMP_MEM_AREAS.tcl          1.6          Wed Nov 28 20:29:30 2007
D102159SCVT214_IST_HIFI_MTL_PING.tcl       1.1          Mon Nov 26 10:46:34 2007
D102159SCVT991PM_SELECT_IM2.tcl            1.1          Thu Oct 25 12:30:00 2007
D102159SCVT993DISTHERMALCONTROL.tcl        1.1          Thu Oct 25 12:30:38 2007
D102159SCVT997PM_RESET_IM2.tcl             1.1          Thu Oct 25 12:31:08 2007
D102159SCVT998_CDMSINTHKCHECK.tcl          1.1          Thu Oct 25 12:31:36 2007
D102159SCVT999_INITCOMPL_BOOTEVENT.tcl     1.1          Thu Oct 25 12:32:07 2007
D102159SPVT001_UFT_TEST.tcl                1.3          Fri Aug 25 10:51:57 2006
D102159SPVT002_UFT_PMB_TEST.tcl            1.3          Fri Aug 25 10:52:24 2006
D201159SCVT029_ON_BOARD_STOR_RETR2.tcl     1.2          Mon Nov  7 16:50:51 2005
DisableRelTCs_RMS.tcl                      1.1          Fri Aug  3 14:24:25 2007
ENTER_SAFE_Mode_Shell.tcl                  1.1          Thu Oct 18 15:48:10 2007
H101999SCVT001_ASDDBGHIFI_PWR_ON_P.tcl     1.2          Tue Aug 21 15:30:23 2007
H101999SCVT002_ASDDBGHIFI_PWR_OFF_P.tcl    1.2          Tue Aug 21 15:31:06 2007
H102999SCVT001_ASDDBGHIFI_PWR_ON_P.tcl     1.16         Thu Nov 15 10:41:35 2007
H102999SCVT002_ASDDBGHIFI_PWR_OFF_P.tcl    1.11         Thu Nov 15 10:46:34 2007
H102999SCVT003_ASDDBGHIFI_PWR_ON_R.tcl     1.9          Thu Nov 15 10:47:14 2007
H102999SCVT004_ASDDBGHIFI_PWR_OFF_R.tcl    1.7          Thu Nov 15 10:47:50 2007
H102999SCVT005_ASDGENHIFI_PWR_ON_P.tcl     1.10         Wed Nov 28 17:27:02 2007
H102999SCVT006_ASDGENHIFI_PWR_OFF_P.tcl    1.4          Tue Nov  6 10:50:47 2007
H102999SCVT007_ASDGENHIFI_PWR_ON_R.tcl     1.9          Wed Nov 28 06:47:58 2007
H102999SCVT008_ASDGENHIFI_PWR_OFF_R.tcl    1.4          Tue Nov  6 10:51:13 2007
HIFIENG_FT_WBS_comb.tcl                    1.1          Mon Aug 27 07:11:32 2007
HIFIENG_WBS_tune.tcl                       1.1          Mon Aug 27 07:17:39 2007
HIFIENG_WBS_zero.tcl                       1.1          Mon Aug 27 07:17:59 2007
HIFIENG_config_spectro.tcl                 1.1          Mon Aug 27 07:01:13 2007
HIFIENG_take_tp_spectra.tcl                1.1          Mon Aug 27 07:16:39 2007
HIFIENG_tp_spectra_only.tcl                1.1          Mon Aug 27 07:17:13 2007
HIFIST_ASED_PatchPtvChecksum.tcl           1.2          Thu Oct 11 08:16:56 2007
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HIFIST_ASED_PatchTempLimits.tcl	1.3	Wed Nov 28 13:59:38 2007
HIFIST_CCS_conf_ptv_checksum.tcl	1.5	Thu Oct 11 16:29:50 2007
HIFIST_HRScable_HRS_conf.tcl	1.1	Fri Sep 28 09:28:40 2007
HIFIST_HRScable_HRS_off.tcl	1.1	Fri Sep 28 08:54:21 2007
HIFIST_HRScable_HRS_startup.tcl	1.1	Fri Sep 28 08:54:44 2007
HIFIST_HRScable_HRS_tune.tcl	1.1	Fri Sep 28 08:55:07 2007
HIFIST_SFT_CSA.tcl	1.4	Thu Oct 11 16:28:29 2007
HIFIST_SFT_Chopper_warm_1.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FCU_INIT_1.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FCU_INIT_2.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FCU_INIT_3.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FCU_INIT_4.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FCU_INIT_5.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FCU_INIT_6.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FCU_INIT_7.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FCU_deflux_1.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FCU_deflux_2.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FCU_deflux_3.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FCU_deflux_4.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FCU_deflux_5.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FT_WBS_Laser1.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FT_WBS_Laser2.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_HBB_test_warm.tcl	1.1	Fri Aug 24 11:09:47 2007
HIFIST_SFT_HRS_FT_1.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_HRS_FT_2_Corr.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_HRS_FT_2_Sine.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_HRS_FT_2_Square_m.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_HRS_FT_2_Square_s.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_HRS_FT_4.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_HRS_tune.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_LCU_IV_1a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_IV_1b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_IV_2a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_IV_2b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_IV_3a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_IV_3b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_IV_4a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_IV_4b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_IV_5a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_IV_5b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_IV_6a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_IV_6b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_IV_7a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_IV_7b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_switch_off.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT1a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT1b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT2a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT2b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT3a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT3b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT4a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT4b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT5a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT5b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT6a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT6b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT7a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT7b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_heater_0.tcl	1.3	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_heater_1.tcl	1.3	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_heater_2.tcl	1.3	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_heater_3.tcl	1.3	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_heater_4.tcl	1.3	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_heater_5.tcl	1.3	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_heater_6.tcl	1.3	Thu Oct 11 16:28:30 2007
HIFIST_SFT_Nominal_offcold.tcl	1.1	Wed Sep 19 09:39:46 2007
HIFIST_SFT_Nominal_offwarm.tcl	1.6	Thu Oct 11 16:28:30 2007

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HIFIST_SFT_Upconv_spectra.tcl	1.1	Thu Oct 11 16:23:30 2007
HIFIST_SFT_WBS_Laser1.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_WBS_Laser2.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_WBS_Lasers_off.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_WBS_comb.tcl	1.4	Fri Aug 24 11:05:03 2007
HIFIST_SFT_WBS_latchup_high.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_WBS_latchup_low.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_WBS_zero.tcl	1.6	Thu Oct 11 16:28:31 2007
HIFIST_SFT_Wait5.tcl	1.1	Wed Sep 19 09:40:03 2007
HIFIST_SFT_chopper_cold_0.tcl	1.1	Fri Aug 24 11:08:23 2007
HIFIST_SFT_chopper_cold_1.tcl	1.1	Fri Aug 24 11:09:07 2007
HIFIST_SFT_chopper_openloop_scan.tcl	1.1	Thu Oct 11 16:23:03 2007
HIFIST_SFT_chopper_warm_0.tcl	1.6	Thu Oct 11 16:28:31 2007
HIFIST_Startup_FCU_on.tcl	1.6	Fri Oct 12 06:27:50 2007
HIFIST_Startup_FPU_standby.tcl	1.6	Thu Oct 11 16:28:48 2007
HIFIST_Startup_HRS_on.tcl	1.6	Fri Oct 12 06:27:50 2007
HIFIST_Startup_HRS_standby.tcl	1.6	Thu Oct 11 16:28:48 2007
HIFIST_Startup_LCU_on.tcl	1.6	Fri Oct 12 06:27:50 2007
HIFIST_Startup_LCU_standby.tcl	1.6	Thu Oct 11 16:28:48 2007
HIFIST_Startup_LCU_table_load.tcl	1.8	Fri Oct 12 06:27:50 2007
HIFIST_Startup_LCU_table_read.tcl	1.7	Fri Oct 12 06:27:50 2007
HIFIST_Startup_LO_Nominal.tcl	1.6	Fri Oct 12 06:27:51 2007
HIFIST_Startup_OBS_SFT.tcl	1.6	Fri Oct 12 06:27:51 2007
HIFIST_Startup_WBSH_on.tcl	1.6	Fri Oct 12 06:27:51 2007
HIFIST_Startup_WBSV_on.tcl	1.6	Fri Oct 12 06:27:51 2007
HIFIST_Startup_WBS_lasertemp40.tcl	1.2	Fri Oct 12 06:27:51 2007
HIFIST_Startup_WBS_standby.tcl	1.6	Thu Oct 11 16:28:48 2007
HIFIST_Startup_all_off.tcl	1.6	Thu Oct 11 16:28:48 2007
HIFIST_Startup_chopper_set0.tcl	1.2	Fri Oct 12 06:27:51 2007
HIFIST_Startup_chopper_set1.tcl	1.2	Fri Oct 12 06:27:51 2007
HIFIST_Startup_force_boot.tcl	1.6	Fri Oct 12 06:27:51 2007
HP_OBSM_ALS.tcl	1.1	Wed Feb 14 14:02:18 2007
INST_something1.tcl	1.1	Mon Oct 29 10:36:35 2007
INST_something2.tcl	1.1	Mon Oct 29 10:37:08 2007
INST_something3.tcl	1.1	Mon Oct 29 10:37:30 2007
K102999ECVT001_ASDGENCCUA_ChkEssTM.tcl	1.12	Thu Mar 22 06:34:45 2007
K102999ECVT001_ASDGENCCUA_DLCMPar.tcl	1.2	Wed Apr 18 08:33:33 2007
K102999ECVT001_ASDGENCCUA_DL_Close.tcl	1.2	Thu Mar 22 10:41:24 2007
K102999ECVT001_ASDGENCCUA_MnDisMd1.tcl	1.4	Thu Mar 22 06:34:31 2007
K102999ECVT001_ASDGENCCUA_MnDisMd2.tcl	1.4	Thu Mar 22 06:34:16 2007
K102999ECVT001_ASDGENCCUA_MnEnaMd1.tcl	1.4	Thu Mar 22 06:34:02 2007
K102999ECVT001_ASDGENCCUA_MnEnaMd2.tcl	1.4	Thu Mar 22 06:33:48 2007
K102999ECVT001_ASDGENCCUA_POWEROFF.tcl	1.11	Tue May 22 14:54:07 2007
K102999ECVT001_ASDGENCCUA_POWERON.tcl	1.19	Fri Nov 2 13:02:48 2007
K102999ECVT001_ASDGENCCUB_ChkEssTM.tcl	1.9	Thu Mar 22 06:33:11 2007
K102999ECVT001_ASDGENCCUB_MnDisMd1.tcl	1.4	Thu Mar 22 06:32:57 2007
K102999ECVT001_ASDGENCCUB_MnDisMd2.tcl	1.5	Thu Mar 22 06:32:41 2007
K102999ECVT001_ASDGENCCUB_MnEnaMd1.tcl	1.4	Thu Mar 22 06:32:23 2007
K102999ECVT001_ASDGENCCUB_MnEnaMd2.tcl	1.4	Thu Mar 22 06:31:54 2007
K102999ECVT001_ASDGENCCUB_POWEROFF.tcl	1.10	Tue May 22 14:55:19 2007
K102999ECVT001_ASDGENCCUB_POWERON.tcl	1.16	Fri Nov 2 13:04:16 2007
K102999ECVT001_ASDGENCCU_ABPWROFF.tcl	1.4	Thu Mar 22 06:36:00 2007
K102999ECVT001_ASDGENCCU_ABPWRON.tcl	1.9	Sat Nov 3 09:57:19 2007
K102999ECVT001_ASDGENCCU_MnDBOTH1.tcl	1.4	Thu Mar 22 06:30:24 2007
K102999ECVT001_ASDGENCCU_MnDBOTH2.tcl	1.5	Thu Mar 22 06:35:35 2007
K102999ECVT001_ASDGENCCU_MnDisDLC.tcl	1.5	Thu Mar 22 06:35:23 2007
K102999ECVT001_ASDGENCCU_MnEBOTH1.tcl	1.4	Thu Mar 22 06:35:11 2007
K102999ECVT001_ASDGENCCU_MnEBOTH2.tcl	1.5	Thu Mar 22 06:34:57 2007
K102999ECVT002_ASDGEN_CCU_GUI_VLV.tcl	1.3	Sat Sep 29 08:40:20 2007
K102999ECVT024_ASDGENCCUB_DL_Close.tcl	1.2	Thu Mar 22 10:41:39 2007
K102999ECVT025_ASDGENCCU_ABCloseDL.tcl	1.2	Thu Mar 22 10:41:05 2007
K102999ECVT026_ASDGENCCU_DL2Close.tcl	1.2	Wed Apr 18 08:33:44 2007
K102999ECVT027_ASDISTCCU_AR5DLOPENX.tcl	1.1	Wed Apr 18 15:34:00 2007
K102999ECVT028_ASTISTCCU_EmgDLCls.tcl	1.2	Wed Apr 18 15:24:25 2007
K102999ECVT029_ASDGENCCUA_MnENaDLCM.tcl	1.2	Wed Aug 15 07:50:05 2007
K102999ECVT030_ASDGENCCUB_MnENaDLCM.tcl	1.2	Wed Aug 15 07:50:36 2007
K102999ECVT031_ASDGEN_CCU_LOG.tcl	1.2	Tue Nov 20 15:39:13 2007
LCU_PTV_patch_dummy_1.3.tcl	1.3	Tue Aug 28 07:59:01 2007

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   2007_12_01_05_16_hercdmu_hpws23_REALTIME_EMC_nois_TestEnvironment_REALTIME
LPS_SCOE_checks.tcl                          1.1          Wed Jul 20 10:35:13 2005
Llock_Close_Shell.tcl                        1.1          Thu Oct 18 15:48:10 2007
Llock_Open_Shell.tcl                         1.1          Thu Oct 18 15:48:10 2007
MTL_PACS_restart_cata_RMS48h.tcl             1.1          Mon May 19 11:21:26 2008
MTL_Tool.tcl                                 1.5          Thu Oct 11 14:17:30 2007
MTL_tclgen15b_H_IST0_MTL_D079_080_20070504_v01.tcl 1.1          Sun May 18
12:06:39 2008
MTL_tclgen15b_H_IST0_MTL_D080_081_20070504_v01.tcl 1.1          Sun May 18
12:06:20 2008
MTL_tclgen15b_H_IST0_MTL_D080_081_20070504_v01_1600_no1.tcl 1.1          Sun May
18 12:07:42 2008
MTL_tclgen15b_H_IST0_MTL_D080_081_20070504_v01_1600_no2.tcl 1.1          Sun May
18 12:08:01 2008
MTL_tclgen15b_H_IST0_MTL_D080_081_20070504_v01_1600_no3.tcl 1.1          Sun May
18 12:08:27 2008
MTL_tclgen15b_H_IST0_MTL_D080_081_20070504_v01_1600_no4.tcl 1.1          Sun May
18 12:08:38 2008
MTL_tclgen15b_H_IST0_MTL_D080_081_20070504_v01_1600_no5.tcl 1.1          Sun May
18 12:08:53 2008
MTL_tclgen15b_H_IST0_MTL_D081_082_20070504_v01.tcl 1.1          Sun May 18
12:09:12 2008
MTL_tclgen15b_H_IST0_MTL_D081_082_20070504_v01_curtailed.tcl 1.1          Wed
May 23 17:20:35 2007
MTL_tclgen24_H_IST0_MTL_D079_080_20070424_v02.tcl 1.1          Mon May  7
12:39:26 2007
MTL_tclgen24_H_IST0_MTL_D079_080_20070424_v02_2007_127.tcl 1.1          Mon May
7 12:39:38 2007
MTL_tclgen24_H_IST0_MTL_D080_081_20070424_v02.tcl 1.1          Mon May  7
12:39:54 2007
MTL_tclgen24b_H_IST0_MTL_D080_081_20070424_v02_seg01_2007_127.tcl 1.1
Mon May  7 12:40:07 2007
MTL_tclgen24b_H_IST0_MTL_D080_081_20070424_v02_seg02_2007_127.tcl 1.1
Mon May  7 12:40:19 2007
MTL_tclgen24b_H_IST0_MTL_D080_081_20070424_v02_seg03_2007_127.tcl 1.1
Mon May  7 12:40:29 2007
MTL_tclgen24b_H_IST0_MTL_D080_081_20070424_v02_seg04_2007_127.tcl 1.1
Mon May  7 12:40:41 2007
MTL_tclgen24b_H_IST0_MTL_D080_081_20070424_v02_seg05_2007_127.tcl 1.1
Mon May  7 12:40:51 2007
MTL_tclgen24b_H_IST0_MTL_D080_081_20070424_v02_seg06_2007_127.tcl 1.1
Mon May  7 12:41:04 2007
MTL_tclgen24b_H_IST0_MTL_D080_081_20070424_v02_seg07_2007_127.tcl 1.1
Mon May  7 12:41:17 2007
MTL_tclgen24b_H_IST0_MTL_D080_081_20070504_v01_cut_for_PACS_OD_debug.tcl 1.1
     Wed May 23 17:21:57 2007
MTL_tclgen24b_H_IST0_MTL_D081_082_20070504_v01_AHHDA002cr.tcl 1.1          Sun
May 18 10:50:48 2008
MTL_tclgen24b_H_IST0_MTL_D081_082_20070504_v01_cut_for_SPIREphot_OD_debug.tcl
1.1          Wed May 23 17:22:54 2007
MTL_tclgen24b_H_IST0_MTL_D082_083_20070504_v01_curtailed.tcl 1.1          Wed
May 23 17:23:52 2007
MTL_tclgen24b_H_IST0_MTL_D082_083_20070504_v01_no1.tcl 1.1          Wed May 23
17:24:50 2007
MTL_tclgen24b_H_IST0_MTL_D082_083_20070504_v01_no2.tcl 1.1          Wed May 23
17:26:06 2007
MTL_tclgen31_H_IST0_MTL_D079_080_20070504_v01_48h_M2_M2B.tcl 1.1          Sun
May 18 07:09:52 2008
MTL_tclgen31_H_IST0_MTL_D080_081_20070504_v01_48h_M1_M2_M2B_M3_M6.tcl 1.1
  Sun May 18 07:11:12 2008
MTL_tclgen31_H_IST0_MTL_D080_081_20070504_v01_48h_M1_M2_M2B_M3_M6_SEG000_1500_cn
t.tcl 1.1          Sun May 18 07:58:47 2008
MTL_tclgen31_H_IST0_MTL_D080_081_20070504_v01_48h_M1_M2_M2B_M3_M6_SEG001_1500_cn
t.tcl 1.1          Sun May 18 07:59:59 2008
MTL_tclgen31_H_IST0_MTL_D080_081_20070504_v01_48h_M1_M2_M2B_M3_M6_SEG002_1500_cn
t.tcl 1.1          Sun May 18 08:01:11 2008
MTL_tclgen31_H_IST0_MTL_D080_081_20070504_v01_48h_M1_M2_M2B_M3_M6_SEG003_1500_cn
t.tcl 1.1          Sun May 18 08:02:27 2008
MTL_tclgen31_H_IST0_MTL_D080_081_20070504_v01_48h_M1_M2_M2B_M3_M6_SEG004_1500_en
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   2007_12_01_05_16_hercdmu_hpws23_REALTIME_EMC_nois_TestEnvironment_REALTIME
d.tcl 1.1          Sun May 18 08:03:42 2008
MTL_tclgen31_H_IST0_MTL_D081_082_20070504_v01_48h_M2_M2B.tcl 1.1          Sun
May 18 07:34:27 2008
MTL_tclgen31_H_IST0_MTL_D081_082_20070504_v01_48h_M2_M2B_SEG000_900_cnt.tcl 1.1
        Sun May 18 08:04:47 2008
MTL_tclgen31_H_IST0_MTL_D081_082_20070504_v01_48h_M2_M2B_SEG001_900_end.tcl 1.1
        Sun May 18 08:06:00 2008
MTL_tclgen31_H_IST0_MTL_D081_082_20070504_v01_48h_patch_after_PACS_OFF_NCR.tcl
1.1          Mon May 19 08:31:40 2008
MTL_tclgen31_H_IST0_MTL_D081_082_20070504_v01_48h_patch_after_run_time_NCR.tcl
1.1          Mon May 19 05:30:59 2008
MTL_tclgen31_H_IST0_MTL_D082_083_20070504_v01_48h_M4_M5_M7.tcl 1.1          Sun
May 18 07:38:03 2008
MTL_tclgen31_H_IST0_MTL_D082_083_20070504_v01_48h_M4_M5_M7_SEG000_1200_cnt.tcl
1.1          Sun May 18 08:07:15 2008
MTL_tclgen31_H_IST0_MTL_D082_083_20070504_v01_48h_M4_M5_M7_SEG000_1200_unbugged_
cnt.tcl 1.1          Mon May 19 12:48:10 2008
MTL_tclgen31_H_IST0_MTL_D082_083_20070504_v01_48h_M4_M5_M7_SEG001_1200_end.tcl
1.1          Sun May 18 08:08:18 2008
MTL_tclgen31_H_IST0_MTL_Dummy_D083_084_20070504_v01_48h_M1_M2_M3.tcl 1.1
 Sun May 18 07:40:44 2008
MTL_tclgen31_H_IST0_MTL_Dummy_D083_084_20070504_v01_48h_M1_M2_M3_SEG000_1500_cnt
.tcl 1.1          Sun May 18 08:09:43 2008
MTL_tclgen31_H_IST0_MTL_Dummy_D083_084_20070504_v01_48h_M1_M2_M3_SEG001_1500_cnt
.tcl 1.1          Sun May 18 08:10:52 2008
MTL_tclgen31_H_IST0_MTL_Dummy_D083_084_20070504_v01_48h_M1_M2_M3_SEG002_1500_cnt
.tcl 1.1          Sun May 18 08:12:03 2008
MTL_tclgen31_H_IST0_MTL_Dummy_D083_084_20070504_v01_48h_M1_M2_M3_SEG003_1500_cnt
.tcl 1.1          Sun May 18 08:13:17 2008
MTL_tclgen31_H_IST0_MTL_Dummy_D083_084_20070504_v01_48h_M1_M2_M3_SEG004_1500_end
.tcl 1.1          Sun May 18 08:14:29 2008
OBCP_chop_scan_phot_OBS_Shell.tcl        1.1          Thu Oct 18 15:48:10 2007
P102999SCVT901_ASDDBGPACS_PWR_ON.tcl     1.11         Thu Aug 16 11:06:28 2007
P102999SCVT902_ASDDBGPACS_PWR_OFF.tcl    1.8          Thu Aug 16 11:07:04 2007
P102999SCVT903_SSMM_FILL_IN.tcl          1.5          Wed Jun  6 07:12:29 2007
P102999SCVT904_ASDGENPACS_NomSpect.tcl   1.3          Fri Oct 26 14:37:07 2007
P102999SCVT905_ASDISTPACS_PWR_ON_N.tcl   1.9          Fri Oct 19 08:47:40 2007
P102999SCVT906_ASDISTPACS_PWR_OFF_N.tcl  1.6          Mon Oct 15 10:48:04 2007
P102999SCVT907_ASDISTPACS_PWR_ON_R.tcl   1.9          Fri Oct 19 07:47:44 2007
P102999SCVT908_ASDISTPACS_PWR_OFF_R.tcl  1.6          Mon Oct 15 10:48:27 2007
P102999SCVT909_ASDGENPACS_PWR_ON_N.tcl   1.11         Wed Oct 31 15:10:14 2007
P102999SCVT910_ASDGENPACS_PWR_OFF_N.tcl  1.8          Mon Oct 15 10:45:54 2007
P102999SCVT911_ASDGENPACS_PWR_ON_R.tcl   1.12         Wed Oct 31 15:10:54 2007
P102999SCVT912_ASDGENPACS_PWR_OFF_R.tcl  1.7          Mon Oct 15 10:47:39 2007
P102999SCVT913_ASDGENPACS_BurstMode.tcl  1.2          Fri Oct 26 10:29:59 2007
P102999SCVT914_ASDGENPACS_DisSciDwnLnk.tcl 1.3          Sat Oct 27 14:19:26 2007
P102999SCVT915_ASDGENPACS_EnSciDwnLnk.tcl 1.3          Sat Oct 27 14:20:17 2007
PACS_Burstmode_Reset_OBS_Shell.tcl       1.1          Thu Oct 18 15:48:10 2007
PACS_Burstmode_Setup_OBS_Shell.tcl       1.1          Thu Oct 18 15:48:10 2007
PACS_CS_SFT_Cold_OBS_Shell.tcl           1.1          Thu Oct 18 15:48:10 2007
PACS_CS_SFT_Warm_OBS_Shell.tcl           1.1          Thu Oct 18 15:48:10 2007
PACS_Chopper_EnDis_PlateauTest_ast_OBS_Shell.tcl 1.1          Thu Oct 18
15:48:10 2007
PACS_Chopper_EnDis_Test_ast1_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:10 2007
PACS_Chopper_EnDis_Test_ast300_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:10
2007
PACS_Chopper_SFT_Cold_OpenLoop_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:10
2007
PACS_Chopper_SFT_Warm_OpenLoop_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:10
2007
PACS_Chopper_uk_move_12000_ast_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:10
2007
PACS_Chopper_uk_move_18000_ast_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:10
2007
PACS_Chopper_uk_move_21000_ast_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:10
2007
PACS_Chopper_uk_move_3000_ast_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:10
2007
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PACS_Close_Launch_Lock_OBS_Shell.tcl      1.1          Thu Oct 18 15:48:10 2007
PACS_DPU_Fill_SSMM_from_SAFE.tcl          1.3          Sun May 18 07:30:42 2008
PACS_DPU_Nominal_Science_Flow.tcl         1.2          Sun May 18 07:32:29 2008
PACS_Diaghk_Reset_OBS_Shell.tcl           1.1          Thu Oct 18 15:48:10 2007
PACS_Diagnostic_Check.tcl                 1.1          Wed Aug 15 07:16:03 2007
PACS_Dis_chopper_ast_OBS_Shell.tcl        1.1          Thu Oct 18 15:48:10 2007
PACS_Disable_HK_OBS_Shell.tcl             1.1          Thu Oct 18 15:48:10 2007
PACS_En_chopper_ast_OBS_Shell.tcl         1.1          Thu Oct 18 15:48:10 2007
PACS_GeGa_SFT_Init_Cold_He2_OBS_Shell.tcl 1.1           Thu Oct 18 15:48:10 2007
PACS_GeGa_SFT_Init_Cold_OBS_Shell.tcl     1.1          Thu Oct 18 15:48:10 2007
PACS_GeGa_SFT_Init_Warm_OBS_Shell.tcl     1.1          Thu Oct 18 15:48:10 2007
PACS_Open_Launch_Lock_OBS_Shell.tcl       1.1          Thu Oct 18 15:48:10 2007
PACS_Phot_Cold_Startinputsignal_OBS_Shell.tcl 1.1           Thu Oct 18 15:48:10
2007
PACS_Phot_DPU_DMC_Setup_OBS_Shell.tcl     1.1          Thu Oct 18 15:48:10 2007
PACS_Phot_Fil_Diaghk_Setup_OBS_Shell.tcl  1.1          Thu Oct 18 15:48:10 2007
PACS_Phot_Fil_Testseq_OBS_Shell.tcl       1.1          Thu Oct 18 15:48:10 2007
PACS_Phot_Fil_nturns_OBS_15_Shell.tcl     1.1          Thu Oct 18 15:48:10 2007
PACS_Phot_HK_Setup_OBS_Shell.tcl          1.1          Thu Oct 18 15:48:10 2007
PACS_Phot_SFT_Cooler_OBS_Shell.tcl        1.1          Thu Oct 18 15:48:10 2007
PACS_Phot_SFT_Cooler_Warm_OBS_Shell.tcl   1.1          Thu Oct 18 15:48:10 2007
PACS_Phot_SPU_Reset_OBS_Shell.tcl         1.1          Thu Oct 18 15:48:10 2007
PACS_Phot_SPU_Setup.tcl                   1.2          Wed Feb 14 16:50:26 2007
PACS_Phot_SPU_Setup_OBS_Shell.tcl         1.1          Thu Oct 18 15:48:10 2007
PACS_Phot_Sequencer_Setup_OBS_Shell.tcl   1.1          Thu Oct 18 15:48:10 2007
PACS_Phot_Switchoff_OBS_Shell.tcl         1.1          Thu Oct 18 15:48:10 2007
PACS_Phot_Switchon_OBS_Shell.tcl          1.1          Thu Oct 18 15:48:10 2007
PACS_Phot_Warm_Startinputsignal_OBS_Shell.tcl 1.1           Thu Oct 18 15:48:11
2007
PACS_SAFE_Mode.tcl                        1.1          Mon May  7 13:41:48 2007
PACS_Spec_CRE_01pF0bias_Setup_Warm_OBS_Shell.tcl 1.1          Thu Oct 18
15:48:11 2007
PACS_Spec_CRE_1pF0bias_Setup_Warm_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:11
2007
PACS_Spec_CRE_Setup_Cold_HeI_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_CRE_Setup_Cold_OBS_Shell.tcl    1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_CRE_Setup_Warm_OBS_Shell.tcl    1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Curing_OBS_Shell.tcl            1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Fil_Diaghk_Setup_OBS_Shell.tcl  1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Fil_Testseq_OBS_Shell.tcl       1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Fil_nturns_OBS_15_Shell.tcl     1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_FlashHeat_Diaghk_Setup_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:11
2007
PACS_Spec_Flash_IST_Shell.tcl             1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Flash_SFT_Shell.tcl             1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Flash_SFT_Warm_Shell.tcl        1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Flash_SWOF_OBS_Shell.tcl        1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Flash_SWON_OBS_Shell.tcl        1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_DEG_MODE_Diaghk_Setup_OBS_Shell.tcl 1.1          Thu Oct 18
15:48:11 2007
PACS_Spec_Gra_Diaghk_SINCOS_Setup_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:11
2007
PACS_Spec_Gra_Diaghk_Setup_OBS_Shell.tcl  1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_Healthcheck_OBS_Shell.tcl   1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_IST_Ampl_OBS_Shell.tcl      1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_IST_Check_1_OBS_Shell.tcl   1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_IST_Check_2_OBS_Shell.tcl   1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_IST_Check_4_OBS_Shell.tcl   1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_IST_Disable_OBS_Shell.tcl   1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_IST_Enable_OBS_Shell.tcl    1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_IST_Full_Charac_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:11
2007
PACS_Spec_Gra_IST_Home_OBS_Shell.tcl      1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_IST_Move_Abs_OBS_Shell.tcl  1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_IST_Move_Rel_OBS_Shell.tcl  1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_IST_PID_Loop_Less_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:11
2007
PACS_Spec_Gra_IST_PID_Loop_OBS_Shell.tcl  1.1          Thu Oct 18 15:48:11 2007
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2007_12_01_05_16_hercdmu_hpws23_REALTIME_EMC_nois_TestEnvironment_REALTIME

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PACS_Spec_Gra_IST_SWOF_OBS_Shell.tcl      1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_IST_SWON_OBS_Shell.tcl      1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_IST_Set_Ctrl_Par_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:11
2007
PACS_Spec_Gra_Mec_Setup_Cold_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_Mec_Setup_Cold_Redun_OBS_Shell.tcl 1.1          Thu Oct 18
15:48:11 2007
PACS_Spec_Gra_SFT_Warm_OBS_Shell.tcl      1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_Slew_Time_Cal_Shell.tcl     1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_HK_Setup_OBS_Shell.tcl          1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Heat_FFT_Shell.tcl              1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Heat_SFT_Shell.tcl              1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Heat_SWOF_OBS_Shell.tcl         1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Heat_SWON_OBS_Shell.tcl         1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_QuickFullSpectrum_OBS_CS1_Shell.tcl 1.1          Thu Oct 18 15:48:11
2007
PACS_Spec_QuickFullSpectrum_OBS_CS2_Shell.tcl 1.1          Thu Oct 18 15:48:11
2007
PACS_Spec_SPU_Buffer_Setup_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_SPU_Reset_OBS_Shell.tcl         1.1          Thu Oct 18 15:48:12 2007
PACS_Spec_SPU_Setup.tcl                   1.2          Wed Feb 14 16:52:22 2007
PACS_Spec_Time_Constant_IST_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:12 2007
PACS_StartAutonomy_Function_14_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:12
2007
PACS_StartAutonomy_Function_17_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:12
2007
PACS_SwOff_chopper_ast_OBS_Shell.tcl      1.1          Thu Oct 18 15:48:12 2007
PACS_SwOn_chopper_ast_OBS_Shell.tcl       1.1          Thu Oct 18 15:48:12 2007
PACS_Switch_Off.tcl                       1.1          Wed Feb 14 07:33:54 2007
PACS_Switch_Off_CCS.tcl                   1.2          Wed Feb 14 16:55:43 2007
PACS_Switch_Off_CCS_Nominal.tcl           1.1          Mon Oct 15 09:54:10 2007
PACS_Switch_Off_CCS_Redundant.tcl         1.1          Mon Oct 15 09:55:00 2007
PACS_Switch_On.tcl                        1.1          Wed Feb 14 07:34:31 2007
PACS_Switch_On_CCS.tcl                    1.2          Wed Feb 14 16:56:24 2007
PACS_Switch_On_CCS_Nominal.tcl            1.1          Mon Oct 15 09:53:52 2007
PACS_Switch_On_CCS_Redundant.tcl          1.1          Mon Oct 15 09:54:35 2007
PACS_Switch_On_CCS_SPU_shifted.tcl        1.1          Tue Jul 31 12:50:38 2007
PACS_Switch_On_DPU_SPULLSW.tcl            1.3          Sat Aug  4 10:09:29 2007
PACS_Wave_Cal_Shell.tcl                   1.1          Thu Oct 18 15:48:12 2007
PACS_pre_test.tcl                         1.1          Wed Feb 14 10:34:59 2007
PCS__mini_IST.tcl                         1.1          Thu Oct 27 00:56:50 2005
PHOT_TestPattern_obs_Shell.tcl            1.1          Thu Oct 18 15:48:12 2007
PHOT_VrlVhBlind_OBS_Shell.tcl             1.1          Thu Oct 18 15:48:12 2007
PHOT_all_aots_OBS_Shell.tcl               1.1          Thu Oct 18 15:48:12 2007
PHOT_cal_recipes_OBS_Shell.tcl            1.1          Thu Oct 18 15:48:12 2007
PHOT_chopped_photometry_OBS_Shell.tcl     1.1          Thu Oct 18 15:48:12 2007
PHOT_low_freq_OBS_Shell.tcl               1.1          Thu Oct 18 15:48:12 2007
PHOT_low_freq_direct_OBS_Shell.tcl        1.1          Thu Oct 18 15:48:12 2007
PHOT_redArray_DDCS_IST_OBS_Shell.tcl      1.1          Thu Oct 18 15:48:12 2007
PHOT_redArray_alternatePolarisation_DDCS_IST_OBS_Shell.tcl 1.1          Thu Oct
18 15:48:12 2007
PHOT_redArray_alternatePolarisation_direct_IST_OBS_Shell.tcl 1.1          Thu
Oct 18 15:48:12 2007
PHOT_redArray_direct_IST_OBS_Shell.tcl    1.1          Thu Oct 18 15:48:12 2007
PHOT_saturation_OBS_Shell.tcl             1.1          Thu Oct 18 15:48:12 2007
PHOT_setup_OBS_EMC_Shell.tcl              1.1          Thu Oct 18 15:48:12 2007
PHOT_setup_OBS_Shell.tcl                  1.1          Thu Oct 18 15:48:12 2007
PHOT_setup_redundant_OBS_Shell.tcl        1.1          Thu Oct 18 15:48:12 2007
PHOT_spu_data_rate_obs_Shell.tcl          1.1          Thu Oct 18 15:48:12 2007
PHOT_spu_reset_OBS_Shell.tcl              1.1          Thu Oct 18 15:48:12 2007
PHOT_spu_setup_OBS_Shell.tcl              1.1          Thu Oct 18 15:48:12 2007
PHOT_thermal_OBS_Shell.tcl                1.1          Thu Oct 18 15:48:12 2007
PHOT_timeconst_fluxchange_OBS_Shell.tcl   1.1          Thu Oct 18 15:48:12 2007
PIERO PROVA RM DISABLE.tcl                1.1          Tue Feb 28 11:14:01 2006
PIERO_PROVA_RM_DISABLE.tcl                1.5          Thu Mar  2 17:26:28 2006
PROVA ALE.tcl                             1.2          Mon Nov  7 16:22:54 2005
PROVA_ALESSIO.tcl                         1.4          Tue May 22 14:24:10 2007
PROVA_luigi.tcl                           1.6          Thu Nov 22 19:44:41 2007
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2007_12_01_05_16_hercdmu_hpws23_REALTIME_EMC_nois_TestEnvironment_REALTIME

Phot_prepareEMC_DDCS_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:12 2007
Phot_prepareEMC_direct_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:12 2007
PhotometrytoNonprime.tcl	1.2	Wed Feb 14 16:57:56 2007
R102479ECVT001_TWTA_CFG.tcl	1.5	Wed Aug 30 07:55:15 2006
R102479ECVT002_XPND_LCL.tcl	1.7	Fri Sep 15 07:09:56 2006
R102479ECVT003_RX_BIT_RATE_SET.tcl	1.6	Tue May 29 21:21:47 2007
R102479ECVT004_XPND_TX.tcl	1.4	Wed Aug 30 08:02:12 2006
R102479ECVT005_XPND1_TC_1553.tcl	1.5	Thu Jun 1 13:43:25 2006
R102479ECVT006_XPND2_TC_1553.tcl	1.5	Thu Jun 1 13:47:13 2006
R102479ECVT008_CDMU_TM_RATE.tcl	1.6	Tue Nov 7 11:46:15 2006
R102479ECVT009_UNITS_SELECTION.tcl	1.10	Mon May 21 22:22:32 2007
R102479ECVT011_TWTA_CFG_ASW.tcl	1.2	Thu Sep 21 10:34:06 2006
R102479ECVT012_XPND_CFG_ASW.tcl	1.3	Thu Sep 21 16:24:15 2006
R102479EMVT011_ANALOGUE_TM.tcl	1.9	Thu May 24 23:15:54 2007
R102479EMVT012_DIGITAL_TM.tcl	1.7	Thu May 24 23:17:34 2007
R102479ETVT007_REPEAT_TC.tcl	1.8	Wed Sep 20 15:50:51 2006
R102479ETVT009_CHECK_RX_LOCK.tcl	1.5	Thu May 24 23:19:45 2007
R102479ETVT010_RFDN_CONF.tcl	1.4	Wed Aug 30 08:01:35 2006
R102479ETVT011_RFDN_CONF_CHECK_TWTA.tcl	1.3	Wed May 23 19:39:09 2007
R102479ETVT013_REPEAT_TC_FAST.tcl	1.1	Thu Sep 21 16:26:19 2006
R102479SMXX001_XPND_HUM_TXT.tcl	1.2	Wed Sep 19 07:08:23 2007
R102479SPVT001_RX1_LOCK_ACQ.tcl	1.9	Mon Sep 25 16:32:54 2006
R102479SPVT002_RX1_LOCK_MNT.tcl	1.10	Wed Sep 27 07:13:14 2006
R102479SPVT003_RX1_TC_LOW_BIT_RATE.tcl	1.6	Mon Sep 25 15:39:13 2006
R102479SPVT004_RX1_TC_HIGH_BIT_RATE.tcl	1.6	Tue Sep 26 11:36:47 2006
R102479SPVT005_TX1_DL_CARRIER.tcl	1.9	Wed Sep 27 12:09:22 2006
R102479SPVT006_TX1_DL_TM_BAND.tcl	1.6	Thu Sep 28 09:16:52 2006
R102479SPVT008_RX1_TC_LBR_RNG.tcl	1.5	Thu Sep 28 10:57:20 2006
R102479SPVT009_RX1_TC_MBR_RNG.tcl	1.5	Thu Sep 28 11:02:37 2006
R102479SPVT010_TX1_DL_RNG_SIGNAL.tcl	1.4	Mon Sep 25 15:55:45 2006
R102479SPVT011_TX1_DL_RNG_PERF.tcl	1.5	Mon Sep 25 16:00:24 2006
R102479SPVT012_TX1_TM+RG.tcl	1.3	Mon Sep 25 16:09:56 2006
R102479SPVT012_TX1_TM_RG_SIGNAL.tcl	1.2	Tue Jan 24 08:33:43 2006
R102479SPVT014_TC1_LBR_LOW_DOPPLER.tcl	1.4	Mon Sep 25 16:14:40 2006
R102479SPVT015_TC1_MBR_HIGH_DOPPLER.tcl	1.4	Mon Sep 25 16:15:58 2006
R102479SPVT016_TC1_MBR_LOW_DOPPLER.tcl	1.4	Mon Sep 25 16:21:28 2006
R102479SPVT017_TC+TM+RNG_END2END1.tcl	1.4	Wed Oct 4 05:24:50 2006
R102479SPVT017_TC_TM_RNG_END2END1.tcl	1.3	Wed Aug 30 08:03:06 2006
R102479SPVT020_RFDN_UFT.tcl	1.4	Sat Sep 30 09:42:24 2006
R102479SPVT031_RX2_LOCK_ACQ.tcl	1.7	Tue May 29 20:36:28 2007
R102479SPVT032_RX2_LOCK_MNT.tcl	1.8	Wed May 30 15:20:16 2007
R102479SPVT033_RX2_TC_LOW_BIT_RATE.tcl	1.7	Wed May 30 15:18:52 2007
R102479SPVT034_RX2_TC_HIGH_BIT_RATE.tcl	1.5	Wed May 30 15:19:29 2007
R102479SPVT035_TX2_DL_CARRIER.tcl	1.6	Wed May 30 15:19:52 2007
R102479SPVT036_TX2_DL_TM_BAND.tcl	1.5	Fri May 25 03:02:24 2007
R102479SPVT038_RX2_TC_LBR_RNG.tcl	1.4	Wed May 30 23:26:52 2007
R102479SPVT039_RX2_TC_MBR_RNG.tcl	1.4	Wed May 30 23:18:54 2007
R102479SPVT040_TX2_DL_RNG_SIGNAL.tcl	1.5	Fri Jun 1 22:05:28 2007
R102479SPVT041_TX2_DL_RNG_PERF.tcl	1.5	Wed Sep 27 14:07:33 2006
R102479SPVT042_TX2_TM+RG.tcl	1.3	Wed May 30 23:26:42 2007
R102479SPVT042_TX2_TM_RG_SIGNAL.tcl	1.2	Wed Aug 30 08:03:20 2006
R102479SPVT044_TC2_LBR_LOW_DOPPLER.tcl	1.4	Fri Jun 1 16:09:31 2007
R102479SPVT045_TC2_MBR_HIGH_DOPPLER.tcl	1.5	Wed May 30 16:00:22 2007
R102479SPVT046_TC2_MBR_LOW_DOPPLER.tcl	1.4	Mon Jun 4 06:27:37 2007
R102479SPVT047_TC_TM_RNG_END2END2.tcl	1.4	Mon Jun 4 06:46:30 2007
R102479SPVT048_TTC_MNGM.tcl	1.3	Thu May 31 01:52:16 2007
R102479SPVT100_TTC_IST_HEALTH_CHECK.tcl	1.6	Wed Dec 6 07:28:52 2006
R102479SPVT101_IST_RFDN_UFT.tcl	1.2	Thu Jul 20 07:30:32 2006
R102479SPVT103_IST_LOCK_ACQ.tcl	1.8	Thu May 24 23:24:53 2007
R102479SPVT104_IST_END2END.tcl	1.7	Thu May 24 23:27:21 2007
R102479SPVT105_IST_DL_VERIFICATION.tcl	1.4	Wed Nov 29 17:32:32 2006
R102479SPVT107_IST_LAUNCH_2_SUN.tcl	1.9	Tue Nov 6 16:06:54 2007
R102479SPVT108_IST_SUN_2_NOM.tcl	1.5	Tue Nov 6 16:11:27 2007
R102479SPVT109_IST_NOM_2_SURV.tcl	1.5	Fri Apr 13 07:47:44 2007
R102479SPVT110_IST_BEFORE_SAM.tcl	1.3	Wed Nov 15 11:56:38 2006
R102479SPVT111_IST_FDIR_2_SAM.tcl	1.2	Wed Nov 15 11:59:30 2006
R102479SPVT113_IST_LOCK_ACQ2.tcl	1.7	Tue Oct 24 09:32:31 2006
R102479SPVT114_IST_END2END2.tcl	1.4	Thu Oct 26 14:55:22 2006

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   2007_12_01_05_16_hercdmu_hpws23_REALTIME_EMC_nois_TestEnvironment_REALTIME
R102479SPVT115_IST_DL_VERIFICATION2.tcl  1.5          Thu Oct 26 14:29:50 2006
R102479SPVT116_IST_DUMP_ON.tcl           1.4          Tue Nov  6 16:14:52 2007
R102479SPVT117_IST_GMSK_2_NOM.tcl        1.1          Tue Feb 20 18:12:33 2007
R102479SPVT117_IST_NOM_2_5k.tcl          1.5          Thu Jul 12 17:30:42 2007
R102479SPVT120_IST_CONF_TTC_HC.tcl       1.3          Wed Apr 18 09:36:56 2007
R102479SPVT121_IST_CONF_TTC2_HC_OFF.tcl  1.2          Wed Apr 18 09:38:02 2007
R102479SPVT122_IST_TTC_COMMISSIONING.tcl 1.6          Thu Nov 22 18:24:22 2007
RMS_DTCP_ASTRIUM.tcl                     1.6          Tue May 20 05:39:20 2008
S102999SCVT005_ASDSFTSPIR_PWR_ON_P.tcl   1.6          Wed Oct 31 15:04:29 2007
S102999SCVT006_ASDSFTSPIR_PWR_ON_R.tcl   1.7          Wed Oct 31 15:04:54 2007
S102999SCVT007_ASDSFTSPIR_PWR_OFF_P.tcl  1.4          Sat Nov 24 11:39:39 2007
S102999SCVT008_ASDSFTSPIR_PWR_OFF_R.tcl  1.5          Sat Nov 24 11:40:04 2007
S102999SCVT009_ASDWFTSPIR_PWR_ON_P.tcl   1.6          Thu Nov 15 11:09:46 2007
S102999SCVT010_ASDWFTSPIR_PWR_ON_R.tcl   1.7          Thu Nov 15 11:10:15 2007
S102999SCVT011_ASDWFTSPIR_PWR_OFF_P.tcl  1.5          Sat Nov 24 11:41:22 2007
S102999SCVT012_ASDWFTSPIR_PWR_OFF_R.tcl  1.5          Sat Nov 24 11:41:47 2007
S102999SCVT013_ASDWFTSPIR_SAFE_OFF_P.tcl 1.2          Mon Oct 22 11:32:03 2007
S102999SCVT014_ASDWFTSPIR_SAFE_OFF_R.tcl 1.2          Mon Oct 22 11:32:44 2007
S102999SCVT015_ASDISTSPIR_STBY2PHOT.tcl  1.2          Thu Nov 15 17:19:41 2007
S102999SCVT016_ASDISTSPIR_PHOT2STBY.tcl  1.2          Thu Nov 15 17:20:14 2007
S102999SCVT017_ASDGENSPIR_PWR_ON_P.tcl   1.2          Thu Nov 15 17:33:55 2007
S102999SCVT018_ASDGENSPIR_PWR_ON_R.tcl   1.1          Thu Nov 15 17:31:07 2007
S102999SCVT019_ASDGENSPIR_PWR_OFF_P.tcl  1.2          Sat Nov 24 11:42:41 2007
S102999SCVT020_ASDGENSPIR_PWR_OFF_R.tcl  1.2          Sat Nov 24 11:45:02 2007
S102999SCVT9013_ASDDBGSPIR_PWR_ON_P_INITMD.tcl 1.2          Thu Jun 28 06:54:30
2007
S102999SCVT9014_ASDDBGSPIR_SW_UPL.tcl    1.3          Mon Jul  2 16:40:01 2007
S102999SCVT901_ASDDBGSPIR_PWR_ON_P.tcl   1.20         Thu Nov 15 17:58:47 2007
S102999SCVT902_ASDDBGSPIR_PWR_ON_R.tcl   1.16         Thu Nov 15 17:58:25 2007
S102999SCVT903_ASDDBGSPIR_PWR_OFF_P.tcl  1.17         Sat Nov 24 11:45:30 2007
S102999SCVT904_ASDDBGSPIR_PWR_OFF_R.tcl  1.10         Sat Nov 24 11:45:53 2007
S102999SCVT907_ASDDBGSPIR_STBY2PHTSTBY.tcl 1.2          Wed Jun  6 07:01:01 2007
S102999SCVT908_ASDDBGSPIR_PHTSTBY2STBY.tcl 1.2          Wed Jun  6 07:01:34 2007
S102999SCVT909_ASDDBGSPIR_STBY2SPECSTBY.tcl 1.2          Wed Jun  6 07:02:03
2007
S102999SCVT910_ASDDBGSPIR_SPECSTBY2STBY.tcl 1.2          Wed Jun  6 07:02:32
2007
S102999SCVT911_ASDDBGSPIR_STBY2OPS.tcl   1.3          Sat Oct 27 15:03:08 2007
S102999SCVT912_ASDDBGSPIR_OPS2STBY.tcl   1.3          Sat Oct 27 15:03:44 2007
S102999SCVT915_ASDDBGSPIR_PWR_OFF_P_INITMD.tcl 1.1          Thu Jun 28 11:20:12
2007
SPEC_CRE_setup_OBS_Shell.tcl             1.1          Thu Oct 18 15:48:12 2007
SPEC_CS_imt511_OBS_Shell.tcl             1.1          Thu Oct 18 15:48:12 2007
SPEC_Chopper_dhk_5hk_1khz_OBS_Shell.tcl  1.1          Thu Oct 18 15:48:12 2007
SPEC_Chopper_dhk_imt_OBS_Shell.tcl       1.1          Thu Oct 18 15:48:12 2007
SPEC_Chopper_dhk_stop_OBS_Shell.tcl      1.1          Thu Oct 18 15:48:12 2007
SPEC_Chopper_fmilt076spec_detsel_llc_ast_V1_OBS_Shell.tcl 1.1          Thu Oct
18 15:48:12 2007
SPEC_Chopper_fmilt076spec_detsel_llc_ast_V2_OBS_Shell.tcl 1.1          Thu Oct
18 15:48:12 2007
SPEC_Chopper_imt504_OBS_Shell.tcl        1.1          Thu Oct 18 15:48:12 2007
SPEC_Chopper_imt504_ast_OBS_Shell.tcl    1.1          Thu Oct 18 15:48:13 2007
SPEC_Gra_move_abs_raw_obs_500_Shell.tcl  1.1          Thu Oct 18 15:48:13 2007
SPEC_Gra_move_abs_raw_obs_775_Shell.tcl  1.1          Thu Oct 18 15:48:13 2007
SPEC_Prepare_EMC_Shell.tcl               1.1          Thu Oct 18 15:48:13 2007
SPEC_Readouts_per_Ramp_Test_01_Shell.tcl 1.1          Thu Oct 18 15:48:13 2007
SPEC_Readouts_per_Ramp_Test_02_Shell.tcl 1.1          Thu Oct 18 15:48:13 2007
SPEC_Readouts_per_Ramp_Test_03_Shell.tcl 1.1          Thu Oct 18 15:48:13 2007
SPEC_Readouts_per_Ramp_Test_04_Shell.tcl 1.1          Thu Oct 18 15:48:13 2007
SPEC_Readouts_per_Ramp_Test_05_Shell.tcl 1.1          Thu Oct 18 15:48:13 2007
SPEC_Readouts_per_Ramp_Test_06_Shell.tcl 1.1          Thu Oct 18 15:48:13 2007
SPEC_all_aots_OBS_Shell.tcl              1.1          Thu Oct 18 15:48:13 2007
SPEC_chopped_SED_OBS_Shell.tcl           1.1          Thu Oct 18 15:48:13 2007
SPEC_cre_setup_OBS_capa00_Shell.tcl      1.1          Thu Oct 18 15:48:13 2007
SPEC_cre_setup_OBS_capa1212_Shell.tcl    1.1          Thu Oct 18 15:48:13 2007
SPEC_cre_setup_OBS_capa12_Shell.tcl      1.1          Thu Oct 18 15:48:13 2007
SPEC_dark_current_imt502_OBS_Shell.tcl   1.1          Thu Oct 18 15:48:13 2007
SPEC_detector_imt509_OBS_Shell.tcl       1.1          Thu Oct 18 15:48:13 2007
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SPEC_fov_scan_imt409_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:13 2007
SPEC_setup_CSOFF_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:13 2007
SPEC_setup_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:13 2007
SPEC_setup_redundant_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:13 2007
SPEC_spu_data_rate_obs_Shell.tcl	1.1	Thu Oct 18 15:48:13 2007
SPEC_spu_reset_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:13 2007
SPEC_spu_setup_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:13 2007
SPEC_spu_setup_OBS_Shell_025s.tcl	1.1	Thu Oct 18 15:48:13 2007
SPEC_thermal_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:13 2007
SPIRE-FM-SFT-BSM-OFF-P.tcl	1.1	Tue Sep 11 17:12:25 2007
SPIRE-FM-SFT-BSM-OFF-R.tcl	1.1	Tue Sep 11 17:12:25 2007
SPIRE-FM-SFT-DPU-START-P-PP.tcl	1.1	Tue Sep 11 17:12:25 2007
SPIRE-FM-SFT-DPU-START-P-SP.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-DPU-START-R-PP.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-DPU-START-R-SP.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-DRCU-OFF-P.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-DRCU-OFF-R.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-DRCU-START-P-STEP1.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-DRCU-START-P-STEP2.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-DRCU-START-R-STEP1.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-DRCU-START-R-STEP2.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-FUNC-BSM-01-P.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-FUNC-BSM-01-R.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-FUNC-DCU-01-P.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-FUNC-DCU-01-R.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-FUNC-DCU-04-PHOT-P.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-FUNC-DCU-04-PHOT-R.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-FUNC-DCU-04-SPEC-P.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-FUNC-DCU-04-SPEC-R.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-FUNC-MCU-01-P.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-FUNC-MCU-01-R.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-FUNC-MCU-02-P.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-FUNC-MCU-02-R.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SCU-01-P.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SCU-01-R.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SCU-03-P.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SCU-03-R.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SCU-04-P.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SCU-04-R.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SCU-05-P.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SCU-05-R.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SCU-06-P.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SCU-06-R.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SCU-07-P.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SCU-07-R.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SMEC-01-P.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SMEC-01-R.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-MCU-OFF-P.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-MCU-OFF-R.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-PHOT-JFET-OFF.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-PHOT-JFET-ON-01.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-PHOT-JFET-ON-02.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-PLIA-OFF-P.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-PLIA-OFF-R.tcl	1.1	Tue Sep 11 17:12:28 2007
SPIRE-FM-SFT-SCU-OFF-P.tcl	1.1	Tue Sep 11 17:12:28 2007
SPIRE-FM-SFT-SCU-OFF-R.tcl	1.1	Tue Sep 11 17:12:28 2007
SPIRE-FM-SFT-SLIA-OFF-P.tcl	1.1	Tue Sep 11 17:12:28 2007
SPIRE-FM-SFT-SLIA-OFF-R.tcl	1.1	Tue Sep 11 17:12:28 2007
SPIRE-FM-SFT-SMEC-OFF-P.tcl	1.1	Tue Sep 11 17:12:28 2007
SPIRE-FM-SFT-SMEC-OFF-R.tcl	1.1	Tue Sep 11 17:12:28 2007
SPIRE-FM-SFT-SPEC-JFET-OFF.tcl	1.1	Tue Sep 11 17:12:28 2007
SPIRE-FM-SFT-SPEC-JFET-ON-01.tcl	1.1	Tue Sep 11 17:12:28 2007
SPIRE-FM-SFT-SPEC-JFET-ON-02.tcl	1.1	Tue Sep 11 17:12:28 2007
SPIRE-FM-WFT-BSM-INIT-P.tcl	1.4	Mon Oct 22 12:13:59 2007
SPIRE-FM-WFT-BSM-INIT-R.tcl	1.4	Mon Oct 22 12:13:59 2007
SPIRE-FM-WFT-BSM-OFF-P.tcl	1.4	Mon Oct 22 12:13:59 2007
SPIRE-FM-WFT-BSM-OFF-R.tcl	1.4	Mon Oct 22 12:13:59 2007
SPIRE-FM-WFT-DPU-START-P-SP.tcl	1.4	Mon Oct 22 12:13:59 2007

SPIRE-FM-WFT-DPU-START-R-PP.tcl	1.4	Mon Oct 22 12:13:59 2007
SPIRE-FM-WFT-DRCU-OFF-P.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-DRCU-OFF-R.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-DRCU-START-P-STEP1.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-DRCU-START-P-STEP2.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-DRCU-START-R-STEP1.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-DRCU-START-R-STEP2.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-01-P.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-01-R.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-02C-P.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-02C-R.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-02J-P.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-02J-R.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-03-P.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-03-R.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-05A-P.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-05A-R.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-05B-P.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-05B-R.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-06-P.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-06-R.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-DCU-01-P.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-DCU-01-R.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-DCU-02-P.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-DCU-02-R.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-DCU-03-P.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-DCU-03-R.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-DCU-04-PHOT-P.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-04-PHOT-R.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-04-SPEC-P.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-04-SPEC-R.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-11-PHOT-P.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-11-PHOT-R.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-11-SPEC-P.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-11-SPEC-R.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-13-PHOT-P.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-13-PHOT-R.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-13-SPEC-P.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-13-SPEC-R.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-14-PHOT-P.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-14-PHOT-R.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-14-SPEC-P.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-14-SPEC-R.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-MCU-01-P.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-MCU-01-R.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-MCU-02-P.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-MCU-02-R.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-MCU-03-P.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-MCU-03-R.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-MCU-04-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-MCU-04-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-01-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-01-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-02-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-02-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-03-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-03-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-04-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-04-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-05-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-05-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-06-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-06-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-07-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-07-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-08-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-08-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SMEC-01-P.tcl	1.4	Mon Oct 22 12:14:02 2007

2007_12_01_05_16_hercdmu_hpws23_REALTIME_EMC_nois_TestEnvironment_REALTIME

SPIRE-FM-WFT-FUNC-SMEC-01-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SMEC-02A-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SMEC-02A-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SMEC-02B-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SMEC-02B-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SMEC-03-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SMEC-03-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SMEC-04A-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SMEC-04A-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SMEC-07-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SMEC-07-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SMEC-09-P.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-FM-WFT-FUNC-SMEC-09-R.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-FM-WFT-MCU-OFF-P.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-FM-WFT-MCU-OFF-R.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-FM-WFT-PDET-OFF-P.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-FM-WFT-PDET-OFF-R.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-FM-WFT-SCU-OFF-P.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-FM-WFT-SCU-OFF-R.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-FM-WFT-SDET-OFF-P.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-FM-WFT-SDET-OFF-R.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-FM-WFT-SMEC-INIT-P.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-FM-WFT-SMEC-INIT-R.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-FM-WFT-SMEC-OFF-P.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-FM-WFT-SMEC-OFF-R.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-IST-DBG-DPUON2STBY.tcl	1.6	Thu Sep 13 09:25:20 2007
SPIRE-IST-DBG-LOAD-VM-TABLES.tcl	1.1	Sat Aug 4 11:04:24 2007
SPIRE-IST-DBG-OFF2DPUON-SP.tcl	1.3	Sat Aug 4 11:03:21 2007
SPIRE-IST-DBG-OFF2DPUON.tcl	1.6	Fri Aug 24 11:18:17 2007
SPIRE-IST-DBG-OPS2STBY.tcl	1.6	Sat Aug 4 11:05:34 2007
SPIRE-IST-DBG-PHOT2STBY.tcl	1.1	Wed May 23 09:24:16 2007
SPIRE-IST-DBG-SPEC2STBY.tcl	1.1	Wed May 23 09:24:45 2007
SPIRE-IST-DBG-STBY-DCU-02.tcl	1.1	Fri Apr 13 14:45:25 2007
SPIRE-IST-DBG-STBY2OFF.tcl	1.4	Sat Aug 4 11:06:12 2007
SPIRE-IST-DBG-STBY2OPS.tcl	1.5	Sat Aug 4 11:06:41 2007
SPIRE-IST-DBG-STBY2PHOT.tcl	1.4	Sat Aug 4 11:26:49 2007
SPIRE-IST-DBG-STBY2SPEC.tcl	1.4	Sat Aug 4 11:57:59 2007
SPIRE-IST-WU-EMC-CE-PHOTOPS2STBY.tcl	1.2	Thu Sep 13 09:35:38 2007
SPIRE-IST-WU-EMC-CE-STBY2PHOTOPS.tcl	1.1	Fri Aug 24 11:14:18 2007
SPIRE-WU-INT-DCU-01-P.tcl	1.4	Tue Mar 27 13:19:51 2007
SPIRE-WU-INT-DCU-01-R.tcl	1.4	Tue Mar 27 13:24:56 2007
SPIRE-WU-INT-DCU-02-P.tcl	1.4	Tue Mar 27 13:20:25 2007
SPIRE-WU-INT-DCU-02-R.tcl	1.4	Tue Mar 27 13:25:16 2007
SPIRE-WU-INT-DPU-START-P-SP.tcl	1.1	Wed Apr 11 16:09:51 2007
SPIRE-WU-INT-DPU-START-P.tcl	1.5	Tue Mar 27 13:20:48 2007
SPIRE-WU-INT-DPU-START-P__new.tcl	1.1	Tue Mar 27 12:29:15 2007
SPIRE-WU-INT-DPU-START-R.tcl	1.4	Tue Mar 27 13:25:40 2007
SPIRE-WU-INT-DRCU-OFF-P.tcl	1.4	Tue Mar 27 13:21:09 2007
SPIRE-WU-INT-DRCU-OFF-R.tcl	1.4	Tue Mar 27 13:26:04 2007
SPIRE-WU-INT-DRCU-START-P-STEP1.tcl	1.4	Tue Mar 27 13:21:31 2007
SPIRE-WU-INT-DRCU-START-P-STEP2.tcl	1.4	Tue Mar 27 13:21:50 2007
SPIRE-WU-INT-DRCU-START-R-STEP1.tcl	1.4	Tue Mar 27 13:26:24 2007
SPIRE-WU-INT-DRCU-START-R-STEP2.tcl	1.4	Tue Mar 27 13:26:33 2007
SPIRE-WU-INT-MCU-01-P.tcl	1.4	Tue Mar 27 13:22:57 2007
SPIRE-WU-INT-MCU-01-R.tcl	1.4	Tue Mar 27 13:27:02 2007
SPIRE-WU-INT-MCU-02-P.tcl	1.4	Tue Mar 27 13:23:27 2007
SPIRE-WU-INT-MCU-02-R.tcl	1.4	Tue Mar 27 13:27:24 2007
SPIRE-WU-INT-MCU-OFF-P.tcl	1.4	Tue Mar 27 13:23:48 2007
SPIRE-WU-INT-MCU-OFF-R.tcl	1.4	Tue Mar 27 13:27:46 2007
SPIRE-WU-INT-SCU-01-P.tcl	1.4	Tue Mar 27 13:24:13 2007
SPIRE-WU-INT-SCU-01-R.tcl	1.4	Tue Mar 27 13:28:05 2007
SPIRE-WU-INT-SCU-02-P.tcl	1.4	Tue Mar 27 13:24:37 2007
SPIRE-WU-INT-SCU-02-R.tcl	1.4	Tue Mar 27 13:28:25 2007
SPIRE_OBS_2-2-G_Upload2DM_CCS.tcl	1.2	Wed May 2 17:50:26 2007
SPIREx_SubscribeParams.tcl	1.2	Thu Apr 19 18:59:12 2007
SPU_MemCheck_All_12_81.tcl	1.1	Thu Feb 15 09:07:43 2007
SPU_MemCheck_All_12_81_0x10000.tcl	1.1	Tue Jul 31 12:53:03 2007
SPU_MemCheck_All_12_81_0x10000_new.tcl	1.1	Sat Aug 4 10:18:37 2007

SWITCH_BUS.tcl	1.2	Sat Jan 14 13:20:26 2006
SetupPhotometrySimulation.tcl	1.4	Thu Jul 19 08:34:41 2007
SetupSpectroscopySimulation.tcl	1.4	Thu Jul 19 08:36:52 2007
SpectroscopytoNonPrime_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:13 2007
SpectroscopytoNonprime.tcl	1.2	Wed Feb 14 17:00:35 2007
T001899MTXX001EPS_DUTY_CYCLE_TM.tcl	1.2	Fri Sep 29 07:48:59 2006
T001899MTXX001EPS_IHMThermListeLignes.tcl	1.2	Fri Sep 29 07:49:26 2006
T001899MTXX001EPS_Load_Thermal_Table.tcl	1.2	Fri Sep 29 07:49:44 2006
T001899MTXX001EPS_TC_IMAGE.tcl	1.2	Fri Sep 29 07:50:12 2006
T001899MTXX001EPS_Thermal_Line.tcl	1.2	Fri Sep 29 07:50:42 2006
TC_aggregation_disable.tcl	1.1	Mon May 7 12:15:10 2007
TC_aggregation_enable.tcl	1.1	Mon May 7 12:23:53 2007
TMTC_DFE_checks.tcl	1.1	Wed Jul 20 10:35:19 2005
TM_data_new.tcl	1.2	Fri Jun 16 07:04:07 2006
TTC_SCOE_checks.tcl	1.2	Wed Nov 2 20:03:08 2005
W010584SPVT001_PCS_HEALTH_CHECK_RT5_B.tcl	1.5	Sat Oct 14 11:34:40 2006
W010584SPVT002_PCS_HEALTH_CHECK_RT6_B.tcl	1.5	Sat Oct 14 11:34:49 2006
W102584EPVT007_CHECK_PCDU.tcl	1.13	Mon May 19 06:46:35 2008
W102584EPVT007_CHECK_PCDU_LAUNCH.tcl	1.2	Mon Nov 27 15:56:16 2006
W102584EPVT007_CHECK_PCS_AIT_LAUNCH.tcl	1.3	Mon May 19 06:49:32 2008
W102584EPVT007_IST_CHECK_PCDU.tcl	1.3	Wed Sep 26 15:42:31 2007
W102584EPVT008_Acq_Data_PCDU.tcl	1.1	Sat Oct 22 08:13:25 2005
W102584SPVT001_BAT_EOC_VERIF.tcl	1.3	Tue Jun 19 09:54:25 2007
W102584SPVT002_BDR_DOD_MANAG_VER.tcl	1.5	Thu Jun 21 15:17:30 2007
W102584SPVT003_COM_AND_MON.tcl	1.13	Thu Jun 21 12:21:18 2007
W102584SPVT004_DNEL_MANAG_VERIF.tcl	1.3	Thu Jun 14 12:37:59 2007
W102584SPVT005_TWO_DOMAINS.tcl	1.7	Tue Jun 19 09:31:05 2007
W102584SPVT010_NCA_VERIFICATION.tcl	1.11	Tue Jun 19 14:33:29 2007
W102584SPVT011_EOC_ECLIPSE_4_SVT0.tcl	1.3	Fri Jul 28 06:01:52 2006
W102584SPVT012_HCS_SWITCH_ON_OFF.tcl	1.2	Tue Oct 10 06:20:08 2006
W102584SPVT013_TRANS_SUN_ECL_SUN.tcl	1.3	Sat Nov 11 10:11:37 2006
W102584SPVT100_PCDU_TRANSITION.tcl	1.9	Mon Jul 30 12:05:54 2007
W102584SPVT101_PCDU_TRANSITION_FDIR.tcl	1.5	Tue May 15 15:53:24 2007
W102584SPVT102_PCDU_TRANSITION_EMC.tcl	1.4	Sat Nov 24 14:50:44 2007
W102584SPVT102_PCDU_TRANSITION_MODE_TR.tcl	1.5	Wed Oct 31 20:11:24 2007
W102584SPVT110_PCS_COMMISSIONING.tcl	1.3	Tue Nov 13 10:51:11 2007
WHEELS_WATCH.tcl	1.1	Wed Sep 5 14:01:56 2007
Wave_Cal_FilA_Shell.tcl	1.1	Thu Oct 18 15:48:13 2007
Wave_Cal_FilB_Shell.tcl	1.1	Thu Oct 18 15:48:13 2007
Y102989ECVT001_TMTC_LINK.tcl	1.4	Thu Nov 16 15:10:27 2006
Y102989ECVT003_TC_DFE_OUT_2_TTC.tcl	1.4	Wed Aug 30 07:59:58 2006
Y102989ECVT004_TTC_RNG_SET.tcl	1.4	Wed Aug 30 07:59:47 2006
Y102989ECVT005_TM_DFE_IN_FROM_TTC.tcl	1.5	Mon Jun 4 06:42:36 2007
Y102989ECVT006_TTC_DL_PORT_SET.tcl	1.6	Wed Aug 30 07:59:32 2006
Y102989ECVT007_TTC_UL_PARAM_SET.tcl	1.4	Wed Aug 30 07:58:52 2006
Y102989ECVT008_TCRG_MI_SET_TTC.tcl	1.6	Thu Nov 16 14:59:04 2006
Y102989ECVT009_TCRG_MI_SET_SSBV_RX.tcl	1.3	Wed Aug 30 07:58:41 2006
Y102989ECVT018_TTC_TC_OP_METHOD.tcl	1.3	Wed Aug 30 07:58:35 2006
Y102989EMVT010_PHASE_NOISE_MEAS.tcl	1.4	Sat Sep 2 12:54:53 2006
Y102989EMVT011_MEAS_FREQ.tcl	1.4	Wed Sep 27 11:17:54 2006
Y102989EMVT012_MEAS_POWER.tcl	1.3	Wed Aug 30 07:58:16 2006
Y102989EMVT013_MEAS_MOD_INDEX.tcl	1.4	Wed Aug 30 07:57:56 2006
Y102989EMVT014_MEAS_ANA_SPEC.tcl	1.3	Wed Aug 30 07:58:10 2006
Y102989EMVT015_AMPLITUDE_RES.tcl	1.4	Thu Sep 21 16:41:07 2006
Y102989EMVT016_MEAS_GROUP_DELAY.tcl	1.3	Wed Aug 30 07:57:49 2006
Y102989EMVT023_TTC_ASA_COPY.tcl	1.3	Wed Aug 30 07:56:23 2006
Y102989EPVT001_EMERGENCY_PWR_SCOE_ON.tcl	1.2	Fri Jan 13 10:00:48 2006
Y102989EPVT001_PWR_SCOE_ON.tcl	1.23	Mon May 19 06:47:32 2008
Y102989EPVT001_PWR_SCOE_ON_AIT_LNC.tcl	1.6	Mon May 19 13:19:03 2008
Y102989EPVT001_PWR_SCOE_ON_EMC.tcl	1.2	Wed Nov 14 15:18:09 2007
Y102989EPVT001_PWR_SCOE_ON_LAUNCH.tcl	1.3	Thu Nov 30 14:52:26 2006
Y102989EPVT002_EMERG_PWR_SCOE_OFF.tcl	1.1	Fri Dec 1 16:21:23 2006
Y102989EPVT002_PWR_SCOE_OFF.tcl	1.17	Mon May 19 06:55:46 2008
Y102989EPVT002_PWR_SCOE_OFF_AIT_LNC.tcl	1.2	Wed Feb 14 07:56:35 2007
Y102989EPVT002_PWR_SCOE_OFF_CLN_LNCH.tcl	1.2	Thu Jul 26 12:36:19 2007
Y102989EPVT002_PWR_SCOE_OFF_LAUNCH.tcl	1.3	Thu Nov 30 14:58:47 2006
Y102989EPVT003_BS_CONN_REM_ONL.tcl	1.1	Sat Oct 22 08:17:42 2005
Y102989EPVT004_SAS_CONN_REM_ONL.tcl	1.1	Sat Oct 22 08:18:07 2005

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Y102989EPVT005_Acq_Data_SCOE_PCDU.tcl     1.2          Tue Jun 12 16:41:07 2007
Y102989EPVT006_Acq_Data_SCOE.tcl          1.1          Sat Oct 22 08:18:42 2005
Y102989EPVT007_IST_PWR_SCOE_ON.tcl        1.7          Mon Nov 26 13:41:43 2007
Y102989ETVT002_TM_DFE_STATISTICS.tcl      1.5          Wed Aug 30 07:56:17 2006
Y102989ETVT017_TTC_CHECK_ROUTINE.tcl      1.5          Wed Oct 25 14:06:12 2006
Y102989ETVT019_TTC_SCOE_ACTIVITY.tcl      1.5          Sat Sep  2 08:34:19 2006
Y102989ETVT020_TTC_SCOE_OFF.tcl           1.4          Wed Jul 25 17:48:15 2007
Y102989ETVT021_TTC_SCOE_ON.tcl            1.3          Thu Jul 20 08:21:38 2006
Y102989ETVT022_TTC_UL_OFFLINE.tcl         1.4          Wed Aug 30 07:55:52 2006
Y102989ETVT030_ASDGEN_PFM_VLV_GUI.tcl     1.5          Wed Oct 17 20:50:42 2007
Y102989ETVT031_ASDGEN_PFM_HEATER_GUI.tcl  1.3          Wed Oct 17 20:46:12 2007
Y102989ETVT032_ASDGEN_PFM_TBTV_LOG.tcl    1.7          Mon Nov 26 16:23:22 2007
Y102989ETVT033_ASDGEN_PFM_SCOE_GUI.tcl    1.6          Mon Nov 26 16:24:12 2007
Y102989ETVT034_ASDGEN_RPFM_LOG.tcl        1.2          Tue Nov 20 15:43:41 2007
Y102989SCVT023_ECL_TO_SUN.tcl             1.1          Wed Jun 28 14:21:32 2006
Z010999MCVT001_POWER_ON.tcl               1.21         Wed Oct 31 16:28:25 2007
Z010999MCVT001_POWER_ON_FAST.tcl          1.5          Fri Oct 27 13:48:09 2006
Z010999MCVT001_POWER_ON_HER_IST.tcl       1.23         Tue Nov 20 18:46:36 2007
Z010999MCVT001_POWER_ON_IM2.tcl           1.1          Thu Oct 25 12:55:00 2007
Z010999MCVT001_POWER_ON_LAUNCH_CONF.tcl   1.3          Thu Nov 30 14:39:58 2006
Z010999MCVT001_POWER_ON_PMAI2.tcl         1.2          Fri Oct 27 14:11:11 2006
Z010999MCVT001_POWER_ON_WITH_LPS.tcl      1.4          Mon Jun 11 15:07:13 2007
Z010999MCVT001__EMERGENCY_POWER_ON.tcl    1.2          Wed Apr 18 08:49:19 2007
Z010999MCVT002_EMERGENCY_POWER_OFF.tcl    1.2          Fri Dec  1 16:20:33 2006
Z010999MCVT002_EMERGENCY_SWITCH_OFF_ALL.tcl 1.4           Thu Feb  1 16:17:36
2007
Z010999MCVT002_POWER_OFF.tcl              1.12         Wed Oct 17 09:43:09 2007
Z010999MCVT002_POWER_OFF_CLN_LNCH.tcl     1.2          Thu Jul 26 12:30:48 2007
Z010999MCVT002_POWER_OFF_FAST.tcl         1.4          Tue Nov 20 03:25:52 2007
Z010999MCVT002_POWER_OFF_HER_IST.tcl      1.2          Wed Feb 14 07:51:09 2007
Z010999MCVT002_POWER_OFF_IM2.tcl          1.3          Fri Oct 26 10:00:42 2007
Z010999MCVT002_POWER_OFF_LAUNCH_CONF.tcl  1.2          Thu Nov 30 14:39:48 2006
Z010999MCVT002_POWER_OFF_PMAI2.tcl        1.2          Fri Oct 27 14:17:20 2006
Z010999MCVT002_POWER_OFF_WITH_LPS.tcl     1.4          Mon Jun 11 15:07:41 2007
Z010999MCVT003_IST_START.tcl              1.44         Wed Nov 21 15:43:55 2007
Z010999MCVT004_IST_END.tcl                1.20         Sat Dec  1 04:45:48 2007
Z010999MCVT005_IST_START_SSMM.tcl         1.7          Tue Nov 27 08:57:31 2007
Z010999MCVT010_SVM_SFT_HERSCHEL_IST.tcl   1.16         Tue Dec  5 20:04:11 2006
Z010999MCVT011_STATUS_SPACECRAFT.tcl      1.18         Tue Nov  6 16:30:43 2007
Z010999MCVT011_STATUS_SPACECRAFT_FDIR.tcl 1.2           Wed Apr 18 08:43:37 2007
Z010999MCVT011_SVM_HERSCHEL_IST_1.tcl     1.14         Tue Nov  6 16:35:52 2007
Z010999MCVT015_SVM_HER_IST_FDIR1.tcl      1.9          Fri Nov 10 15:45:24 2006
Z010999MCVT020_SVM_HER_IST_FDIR2.tcl      1.13         Wed Nov 22 12:07:11 2006
Z010999MCVT040_SVM_HER_IST_FDIR3.tcl      1.7          Thu Apr  5 09:46:32 2007
Z010999MCVT050_SVM_HER_IST_FDIR4.tcl      1.7          Sat Nov 11 13:44:04 2006
Z010999MCVT060_SVM_HER_IST_FDIR5.tcl      1.5          Fri Nov 10 14:11:46 2006
Z010999MCVT070_SVM_HER_IST_FDIR6.tcl      1.10         Sat Nov 11 12:01:23 2006
Z010999MCVT080_IST_FDIR_ASTRIUM.tcl       1.35         Wed Nov 21 16:00:11 2007
Z010999MCVT081_IST_DEGRADED_ASTRIUM.tcl   1.2          Mon Jun 11 13:34:49 2007
Z010999MCVT082_IST_LAUNCH_SEQ_ROBUST.tcl  1.15         Wed Nov 21 15:57:46 2007
Z010999MCVT083_IST_NOM_MODE_ROBUST.tcl    1.12         Wed Nov 21 16:00:50 2007
Z010999MCVT085_IST_RMS_ASTRIUM.tcl        1.18         Tue Nov  6 11:17:01 2007
Z010999MCVT086_IST_DTCP_WORST_CASE.tcl    1.6          Fri Sep 14 14:00:28 2007
Z010999MCVT087_WCS_DTCP.tcl               1.3          Thu Jul  5 15:58:29 2007
Z010999MCVT088_WCS_MTL.tcl                1.2          Tue Jun 26 19:19:05 2007
Z010999MCVT089_IST_SAT_COMMIS_ACMS.tcl    1.9          Sat Dec  1 04:29:44 2007
Z010999MCVT090_IST_DTCP_TRACE_CR.tcl      1.2          Thu Sep 27 15:29:47 2007
Z010999MCVT090_IST_DTCP_TRACE_EPH.tcl     1.4          Mon Nov  5 15:06:36 2007
Z010999MCVT091_IST_RMS_DTCP.tcl           1.6          Thu Sep 27 15:59:15 2007
Z010999MCVT092_IST_LAUNCH_CLEAN_RUN.tcl   1.3          Sat Nov 10 08:17:18 2007
Z010999MCVT093_IST_RMS_Date_Watch.tcl     1.4          Wed May 21 09:51:38 2008
Z010999MCVT094_IST_DTCP_CDMS_MANAGM.tcl   1.13         Wed Nov 21 15:58:35 2007
Z010999MCVT095_IST_CDMS_DTCP.tcl          1.7          Wed Nov 21 15:59:15 2007
Z010999MCVT096_IST_SAT_COMMISSIONING.tcl  1.4          Fri Nov 23 21:54:37 2007
Z010999MCVT097_ASDGEN_CRIT_PARS_CHECK.tcl 1.12          Sat Dec  1 04:27:53 2007
Z010999MCVT100_IST_NOMINAL_LAUNCH.tcl     1.5          Sat Oct 13 08:15:28 2007
Z010999MCVT101_IST_MODE_TRANSITIONS.tcl   1.4          Thu Aug 16 12:34:27 2007
Z010999MCVT101_SAT_HER_IST_FDIR.tcl       1.1          Wed Mar 28 08:59:15 2007
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Z010999MCVT110_STATUS_SATELLITE.tcl      1.5          Wed Apr 18 08:47:43 2007
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Z010999MCVT121_IST_LAUNCH_CNTR_LSR.tcl   1.10         Sat Nov 17 08:54:30 2007
Z010999MCVT130_IST_INSTR_COMMISSIONING.tcl 1.6          Tue Oct  2 09:17:45 2007
Z010999MCVT131_IST_INSTR_FDIR.tcl        1.4          Mon Nov 26 10:18:41 2007
Z010999MCVT132_TCprotMode_BD_AD_BD.tcl   1.2          Fri Oct 19 11:58:10 2007
Z010999MCVT133_CRYO_COVER_OPEN.tcl       1.2          Tue Oct 23 15:20:25 2007
Z010999MCVT13_CRYO_COVER_OPEN.tcl        1.1          Fri Oct 19 12:03:00 2007
Z010999MCVT153_IST_STATUS.tcl            1.23         Wed Nov 21 15:42:55 2007
Z010999MCVT154_EMC_STATUS.tcl            1.3          Tue Nov 20 18:47:39 2007
Z010999MCVT200_EMC.tcl                   1.9          Fri Nov 30 17:04:13 2007
Z010999MCVT90_IST_DTCP_TRACE_CR_EPH.tcl  1.4          Sun May 18 21:26:23 2008
Z010999MMXX002UNITS_CHECK.tcl            1.16         Mon Oct  1 13:42:41 2007
Z010999MMXX003UNITS_CHECK_PWR_OFF.tcl    1.16         Mon Oct  1 13:43:14 2007
Z102999GTVT000_GEN_COMMON_PROC_LIB.tcl   1.7          Fri Nov 30 17:03:08 2007
Z102999GTVT000_GEN_COMMON_PROC_LIB_DEBUG.tcl 1.2          Sat Nov 24 16:21:25
2007
Z102999SCVT000_SAT_COM_SREM.tcl          1.2          Fri Sep 14 14:03:38 2007
Z102999SCVT001_SREM_ON.tcl               1.13         Mon Aug 27 09:47:17 2007
Z102999SCVT002_SREM_OFF.tcl              1.6          Tue Nov 27 14:49:18 2007
Z102999SCVT003_SREM_ACQ_START.tcl        1.11         Sat Sep 15 10:57:26 2007
Z102999SCVT004_ASDGEN_SPIREPWRON_P.tcl   1.6          Thu Nov 15 17:59:30 2007
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Z102999SCVT008_ASDGEN_SPIRESTBY2OPS.tcl  1.3          Mon Aug 20 13:06:30 2007
Z102999SCVT009_ASDGEN_SPIREOPS2STBY.tcl  1.3          Mon Aug 20 13:06:01 2007
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Z102999SCVT012_ASDGEN_PACSPWRON_R.tcl    1.6          Sat Nov 24 08:50:55 2007
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Z102999SCVT014_ASDGEN_HIFIPWRON_P.tcl    1.3          Thu Oct 18 10:18:47 2007
Z102999SCVT015_ASDGEN_HIFIPWROFF_P.tcl   1.3          Thu Oct 18 10:18:16 2007
Z102999SCVT016_ASDGEN_HIFIPWRON_R.tcl    1.3          Thu Oct 18 10:17:53 2007
Z102999SCVT017_ASDGEN_HIFIPWROFF_R.tcl   1.3          Thu Oct 18 10:17:17 2007
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acms_mdl.tcl                             1.2          Wed Apr 18 09:25:41 2007
acms_mdl_fcv.tcl                         1.5          Wed Apr 18 09:25:41 2007
acms_mdl_wheel.tcl                       1.2          Fri Oct 27 17:29:14 2006
acms_prova.tcl                           1.2          Wed Apr 18 09:25:41 2007
acms_prova_stefano.tcl                   1.2          Wed Apr 18 09:22:32 2007
acms_tslew.tcl                           1.1          Fri Oct 20 14:27:44 2006
date_watch.tcl                           1.3          Tue Jul 24 12:27:18 2007
dry_loop_commands.tcl                    1.1          Wed Apr  4 11:47:37 2007
getLogHistory.tcl                        1.1          Tue Oct 30 09:32:13 2007
mclistbox.tcl                            1.1          Thu Jun 15 14:28:32 2006
modify_NM_conf.tcl                       1.8          Sat Sep 29 14:11:47 2007
modify_SM_conf.tcl                       1.5          Sat Sep 29 14:12:11 2007
mtl_upload.tcl                           1.2          Mon Nov 27 08:48:55 2006
provavlg.tcl                             1.2          Mon Sep 10 11:45:43 2007
provola.tcl                              1.1          Wed Mar 28 17:03:31 2007
simon_test1.tcl                          1.1          Fri Oct 26 11:36:31 2007
test_jeff.tcl                            1.1          Tue Jul 10 13:36:39 2007
vlg_temp.tcl                             1.4          Mon Jul 23 15:21:53 2007
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CDM_only                                 1.1          Thu Jun 15 14:29:55 2006
ETM_only                                 1.1          Thu Jun 15 14:30:00 2006
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OCM_only                                 1.1          Thu Jun 15 14:30:04 2006
SAM_only                                 1.1          Thu Jun 15 14:30:09 2006
SBM_only                                 1.1          Thu Jun 15 14:30:13 2006
SCM_only                                 1.1          Thu Jun 15 14:30:18 2006
SSM_only                                 1.1          Thu Jun 15 14:30:24 2006
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caf.dat	1.42	Sat Oct 27 07:17:36 2007
cap.dat	1.41	Sat Oct 27 07:17:36 2007
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ccf.dat	1.70	Thu Nov 15 05:41:30 2007
ccs.dat	1.19	Thu Aug 30 04:58:34 2007
cdf.dat	1.61	Thu Nov 15 06:01:54 2007
cpc.dat	1.44	Thu Nov 15 06:02:20 2007
cps.dat	1.1.1.1	Fri Jun 3 05:56:08 2005
csf.dat	1.1.1.1	Fri Jun 3 05:56:08 2005
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css.dat	1.1.1.1	Fri Jun 3 05:56:08 2005
cur.dat	1.13	Wed Jul 18 17:21:52 2007
cve.dat	1.15	Mon Jul 23 16:22:44 2007
cvp.dat	1.52	Thu Nov 15 05:41:47 2007
cvs.dat	1.31	Sat Oct 27 07:17:53 2007
dpc.dat	1.92	Sat Oct 27 07:17:53 2007
dpf.dat	1.67	Sat Oct 27 07:17:55 2007
dst.dat	1.58	Sat Oct 13 12:02:32 2007
gpc.dat	1.12	Sat Oct 27 07:17:55 2007
gpf.dat	1.9	Mon Jul 23 16:22:47 2007
grp.dat	1.4	Wed Feb 15 13:37:53 2006
grpa.dat	1.6	Wed Feb 15 13:37:53 2006
grpk.dat	1.1.1.1	Fri Jun 3 05:56:08 2005
lgf.dat	1.1.1.1	Fri Jun 3 05:56:08 2005
mcf.dat	1.22	Tue Jun 26 09:54:15 2007
ocf.dat	1.33	Sat Oct 27 07:17:56 2007
ocp.dat	1.34	Sat Oct 27 07:17:56 2007
paf.dat	1.36	Sat Oct 27 07:17:56 2007
pas.dat	1.37	Sat Oct 27 07:17:56 2007
pcdf.dat	1.8	Fri May 4 05:01:01 2007
pcf.dat	1.69	Thu Nov 15 05:41:47 2007
pcpc.dat	1.8	Fri May 4 05:01:05 2007
pic.dat	1.11	Fri May 4 05:01:05 2007
pid.dat	1.63	Thu Nov 15 05:41:50 2007
plf.dat	1.61	Thu Nov 15 05:41:51 2007
prf.dat	1.9	Mon Jul 23 16:22:54 2007
prv.dat	1.9	Mon Jul 23 16:22:54 2007
psm.dat	1.1.1.1	Fri Jun 3 05:56:08 2005
pst.dat	1.1.1.1	Fri Jun 3 05:56:08 2005
psv.dat	1.1.1.1	Fri Jun 3 05:56:08 2005
ptv.dat	1.9	Mon Jul 23 16:22:54 2007
pvs.dat	1.1.1.1	Fri Jun 3 05:56:08 2005
sco.dat	1.27	Sat Oct 27 07:18:02 2007
sdf.dat	1.1.1.1	Fri Jun 3 05:56:08 2005
spc.dat	1.7	Mon Jul 23 16:22:54 2007
spf.dat	1.7	Mon Jul 23 16:22:54 2007
tcd.dat	1.23	Sat Oct 13 11:56:29 2007
tcp.dat	1.11	Thu Sep 13 14:53:50 2007
tmd.dat	1.26	Sat Oct 27 07:18:02 2007
tpcf.dat	1.55	Thu Nov 15 05:41:54 2007
txf.dat	1.47	Sat Oct 27 07:18:02 2007
txp.dat	1.51	Sat Oct 27 07:18:02 2007
vdf.dat	1.51	Thu Nov 15 05:41:54 2007
vpd.dat	1.26	Sat Oct 27 07:18:03 2007

Contents of /HPCCS/VARIABLE/ENVIRONMENTS/EXEC/data/ASCII/synthetic

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AD001001	1.9	Fri May 4 12:37:42 2007
AD001002	1.8	Fri May 4 12:37:42 2007
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AD001092	1.6	Fri May 4 12:37:42 2007

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AD001093	1.6	Fri May 4 12:37:42 2007
AD001094	1.6	Fri May 4 12:37:42 2007
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AD700001	1.7	Fri May 4 12:37:42 2007
AD700034	1.7	Fri May 4 12:37:42 2007
AD700035	1.7	Fri May 4 12:37:42 2007
AD701001	1.7	Fri May 4 12:37:42 2007
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HD212192	1.1	Mon Jul 23 16:22:59 2007
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KD230300	1.8	Sat Oct 27 07:18:05 2007
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2007_12_01_05_16_hercdmu_hpws23_REALTIME_EMC_nois_TestEnvironment_REALTIME

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KD246302	1.5	Fri May 4 12:37:45 2007
KD246303	1.5	Fri May 4 12:37:45 2007
KD247300	1.8	Sat Oct 27 07:18:05 2007
KD247301	1.8	Sat Oct 27 07:18:05 2007
KD248302	1.5	Fri May 4 12:37:45 2007
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KD264303	1.5	Fri May 4 12:37:45 2007
KD265302	1.5	Fri May 4 12:37:45 2007
KD265303	1.5	Fri May 4 12:37:45 2007
KD266300	1.8	Sat Oct 27 07:18:05 2007
KD266301	1.8	Sat Oct 27 07:18:05 2007
KD267300	1.8	Sat Oct 27 07:18:05 2007
KD267301	1.8	Sat Oct 27 07:18:05 2007
KD268300	1.8	Sat Oct 27 07:18:05 2007
KD268301	1.8	Sat Oct 27 07:18:05 2007
KD272300	1.5	Fri May 4 12:37:45 2007
KD272301	1.5	Fri May 4 12:37:45 2007

2007_12_01_05_16_hercdmu_hpws23_REALTIME_EMC_nois_TestEnvironment_REALTIME

KD272302	1.8	Sat Oct 27 07:18:05 2007
KD272303	1.8	Sat Oct 27 07:18:05 2007
KD273300	1.5	Fri May 4 12:37:45 2007
KD273301	1.5	Fri May 4 12:37:45 2007
KD273302	1.8	Sat Oct 27 07:18:05 2007
KD273303	1.8	Sat Oct 27 07:18:05 2007
KD274302	1.5	Fri May 4 12:37:45 2007
KD274303	1.5	Fri May 4 12:37:45 2007
KD275302	1.5	Fri May 4 12:37:45 2007
KD275303	1.5	Fri May 4 12:37:45 2007
KD276302	1.5	Fri May 4 12:37:45 2007
KD276303	1.5	Fri May 4 12:37:45 2007
KD277302	1.5	Fri May 4 12:37:45 2007
KD277303	1.5	Fri May 4 12:37:45 2007
KD278302	1.5	Fri May 4 12:37:45 2007
KD278303	1.5	Fri May 4 12:37:45 2007
KD600300	1.5	Fri May 4 12:37:45 2007
KD600301	1.8	Sat Oct 27 07:18:05 2007
KD601300	1.5	Fri May 4 12:37:45 2007
KD601301	1.5	Fri May 4 12:37:45 2007
KD602300	1.5	Fri May 4 12:37:45 2007
KD602301	1.5	Fri May 4 12:37:45 2007
KD603300	1.5	Fri May 4 12:37:45 2007
KD603301	1.5	Fri May 4 12:37:45 2007
KD610300	1.8	Sat Oct 27 07:18:05 2007
KD610301	1.8	Sat Oct 27 07:18:05 2007
KD611300	1.8	Sat Oct 27 07:18:05 2007
KD611301	1.8	Sat Oct 27 07:18:05 2007
KD612300	1.8	Sat Oct 27 07:18:05 2007
KD612301	1.8	Sat Oct 27 07:18:05 2007
KD613300	1.8	Sat Oct 27 07:18:05 2007
KD613301	1.8	Sat Oct 27 07:18:05 2007
PD179380	1.5	Fri May 4 12:37:45 2007
PD180380	1.5	Fri May 4 12:37:45 2007
PD421410	1.5	Fri May 4 12:37:45 2007
RD003442	1.7	Fri May 4 12:37:45 2007
RD004442	1.7	Fri May 4 12:37:45 2007
SD00M510	1.5	Fri May 4 12:37:45 2007
SD00M515	1.5	Fri May 4 12:37:45 2007
SD00M520	1.5	Fri May 4 12:37:45 2007
SDB0H510	1.7	Fri May 4 12:37:45 2007
SDB1H510	1.7	Fri May 4 12:37:45 2007
SDF0H510	1.7	Fri May 4 12:37:45 2007
SDH03510	1.3	Fri May 4 12:37:45 2007
SDH03520	1.3	Fri May 4 12:37:45 2007
SDH15520	1.3	Fri May 4 12:37:46 2007
SDP0H510	1.7	Fri May 4 12:37:46 2007
SDS0P515	1.5	Fri May 4 12:37:46 2007
SD_0X510	1.5	Fri May 4 12:37:46 2007
SD_1X510	1.5	Fri May 4 12:37:46 2007
WD011565	1.7	Fri May 4 12:37:46 2007
WD012565	1.7	Fri May 4 12:37:46 2007
WD027565	1.7	Fri May 4 12:37:46 2007
WD028565	1.7	Fri May 4 12:37:46 2007
WD029565	1.7	Fri May 4 12:37:46 2007
WD030565	1.7	Fri May 4 12:37:46 2007
WD045565	1.7	Fri May 4 12:37:46 2007
WD046565	1.7	Fri May 4 12:37:46 2007
WD047565	1.7	Fri May 4 12:37:46 2007
WD048565	1.7	Fri May 4 12:37:46 2007
WD049565	1.7	Fri May 4 12:37:46 2007
WD050565	1.7	Fri May 4 12:37:46 2007
WD051565	1.7	Fri May 4 12:37:46 2007
WD052565	1.7	Fri May 4 12:37:46 2007
WD053565	1.7	Fri May 4 12:37:46 2007
WD054565	1.7	Fri May 4 12:37:46 2007
WD055565	1.7	Fri May 4 12:37:46 2007
WD056565	1.7	Fri May 4 12:37:46 2007

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   2007_12_01_05_16_hercdmu_hpws23_REALTIME_EMC_nois_TestEnvironment_REALTIME
WD057565                                  1.7          Fri May  4 12:37:46 2007
WD058565                                  1.7          Fri May  4 12:37:46 2007
WD059565                                  1.7          Fri May  4 12:37:46 2007
WD060565                                  1.7          Fri May  4 12:37:46 2007
WD061565                                  1.7          Fri May  4 12:37:46 2007
WD062565                                  1.7          Fri May  4 12:37:46 2007
WD063565                                  1.7          Fri May  4 12:37:46 2007
WD064565                                  1.7          Fri May  4 12:37:46 2007
WD065565                                  1.7          Fri May  4 12:37:46 2007
WD066565                                  1.7          Fri May  4 12:37:46 2007
WD067565                                  1.7          Fri May  4 12:37:46 2007
WD068565                                  1.7          Fri May  4 12:37:46 2007
WD069565                                  1.7          Fri May  4 12:37:46 2007
WD070565                                  1.7          Fri May  4 12:37:46 2007
WD100565                                  1.5          Fri May  4 12:37:46 2007
WD101565                                  1.5          Fri May  4 12:37:46 2007
WD12A565                                  1.7          Fri May  4 12:37:46 2007
WD22A565                                  1.7          Fri May  4 12:37:46 2007
WD311565                                  1.7          Fri May  4 12:37:46 2007
WD32D565                                  1.7          Fri May  4 12:37:46 2007
WD32E565                                  1.7          Fri May  4 12:37:46 2007
WD42A565                                  1.7          Fri May  4 12:37:46 2007
WD42B565                                  1.7          Fri May  4 12:37:46 2007
WD72A565                                  1.7          Fri May  4 12:37:46 2007
WD72E565                                  1.7          Fri May  4 12:37:46 2007
WD72F565                                  1.7          Fri May  4 12:37:46 2007
WD811565                                  1.7          Fri May  4 12:37:46 2007
WD82A565                                  1.7          Fri May  4 12:37:46 2007
WD82D565                                  1.7          Fri May  4 12:37:46 2007
WD82E565                                  1.7          Fri May  4 12:37:46 2007
WDA06565                                  1.7          Fri May  4 12:37:46 2007
WDA2A565                                  1.7          Fri May  4 12:37:46 2007
YD001940                                  1.11         Fri May  4 12:37:46 2007
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Contents of /HPCCS/VARIABLE/ENVIRONMENTS/EXEC/data/rawTM
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(empty)
--------------------------------------------------
```

4. End Configuration

End Configuration

2007_12_03_05_00_hercdmu_hpws23_REALTIME_EMC_nois2_TestEnvironment_REALTIME

Files are from /HPCCS/VARIABLE/REPOSITORIES/USER

HPCCS version is hpccs-2.0-1166

WARNING:- online patches are not reflected in this
file unless added manually to the end of the file

Contents of /HPCCS/VARIABLE/ENVIRONMENTS/EXEC/LOG

ImportMIB.log	1.1	Thu Nov 16 08:30:19 2006
consistency.log	1.1	Thu Nov 16 08:25:01 2006
sessionlog	1.14503	Sat Dec 1 12:58:25 2007

Contents of /HPCCS/VARIABLE/ENVIRONMENTS/EXEC/MISC

(empty)

Contents of /HPCCS/VARIABLE/ENVIRONMENTS/EXEC/PICT

ACMS_BLOCDIAG.ilv	1.3	Fri Feb 17 10:18:11 2006
ACMS_H_BLOC.ilv	1.26	Sat Nov 10 16:05:07 2007
ASTRIUM_TEST.ilv	1.4	Tue Mar 27 11:28:07 2007
BS_PROVA.ilv	1.1	Tue Sep 13 10:14:50 2005
CCU.ilv	1.16	Sat Nov 17 17:30:14 2007
CCU_HE_TANK.ilv	1.9	Fri Oct 19 10:09:24 2007
CCU_OPT_BENCH.ilv	1.9	Wed Oct 24 07:21:44 2007
CDMU_BLOCDIAG.ilv	1.15	Mon Aug 20 12:36:52 2007
EGSE_CCU.ilv	1.1	Thu Nov 29 14:42:56 2007
EGSE_CCU_HE_TANK.ilv	1.1	Thu Nov 29 14:43:19 2007
EGSE_CCU_OPT_BENCH.ilv	1.1	Thu Nov 29 14:43:28 2007
EGSE_CONN.ilv	1.1	Tue Sep 13 10:15:19 2005
FCL.ilv	1.1	Tue Sep 13 10:15:13 2005
GEN_POWER.ilv	1.7	Mon Aug 20 12:36:33 2007
HEATERS.ilv	1.3	Wed Apr 18 09:43:15 2007
INSTRUMENTS_H.ilv	1.12	Thu Aug 30 05:10:25 2007
LCL.ilv	1.1	Tue Sep 13 10:15:07 2005
LCL_HERSCHEL.ilv	1.5	Tue Apr 10 16:00:12 2007
LCL_PLANCK.ilv	1.1	Tue Sep 13 10:15:34 2005
RCS.ilv	1.3	Mon Aug 20 12:37:10 2007
RFDN.ilv	1.1	Tue Sep 13 10:15:39 2005
SAT.ilv	1.48	Sat Nov 17 17:29:25 2007
SINOTTICO.ilv	1.1	Tue Sep 13 10:15:02 2005
TT&C_H-P.ilv	1.3	Thu Nov 23 11:46:21 2006
TT&C_HER.ilv	1.6	Tue Jan 24 11:16:44 2006
TT&C_HERSCHEL.ilv	1.2	Thu Jan 12 08:51:35 2006
TT&C_HER_2.ilv	1.9	Mon Aug 4 08:08:14 2008
TTC_H_P.ilv	1.5	Mon Sep 24 12:23:08 2007
TWTA_1.ilv	1.1	Tue Sep 13 10:15:24 2005
TWTA_2.ilv	1.1	Tue Sep 13 10:15:45 2005
XPOND1.ilv	1.1	Tue Sep 13 10:16:29 2005
XPOND2.ilv	1.1	Tue Sep 13 10:14:37 2005

Contents of /HPCCS/VARIABLE/ENVIRONMENTS/EXEC/TSEQ

A101109SPVT201_ACMS_STATUS.tcl	1.1	Tue Nov 6 15:51:59 2007
A102109ETVT000_ACMS_PROCESS.tcl	1.72	Sat Nov 3 13:37:59 2007

2007_12_03_05_00_hercdmu_hpws23_REALTIME_EMC_nois2_TestEnvironment_REALTIME

A102109SPVT003_ACMS_CONFIG25.tcl	1.20	Mon Jul 10 11:18:26 2006
A102109SPVT004_ACMS_LOADCONFIG1.tcl	1.3	Mon May 15 13:43:03 2006
A102109SPVT005_ACMS_EE_04.tcl	1.10	Wed Feb 22 19:23:18 2006
A102109SPVT006_ACMS_EE_05.tcl	1.7	Wed Feb 22 19:23:04 2006
A102109SPVT007_ACMS_PT_01.tcl	1.5	Fri Jul 21 13:26:38 2006
A102109SPVT008_ACMS_HC_04.tcl	1.12	Mon Mar 13 13:49:55 2006
A102109SPVT009_ACMS_HC_05.tcl	1.8	Wed Mar 8 11:09:33 2006
A102109SPVT010_ACMS_SCOE_CONFIG1.tcl	1.44	Fri Oct 26 09:47:29 2007
A102109SPVT011_ACMS_ON.tcl	1.36	Sat Sep 29 13:51:37 2007
A102109SPVT012_ACMS_OFF.tcl	1.35	Sat Nov 10 17:07:01 2007
A102109SPVT012_EMERGENCY_ACMS_OFF.tcl	1.2	Mon Dec 4 16:56:34 2006
A102109SPVT013_ACMS_EE_01.tcl	1.11	Tue Nov 27 04:22:27 2007
A102109SPVT014_ACMS_STR_Test.tcl	1.7	Mon Jun 5 07:08:43 2006
A102109SPVT015_ACMS_HC_01.tcl	1.12	Wed Oct 31 15:45:55 2007
A102109SPVT016_ACMS_EE_02.tcl	1.6	Sat Mar 4 13:43:54 2006
A102109SPVT017_ACMS_CRS_BACKGROUND.tcl	1.6	Thu Mar 22 11:42:10 2007
A102109SPVT018_ACMS_Units_Stim.tcl	1.5	Mon Jun 5 07:09:55 2006
A102109SPVT019_ACMS_HC_02.tcl	1.9	Sat Oct 14 15:08:31 2006
A102109SPVT020_ACMS_EE_06.tcl	1.4	Wed May 10 16:51:09 2006
A102109SPVT021_ACMS_ACC_SEPARA.tcl	1.29	Fri May 11 11:58:05 2007
A102109SPVT022_ACMS_SEP_DELAY.tcl	1.3	Mon May 15 13:44:52 2006
A102109SPVT028_ACMS_GYR_Test.tcl	1.2	Tue Jun 19 07:35:45 2007
A102109SPVT029_ACMS_HC_07.tcl	1.12	Tue Mar 6 17:07:57 2007
A102109SPVT030_ACMS_HC_03.tcl	1.20	Tue Dec 5 23:17:58 2006
A102109SPVT031_ACMS_EE_03.tcl	1.6	Fri Oct 27 16:11:41 2006
A102109SPVT032_DEFAULT_CONF_SEP.tcl	1.24	Tue Nov 6 12:55:34 2007
A102109SPVT034_ACMS_SAM_MON.tcl	1.12	Thu Oct 19 11:27:46 2006
A102109SPVT035_ACMS_GYRO_UFT.tcl	1.2	Mon Jun 5 07:11:38 2006
A102109SPVT035_ACMS_SEP_DELAY.tcl	1.4	Mon May 15 13:08:41 2006
A102109SPVT036_ACMS_STR_ON.tcl	1.6	Mon Aug 28 14:45:37 2006
A102109SPVT037_SUN_INER.tcl	1.5	Tue Feb 27 16:26:09 2007
A102109SPVT038_RWL_ON.tcl	1.10	Mon May 21 13:45:35 2007
A102109SPVT039_STR_ON.tcl	1.3	Wed Mar 15 17:22:54 2006
A102109SPVT040_STR_OFF.tcl	1.2	Mon May 15 13:46:25 2006
A102109SPVT041_RWL_CONF.tcl	1.4	Mon May 15 13:46:43 2006
A102109SPVT042_RWL_SPINUP.tcl	1.6	Mon Oct 2 14:56:38 2006
A102109SPVT043_TRANSITION_TO_OCM.tcl	1.20	Thu Oct 11 12:50:04 2007
A102109SPVT044_RCS_MODE.tcl	1.4	Sat Sep 23 12:25:07 2006
A102109SPVT045_SCM_RASTER.tcl	1.2	Thu Aug 23 11:55:24 2007
A102109SPVT046_ROT_QUAT.tcl	1.2	Mon May 15 13:47:38 2006
A102109SPVT047_RM_DUMP.tcl	1.2	Mon May 15 13:37:36 2006
A102109SPVT048_TEST_DW_ENA.tcl	1.1	Mon May 15 13:08:59 2006
A102109SPVT049_ACMS_HC_09.tcl	1.14	Wed Dec 6 11:22:50 2006
A102109SPVT050_BACK_TO_PRESEP.tcl	1.6	Wed Jul 25 08:45:06 2007
A102109SPVT051_ACMS_OCM_01.tcl	1.6	Wed Apr 18 09:22:19 2007
A102109SPVT052_TRANSITION_TO_SCM.tcl	1.22	Sat Nov 3 13:39:16 2007
A102109SPVT053_ACMS_SAM_01.tcl	1.10	Tue Nov 27 23:55:36 2007
A102109SPVT056_ACMS_HC_06.tcl	1.3	Wed Apr 18 09:22:21 2007
A102109SPVT057_TRANSITION_TO_SAM.tcl	1.2	Mon Oct 2 14:57:09 2006
A102109SPVT058_ACMS_SCM_02.tcl	1.16	Wed Jul 25 04:33:47 2007
A102109SPVT059_ACC_TIMESYNC.tcl	1.1	Mon May 15 13:10:02 2006
A102109SPVT060_ACMS_SCM_05.tcl	1.12	Tue Oct 2 14:12:36 2007
A102109SPVT061_RWL_SPINDOWN.tcl	1.3	Tue Sep 26 19:48:34 2006
A102109SPVT062_ACMS_SCM_01.tcl	1.10	Fri Jul 20 14:06:48 2007
A102109SPVT063_MODIFY_CROME_REGISTER.tcl	1.1	Mon May 22 08:28:27 2006
A102109SPVT064_ACMS_CIRSIR_03.tcl	1.3	Thu Jun 8 09:58:33 2006
A102109SPVT065_ACMS_CIRSIR_04_I.tcl	1.3	Thu Jun 8 09:59:06 2006
A102109SPVT066_ACMS_CIRSIR_04_II.tcl	1.3	Thu Jun 8 09:59:46 2006
A102109SPVT067_ACMS_SCM_04.tcl	1.12	Wed Jul 25 04:45:20 2007
A102109SPVT069_ACMS_AV_02.tcl	1.3	Thu Jun 8 10:02:23 2006
A102109SPVT070_ACMS_AV_04.tcl	1.4	Thu Jun 15 06:53:41 2006
A102109SPVT071_ACMS_RECOVERY.tcl	1.2	Thu Oct 5 10:59:51 2006
A102109SPVT072_ACMS_GYRCHK_01.tcl	1.3	Wed Apr 18 09:22:22 2007
A102109SPVT073_ACMS_PT_03.tcl	1.3	Tue Jul 25 15:46:54 2006
A102109SPVT074_ACMS_PT_06.tcl	1.4	Wed Apr 18 09:22:23 2007
A102109SPVT075_ACMS_PT_07.tcl	1.3	Wed Apr 18 09:22:24 2007
A102109SPVT076_ACMS_PT_08.tcl	1.3	Wed Apr 18 09:22:25 2007
A102109SPVT077_ACMS_PT_09.tcl	1.3	Wed Apr 18 09:22:26 2007

2007_12_03_05_00_hercdmu_hpws23_REALTIME_EMC_nois2_TestEnvironment_REALTIME

A102109SPVT078_ACMS_PT_04.tcl	1.2	Thu Oct 12 13:48:06 2006
A102109SPVT079_ACMS_PT_05.tcl	1.3	Fri Oct 13 11:02:31 2006
A102109SPVT081_ACMS_ARAD_01.tcl	1.5	Thu Nov 30 09:59:57 2006
A102109SPVT082_ACMS_ARAD_05.tcl	1.6	Fri Nov 10 15:21:41 2006
A102109SPVT083_ACMS_FDIR_01.tcl	1.4	Mon Nov 27 14:15:17 2006
A102109SPVT084_ACMS_FDIR_02.tcl	1.3	Thu Nov 30 09:54:44 2006
A102109SPVT085_ACMS_PT_02.tcl	1.4	Mon Dec 4 20:01:01 2006
A102109SPVT086_ACMS_TIME_SYNCH.tcl	1.1	Tue Dec 5 17:49:39 2006
A102109SPVT087_ACMS_IST_FN.tcl	1.19	Mon Nov 5 06:02:45 2007
A102109SPVT088_ACMS_IST_UNIT_CHECKOUT.tcl	1.6	Sat Dec 1 04:53:19 2007
A102109SPVT089_ACMS_IST_ACC_HEALTH.tcl	1.8	Thu Nov 29 15:54:05 2007
A102109SPVT090_ACMS_IST_TANGO.tcl	1.6	Wed Jul 11 08:24:45 2007
A102109SPVT091_ACMS_IST_RWL_HEALTH.tcl	1.5	Fri Jul 13 13:58:58 2007
A102109SPVT092_ACMS_GC_03.tcl	1.2	Wed Nov 28 21:47:58 2007
A102109SPVT0XX_ACMS_RWL_UFT.tcl	1.4	Thu Sep 28 08:48:29 2006
A102109SPVT0xx_ACMS_RWL_Test_V2.tcl	1.1	Thu Jan 19 07:30:22 2006
A102109SPVT100_ACMS_OPS.tcl	1.42	Wed Nov 28 21:36:28 2007
A102109SPVT200_ACMS_DELTA_V_IST.tcl	1.3	Thu Aug 30 08:13:36 2007
A102109SPVT201_ACMS_STATUS.tcl	1.12	Tue Nov 21 07:25:29 2006
A102109SPVT202_ACMS_STATUS_H.tcl	1.7	Tue Nov 6 16:34:27 2007
A102109SPVT203_RWL_SPINUP_IST.tcl	1.1	Fri Nov 3 10:43:13 2006
A102109SPVT204_ACMS_IST_FDIR.tcl	1.12	Thu Aug 30 08:14:06 2007
A102109SPVT204_ACMS_TRANSIT_SCM_OCM_SCM.tcl	1.3	Tue Nov 21 07:32:07 2006
A102109SPVT205_ACMS_IST_RCS_HC.tcl	1.2	Wed Apr 18 09:22:27 2007
A102109SPVT205_ACMS_IST_RCS_HC_PART1.tcl	1.3	Tue Dec 5 15:58:25 2006
A102109SPVT205_ACMS_IST_RCS_HC_PART2.tcl	1.4	Wed Dec 6 11:27:11 2006
A102109SPVT206_ACMS_RWL_EMC_SETUP.tcl	1.5	Wed Nov 28 09:18:14 2007
A102109SPVT207_ACMS_STR_EMC_SETUP.tcl	1.3	Wed Nov 28 09:22:00 2007
ACMS_ACC_CLOSE_STRAPS.tcl	1.1	Mon May 15 13:11:18 2006
ACMS_ACC_SEPARA.tcl	1.1	Mon May 15 13:11:28 2006
ACMS_Error_Inj_AAD_curr.tcl	1.1	Mon May 15 13:11:40 2006
ACMS_FOP_RM_DISABLE.tcl	1.1	Mon May 15 13:11:51 2006
ACMS_FOP_RM_ENABLE.tcl	1.1	Mon May 15 13:12:02 2006
ACMS_FOP_STR_OFF.tcl	1.1	Mon May 15 13:12:18 2006
ACMS_FOP_STR_ON.tcl	1.1	Mon May 15 13:11:08 2006
ACMS_MON.tcl	1.1	Mon May 15 13:10:57 2006
ACMS_QSL.tcl	1.2	Mon May 15 13:48:23 2006
ACMS_RECOVERY.tcl	1.2	Wed Apr 18 09:22:28 2007
ACMS_RECOVERY_from_AutoPeriod.tcl	1.2	Mon May 19 06:33:34 2008
ACMS_RWL_RUN_IN.tcl	1.2	Wed Apr 18 09:22:29 2007
ACMS_RWL_RUN_IN_PASSIVE.tcl	1.3	Tue Jun 5 07:56:32 2007
ACMS_SAM_MON_P.tcl	1.1	Mon May 15 13:10:47 2006
ACMS_SCOE_checks.tcl	1.4	Fri Nov 18 18:48:04 2005
ACMS_SEP_DELAY.tcl	1.1	Mon May 15 13:10:38 2006
ACMS_STAR_VECTOR_TO_CCD.tcl	1.2	Wed Apr 18 09:22:30 2007
ACMS_STR_CHECK.tcl	1.1	Tue Jul 17 15:41:47 2007
ACMS_SVT0.tcl	1.4	Wed Apr 18 09:25:41 2007
ACMS_event_buffer_dump.tcl	1.8	Tue Jul 24 05:33:34 2007
ACMS_get_RM_status.tcl	1.9	Wed Oct 11 07:26:48 2006
ALL_SubscribeParams.tcl	1.2	Thu Apr 19 19:02:09 2007
ApidTracer.tcl	1.3	Tue Sep 25 17:52:30 2007
BARBARA_TEMPO.tcl	1.1	Tue Oct 25 09:08:46 2005
BOLO_cooler_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:09 2007
BS_SCOE_checks.tcl	1.1	Wed Jul 20 10:35:01 2005
Background_Adjustment_01_Shell.tcl	1.1	Thu Oct 18 15:48:09 2007
Background_Adjustment_02_Shell.tcl	1.1	Thu Oct 18 15:48:09 2007
Background_Adjustment_03_Shell.tcl	1.1	Thu Oct 18 15:48:09 2007
Background_Adjustment_04_Shell.tcl	1.1	Thu Oct 18 15:48:09 2007
Background_Adjustment_05_Shell.tcl	1.1	Thu Oct 18 15:48:10 2007
Background_Adjustment_06_Shell.tcl	1.1	Thu Oct 18 15:48:10 2007
CALIBRATION_FOR_GYRO.tcl	1.6	Thu Mar 8 17:44:26 2007
CCS-IEGSE-IFTest-HIFI_102cmds.tcl	1.1	Thu Apr 19 18:42:54 2007
CCS-IEGSE-IFTest-HIFI_200cmds.tcl	1.1	Thu Apr 19 18:43:02 2007
CCS-IEGSE-IFTest-HIFI_20cmds.tcl	1.1	Thu Apr 19 18:42:43 2007
CCS-IEGSE-IFTest-PACS_102cmds.tcl	1.1	Thu Apr 19 18:43:09 2007
CCS-IEGSE-IFTest-PACS_200cmds.tcl	1.1	Thu Apr 19 18:43:17 2007
CCS-IEGSE-IFTest-PACS_20cmds.tcl	1.1	Thu Apr 19 18:43:25 2007

2007_12_03_05_00_hercdmu_hpws23_REALTIME_EMC_nois2_TestEnvironment_REALTIME

CCS-IEGSE-IFTest-SPIRE_102cmds.tcl	1.1	Thu Apr 19 18:43:32 2007
CCS-IEGSE-IFTest-SPIRE_200cmds.tcl	1.1	Thu Apr 19 18:43:39 2007
CCS-IEGSE-IFTest-SPIRE_20cmds.tcl	1.1	Thu Apr 19 18:44:01 2007
CDMU_SCOE_checks.tcl	1.2	Fri Aug 26 10:23:33 2005
CONF_chopper_ast_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:10 2007
CONF_grating_IST_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:10 2007
CONF_grating_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:10 2007
CONF_grating_SFTHeII_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:10 2007
CONF_grating_SFTHeII_R_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:10 2007
CONF_phot_fltw_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:10 2007
CONF_phot_fltw_R_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:10 2007
CONF_phot_fltw_R_Warm_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:10 2007
CONF_phot_fltw_Warm_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:10 2007
CONF_spec_fltw_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:10 2007
CONF_spec_fltw_R_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:10 2007
CONF_spec_fltw_R_Warm_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:10 2007
CONF_spec_fltw_Warm_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:10 2007
CcsHanderTest.tcl	1.1	Thu Mar 8 18:04:22 2007
CcsHanderTest200.tcl	1.2	Wed Apr 18 09:33:02 2007
Check_PCDU_mini_IST.tcl	1.1	Tue Oct 25 23:21:04 2005
Chop_mov_abs_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:10 2007
D101159SCVT003DISTHERMALCONTROL.tcl	1.1	Thu Jun 28 12:49:05 2007
D101159SCVT008MEM_MAN_RD_WR_CHK.tcl	1.1	Sat Oct 29 09:55:16 2005
D101159SCVT028SSMM_OFF.tcl	1.1	Fri Mar 10 17:14:17 2006
D102159SCVT000SUBSEQ.tcl	1.2	Mon Nov 7 20:47:54 2005
D102159SCVT000SUBSEQDOWN1.tcl	1.14	Wed Nov 2 10:36:30 2005
D102159SCVT000_SYSTEMINIT.tcl	1.2	Thu Sep 15 12:42:21 2005
D102159SCVT001PM_SELECT.tcl	1.19	Thu Nov 15 12:14:21 2007
D102159SCVT002TC_VERIF_SERVICE.tcl	1.3	Fri Oct 28 11:15:48 2005
D102159SCVT003DISTHERMALCONTROL.tcl	1.7	Mon Oct 29 15:39:11 2007
D102159SCVT003_EMERGENCY_DISTHERMALCONTROL.tcl	1.2	Wed Apr 18 08:46:13 2007
D102159SCVT004DEVICE_COMMAND_DISTR.tcl	1.5	Fri Nov 11 08:21:13 2005
D102159SCVT005HK_DATA_REPORTING.tcl	1.2	Fri Oct 28 10:19:12 2005
D102159SCVT006TIMEMANAGEMENT.tcl	1.3	Tue Oct 25 17:59:31 2005
D102159SCVT007PM_RESET.tcl	1.19	Mon Oct 29 16:46:49 2007
D102159SCVT007PM_RESET_IT.tcl	1.2	Mon Jun 11 14:03:15 2007
D102159SCVT008MEM_MAN_RD_WR_CHK.tcl	1.3	Sat Oct 29 10:03:55 2005
D102159SCVT009MEMORYMANAGEMENT.tcl	1.3	Sat Oct 29 09:54:54 2005
D102159SCVT010HK_DATA_REPORTING.tcl	1.1	Fri Oct 28 10:22:00 2005
D102159SCVT011MEM_MAN_COPY_MEM.tcl	1.4	Sat Oct 29 09:55:59 2005
D102159SCVT012_INITCOMPL_BOOTEVENT.tcl	1.3	Mon Oct 29 16:08:07 2007
D102159SCVT012_INITCOMPL_BOOTEVENT_IM2.tcl	1.2	Fri Oct 26 09:27:01 2007
D102159SCVT013_CDMSINTHKCHECK.tcl	1.3	Wed Jan 11 09:59:28 2006
D102159SCVT013_CDMSINTHKCHECK_PMB.tcl	1.4	Tue May 16 15:36:26 2006
D102159SCVT014CONNECT_TEST_SERVICE.tcl	1.2	Fri Oct 28 10:17:23 2005
D102159SCVT015_PACKET_TRASM_CONTR.tcl	1.4	Thu Nov 10 22:18:01 2005
D102159SCVT015_PACKET_TRASM_CTR_BA.tcl	1.1	Tue Nov 8 13:42:30 2005
D102159SCVT016HK_DATA_REPORTING.tcl	1.2	Fri Oct 28 10:20:24 2005
D102159SCVT017HK_DATA_REPORTING.tcl	1.2	Fri Oct 28 10:20:55 2005
D102159SCVT018HK_DATA_REPORTING.tcl	1.2	Fri Oct 28 10:21:15 2005
D102159SCVT019HK_DATA_REPORTING.tcl	1.2	Fri Oct 28 10:21:34 2005
D102159SCVT020ASW_PCDU_MANAGEMENT.tcl	1.4	Fri Nov 3 07:34:45 2006
D102159SCVT021_EVENTREPORTING.tcl	1.2	Sat Oct 29 09:45:57 2005
D102159SCVT022ASW_TT_C_MANAGEMENT.tcl	1.7	Tue Nov 6 20:48:11 2007
D102159SCVT023_TC_MODEVERIFICATION.tcl	1.3	Thu Oct 27 19:22:29 2005
D102159SCVT023_TC_MODE_VERIF_DECB.tcl	1.3	Thu Oct 27 19:22:41 2005
D102159SCVT024_THERM_CTRL_MANAG.tcl	1.5	Tue Nov 6 17:40:00 2007
D102159SCVT025ON_BOARD_MONITORING.tcl	1.4	Thu Nov 2 09:36:28 2006
D102159SCVT026BUS_COMM.tcl	1.2	Tue Oct 25 20:06:15 2005
D102159SCVT027BUS_PROF_MAN.tcl	1.5	Mon Oct 24 18:05:46 2005
D102159SCVT028SSMM_OFF.tcl	1.7	Sat Oct 27 10:50:38 2007
D102159SCVT028SSMM_OFF_IM2.tcl	1.1	Fri Oct 26 07:50:14 2007
D102159SCVT028SSMM_ON.tcl	1.8	Sat Oct 27 10:50:51 2007
D102159SCVT028SSMM_ON_IM2.tcl	1.1	Fri Oct 26 06:44:51 2007
D102159SCVT028_SSMM_MANAGEMENT.tcl	1.4	Sat Oct 29 12:31:09 2005
D102159SCVT029_ON_BOARD_STOR_RETR.tcl	1.15	Mon Nov 14 16:15:08 2005
D102159SCVT029_ON_BOARD_STOR_RETR1.tcl	1.8	Mon Nov 14 14:03:10 2005

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D102159SCVT029_ON_BOARD_STOR_RETR2.tcl	1.1	Tue Nov 8 11:54:45 2005
D102159SCVT029_SEL_&_CEL.tcl	1.1	Wed Nov 9 14:25:55 2005
D102159SCVT029_SEL_AND_CEL.tcl	1.11	Tue Nov 15 14:26:33 2005
D102159SCVT029_STORING_DOWNLOAD.tcl	1.5	Wed Oct 26 07:21:59 2005
D102159SCVT029_STORING_DOWNLOAD1.tcl	1.4	Mon Nov 14 15:42:27 2005
D102159SCVT029_STORING_DOWNLOAD2.tcl	1.5	Mon Nov 14 08:49:05 2005
D102159SCVT030_DLL1_ERR_BUSAB_HEAL.tcl	1.2	Wed Oct 26 12:18:40 2005
D102159SCVT030_DLL2_CORE.tcl	1.1	Tue Sep 5 07:32:49 2006
D102159SCVT030_DLL2_ERRA_BUSB_UNHE.tcl	1.2	Thu Nov 2 06:12:57 2006
D102159SCVT030_DLL2_ERR_A_B_UNHEAL.tcl	1.1	Tue Sep 5 07:33:09 2006
D102159SCVT030_DLL3_1_NN_VITAL_RT.tcl	1.2	Wed Oct 26 19:22:33 2005
D102159SCVT030_DLL4_VITAL_RT.tcl	1.2	Thu Nov 2 06:12:32 2006
D102159SCVT030_DLL5_NON_VITAL_RT_FAIL.tcl	1.3	Thu Nov 2 06:11:44 2006
D102159SCVT031OBCP_MANAGEMENT.tcl	1.7	Thu Nov 2 14:38:44 2006
D102159SCVT032EnNomTCSLoops.tcl	1.8	Tue Nov 6 15:59:57 2007
D102159SCVT032TIMESYNCRO.tcl	1.7	Fri Oct 26 07:35:53 2007
D102159SCVT033_TCS_END_TO_END_NR.tcl	1.1	Wed Sep 20 15:51:59 2006
D102159SCVT034_TTR_MANAGEMENT.tcl	1.13	Mon Oct 29 18:39:30 2007
D102159SCVT035_LOW_RATE_PACKET.tcl	1.4	Thu May 3 14:27:19 2007
D102159SCVT035_PAYLOAD_MANAGEMENT.tcl	1.14	Tue Oct 30 12:19:28 2007
D102159SCVT036_ASW_VAL_TEST46.tcl	1.8	Fri Mar 3 06:38:26 2006
D102159SCVT037_ASW_VAL_TEST47.tcl	1.6	Thu Feb 23 11:17:20 2006
D102159SCVT038_DECONT_HEAT_MANAG.tcl	1.2	Wed Feb 15 19:43:48 2006
D102159SCVT039_EVENT_ACTION_MANAG.tcl	1.5	Thu Nov 2 09:25:07 2006
D102159SCVT040_ASW_VAL_TEST43.tcl	1.3	Sat Feb 18 11:56:22 2006
D102159SCVT040_FAILTC_READ_BOTH_MM.tcl	1.6	Wed Dec 21 14:56:42 2005
D102159SCVT041_ASW_VAL_TEST41.tcl	1.2	Tue Feb 21 16:21:57 2006
D102159SCVT042_ASW_VAL_TEST42.tcl	1.2	Tue Feb 21 14:57:07 2006
D102159SCVT043_MODE_MANAGEMENT.tcl	1.1	Tue Feb 28 06:50:55 2006
D102159SCVT044_ASW_VAL_TEST44.tcl	1.3	Mon Feb 20 06:01:24 2006
D102159SCVT045_LAUNCH_LAUNCH.tcl	1.10	Thu Nov 2 09:57:22 2006
D102159SCVT046_LAUNCH_SUNACQ.tcl	1.5	Tue Nov 6 19:01:15 2007
D102159SCVT047_SUNACQ_SUNACQ.tcl	1.4	Tue May 20 01:35:42 2008
D102159SCVT048_SUNACQ_NOMINAL.tcl	1.6	Tue Nov 6 20:38:54 2007
D102159SCVT049_NOMINAL_NOMINAL.tcl	1.2	Tue May 20 01:35:19 2008
D102159SCVT050_NOMINAL_EARTH.tcl	1.3	Tue Nov 6 18:55:09 2007
D102159SCVT051_EARTH_EARTH.tcl	1.2	Tue May 20 01:33:19 2008
D102159SCVT052_EARTH_NOMINAL.tcl	1.2	Tue Nov 6 20:55:26 2007
D102159SCVT053_NOMINAL_SUNACQ.tcl	1.2	Tue Nov 6 21:05:19 2007
D102159SCVT054_SUNACQ_SURV.tcl	1.7	Mon Nov 6 19:48:11 2006
D102159SCVT055_SURV_SURV.tcl	1.1	Wed Jun 27 19:30:44 2007
D102159SCVT057_NOMINAL_SURV.tcl	1.1	Tue Nov 6 21:15:00 2007
D102159SCVT059_EARTH_SURV.tcl	1.1	Wed Jun 27 19:31:47 2007
D102159SCVT060_ASW_VAL_TEST45.tcl	1.7	Fri Feb 17 16:32:37 2006
D102159SCVT062_DLL2_ERRA_BUSB_UNHE.tcl	1.3	Fri Feb 17 15:01:57 2006
D102159SCVT067_NOMINAL_SURV_FDIR4.tcl	1.2	Mon Jul 31 08:16:45 2006
D102159SCVT070_CDMS_RECONF_ANALYSIS.tcl	1.2	Wed Feb 22 10:30:51 2006
D102159SCVT071_ONBOARD_SCHEDULING.tcl	1.2	Wed Nov 8 20:58:34 2006
D102159SCVT072_SHUT_DOWN_LCL_XPND1.tcl	1.3	Tue Nov 6 20:59:45 2007
D102159SCVT073_OBCP_MAST_INST_LOAD.tcl	1.3	Tue Nov 6 20:48:50 2007
D102159SCVT076_LOAD_SHORT_MTL.tcl	1.1	Fri Jun 30 08:41:44 2006
D102159SCVT080_CEL_DOWNLINK.tcl	1.5	Tue Oct 9 08:29:34 2007
D102159SCVT081DIS_EATTABLE_ENTRIES.tcl	1.1	Sat Jul 29 13:58:13 2006
D102159SCVT081_PAP4_PM_A_NOM.tcl	1.2	Tue Apr 24 12:40:43 2007
D102159SCVT082_PAP3_PM_A_NOM.tcl	1.2	Tue Apr 24 12:42:00 2007
D102159SCVT082_SVT0_6BK_PSTORE_DEF.tcl	1.2	Wed Aug 2 16:34:09 2006
D102159SCVT083_PAP4_PM_B_NOM.tcl	1.3	Tue Apr 24 12:42:25 2007
D102159SCVT083_SVT0_2BK_PSTORE_DEF.tcl	1.2	Wed Aug 2 16:34:20 2006
D102159SCVT084_PAP3_PM_B_NOM.tcl	1.4	Tue Apr 24 12:42:56 2007
D102159SCVT084_TTEC_CHAIN2_NOMINAL.tcl	1.1	Sat Jul 29 14:46:45 2006
D102159SCVT085_FDIR_LEVEL2_AIR.tcl	1.3	Thu Nov 9 23:43:32 2006
D102159SCVT085_PAP0_PM_A_NOM.tcl	1.1	Fri Jun 29 13:01:37 2007
D102159SCVT085_PAP0_PM_B_ONLY.tcl	1.2	Tue Apr 24 12:43:29 2007
D102159SCVT086_LAUNCH_LAUNCH_IST.tcl	1.4	Tue May 22 07:04:59 2007
D102159SCVT086_PAP5_PM_B_NOM.tcl	1.2	Tue Apr 24 12:44:16 2007
D102159SCVT087_PAP1_PM_B_ONLY.tcl	1.2	Tue Apr 24 12:44:42 2007
D102159SCVT088_PAP1_PM_A_ONLY.tcl	1.2	Tue Apr 24 12:41:25 2007
D102159SCVT089_PAP2_PM_B_NOM.tcl	1.1	Fri Jun 29 13:12:02 2007

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D102159SCVT090_PAP0_PM_A_NOM.tcl          1.1          Fri Jun 29 13:09:19 2007
D102159SCVT095_ASW_VAL_TEST48.tcl         1.1          Wed Jul  4 07:04:09 2007
D102159SCVT100_CHKTM_UNITHEALTH.tcl       1.1          Fri May 12 14:53:16 2006
D102159SCVT101_CDMS_RED_CONFIG.tcl        1.2          Fri May 12 14:41:02 2006
D102159SCVT103_CDMS_NOM_CONFIG.tcl        1.1          Fri Mar 10 15:57:38 2006
D102159SCVT104_ENCODER_SELECT.tcl         1.7          Thu Jul  5 13:40:13 2007
D102159SCVT105_IST_NOMLNCH_LNCH_SAM.tcl   1.3          Mon Sep  3 09:08:05 2007
D102159SCVT105_LAUNCH_SUNACQ.tcl          1.7          Thu Aug 30 06:51:30 2007
D102159SCVT107_PACKET_STORE_DEF.tcl       1.6          Fri May 11 10:16:36 2007
D102159SCVT108_SUNACQ_NOMINAL.tcl         1.7          Mon May 14 16:11:25 2007
D102159SCVT110_MTL_RASTER_POINTING.tcl    1.8          Tue Nov 21 08:26:42 2006
D102159SCVT111_MTL_LINE_SCANNING.tcl      1.6          Tue Nov 21 08:29:40 2006
D102159SCVT112_MTL_PACS_BURST_CHECK.tcl   1.3          Fri Nov 17 15:56:53 2006
D102159SCVT113_3A_NOMINAL_PMA.tcl         1.8          Mon Nov 13 17:10:34 2006
D102159SCVT114_3B_SAM_PMA.tcl             1.6          Mon Nov 13 17:37:22 2006
D102159SCVT115_CHECK_HCS_OFF.tcl          1.3          Sun May 18 06:25:01 2008
D102159SCVT116_IST_16_OBCP_LOADING.tcl    1.1          Tue Sep 26 15:31:12 2006
D102159SCVT117_IST_OBCP_FULL_LOAD.tcl     1.1          Tue Sep 26 15:31:54 2006
D102159SCVT118_FDIR4_EARTHSURV_PMB.tcl    1.6          Wed Nov 22 10:52:32 2006
D102159SCVT119_MTL_DUMMY.tcl              1.1          Mon Oct 16 16:24:29 2006
D102159SCVT120_TM_LINK_5_KBPS.tcl         1.1          Mon Oct 16 16:22:27 2006
D102159SCVT121_P_L_SIMULATION.tcl         1.3          Fri Nov 10 14:50:21 2006
D102159SCVT122_P_L_SIMULATION_OFF.tcl     1.2          Fri Nov 10 14:50:36 2006
D102159SCVT123_TM_LINK_150_KBPS.tcl       1.2          Tue May 20 01:29:23 2008
D102159SCVT124__IST_TM_LINK_150_KBPS.tcl  1.6          Sat Nov 11 10:04:06 2006
D102159SCVT125_IST_CDMS_REDCON_PMB.tcl    1.2          Sat Nov 11 11:23:33 2006
D102159SCVT126_LCL_OFF_BEF_SC_OFF.tcl     1.3          Wed Feb 21 15:29:14 2007
D102159SCVT127_FDIR4_NOMSURV_PMA_ONLY.tcl 1.7          Thu Apr  5 09:45:21 2007
D102159SCVT128_RESTORE_FROM_SURV.tcl      1.4          Wed Jun 27 19:28:54 2007
D102159SCVT129_LANCH_SACQ_FDIR_SEP.tcl    1.2          Fri Nov 10 14:37:05 2006
D102159SCVT130_3A_NOMINAL_PMB.tcl         1.3          Wed Nov 22 10:54:00 2006
D102159SCVT131_DLL2_CORE_BUSB.tcl         1.3          Wed Nov 22 10:54:37 2006
D102159SCVT132_FDIR4_SAMSURV_PMA.tcl      1.3          Wed Nov 22 10:55:15 2006
D102159SCVT133_1553_BUS_SWITCH_OVER.tcl   1.1          Fri Nov 17 11:00:24 2006
D102159SCVT134_PCDU_1553_BUS_FAIL_RECOV.tcl 1.2          Tue Oct 30 08:00:27
2007
D102159SCVT134_RECOVERY_SGM_FAILURE.tcl   1.5          Thu Dec 21 13:41:12 2006
D102159SCVT135_AIR_DGN_DEFINITION.tcl     1.1          Wed Nov 22 18:35:18 2006
D102159SCVT135_TTC_RX_POWER_OOL.tcl       1.2          Tue Nov  6 18:10:52 2007
D102159SCVT135_TTC_XPND_INVALID_RT.tcl    1.2          Tue Nov  6 18:45:58 2007
D102159SCVT136_FUNC_FAIL_MODE_RFDN_SWITCH.tcl 1.4          Tue Nov  6 17:49:01
2007
D102159SCVT136_VERIFY_PKT_VMC.tcl         1.3          Thu Dec 21 13:42:29 2006
D102159SCVT137_IST_SUNACQ_NOM.tcl         1.5          Tue Nov 27 17:29:15 2007
D102159SCVT138_IST_LAUNCH_SUNACQ.tcl      1.4          Fri Nov  2 13:33:26 2007
D102159SCVT150_DELIBERATE_ERRORS.tcl      1.7          Sat Nov 17 06:56:57 2007
D102159SCVT151_HER_IST_PKT_STR_DEF.tcl    1.1          Wed Feb 21 07:36:55 2007
D102159SCVT152_LAUNCH_SUNACQ_IST_FDIR.tcl 1.9          Tue May 29 07:38:09 2007
D102159SCVT153_MTL_FDIR.tcl               1.11         Mon Sep  3 14:06:29 2007
D102159SCVT154_FDIR_NOM_EARTH_3a.tcl      1.10         Thu Sep 20 14:15:01 2007
D102159SCVT155_SHORT_MTL_FDIR.tcl         1.2          Fri Jun 15 07:55:04 2007
D102159SCVT156_CDMS_ANALYSIS_FDIR_IST.tcl 1.3          Thu May  3 07:52:50 2007
D102159SCVT157_FDIR_EARTH_EARTH_3b.tcl    1.15         Thu Oct  4 12:30:57 2007
D102159SCVT158_FDIR_NOMINAL_SUNACQ.tcl    1.8          Wed Sep 26 11:27:58 2007
D102159SCVT159_FDIR_CHECK_SUNACQMODE.tcl  1.4          Wed Sep 26 11:32:44 2007
D102159SCVT160_FDIR_NOM_SURV_DOD.tcl      1.5          Tue Oct  9 11:57:20 2007
D102159SCVT161_IST_MM_NOM_NOM.tcl         1.14         Fri Sep 14 12:03:34 2007
D102159SCVT162_IST_MM_SAM_SAM.tcl         1.10         Sat Nov 10 16:31:44 2007
D102159SCVT163_IST_MM_LAN_SAM.tcl         1.9          Sat Nov 10 08:21:03 2007
D102159SCVT164_IST_MM_SAM_NOM.tcl         1.9          Sat Nov 10 09:00:57 2007
D102159SCVT165_RECOVERY_PCDUA_FAST.tcl    1.3          Sat Oct 27 11:17:45 2007
D102159SCVT166_SHORT_MTL_FDIR_MM.tcl      1.6          Thu Jun 14 08:58:15 2007
D102159SCVT167_IST_MM_NOM_EAM.tcl         1.8          Fri Jun  1 13:42:08 2007
D102159SCVT168_IST_MM_EAM_EAM.tcl         1.3          Fri Apr 20 13:12:27 2007
D102159SCVT169_IST_MM_EAM_NOM.tcl         1.6          Thu May 31 16:05:56 2007
D102159SCVT170_IST_MM_NOM_SM.tcl          1.6          Fri Jun  1 08:51:09 2007
D102159SCVT171_IST_MM_SM_SM.tcl           1.5          Fri Jun  1 08:50:39 2007
D102159SCVT172_IST_MM_SM_SAM.tcl          1.3          Fri Jun  1 10:44:00 2007
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D102159SCVT173_IST_MM_EAM_SAM.tcl	1.4	Fri Jun 1 13:46:56 2007
D102159SCVT174_IST_REDUNDANT_CONF.tcl	1.19	Fri Nov 2 08:02:00 2007
D102159SCVT175_SET_SURV_REG.tcl	1.16	Sat Nov 17 16:39:19 2007
D102159SCVT176_WRITE_CROME.tcl	1.23	Sat Dec 1 05:59:51 2007
D102159SCVT177_IST_MM_NOM_SAM.tcl	1.3	Fri Jun 1 14:09:55 2007
D102159SCVT178_RMS_PKT_STORE_DEF.tcl	1.7	Fri Nov 30 17:04:24 2007
D102159SCVT179_DBG_PKT_STORE_DEF.tcl	1.11	Fri Nov 30 17:04:38 2007
D102159SCVT180_DUMP_PKT_STORE.tcl	1.2	Thu May 31 16:38:41 2007
D102159SCVT181_DISABLE_PKT_STORE.tcl	1.3	Thu May 31 16:26:37 2007
D102159SCVT182_DUMP_PKT_STORE_RMS_DTCP.tcl	1.3	Fri Aug 3 18:25:46 2007
D102159SCVT183_CEL_DOWNLINK_RMS_DTCP.tcl	1.2	Tue May 20 13:25:00 2008
D102159SCVT184_SWITCH_TO_PCDUB_BUS_B.tcl	1.4	Thu Jul 19 12:36:12 2007
D102159SCVT185_IST_PKT_STORE_DEF.tcl	1.13	Tue Nov 27 08:48:02 2007
D102159SCVT186_IST_SSMM_ON.tcl	1.21	Sat Oct 27 10:58:02 2007
D102159SCVT187_IST_SSMM_OFF.tcl	1.8	Fri Nov 30 17:03:49 2007
D102159SCVT188_IST_DUMP_PKT_STORE.tcl	1.10	Wed Nov 21 15:43:44 2007
D102159SCVT189_IST_PKT_STORE_DEF_2.tcl	1.10	Fri Nov 30 17:03:34 2007
D102159SCVT190_IST_WCS_PKTSTORE_DUMP_BREAK.tcl	1.3	Wed Nov 7 08:31:31 2007
D102159SCVT191_TM_5_KBPS_IST.tcl	1.2	Thu Jul 12 16:31:57 2007
D102159SCVT192_GET_EAT_REPORT.tcl	1.7	Fri Nov 30 17:04:01 2007
D102159SCVT192_IST_UPLOAD_EAT.tcl	1.11	Mon Nov 19 14:02:48 2007
D102159SCVT193_IST_UPLOAD_OBCP.tcl	1.12	Sat Nov 10 09:10:11 2007
D102159SCVT196_IST_ONBOARD_SCHEDULING.tcl	1.6	Mon Nov 5 15:01:57 2007
D102159SCVT197_IST_OBCP_MANAGM.tcl	1.5	Wed Oct 31 11:21:46 2007
D102159SCVT198_IST_SSMM_MANAGM.tcl	1.12	Wed Oct 31 09:46:29 2007
D102159SCVT199_IST_OBT_MANAGM.tcl	1.8	Tue Oct 9 11:56:09 2007
D102159SCVT200_IST_CDMS_MANAGM_FDIR.tcl	1.10	Thu Oct 25 13:12:43 2007
D102159SCVT201_IST_SAT_COM_CDMS.tcl	1.7	Wed Nov 28 20:25:56 2007
D102159SCVT202_IST_MTL_PING_TEST.tcl	1.5	Wed Nov 21 14:38:14 2007
D102159SCVT203_IST_MTL_ReportCat.tcl	1.1	Thu Oct 11 14:03:23 2007
D102159SCVT204_GET_MOT.tcl	1.3	Tue Sep 11 14:32:01 2007
D102159SCVT205_SAT_COM_TCT.tcl	1.2	Mon Nov 26 19:59:13 2007
D102159SCVT206_IST_SCIENCE_DOWNLNK.tcl	1.6	Mon Nov 5 14:49:30 2007
D102159SCVT207_SAT_COM_FCCT.tcl	1.2	Fri Sep 14 13:52:17 2007
D102159SCVT209_START_ON_BOARD_SCHEDULE.tcl	1.8	Sat Dec 1 07:18:17 2007
D102159SCVT210_GET_ALARM_STATUS.tcl	1.2	Mon Nov 19 08:33:37 2007
D102159SCVT211_IST_INSTR_MTL_PING.tcl	1.2	Wed Nov 7 16:38:50 2007
D102159SCVT212_EMC_LAUNCH_SUNACQ.tcl	1.2	Wed Nov 28 13:19:04 2007
D102159SCVT213_DUMP_MEM_AREAS.tcl	1.6	Wed Nov 28 20:29:30 2007
D102159SCVT214_IST_HIFI_MTL_PING.tcl	1.1	Mon Nov 26 10:46:34 2007
D102159SCVT991PM_SELECT_IM2.tcl	1.1	Thu Oct 25 12:30:00 2007
D102159SCVT993DISTHERMALCONTROL.tcl	1.1	Thu Oct 25 12:30:38 2007
D102159SCVT997PM_RESET_IM2.tcl	1.1	Thu Oct 25 12:31:08 2007
D102159SCVT998_CDMSINTHKCHECK.tcl	1.1	Thu Oct 25 12:31:36 2007
D102159SCVT999_INITCOMPL_BOOTEVENT.tcl	1.1	Thu Oct 25 12:32:07 2007
D102159SPVT001_UFT_TEST.tcl	1.3	Fri Aug 25 10:51:57 2006
D102159SPVT002_UFT_PMB_TEST.tcl	1.3	Fri Aug 25 10:52:24 2006
D201159SCVT029_ON_BOARD_STOR_RETR2.tcl	1.2	Mon Nov 7 16:50:51 2005
DisableRelTCs_RMS.tcl	1.1	Fri Aug 3 14:24:25 2007
ENTER_SAFE_Mode_Shell.tcl	1.1	Thu Oct 18 15:48:10 2007
H101999SCVT001_ASDDBGHIFI_PWR_ON_P.tcl	1.2	Tue Aug 21 15:30:23 2007
H101999SCVT002_ASDDBGHIFI_PWR_OFF_P.tcl	1.2	Tue Aug 21 15:31:06 2007
H102999SCVT001_ASDDBGHIFI_PWR_ON_P.tcl	1.16	Thu Nov 15 10:41:35 2007
H102999SCVT002_ASDDBGHIFI_PWR_OFF_P.tcl	1.11	Thu Nov 15 10:46:34 2007
H102999SCVT003_ASDDBGHIFI_PWR_ON_R.tcl	1.9	Thu Nov 15 10:47:14 2007
H102999SCVT004_ASDDBGHIFI_PWR_OFF_R.tcl	1.7	Thu Nov 15 10:47:50 2007
H102999SCVT005_ASDGENHIFI_PWR_ON_P.tcl	1.10	Wed Nov 28 17:27:02 2007
H102999SCVT006_ASDGENHIFI_PWR_OFF_P.tcl	1.4	Tue Nov 6 10:50:47 2007
H102999SCVT007_ASDGENHIFI_PWR_ON_R.tcl	1.9	Wed Nov 28 06:47:58 2007
H102999SCVT008_ASDGENHIFI_PWR_OFF_R.tcl	1.4	Tue Nov 6 10:51:13 2007
HIFIENG_FT_WBS_comb.tcl	1.1	Mon Aug 27 07:11:32 2007
HIFIENG_WBS_tune.tcl	1.1	Mon Aug 27 07:17:39 2007
HIFIENG_WBS_zero.tcl	1.1	Mon Aug 27 07:17:59 2007
HIFIENG_config_spectro.tcl	1.1	Mon Aug 27 07:01:13 2007
HIFIENG_take_tp_spectra.tcl	1.1	Mon Aug 27 07:16:39 2007
HIFIENG_tp_spectra_only.tcl	1.1	Mon Aug 27 07:17:13 2007
HIFIST_ASED_PatchPtvChecksum.tcl	1.2	Thu Oct 11 08:16:56 2007

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HIFIST_ASED_PatchTempLimits.tcl	1.3	Wed Nov 28 13:59:38 2007
HIFIST_CCS_conf_ptv_checksum.tcl	1.5	Thu Oct 11 16:29:50 2007
HIFIST_HRScable_HRS_conf.tcl	1.1	Fri Sep 28 09:28:40 2007
HIFIST_HRScable_HRS_off.tcl	1.1	Fri Sep 28 08:54:21 2007
HIFIST_HRScable_HRS_startup.tcl	1.1	Fri Sep 28 08:54:44 2007
HIFIST_HRScable_HRS_tune.tcl	1.1	Fri Sep 28 08:55:07 2007
HIFIST_SFT_CSA.tcl	1.4	Thu Oct 11 16:28:29 2007
HIFIST_SFT_Chopper_warm_1.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FCU_INIT_1.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FCU_INIT_2.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FCU_INIT_3.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FCU_INIT_4.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FCU_INIT_5.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FCU_INIT_6.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FCU_INIT_7.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FCU_deflux_1.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FCU_deflux_2.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FCU_deflux_3.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FCU_deflux_4.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FCU_deflux_5.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FT_WBS_Laser1.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_FT_WBS_Laser2.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_HBB_test_warm.tcl	1.1	Fri Aug 24 11:09:47 2007
HIFIST_SFT_HRS_FT_1.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_HRS_FT_2_Corr.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_HRS_FT_2_Sine.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_HRS_FT_2_Square_m.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_HRS_FT_2_Square_s.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_HRS_FT_4.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_HRS_tune.tcl	1.6	Thu Oct 11 16:28:29 2007
HIFIST_SFT_LCU_IV_1a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_IV_1b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_IV_2a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_IV_2b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_IV_3a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_IV_3b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_IV_4a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_IV_4b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_IV_5a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_IV_5b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_IV_6a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_IV_6b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_IV_7a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_IV_7b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LCU_switch_off.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT1a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT1b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT2a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT2b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT3a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT3b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT4a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT4b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT5a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT5b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT6a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT6b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT7a.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_SFT7b.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_heater_0.tcl	1.3	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_heater_1.tcl	1.3	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_heater_2.tcl	1.3	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_heater_3.tcl	1.3	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_heater_4.tcl	1.3	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_heater_5.tcl	1.3	Thu Oct 11 16:28:30 2007
HIFIST_SFT_LO_heater_6.tcl	1.3	Thu Oct 11 16:28:30 2007
HIFIST_SFT_Nominal_offcold.tcl	1.1	Wed Sep 19 09:39:46 2007
HIFIST_SFT_Nominal_offwarm.tcl	1.6	Thu Oct 11 16:28:30 2007

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HIFIST_SFT_Upconv_spectra.tcl	1.1	Thu Oct 11 16:23:30 2007
HIFIST_SFT_WBS_Laser1.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_WBS_Laser2.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_WBS_Lasers_off.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_WBS_comb.tcl	1.4	Fri Aug 24 11:05:03 2007
HIFIST_SFT_WBS_latchup_high.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_WBS_latchup_low.tcl	1.6	Thu Oct 11 16:28:30 2007
HIFIST_SFT_WBS_zero.tcl	1.6	Thu Oct 11 16:28:31 2007
HIFIST_SFT_Wait5.tcl	1.1	Wed Sep 19 09:40:03 2007
HIFIST_SFT_chopper_cold_0.tcl	1.1	Fri Aug 24 11:08:23 2007
HIFIST_SFT_chopper_cold_1.tcl	1.1	Fri Aug 24 11:09:07 2007
HIFIST_SFT_chopper_openloop_scan.tcl	1.1	Thu Oct 11 16:23:03 2007
HIFIST_SFT_chopper_warm_0.tcl	1.6	Thu Oct 11 16:28:31 2007
HIFIST_Startup_FCU_on.tcl	1.6	Fri Oct 12 06:27:50 2007
HIFIST_Startup_FPU_standby.tcl	1.6	Thu Oct 11 16:28:48 2007
HIFIST_Startup_HRS_on.tcl	1.6	Fri Oct 12 06:27:50 2007
HIFIST_Startup_HRS_standby.tcl	1.6	Thu Oct 11 16:28:48 2007
HIFIST_Startup_LCU_on.tcl	1.6	Fri Oct 12 06:27:50 2007
HIFIST_Startup_LCU_standby.tcl	1.6	Thu Oct 11 16:28:48 2007
HIFIST_Startup_LCU_table_load.tcl	1.8	Fri Oct 12 06:27:50 2007
HIFIST_Startup_LCU_table_read.tcl	1.7	Fri Oct 12 06:27:50 2007
HIFIST_Startup_LO_Nominal.tcl	1.6	Fri Oct 12 06:27:51 2007
HIFIST_Startup_OBS_SFT.tcl	1.6	Fri Oct 12 06:27:51 2007
HIFIST_Startup_WBSH_on.tcl	1.6	Fri Oct 12 06:27:51 2007
HIFIST_Startup_WBSV_on.tcl	1.6	Fri Oct 12 06:27:51 2007
HIFIST_Startup_WBS_lasertemp40.tcl	1.2	Fri Oct 12 06:27:51 2007
HIFIST_Startup_WBS_standby.tcl	1.6	Thu Oct 11 16:28:48 2007
HIFIST_Startup_all_off.tcl	1.6	Thu Oct 11 16:28:48 2007
HIFIST_Startup_chopper_set0.tcl	1.2	Fri Oct 12 06:27:51 2007
HIFIST_Startup_chopper_set1.tcl	1.2	Fri Oct 12 06:27:51 2007
HIFIST_Startup_force_boot.tcl	1.6	Fri Oct 12 06:27:51 2007
HP_OBSM_ALS.tcl	1.1	Wed Feb 14 14:02:18 2007
INST_something1.tcl	1.1	Mon Oct 29 10:36:35 2007
INST_something2.tcl	1.1	Mon Oct 29 10:37:08 2007
INST_something3.tcl	1.1	Mon Oct 29 10:37:30 2007
K102999ECVT001_ASDGENCCUA_ChkEssTM.tcl	1.12	Thu Mar 22 06:34:45 2007
K102999ECVT001_ASDGENCCUA_DLCMPar.tcl	1.2	Wed Apr 18 08:33:33 2007
K102999ECVT001_ASDGENCCUA_DL_Close.tcl	1.2	Thu Mar 22 10:41:24 2007
K102999ECVT001_ASDGENCCUA_MnDisMd1.tcl	1.4	Thu Mar 22 06:34:31 2007
K102999ECVT001_ASDGENCCUA_MnDisMd2.tcl	1.4	Thu Mar 22 06:34:16 2007
K102999ECVT001_ASDGENCCUA_MnEnaMd1.tcl	1.4	Thu Mar 22 06:34:02 2007
K102999ECVT001_ASDGENCCUA_MnEnaMd2.tcl	1.4	Thu Mar 22 06:33:48 2007
K102999ECVT001_ASDGENCCUA_POWEROFF.tcl	1.11	Tue May 22 14:54:07 2007
K102999ECVT001_ASDGENCCUA_POWERON.tcl	1.19	Fri Nov 2 13:02:48 2007
K102999ECVT001_ASDGENCCUB_ChkEssTM.tcl	1.9	Thu Mar 22 06:33:11 2007
K102999ECVT001_ASDGENCCUB_MnDisMd1.tcl	1.4	Thu Mar 22 06:32:57 2007
K102999ECVT001_ASDGENCCUB_MnDisMd2.tcl	1.5	Thu Mar 22 06:32:41 2007
K102999ECVT001_ASDGENCCUB_MnEnaMd1.tcl	1.4	Thu Mar 22 06:32:23 2007
K102999ECVT001_ASDGENCCUB_MnEnaMd2.tcl	1.4	Thu Mar 22 06:31:54 2007
K102999ECVT001_ASDGENCCUB_POWEROFF.tcl	1.10	Tue May 22 14:55:19 2007
K102999ECVT001_ASDGENCCUB_POWERON.tcl	1.16	Fri Nov 2 13:04:16 2007
K102999ECVT001_ASDGENCCU_ABPWROFF.tcl	1.4	Thu Mar 22 06:36:00 2007
K102999ECVT001_ASDGENCCU_ABPWRON.tcl	1.9	Sat Nov 3 09:57:19 2007
K102999ECVT001_ASDGENCCU_MnDBOTH1.tcl	1.4	Thu Mar 22 06:30:24 2007
K102999ECVT001_ASDGENCCU_MnDBOTH2.tcl	1.5	Thu Mar 22 06:35:35 2007
K102999ECVT001_ASDGENCCU_MnDisDLC.tcl	1.5	Thu Mar 22 06:35:23 2007
K102999ECVT001_ASDGENCCU_MnEBOTH1.tcl	1.4	Thu Mar 22 06:35:11 2007
K102999ECVT001_ASDGENCCU_MnEBOTH2.tcl	1.5	Thu Mar 22 06:34:57 2007
K102999ECVT002_ASDGEN_CCU_GUI_VLV.tcl	1.3	Sat Sep 29 08:40:20 2007
K102999ECVT024_ASDGENCCUB_DL_Close.tcl	1.2	Thu Mar 22 10:41:39 2007
K102999ECVT025_ASDGENCCU_ABCloseDL.tcl	1.2	Thu Mar 22 10:41:05 2007
K102999ECVT026_ASDGENCCU_DL2Close.tcl	1.2	Wed Apr 18 08:33:44 2007
K102999ECVT027_ASDISTCCU_AR5DLOPENX.tcl	1.1	Wed Apr 18 15:34:00 2007
K102999ECVT028_ASTISTCCU_EmgDLCls.tcl	1.2	Wed Apr 18 15:24:25 2007
K102999ECVT029_ASDGENCCUA_MnENaDLCM.tcl	1.2	Wed Aug 15 07:50:05 2007
K102999ECVT030_ASDGENCCUB_MnENaDLCM.tcl	1.2	Wed Aug 15 07:50:36 2007
K102999ECVT031_ASDGEN_CCU_LOG.tcl	1.2	Tue Nov 20 15:39:13 2007
LCU_PTV_patch_dummy_1.3.tcl	1.3	Tue Aug 28 07:59:01 2007

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   2007_12_03_05_00_hercdmu_hpws23_REALTIME_EMC_nois2_TestEnvironment_REALTIME
LPS_SCOE_checks.tcl                          1.1          Wed Jul 20 10:35:13 2005
Llock_Close_Shell.tcl                        1.1          Thu Oct 18 15:48:10 2007
Llock_Open_Shell.tcl                         1.1          Thu Oct 18 15:48:10 2007
MTL_PACS_restart_cata_RMS48h.tcl             1.1          Mon May 19 11:21:26 2008
MTL_Tool.tcl                                 1.5          Thu Oct 11 14:17:30 2007
MTL_tclgen15b_H_IST0_MTL_D079_080_20070504_v01.tcl 1.1          Sun May 18
12:06:39 2008
MTL_tclgen15b_H_IST0_MTL_D080_081_20070504_v01.tcl 1.1          Sun May 18
12:06:20 2008
MTL_tclgen15b_H_IST0_MTL_D080_081_20070504_v01_1600_no1.tcl 1.1          Sun May
18 12:07:42 2008
MTL_tclgen15b_H_IST0_MTL_D080_081_20070504_v01_1600_no2.tcl 1.1          Sun May
18 12:08:01 2008
MTL_tclgen15b_H_IST0_MTL_D080_081_20070504_v01_1600_no3.tcl 1.1          Sun May
18 12:08:27 2008
MTL_tclgen15b_H_IST0_MTL_D080_081_20070504_v01_1600_no4.tcl 1.1          Sun May
18 12:08:38 2008
MTL_tclgen15b_H_IST0_MTL_D080_081_20070504_v01_1600_no5.tcl 1.1          Sun May
18 12:08:53 2008
MTL_tclgen15b_H_IST0_MTL_D081_082_20070504_v01.tcl 1.1          Sun May 18
12:09:12 2008
MTL_tclgen15b_H_IST0_MTL_D081_082_20070504_v01_curtailed.tcl 1.1          Wed
May 23 17:20:35 2007
MTL_tclgen24_H_IST0_MTL_D079_080_20070424_v02.tcl 1.1          Mon May  7
12:39:26 2007
MTL_tclgen24_H_IST0_MTL_D079_080_20070424_v02_2007_127.tcl 1.1          Mon May
7 12:39:38 2007
MTL_tclgen24_H_IST0_MTL_D080_081_20070424_v02.tcl 1.1          Mon May  7
12:39:54 2007
MTL_tclgen24b_H_IST0_MTL_D080_081_20070424_v02_seg01_2007_127.tcl 1.1
Mon May  7 12:40:07 2007
MTL_tclgen24b_H_IST0_MTL_D080_081_20070424_v02_seg02_2007_127.tcl 1.1
Mon May  7 12:40:19 2007
MTL_tclgen24b_H_IST0_MTL_D080_081_20070424_v02_seg03_2007_127.tcl 1.1
Mon May  7 12:40:29 2007
MTL_tclgen24b_H_IST0_MTL_D080_081_20070424_v02_seg04_2007_127.tcl 1.1
Mon May  7 12:40:41 2007
MTL_tclgen24b_H_IST0_MTL_D080_081_20070424_v02_seg05_2007_127.tcl 1.1
Mon May  7 12:40:51 2007
MTL_tclgen24b_H_IST0_MTL_D080_081_20070424_v02_seg06_2007_127.tcl 1.1
Mon May  7 12:41:04 2007
MTL_tclgen24b_H_IST0_MTL_D080_081_20070424_v02_seg07_2007_127.tcl 1.1
Mon May  7 12:41:17 2007
MTL_tclgen24b_H_IST0_MTL_D080_081_20070504_v01_cut_for_PACS_OD_debug.tcl 1.1
     Wed May 23 17:21:57 2007
MTL_tclgen24b_H_IST0_MTL_D081_082_20070504_v01_AHHDA002cr.tcl 1.1          Sun
May 18 10:50:48 2008
MTL_tclgen24b_H_IST0_MTL_D081_082_20070504_v01_cut_for_SPIREphot_OD_debug.tcl
1.1          Wed May 23 17:22:54 2007
MTL_tclgen24b_H_IST0_MTL_D082_083_20070504_v01_curtailed.tcl 1.1          Wed
May 23 17:23:52 2007
MTL_tclgen24b_H_IST0_MTL_D082_083_20070504_v01_no1.tcl 1.1          Wed May 23
17:24:50 2007
MTL_tclgen24b_H_IST0_MTL_D082_083_20070504_v01_no2.tcl 1.1          Wed May 23
17:26:06 2007
MTL_tclgen31_H_IST0_MTL_D079_080_20070504_v01_48h_M2_M2B.tcl 1.1          Sun
May 18 07:09:52 2008
MTL_tclgen31_H_IST0_MTL_D080_081_20070504_v01_48h_M1_M2_M2B_M3_M6.tcl 1.1
  Sun May 18 07:11:12 2008
MTL_tclgen31_H_IST0_MTL_D080_081_20070504_v01_48h_M1_M2_M2B_M3_M6_SEG000_1500_cn
t.tcl 1.1          Sun May 18 07:58:47 2008
MTL_tclgen31_H_IST0_MTL_D080_081_20070504_v01_48h_M1_M2_M2B_M3_M6_SEG001_1500_cn
t.tcl 1.1          Sun May 18 07:59:59 2008
MTL_tclgen31_H_IST0_MTL_D080_081_20070504_v01_48h_M1_M2_M2B_M3_M6_SEG002_1500_cn
t.tcl 1.1          Sun May 18 08:01:11 2008
MTL_tclgen31_H_IST0_MTL_D080_081_20070504_v01_48h_M1_M2_M2B_M3_M6_SEG003_1500_cn
t.tcl 1.1          Sun May 18 08:02:27 2008
MTL_tclgen31_H_IST0_MTL_D080_081_20070504_v01_48h_M1_M2_M2B_M3_M6_SEG004_1500_en
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   2007_12_03_05_00_hercdmu_hpws23_REALTIME_EMC_nois2_TestEnvironment_REALTIME
d.tcl 1.1          Sun May 18 08:03:42 2008
MTL_tclgen31_H_IST0_MTL_D081_082_20070504_v01_48h_M2_M2B.tcl 1.1          Sun
May 18 07:34:27 2008
MTL_tclgen31_H_IST0_MTL_D081_082_20070504_v01_48h_M2_M2B_SEG000_900_cnt.tcl 1.1
        Sun May 18 08:04:47 2008
MTL_tclgen31_H_IST0_MTL_D081_082_20070504_v01_48h_M2_M2B_SEG001_900_end.tcl 1.1
        Sun May 18 08:06:00 2008
MTL_tclgen31_H_IST0_MTL_D081_082_20070504_v01_48h_patch_after_PACS_OFF_NCR.tcl
1.1          Mon May 19 08:31:40 2008
MTL_tclgen31_H_IST0_MTL_D081_082_20070504_v01_48h_patch_after_run_time_NCR.tcl
1.1          Mon May 19 05:30:59 2008
MTL_tclgen31_H_IST0_MTL_D082_083_20070504_v01_48h_M4_M5_M7.tcl 1.1          Sun
May 18 07:38:03 2008
MTL_tclgen31_H_IST0_MTL_D082_083_20070504_v01_48h_M4_M5_M7_SEG000_1200_cnt.tcl
1.1          Sun May 18 08:07:15 2008
MTL_tclgen31_H_IST0_MTL_D082_083_20070504_v01_48h_M4_M5_M7_SEG000_1200_unbugged_
cnt.tcl 1.1          Mon May 19 12:48:10 2008
MTL_tclgen31_H_IST0_MTL_D082_083_20070504_v01_48h_M4_M5_M7_SEG001_1200_end.tcl
1.1          Sun May 18 08:08:18 2008
MTL_tclgen31_H_IST0_MTL_Dummy_D083_084_20070504_v01_48h_M1_M2_M3.tcl 1.1
 Sun May 18 07:40:44 2008
MTL_tclgen31_H_IST0_MTL_Dummy_D083_084_20070504_v01_48h_M1_M2_M3_SEG000_1500_cnt
.tcl 1.1          Sun May 18 08:09:43 2008
MTL_tclgen31_H_IST0_MTL_Dummy_D083_084_20070504_v01_48h_M1_M2_M3_SEG001_1500_cnt
.tcl 1.1          Sun May 18 08:10:52 2008
MTL_tclgen31_H_IST0_MTL_Dummy_D083_084_20070504_v01_48h_M1_M2_M3_SEG002_1500_cnt
.tcl 1.1          Sun May 18 08:12:03 2008
MTL_tclgen31_H_IST0_MTL_Dummy_D083_084_20070504_v01_48h_M1_M2_M3_SEG003_1500_cnt
.tcl 1.1          Sun May 18 08:13:17 2008
MTL_tclgen31_H_IST0_MTL_Dummy_D083_084_20070504_v01_48h_M1_M2_M3_SEG004_1500_end
.tcl 1.1          Sun May 18 08:14:29 2008
OBCP_chop_scan_phot_OBS_Shell.tcl        1.1          Thu Oct 18 15:48:10 2007
P102999SCVT901_ASDDBGPACS_PWR_ON.tcl     1.11         Thu Aug 16 11:06:28 2007
P102999SCVT902_ASDDBGPACS_PWR_OFF.tcl    1.8          Thu Aug 16 11:07:04 2007
P102999SCVT903_SSMM_FILL_IN.tcl          1.5          Wed Jun  6 07:12:29 2007
P102999SCVT904_ASDGENPACS_NomSpect.tcl   1.3          Fri Oct 26 14:37:07 2007
P102999SCVT905_ASDISTPACS_PWR_ON_N.tcl   1.9          Fri Oct 19 08:47:40 2007
P102999SCVT906_ASDISTPACS_PWR_OFF_N.tcl  1.6          Mon Oct 15 10:48:04 2007
P102999SCVT907_ASDISTPACS_PWR_ON_R.tcl   1.9          Fri Oct 19 07:47:44 2007
P102999SCVT908_ASDISTPACS_PWR_OFF_R.tcl  1.6          Mon Oct 15 10:48:27 2007
P102999SCVT909_ASDGENPACS_PWR_ON_N.tcl   1.11         Wed Oct 31 15:10:14 2007
P102999SCVT910_ASDGENPACS_PWR_OFF_N.tcl  1.8          Mon Oct 15 10:45:54 2007
P102999SCVT911_ASDGENPACS_PWR_ON_R.tcl   1.12         Wed Oct 31 15:10:54 2007
P102999SCVT912_ASDGENPACS_PWR_OFF_R.tcl  1.7          Mon Oct 15 10:47:39 2007
P102999SCVT913_ASDGENPACS_BurstMode.tcl  1.2          Fri Oct 26 10:29:59 2007
P102999SCVT914_ASDGENPACS_DisSciDwnLnk.tcl 1.3          Sat Oct 27 14:19:26 2007
P102999SCVT915_ASDGENPACS_EnSciDwnLnk.tcl 1.3          Sat Oct 27 14:20:17 2007
PACS_Burstmode_Reset_OBS_Shell.tcl       1.1          Thu Oct 18 15:48:10 2007
PACS_Burstmode_Setup_OBS_Shell.tcl       1.1          Thu Oct 18 15:48:10 2007
PACS_CS_SFT_Cold_OBS_Shell.tcl           1.1          Thu Oct 18 15:48:10 2007
PACS_CS_SFT_Warm_OBS_Shell.tcl           1.1          Thu Oct 18 15:48:10 2007
PACS_Chopper_EnDis_PlateauTest_ast_OBS_Shell.tcl 1.1          Thu Oct 18
15:48:10 2007
PACS_Chopper_EnDis_Test_ast1_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:10 2007
PACS_Chopper_EnDis_Test_ast300_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:10
2007
PACS_Chopper_SFT_Cold_OpenLoop_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:10
2007
PACS_Chopper_SFT_Warm_OpenLoop_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:10
2007
PACS_Chopper_uk_move_12000_ast_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:10
2007
PACS_Chopper_uk_move_18000_ast_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:10
2007
PACS_Chopper_uk_move_21000_ast_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:10
2007
PACS_Chopper_uk_move_3000_ast_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:10
2007
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2007_12_03_05_00_hercdmu_hpws23_REALTIME_EMC_nois2_TestEnvironment_REALTIME

```
PACS_Close_Launch_Lock_OBS_Shell.tcl      1.1          Thu Oct 18 15:48:10 2007
PACS_DPU_Fill_SSMM_from_SAFE.tcl          1.3          Sun May 18 07:30:42 2008
PACS_DPU_Nominal_Science_Flow.tcl         1.2          Sun May 18 07:32:29 2008
PACS_Diaghk_Reset_OBS_Shell.tcl           1.1          Thu Oct 18 15:48:10 2007
PACS_Diagnostic_Check.tcl                 1.1          Wed Aug 15 07:16:03 2007
PACS_Dis_chopper_ast_OBS_Shell.tcl        1.1          Thu Oct 18 15:48:10 2007
PACS_Disable_HK_OBS_Shell.tcl             1.1          Thu Oct 18 15:48:10 2007
PACS_En_chopper_ast_OBS_Shell.tcl         1.1          Thu Oct 18 15:48:10 2007
PACS_GeGa_SFT_Init_Cold_He2_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:10 2007
PACS_GeGa_SFT_Init_Cold_OBS_Shell.tcl     1.1          Thu Oct 18 15:48:10 2007
PACS_GeGa_SFT_Init_Warm_OBS_Shell.tcl     1.1          Thu Oct 18 15:48:10 2007
PACS_Open_Launch_Lock_OBS_Shell.tcl       1.1          Thu Oct 18 15:48:10 2007
PACS_Phot_Cold_Startinputsignal_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:10
2007
PACS_Phot_DPU_DMC_Setup_OBS_Shell.tcl     1.1          Thu Oct 18 15:48:10 2007
PACS_Phot_Fil_Diaghk_Setup_OBS_Shell.tcl  1.1          Thu Oct 18 15:48:10 2007
PACS_Phot_Fil_Testseq_OBS_Shell.tcl       1.1          Thu Oct 18 15:48:10 2007
PACS_Phot_Fil_nturns_OBS_15_Shell.tcl     1.1          Thu Oct 18 15:48:10 2007
PACS_Phot_HK_Setup_OBS_Shell.tcl          1.1          Thu Oct 18 15:48:10 2007
PACS_Phot_SFT_Cooler_OBS_Shell.tcl        1.1          Thu Oct 18 15:48:10 2007
PACS_Phot_SFT_Cooler_Warm_OBS_Shell.tcl   1.1          Thu Oct 18 15:48:10 2007
PACS_Phot_SPU_Reset_OBS_Shell.tcl         1.1          Thu Oct 18 15:48:10 2007
PACS_Phot_SPU_Setup.tcl                   1.2          Wed Feb 14 16:50:26 2007
PACS_Phot_SPU_Setup_OBS_Shell.tcl         1.1          Thu Oct 18 15:48:10 2007
PACS_Phot_Sequencer_Setup_OBS_Shell.tcl   1.1          Thu Oct 18 15:48:10 2007
PACS_Phot_Switchoff_OBS_Shell.tcl         1.1          Thu Oct 18 15:48:10 2007
PACS_Phot_Switchon_OBS_Shell.tcl          1.1          Thu Oct 18 15:48:10 2007
PACS_Phot_Warm_Startinputsignal_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:11
2007
PACS_SAFE_Mode.tcl                        1.1          Mon May  7 13:41:48 2007
PACS_Spec_CRE_01pF0bias_Setup_Warm_OBS_Shell.tcl 1.1          Thu Oct 18
15:48:11 2007
PACS_Spec_CRE_1pF0bias_Setup_Warm_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:11
2007
PACS_Spec_CRE_Setup_Cold_HeI_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_CRE_Setup_Cold_OBS_Shell.tcl    1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_CRE_Setup_Warm_OBS_Shell.tcl    1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Curing_OBS_Shell.tcl            1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Fil_Diaghk_Setup_OBS_Shell.tcl  1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Fil_Testseq_OBS_Shell.tcl       1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Fil_nturns_OBS_15_Shell.tcl     1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_FlashHeat_Diaghk_Setup_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:11
2007
PACS_Spec_Flash_IST_Shell.tcl             1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Flash_SFT_Shell.tcl             1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Flash_SFT_Warm_Shell.tcl        1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Flash_SWOF_OBS_Shell.tcl        1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Flash_SWON_OBS_Shell.tcl        1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_DEG_MODE_Diaghk_Setup_OBS_Shell.tcl 1.1          Thu Oct 18
15:48:11 2007
PACS_Spec_Gra_Diaghk_SINCOS_Setup_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:11
2007
PACS_Spec_Gra_Diaghk_Setup_OBS_Shell.tcl  1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_Healthcheck_OBS_Shell.tcl   1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_IST_Ampl_OBS_Shell.tcl      1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_IST_Check_1_OBS_Shell.tcl   1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_IST_Check_2_OBS_Shell.tcl   1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_IST_Check_4_OBS_Shell.tcl   1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_IST_Disable_OBS_Shell.tcl   1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_IST_Enable_OBS_Shell.tcl    1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_IST_Full_Charac_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:11
2007
PACS_Spec_Gra_IST_Home_OBS_Shell.tcl      1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_IST_Move_Abs_OBS_Shell.tcl  1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_IST_Move_Rel_OBS_Shell.tcl  1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_IST_PID_Loop_Less_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:11
2007
PACS_Spec_Gra_IST_PID_Loop_OBS_Shell.tcl  1.1          Thu Oct 18 15:48:11 2007
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2007_12_03_05_00_hercdmu_hpws23_REALTIME_EMC_nois2_TestEnvironment_REALTIME

```
PACS_Spec_Gra_IST_SWOF_OBS_Shell.tcl     1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_IST_SWON_OBS_Shell.tcl     1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_IST_Set_Ctrl_Par_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:11
2007
PACS_Spec_Gra_Mec_Setup_Cold_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_Mec_Setup_Cold_Redun_OBS_Shell.tcl 1.1          Thu Oct 18
15:48:11 2007
PACS_Spec_Gra_SFT_Warm_OBS_Shell.tcl     1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Gra_Slew_Time_Cal_Shell.tcl    1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_HK_Setup_OBS_Shell.tcl         1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Heat_FFT_Shell.tcl             1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Heat_SFT_Shell.tcl             1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Heat_SWOF_OBS_Shell.tcl        1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_Heat_SWON_OBS_Shell.tcl        1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_QuickFullSpectrum_OBS_CS1_Shell.tcl 1.1          Thu Oct 18 15:48:11
2007
PACS_Spec_QuickFullSpectrum_OBS_CS2_Shell.tcl 1.1          Thu Oct 18 15:48:11
2007
PACS_Spec_SPU_Buffer_Setup_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:11 2007
PACS_Spec_SPU_Reset_OBS_Shell.tcl        1.1          Thu Oct 18 15:48:12 2007
PACS_Spec_SPU_Setup.tcl                  1.2          Wed Feb 14 16:52:22 2007
PACS_Spec_Time_Constant_IST_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:12 2007
PACS_StartAutonomy_Function_14_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:12
2007
PACS_StartAutonomy_Function_17_OBS_Shell.tcl 1.1          Thu Oct 18 15:48:12
2007
PACS_SwOff_chopper_ast_OBS_Shell.tcl     1.1          Thu Oct 18 15:48:12 2007
PACS_SwOn_chopper_ast_OBS_Shell.tcl      1.1          Thu Oct 18 15:48:12 2007
PACS_Switch_Off.tcl                      1.1          Wed Feb 14 07:33:54 2007
PACS_Switch_Off_CCS.tcl                  1.2          Wed Feb 14 16:55:43 2007
PACS_Switch_Off_CCS_Nominal.tcl          1.1          Mon Oct 15 09:54:10 2007
PACS_Switch_Off_CCS_Redundant.tcl        1.1          Mon Oct 15 09:55:00 2007
PACS_Switch_On.tcl                       1.1          Wed Feb 14 07:34:31 2007
PACS_Switch_On_CCS.tcl                   1.2          Wed Feb 14 16:56:24 2007
PACS_Switch_On_CCS_Nominal.tcl           1.1          Mon Oct 15 09:53:52 2007
PACS_Switch_On_CCS_Redundant.tcl         1.1          Mon Oct 15 09:54:35 2007
PACS_Switch_On_CCS_SPU_shifted.tcl       1.1          Tue Jul 31 12:50:38 2007
PACS_Switch_On_DPU_SPULLSW.tcl           1.3          Sat Aug  4 10:09:29 2007
PACS_Wave_Cal_Shell.tcl                  1.1          Thu Oct 18 15:48:12 2007
PACS_pre_test.tcl                        1.1          Wed Feb 14 10:34:59 2007
PCS__mini_IST.tcl                        1.1          Thu Oct 27 00:56:50 2005
PHOT_TestPattern_obs_Shell.tcl           1.1          Thu Oct 18 15:48:12 2007
PHOT_VrlVhBlind_OBS_Shell.tcl            1.1          Thu Oct 18 15:48:12 2007
PHOT_all_aots_OBS_Shell.tcl              1.1          Thu Oct 18 15:48:12 2007
PHOT_cal_recipes_OBS_Shell.tcl           1.1          Thu Oct 18 15:48:12 2007
PHOT_chopped_photometry_OBS_Shell.tcl    1.1          Thu Oct 18 15:48:12 2007
PHOT_low_freq_OBS_Shell.tcl              1.1          Thu Oct 18 15:48:12 2007
PHOT_low_freq_direct_OBS_Shell.tcl       1.1          Thu Oct 18 15:48:12 2007
PHOT_redArray_DDCS_IST_OBS_Shell.tcl     1.1          Thu Oct 18 15:48:12 2007
PHOT_redArray_alternatePolarisation_DDCS_IST_OBS_Shell.tcl 1.1          Thu Oct
18 15:48:12 2007
PHOT_redArray_alternatePolarisation_direct_IST_OBS_Shell.tcl 1.1          Thu
Oct 18 15:48:12 2007
PHOT_redArray_direct_IST_OBS_Shell.tcl   1.1          Thu Oct 18 15:48:12 2007
PHOT_saturation_OBS_Shell.tcl            1.1          Thu Oct 18 15:48:12 2007
PHOT_setup_OBS_EMC_Shell.tcl             1.1          Thu Oct 18 15:48:12 2007
PHOT_setup_OBS_Shell.tcl                 1.1          Thu Oct 18 15:48:12 2007
PHOT_setup_redundant_OBS_Shell.tcl       1.1          Thu Oct 18 15:48:12 2007
PHOT_spu_data_rate_obs_Shell.tcl         1.1          Thu Oct 18 15:48:12 2007
PHOT_spu_reset_OBS_Shell.tcl             1.1          Thu Oct 18 15:48:12 2007
PHOT_spu_setup_OBS_Shell.tcl             1.1          Thu Oct 18 15:48:12 2007
PHOT_thermal_OBS_Shell.tcl               1.1          Thu Oct 18 15:48:12 2007
PHOT_timeconst_fluxchange_OBS_Shell.tcl  1.1          Thu Oct 18 15:48:12 2007
PIERO PROVA RM DISABLE.tcl               1.1          Tue Feb 28 11:14:01 2006
PIERO_PROVA_RM_DISABLE.tcl               1.5          Thu Mar  2 17:26:28 2006
PROVA ALE.tcl                            1.2          Mon Nov  7 16:22:54 2005
PROVA_ALESSIO.tcl                        1.4          Tue May 22 14:24:10 2007
PROVA_luigi.tcl                          1.6          Thu Nov 22 19:44:41 2007
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2007_12_03_05_00_hercdmu_hpws23_REALTIME_EMC_nois2_TestEnvironment_REALTIME

Phot_prepareEMC_DDCS_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:12 2007
Phot_prepareEMC_direct_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:12 2007
PhotometrytoNonprime.tcl	1.2	Wed Feb 14 16:57:56 2007
R102479ECVT001_TWTA_CFG.tcl	1.5	Wed Aug 30 07:55:15 2006
R102479ECVT002_XPND_LCL.tcl	1.7	Fri Sep 15 07:09:56 2006
R102479ECVT003_RX_BIT_RATE_SET.tcl	1.6	Tue May 29 21:21:47 2007
R102479ECVT004_XPND_TX.tcl	1.4	Wed Aug 30 08:02:12 2006
R102479ECVT005_XPND1_TC_1553.tcl	1.5	Thu Jun 1 13:43:25 2006
R102479ECVT006_XPND2_TC_1553.tcl	1.5	Thu Jun 1 13:47:13 2006
R102479ECVT008_CDMU_TM_RATE.tcl	1.6	Tue Nov 7 11:46:15 2006
R102479ECVT009_UNITS_SELECTION.tcl	1.10	Mon May 21 22:22:32 2007
R102479ECVT011_TWTA_CFG_ASW.tcl	1.2	Thu Sep 21 10:34:06 2006
R102479ECVT012_XPND_CFG_ASW.tcl	1.3	Thu Sep 21 16:24:15 2006
R102479EMVT011_ANALOGUE_TM.tcl	1.9	Thu May 24 23:15:54 2007
R102479EMVT012_DIGITAL_TM.tcl	1.7	Thu May 24 23:17:34 2007
R102479ETVT007_REPEAT_TC.tcl	1.8	Wed Sep 20 15:50:51 2006
R102479ETVT009_CHECK_RX_LOCK.tcl	1.5	Thu May 24 23:19:45 2007
R102479ETVT010_RFDN_CONF.tcl	1.4	Wed Aug 30 08:01:35 2006
R102479ETVT011_RFDN_CONF_CHECK_TWTA.tcl	1.3	Wed May 23 19:39:09 2007
R102479ETVT013_REPEAT_TC_FAST.tcl	1.1	Thu Sep 21 16:26:19 2006
R102479SMXX001_XPND_HUM_TXT.tcl	1.2	Wed Sep 19 07:08:23 2007
R102479SPVT001_RX1_LOCK_ACQ.tcl	1.9	Mon Sep 25 16:32:54 2006
R102479SPVT002_RX1_LOCK_MNT.tcl	1.10	Wed Sep 27 07:13:14 2006
R102479SPVT003_RX1_TC_LOW_BIT_RATE.tcl	1.6	Mon Sep 25 15:39:13 2006
R102479SPVT004_RX1_TC_HIGH_BIT_RATE.tcl	1.6	Tue Sep 26 11:36:47 2006
R102479SPVT005_TX1_DL_CARRIER.tcl	1.9	Wed Sep 27 12:09:22 2006
R102479SPVT006_TX1_DL_TM_BAND.tcl	1.6	Thu Sep 28 09:16:52 2006
R102479SPVT008_RX1_TC_LBR_RNG.tcl	1.5	Thu Sep 28 10:57:20 2006
R102479SPVT009_RX1_TC_MBR_RNG.tcl	1.5	Thu Sep 28 11:02:37 2006
R102479SPVT010_TX1_DL_RNG_SIGNAL.tcl	1.4	Mon Sep 25 15:55:45 2006
R102479SPVT011_TX1_DL_RNG_PERF.tcl	1.5	Mon Sep 25 16:00:24 2006
R102479SPVT012_TX1_TM+RG.tcl	1.3	Mon Sep 25 16:09:56 2006
R102479SPVT012_TX1_TM_RG_SIGNAL.tcl	1.2	Tue Jan 24 08:33:43 2006
R102479SPVT014_TC1_LBR_LOW_DOPPLER.tcl	1.4	Mon Sep 25 16:14:40 2006
R102479SPVT015_TC1_MBR_HIGH_DOPPLER.tcl	1.4	Mon Sep 25 16:15:58 2006
R102479SPVT016_TC1_MBR_LOW_DOPPLER.tcl	1.4	Mon Sep 25 16:21:28 2006
R102479SPVT017_TC+TM+RNG_END2END1.tcl	1.4	Wed Oct 4 05:24:50 2006
R102479SPVT017_TC_TM_RNG_END2END1.tcl	1.3	Wed Aug 30 08:03:06 2006
R102479SPVT020_RFDN_UFT.tcl	1.4	Sat Sep 30 09:42:24 2006
R102479SPVT031_RX2_LOCK_ACQ.tcl	1.7	Tue May 29 20:36:28 2007
R102479SPVT032_RX2_LOCK_MNT.tcl	1.8	Wed May 30 15:20:16 2007
R102479SPVT033_RX2_TC_LOW_BIT_RATE.tcl	1.7	Wed May 30 15:18:52 2007
R102479SPVT034_RX2_TC_HIGH_BIT_RATE.tcl	1.5	Wed May 30 15:19:29 2007
R102479SPVT035_TX2_DL_CARRIER.tcl	1.6	Wed May 30 15:19:52 2007
R102479SPVT036_TX2_DL_TM_BAND.tcl	1.5	Fri May 25 03:02:24 2007
R102479SPVT038_RX2_TC_LBR_RNG.tcl	1.4	Wed May 30 23:26:52 2007
R102479SPVT039_RX2_TC_MBR_RNG.tcl	1.4	Wed May 30 23:18:54 2007
R102479SPVT040_TX2_DL_RNG_SIGNAL.tcl	1.5	Fri Jun 1 22:05:28 2007
R102479SPVT041_TX2_DL_RNG_PERF.tcl	1.5	Wed Sep 27 14:07:33 2006
R102479SPVT042_TX2_TM+RG.tcl	1.3	Wed May 30 23:26:42 2007
R102479SPVT042_TX2_TM_RG_SIGNAL.tcl	1.2	Wed Aug 30 08:03:20 2006
R102479SPVT044_TC2_LBR_LOW_DOPPLER.tcl	1.4	Fri Jun 1 16:09:31 2007
R102479SPVT045_TC2_MBR_HIGH_DOPPLER.tcl	1.5	Wed May 30 16:00:22 2007
R102479SPVT046_TC2_MBR_LOW_DOPPLER.tcl	1.4	Mon Jun 4 06:27:37 2007
R102479SPVT047_TC_TM_RNG_END2END2.tcl	1.4	Mon Jun 4 06:46:30 2007
R102479SPVT048_TTC_MNGM.tcl	1.3	Thu May 31 01:52:16 2007
R102479SPVT100_TTC_IST_HEALTH_CHECK.tcl	1.6	Wed Dec 6 07:28:52 2006
R102479SPVT101_IST_RFDN_UFT.tcl	1.2	Thu Jul 20 07:30:32 2006
R102479SPVT103_IST_LOCK_ACQ.tcl	1.8	Thu May 24 23:24:53 2007
R102479SPVT104_IST_END2END.tcl	1.7	Thu May 24 23:27:21 2007
R102479SPVT105_IST_DL_VERIFICATION.tcl	1.4	Wed Nov 29 17:32:32 2006
R102479SPVT107_IST_LAUNCH_2_SUN.tcl	1.9	Tue Nov 6 16:06:54 2007
R102479SPVT108_IST_SUN_2_NOM.tcl	1.5	Tue Nov 6 16:11:27 2007
R102479SPVT109_IST_NOM_2_SURV.tcl	1.5	Fri Apr 13 07:47:44 2007
R102479SPVT110_IST_BEFORE_SAM.tcl	1.3	Wed Nov 15 11:56:38 2006
R102479SPVT111_IST_FDIR_2_SAM.tcl	1.2	Wed Nov 15 11:59:30 2006
R102479SPVT113_IST_LOCK_ACQ2.tcl	1.7	Tue Oct 24 09:32:31 2006
R102479SPVT114_IST_END2END2.tcl	1.4	Thu Oct 26 14:55:22 2006

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   2007_12_03_05_00_hercdmu_hpws23_REALTIME_EMC_nois2_TestEnvironment_REALTIME
R102479SPVT115_IST_DL_VERIFICATION2.tcl  1.5          Thu Oct 26 14:29:50 2006
R102479SPVT116_IST_DUMP_ON.tcl           1.4          Tue Nov  6 16:14:52 2007
R102479SPVT117_IST_GMSK_2_NOM.tcl        1.1          Tue Feb 20 18:12:33 2007
R102479SPVT117_IST_NOM_2_5k.tcl          1.5          Thu Jul 12 17:30:42 2007
R102479SPVT120_IST_CONF_TTC_HC.tcl       1.3          Wed Apr 18 09:36:56 2007
R102479SPVT121_IST_CONF_TTC2_HC_OFF.tcl  1.2          Wed Apr 18 09:38:02 2007
R102479SPVT122_IST_TTC_COMMISSIONING.tcl 1.6          Thu Nov 22 18:24:22 2007
RMS_DTCP_ASTRIUM.tcl                     1.6          Tue May 20 05:39:20 2008
S102999SCVT005_ASDSFTSPIR_PWR_ON_P.tcl   1.6          Wed Oct 31 15:04:29 2007
S102999SCVT006_ASDSFTSPIR_PWR_ON_R.tcl   1.7          Wed Oct 31 15:04:54 2007
S102999SCVT007_ASDSFTSPIR_PWR_OFF_P.tcl  1.4          Sat Nov 24 11:39:39 2007
S102999SCVT008_ASDSFTSPIR_PWR_OFF_R.tcl  1.5          Sat Nov 24 11:40:04 2007
S102999SCVT009_ASDWFTSPIR_PWR_ON_P.tcl   1.6          Thu Nov 15 11:09:46 2007
S102999SCVT010_ASDWFTSPIR_PWR_ON_R.tcl   1.7          Thu Nov 15 11:10:15 2007
S102999SCVT011_ASDWFTSPIR_PWR_OFF_P.tcl  1.5          Sat Nov 24 11:41:22 2007
S102999SCVT012_ASDWFTSPIR_PWR_OFF_R.tcl  1.5          Sat Nov 24 11:41:47 2007
S102999SCVT013_ASDWFTSPIR_SAFE_OFF_P.tcl 1.2          Mon Oct 22 11:32:03 2007
S102999SCVT014_ASDWFTSPIR_SAFE_OFF_R.tcl 1.2          Mon Oct 22 11:32:44 2007
S102999SCVT015_ASDISTSPIR_STBY2PHOT.tcl  1.2          Thu Nov 15 17:19:41 2007
S102999SCVT016_ASDISTSPIR_PHOT2STBY.tcl  1.2          Thu Nov 15 17:20:14 2007
S102999SCVT017_ASDGENSPIR_PWR_ON_P.tcl   1.2          Thu Nov 15 17:33:55 2007
S102999SCVT018_ASDGENSPIR_PWR_ON_R.tcl   1.1          Thu Nov 15 17:31:07 2007
S102999SCVT019_ASDGENSPIR_PWR_OFF_P.tcl  1.2          Sat Nov 24 11:42:41 2007
S102999SCVT020_ASDGENSPIR_PWR_OFF_R.tcl  1.2          Sat Nov 24 11:45:02 2007
S102999SCVT9013_ASDDBGSPIR_PWR_ON_P_INITMD.tcl 1.2          Thu Jun 28 06:54:30
2007
S102999SCVT9014_ASDDBGSPIR_SW_UPL.tcl    1.3          Mon Jul  2 16:40:01 2007
S102999SCVT901_ASDDBGSPIR_PWR_ON_P.tcl   1.20         Thu Nov 15 17:58:47 2007
S102999SCVT902_ASDDBGSPIR_PWR_ON_R.tcl   1.16         Thu Nov 15 17:58:25 2007
S102999SCVT903_ASDDBGSPIR_PWR_OFF_P.tcl  1.17         Sat Nov 24 11:45:30 2007
S102999SCVT904_ASDDBGSPIR_PWR_OFF_R.tcl  1.10         Sat Nov 24 11:45:53 2007
S102999SCVT907_ASDDBGSPIR_STBY2PHTSTBY.tcl 1.2          Wed Jun  6 07:01:01 2007
S102999SCVT908_ASDDBGSPIR_PHTSTBY2STBY.tcl 1.2          Wed Jun  6 07:01:34 2007
S102999SCVT909_ASDDBGSPIR_STBY2SPECSTBY.tcl 1.2          Wed Jun  6 07:02:03
2007
S102999SCVT910_ASDDBGSPIR_SPECSTBY2STBY.tcl 1.2          Wed Jun  6 07:02:32
2007
S102999SCVT911_ASDDBGSPIR_STBY2OPS.tcl   1.3          Sat Oct 27 15:03:08 2007
S102999SCVT912_ASDDBGSPIR_OPS2STBY.tcl   1.3          Sat Oct 27 15:03:44 2007
S102999SCVT915_ASDDBGSPIR_PWR_OFF_P_INITMD.tcl 1.1          Thu Jun 28 11:20:12
2007
SPEC_CRE_setup_OBS_Shell.tcl             1.1          Thu Oct 18 15:48:12 2007
SPEC_CS_imt511_OBS_Shell.tcl             1.1          Thu Oct 18 15:48:12 2007
SPEC_Chopper_dhk_5hk_1khz_OBS_Shell.tcl  1.1          Thu Oct 18 15:48:12 2007
SPEC_Chopper_dhk_imt_OBS_Shell.tcl       1.1          Thu Oct 18 15:48:12 2007
SPEC_Chopper_dhk_stop_OBS_Shell.tcl      1.1          Thu Oct 18 15:48:12 2007
SPEC_Chopper_fmilt076spec_detsel_llc_ast_V1_OBS_Shell.tcl 1.1          Thu Oct
18 15:48:12 2007
SPEC_Chopper_fmilt076spec_detsel_llc_ast_V2_OBS_Shell.tcl 1.1          Thu Oct
18 15:48:12 2007
SPEC_Chopper_imt504_OBS_Shell.tcl        1.1          Thu Oct 18 15:48:12 2007
SPEC_Chopper_imt504_ast_OBS_Shell.tcl    1.1          Thu Oct 18 15:48:13 2007
SPEC_Gra_move_abs_raw_obs_500_Shell.tcl  1.1          Thu Oct 18 15:48:13 2007
SPEC_Gra_move_abs_raw_obs_775_Shell.tcl  1.1          Thu Oct 18 15:48:13 2007
SPEC_Prepare_EMC_Shell.tcl               1.1          Thu Oct 18 15:48:13 2007
SPEC_Readouts_per_Ramp_Test_01_Shell.tcl 1.1          Thu Oct 18 15:48:13 2007
SPEC_Readouts_per_Ramp_Test_02_Shell.tcl 1.1          Thu Oct 18 15:48:13 2007
SPEC_Readouts_per_Ramp_Test_03_Shell.tcl 1.1          Thu Oct 18 15:48:13 2007
SPEC_Readouts_per_Ramp_Test_04_Shell.tcl 1.1          Thu Oct 18 15:48:13 2007
SPEC_Readouts_per_Ramp_Test_05_Shell.tcl 1.1          Thu Oct 18 15:48:13 2007
SPEC_Readouts_per_Ramp_Test_06_Shell.tcl 1.1          Thu Oct 18 15:48:13 2007
SPEC_all_aots_OBS_Shell.tcl              1.1          Thu Oct 18 15:48:13 2007
SPEC_chopped_SED_OBS_Shell.tcl           1.1          Thu Oct 18 15:48:13 2007
SPEC_cre_setup_OBS_capa00_Shell.tcl      1.1          Thu Oct 18 15:48:13 2007
SPEC_cre_setup_OBS_capa1212_Shell.tcl    1.1          Thu Oct 18 15:48:13 2007
SPEC_cre_setup_OBS_capa12_Shell.tcl      1.1          Thu Oct 18 15:48:13 2007
SPEC_dark_current_imt502_OBS_Shell.tcl   1.1          Thu Oct 18 15:48:13 2007
SPEC_detector_imt509_OBS_Shell.tcl       1.1          Thu Oct 18 15:48:13 2007
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SPEC_fov_scan_imt409_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:13 2007
SPEC_setup_CSOFF_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:13 2007
SPEC_setup_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:13 2007
SPEC_setup_redundant_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:13 2007
SPEC_spu_data_rate_obs_Shell.tcl	1.1	Thu Oct 18 15:48:13 2007
SPEC_spu_reset_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:13 2007
SPEC_spu_setup_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:13 2007
SPEC_spu_setup_OBS_Shell_025s.tcl	1.1	Thu Oct 18 15:48:13 2007
SPEC_thermal_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:13 2007
SPIRE-FM-SFT-BSM-OFF-P.tcl	1.1	Tue Sep 11 17:12:25 2007
SPIRE-FM-SFT-BSM-OFF-R.tcl	1.1	Tue Sep 11 17:12:25 2007
SPIRE-FM-SFT-DPU-START-P-PP.tcl	1.1	Tue Sep 11 17:12:25 2007
SPIRE-FM-SFT-DPU-START-P-SP.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-DPU-START-R-PP.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-DPU-START-R-SP.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-DRCU-OFF-P.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-DRCU-OFF-R.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-DRCU-START-P-STEP1.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-DRCU-START-P-STEP2.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-DRCU-START-R-STEP1.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-DRCU-START-R-STEP2.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-FUNC-BSM-01-P.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-FUNC-BSM-01-R.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-FUNC-DCU-01-P.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-FUNC-DCU-01-R.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-FUNC-DCU-04-PHOT-P.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-FUNC-DCU-04-PHOT-R.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-FUNC-DCU-04-SPEC-P.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-FUNC-DCU-04-SPEC-R.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-FUNC-MCU-01-P.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-FUNC-MCU-01-R.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-FUNC-MCU-02-P.tcl	1.1	Tue Sep 11 17:12:26 2007
SPIRE-FM-SFT-FUNC-MCU-02-R.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SCU-01-P.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SCU-01-R.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SCU-03-P.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SCU-03-R.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SCU-04-P.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SCU-04-R.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SCU-05-P.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SCU-05-R.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SCU-06-P.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SCU-06-R.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SCU-07-P.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SCU-07-R.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SMEC-01-P.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-FUNC-SMEC-01-R.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-MCU-OFF-P.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-MCU-OFF-R.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-PHOT-JFET-OFF.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-PHOT-JFET-ON-01.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-PHOT-JFET-ON-02.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-PLIA-OFF-P.tcl	1.1	Tue Sep 11 17:12:27 2007
SPIRE-FM-SFT-PLIA-OFF-R.tcl	1.1	Tue Sep 11 17:12:28 2007
SPIRE-FM-SFT-SCU-OFF-P.tcl	1.1	Tue Sep 11 17:12:28 2007
SPIRE-FM-SFT-SCU-OFF-R.tcl	1.1	Tue Sep 11 17:12:28 2007
SPIRE-FM-SFT-SLIA-OFF-P.tcl	1.1	Tue Sep 11 17:12:28 2007
SPIRE-FM-SFT-SLIA-OFF-R.tcl	1.1	Tue Sep 11 17:12:28 2007
SPIRE-FM-SFT-SMEC-OFF-P.tcl	1.1	Tue Sep 11 17:12:28 2007
SPIRE-FM-SFT-SMEC-OFF-R.tcl	1.1	Tue Sep 11 17:12:28 2007
SPIRE-FM-SFT-SPEC-JFET-OFF.tcl	1.1	Tue Sep 11 17:12:28 2007
SPIRE-FM-SFT-SPEC-JFET-ON-01.tcl	1.1	Tue Sep 11 17:12:28 2007
SPIRE-FM-SFT-SPEC-JFET-ON-02.tcl	1.1	Tue Sep 11 17:12:28 2007
SPIRE-FM-WFT-BSM-INIT-P.tcl	1.4	Mon Oct 22 12:13:59 2007
SPIRE-FM-WFT-BSM-INIT-R.tcl	1.4	Mon Oct 22 12:13:59 2007
SPIRE-FM-WFT-BSM-OFF-P.tcl	1.4	Mon Oct 22 12:13:59 2007
SPIRE-FM-WFT-BSM-OFF-R.tcl	1.4	Mon Oct 22 12:13:59 2007
SPIRE-FM-WFT-DPU-START-P-SP.tcl	1.4	Mon Oct 22 12:13:59 2007

2007_12_03_05_00_hercdmu_hpws23_REALTIME_EMC_nois2_TestEnvironment_REALTIME

SPIRE-FM-WFT-DPU-START-R-PP.tcl	1.4	Mon Oct 22 12:13:59 2007
SPIRE-FM-WFT-DRCU-OFF-P.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-DRCU-OFF-R.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-DRCU-START-P-STEP1.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-DRCU-START-P-STEP2.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-DRCU-START-R-STEP1.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-DRCU-START-R-STEP2.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-01-P.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-01-R.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-02C-P.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-02C-R.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-02J-P.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-02J-R.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-03-P.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-03-R.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-05A-P.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-05A-R.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-05B-P.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-05B-R.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-06-P.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-BSM-06-R.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-DCU-01-P.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-DCU-01-R.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-DCU-02-P.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-DCU-02-R.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-DCU-03-P.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-DCU-03-R.tcl	1.4	Mon Oct 22 12:14:00 2007
SPIRE-FM-WFT-FUNC-DCU-04-PHOT-P.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-04-PHOT-R.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-04-SPEC-P.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-04-SPEC-R.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-11-PHOT-P.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-11-PHOT-R.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-11-SPEC-P.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-11-SPEC-R.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-13-PHOT-P.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-13-PHOT-R.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-13-SPEC-P.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-13-SPEC-R.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-14-PHOT-P.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-14-PHOT-R.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-14-SPEC-P.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-DCU-14-SPEC-R.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-MCU-01-P.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-MCU-01-R.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-MCU-02-P.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-MCU-02-R.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-MCU-03-P.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-MCU-03-R.tcl	1.4	Mon Oct 22 12:14:01 2007
SPIRE-FM-WFT-FUNC-MCU-04-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-MCU-04-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-01-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-01-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-02-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-02-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-03-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-03-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-04-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-04-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-05-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-05-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-06-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-06-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-07-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-07-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-08-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SCU-08-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SMEC-01-P.tcl	1.4	Mon Oct 22 12:14:02 2007

2007_12_03_05_00_hercdmu_hpws23_REALTIME_EMC_nois2_TestEnvironment_REALTIME

SPIRE-FM-WFT-FUNC-SMEC-01-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SMEC-02A-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SMEC-02A-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SMEC-02B-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SMEC-02B-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SMEC-03-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SMEC-03-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SMEC-04A-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SMEC-04A-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SMEC-07-P.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SMEC-07-R.tcl	1.4	Mon Oct 22 12:14:02 2007
SPIRE-FM-WFT-FUNC-SMEC-09-P.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-FM-WFT-FUNC-SMEC-09-R.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-FM-WFT-MCU-OFF-P.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-FM-WFT-MCU-OFF-R.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-FM-WFT-PDET-OFF-P.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-FM-WFT-PDET-OFF-R.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-FM-WFT-SCU-OFF-P.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-FM-WFT-SCU-OFF-R.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-FM-WFT-SDET-OFF-P.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-FM-WFT-SDET-OFF-R.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-FM-WFT-SMEC-INIT-P.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-FM-WFT-SMEC-INIT-R.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-FM-WFT-SMEC-OFF-P.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-FM-WFT-SMEC-OFF-R.tcl	1.4	Mon Oct 22 12:14:03 2007
SPIRE-IST-DBG-DPUON2STBY.tcl	1.6	Thu Sep 13 09:25:20 2007
SPIRE-IST-DBG-LOAD-VM-TABLES.tcl	1.1	Sat Aug 4 11:04:24 2007
SPIRE-IST-DBG-OFF2DPUON-SP.tcl	1.3	Sat Aug 4 11:03:21 2007
SPIRE-IST-DBG-OFF2DPUON.tcl	1.6	Fri Aug 24 11:18:17 2007
SPIRE-IST-DBG-OPS2STBY.tcl	1.6	Sat Aug 4 11:05:34 2007
SPIRE-IST-DBG-PHOT2STBY.tcl	1.1	Wed May 23 09:24:16 2007
SPIRE-IST-DBG-SPEC2STBY.tcl	1.1	Wed May 23 09:24:45 2007
SPIRE-IST-DBG-STBY-DCU-02.tcl	1.1	Fri Apr 13 14:45:25 2007
SPIRE-IST-DBG-STBY2OFF.tcl	1.4	Sat Aug 4 11:06:12 2007
SPIRE-IST-DBG-STBY2OPS.tcl	1.5	Sat Aug 4 11:06:41 2007
SPIRE-IST-DBG-STBY2PHOT.tcl	1.4	Sat Aug 4 11:26:49 2007
SPIRE-IST-DBG-STBY2SPEC.tcl	1.4	Sat Aug 4 11:57:59 2007
SPIRE-IST-WU-EMC-CE-PHOTOPS2STBY.tcl	1.2	Thu Sep 13 09:35:38 2007
SPIRE-IST-WU-EMC-CE-STBY2PHOTOPS.tcl	1.1	Fri Aug 24 11:14:18 2007
SPIRE-WU-INT-DCU-01-P.tcl	1.4	Tue Mar 27 13:19:51 2007
SPIRE-WU-INT-DCU-01-R.tcl	1.4	Tue Mar 27 13:24:56 2007
SPIRE-WU-INT-DCU-02-P.tcl	1.4	Tue Mar 27 13:20:25 2007
SPIRE-WU-INT-DCU-02-R.tcl	1.4	Tue Mar 27 13:25:16 2007
SPIRE-WU-INT-DPU-START-P-SP.tcl	1.1	Wed Apr 11 16:09:51 2007
SPIRE-WU-INT-DPU-START-P.tcl	1.5	Tue Mar 27 13:20:48 2007
SPIRE-WU-INT-DPU-START-P__new.tcl	1.1	Tue Mar 27 12:29:15 2007
SPIRE-WU-INT-DPU-START-R.tcl	1.4	Tue Mar 27 13:25:40 2007
SPIRE-WU-INT-DRCU-OFF-P.tcl	1.4	Tue Mar 27 13:21:09 2007
SPIRE-WU-INT-DRCU-OFF-R.tcl	1.4	Tue Mar 27 13:26:04 2007
SPIRE-WU-INT-DRCU-START-P-STEP1.tcl	1.4	Tue Mar 27 13:21:31 2007
SPIRE-WU-INT-DRCU-START-P-STEP2.tcl	1.4	Tue Mar 27 13:21:50 2007
SPIRE-WU-INT-DRCU-START-R-STEP1.tcl	1.4	Tue Mar 27 13:26:24 2007
SPIRE-WU-INT-DRCU-START-R-STEP2.tcl	1.4	Tue Mar 27 13:26:33 2007
SPIRE-WU-INT-MCU-01-P.tcl	1.4	Tue Mar 27 13:22:57 2007
SPIRE-WU-INT-MCU-01-R.tcl	1.4	Tue Mar 27 13:27:02 2007
SPIRE-WU-INT-MCU-02-P.tcl	1.4	Tue Mar 27 13:23:27 2007
SPIRE-WU-INT-MCU-02-R.tcl	1.4	Tue Mar 27 13:27:24 2007
SPIRE-WU-INT-MCU-OFF-P.tcl	1.4	Tue Mar 27 13:23:48 2007
SPIRE-WU-INT-MCU-OFF-R.tcl	1.4	Tue Mar 27 13:27:46 2007
SPIRE-WU-INT-SCU-01-P.tcl	1.4	Tue Mar 27 13:24:13 2007
SPIRE-WU-INT-SCU-01-R.tcl	1.4	Tue Mar 27 13:28:05 2007
SPIRE-WU-INT-SCU-02-P.tcl	1.4	Tue Mar 27 13:24:37 2007
SPIRE-WU-INT-SCU-02-R.tcl	1.4	Tue Mar 27 13:28:25 2007
SPIRE_OBS_2-2-G_Upload2DM_CCS.tcl	1.2	Wed May 2 17:50:26 2007
SPIREx_SubscribeParams.tcl	1.2	Thu Apr 19 18:59:12 2007
SPU_MemCheck_All_12_81.tcl	1.1	Thu Feb 15 09:07:43 2007
SPU_MemCheck_All_12_81_0x10000.tcl	1.1	Tue Jul 31 12:53:03 2007
SPU_MemCheck_All_12_81_0x10000_new.tcl	1.1	Sat Aug 4 10:18:37 2007

2007_12_03_05_00_hercdmu_hpws23_REALTIME_EMC_nois2_TestEnvironment_REALTIME

SWITCH_BUS.tcl	1.2	Sat Jan 14 13:20:26 2006
SetupPhotometrySimulation.tcl	1.4	Thu Jul 19 08:34:41 2007
SetupSpectroscopySimulation.tcl	1.4	Thu Jul 19 08:36:52 2007
SpectroscopytoNonPrime_OBS_Shell.tcl	1.1	Thu Oct 18 15:48:13 2007
SpectroscopytoNonprime.tcl	1.2	Wed Feb 14 17:00:35 2007
T001899MTXX001EPS_DUTY_CYCLE_TM.tcl	1.2	Fri Sep 29 07:48:59 2006
T001899MTXX001EPS_IHMThermListeLignes.tcl	1.2	Fri Sep 29 07:49:26 2006
T001899MTXX001EPS_Load_Thermal_Table.tcl	1.2	Fri Sep 29 07:49:44 2006
T001899MTXX001EPS_TC_IMAGE.tcl	1.2	Fri Sep 29 07:50:12 2006
T001899MTXX001EPS_Thermal_Line.tcl	1.2	Fri Sep 29 07:50:42 2006
TC_aggregation_disable.tcl	1.1	Mon May 7 12:15:10 2007
TC_aggregation_enable.tcl	1.1	Mon May 7 12:23:53 2007
TMTC_DFE_checks.tcl	1.1	Wed Jul 20 10:35:19 2005
TM_data_new.tcl	1.2	Fri Jun 16 07:04:07 2006
TTC_SCOE_checks.tcl	1.2	Wed Nov 2 20:03:08 2005
W010584SPVT001_PCS_HEALTH_CHECK_RT5_B.tcl	1.5	Sat Oct 14 11:34:40 2006
W010584SPVT002_PCS_HEALTH_CHECK_RT6_B.tcl	1.5	Sat Oct 14 11:34:49 2006
W102584EPVT007_CHECK_PCDU.tcl	1.13	Mon May 19 06:46:35 2008
W102584EPVT007_CHECK_PCDU_LAUNCH.tcl	1.2	Mon Nov 27 15:56:16 2006
W102584EPVT007_CHECK_PCS_AIT_LAUNCH.tcl	1.3	Mon May 19 06:49:32 2008
W102584EPVT007_IST_CHECK_PCDU.tcl	1.3	Wed Sep 26 15:42:31 2007
W102584EPVT008_Acq_Data_PCDU.tcl	1.1	Sat Oct 22 08:13:25 2005
W102584SPVT001_BAT_EOC_VERIF.tcl	1.3	Tue Jun 19 09:54:25 2007
W102584SPVT002_BDR_DOD_MANAG_VER.tcl	1.5	Thu Jun 21 15:17:30 2007
W102584SPVT003_COM_AND_MON.tcl	1.13	Thu Jun 21 12:21:18 2007
W102584SPVT004_DNEL_MANAG_VERIF.tcl	1.3	Thu Jun 14 12:37:59 2007
W102584SPVT005_TWO_DOMAINS.tcl	1.7	Tue Jun 19 09:31:05 2007
W102584SPVT010_NCA_VERIFICATION.tcl	1.11	Tue Jun 19 14:33:29 2007
W102584SPVT011_EOC_ECLIPSE_4_SVT0.tcl	1.3	Fri Jul 28 06:01:52 2006
W102584SPVT012_HCS_SWITCH_ON_OFF.tcl	1.2	Tue Oct 10 06:20:08 2006
W102584SPVT013_TRANS_SUN_ECL_SUN.tcl	1.3	Sat Nov 11 10:11:37 2006
W102584SPVT100_PCDU_TRANSITION.tcl	1.9	Mon Jul 30 12:05:54 2007
W102584SPVT101_PCDU_TRANSITION_FDIR.tcl	1.5	Tue May 15 15:53:24 2007
W102584SPVT102_PCDU_TRANSITION_EMC.tcl	1.4	Sat Nov 24 14:50:44 2007
W102584SPVT102_PCDU_TRANSITION_MODE_TR.tcl	1.5	Wed Oct 31 20:11:24 2007
W102584SPVT110_PCS_COMMISSIONING.tcl	1.3	Tue Nov 13 10:51:11 2007
WHEELS_WATCH.tcl	1.1	Wed Sep 5 14:01:56 2007
Wave_Cal_FilA_Shell.tcl	1.1	Thu Oct 18 15:48:13 2007
Wave_Cal_FilB_Shell.tcl	1.1	Thu Oct 18 15:48:13 2007
Y102989ECVT001_TMTC_LINK.tcl	1.4	Thu Nov 16 15:10:27 2006
Y102989ECVT003_TC_DFE_OUT_2_TTC.tcl	1.4	Wed Aug 30 07:59:58 2006
Y102989ECVT004_TTC_RNG_SET.tcl	1.4	Wed Aug 30 07:59:47 2006
Y102989ECVT005_TM_DFE_IN_FROM_TTC.tcl	1.5	Mon Jun 4 06:42:36 2007
Y102989ECVT006_TTC_DL_PORT_SET.tcl	1.6	Wed Aug 30 07:59:32 2006
Y102989ECVT007_TTC_UL_PARAM_SET.tcl	1.4	Wed Aug 30 07:58:52 2006
Y102989ECVT008_TCRG_MI_SET_TTC.tcl	1.6	Thu Nov 16 14:59:04 2006
Y102989ECVT009_TCRG_MI_SET_SSBV_RX.tcl	1.3	Wed Aug 30 07:58:41 2006
Y102989ECVT018_TTC_TC_OP_METHOD.tcl	1.3	Wed Aug 30 07:58:35 2006
Y102989EMVT010_PHASE_NOISE_MEAS.tcl	1.4	Sat Sep 2 12:54:53 2006
Y102989EMVT011_MEAS_FREQ.tcl	1.4	Wed Sep 27 11:17:54 2006
Y102989EMVT012_MEAS_POWER.tcl	1.3	Wed Aug 30 07:58:16 2006
Y102989EMVT013_MEAS_MOD_INDEX.tcl	1.4	Wed Aug 30 07:57:56 2006
Y102989EMVT014_MEAS_ANA_SPEC.tcl	1.3	Wed Aug 30 07:58:10 2006
Y102989EMVT015_AMPLITUDE_RES.tcl	1.4	Thu Sep 21 16:41:07 2006
Y102989EMVT016_MEAS_GROUP_DELAY.tcl	1.3	Wed Aug 30 07:57:49 2006
Y102989EMVT023_TTC_ASA_COPY.tcl	1.3	Wed Aug 30 07:56:23 2006
Y102989EPVT001_EMERGENCY_PWR_SCOE_ON.tcl	1.2	Fri Jan 13 10:00:48 2006
Y102989EPVT001_PWR_SCOE_ON.tcl	1.23	Mon May 19 06:47:32 2008
Y102989EPVT001_PWR_SCOE_ON_AIT_LNC.tcl	1.6	Mon May 19 13:19:03 2008
Y102989EPVT001_PWR_SCOE_ON_EMC.tcl	1.2	Wed Nov 14 15:18:09 2007
Y102989EPVT001_PWR_SCOE_ON_LAUNCH.tcl	1.3	Thu Nov 30 14:52:26 2006
Y102989EPVT002_EMERG_PWR_SCOE_OFF.tcl	1.1	Fri Dec 1 16:21:23 2006
Y102989EPVT002_PWR_SCOE_OFF.tcl	1.17	Mon May 19 06:55:46 2008
Y102989EPVT002_PWR_SCOE_OFF_AIT_LNC.tcl	1.2	Wed Feb 14 07:56:35 2007
Y102989EPVT002_PWR_SCOE_OFF_CLN_LNCH.tcl	1.2	Thu Jul 26 12:36:19 2007
Y102989EPVT002_PWR_SCOE_OFF_LAUNCH.tcl	1.3	Thu Nov 30 14:58:47 2006
Y102989EPVT003_BS_CONN_REM_ONL.tcl	1.1	Sat Oct 22 08:17:42 2005
Y102989EPVT004_SAS_CONN_REM_ONL.tcl	1.1	Sat Oct 22 08:18:07 2005

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Y102989EPVT005_Acq_Data_SCOE_PCDU.tcl     1.2          Tue Jun 12 16:41:07 2007
Y102989EPVT006_Acq_Data_SCOE.tcl          1.1          Sat Oct 22 08:18:42 2005
Y102989EPVT007_IST_PWR_SCOE_ON.tcl        1.7          Mon Nov 26 13:41:43 2007
Y102989ETVT002_TM_DFE_STATISTICS.tcl      1.5          Wed Aug 30 07:56:17 2006
Y102989ETVT017_TTC_CHECK_ROUTINE.tcl      1.5          Wed Oct 25 14:06:12 2006
Y102989ETVT019_TTC_SCOE_ACTIVITY.tcl      1.5          Sat Sep  2 08:34:19 2006
Y102989ETVT020_TTC_SCOE_OFF.tcl           1.4          Wed Jul 25 17:48:15 2007
Y102989ETVT021_TTC_SCOE_ON.tcl            1.3          Thu Jul 20 08:21:38 2006
Y102989ETVT022_TTC_UL_OFFLINE.tcl         1.4          Wed Aug 30 07:55:52 2006
Y102989ETVT030_ASDGEN_PFM_VLV_GUI.tcl     1.5          Wed Oct 17 20:50:42 2007
Y102989ETVT031_ASDGEN_PFM_HEATER_GUI.tcl  1.3          Wed Oct 17 20:46:12 2007
Y102989ETVT032_ASDGEN_PFM_TBTV_LOG.tcl    1.7          Mon Nov 26 16:23:22 2007
Y102989ETVT033_ASDGEN_PFM_SCOE_GUI.tcl    1.6          Mon Nov 26 16:24:12 2007
Y102989ETVT034_ASDGEN_RPFM_LOG.tcl        1.2          Tue Nov 20 15:43:41 2007
Y102989SCVT023_ECL_TO_SUN.tcl             1.1          Wed Jun 28 14:21:32 2006
Z010999MCVT001_POWER_ON.tcl               1.21         Wed Oct 31 16:28:25 2007
Z010999MCVT001_POWER_ON_FAST.tcl          1.5          Fri Oct 27 13:48:09 2006
Z010999MCVT001_POWER_ON_HER_IST.tcl       1.23         Tue Nov 20 18:46:36 2007
Z010999MCVT001_POWER_ON_IM2.tcl           1.1          Thu Oct 25 12:55:00 2007
Z010999MCVT001_POWER_ON_LAUNCH_CONF.tcl   1.3          Thu Nov 30 14:39:58 2006
Z010999MCVT001_POWER_ON_PMAI2.tcl         1.2          Fri Oct 27 14:11:11 2006
Z010999MCVT001_POWER_ON_WITH_LPS.tcl      1.4          Mon Jun 11 15:07:13 2007
Z010999MCVT001__EMERGENCY_POWER_ON.tcl    1.2          Wed Apr 18 08:49:19 2007
Z010999MCVT002_EMERGENCY_POWER_OFF.tcl    1.2          Fri Dec  1 16:20:33 2006
Z010999MCVT002_EMERGENCY_SWITCH_OFF_ALL.tcl 1.4          Thu Feb  1 16:17:36
2007
Z010999MCVT002_POWER_OFF.tcl              1.12         Wed Oct 17 09:43:09 2007
Z010999MCVT002_POWER_OFF_CLN_LNCH.tcl     1.2          Thu Jul 26 12:30:48 2007
Z010999MCVT002_POWER_OFF_FAST.tcl         1.4          Tue Nov 20 03:25:52 2007
Z010999MCVT002_POWER_OFF_HER_IST.tcl      1.2          Wed Feb 14 07:51:09 2007
Z010999MCVT002_POWER_OFF_IM2.tcl          1.3          Fri Oct 26 10:00:42 2007
Z010999MCVT002_POWER_OFF_LAUNCH_CONF.tcl  1.2          Thu Nov 30 14:39:48 2006
Z010999MCVT002_POWER_OFF_PMAI2.tcl        1.2          Fri Oct 27 14:17:20 2006
Z010999MCVT002_POWER_OFF_WITH_LPS.tcl     1.4          Mon Jun 11 15:07:41 2007
Z010999MCVT003_IST_START.tcl              1.44         Wed Nov 21 15:43:55 2007
Z010999MCVT004_IST_END.tcl                1.20         Sat Dec  1 04:45:48 2007
Z010999MCVT005_IST_START_SSMM.tcl         1.7          Tue Nov 27 08:57:31 2007
Z010999MCVT010_SVM_SFT_HERSCHEL_IST.tcl   1.16         Tue Dec  5 20:04:11 2006
Z010999MCVT011_STATUS_SPACECRAFT.tcl      1.18         Tue Nov  6 16:30:43 2007
Z010999MCVT011_STATUS_SPACECRAFT_FDIR.tcl 1.2          Wed Apr 18 08:43:37 2007
Z010999MCVT011_SVM_HERSCHEL_IST_1.tcl     1.14         Tue Nov  6 16:35:52 2007
Z010999MCVT015_SVM_HER_IST_FDIR1.tcl      1.9          Fri Nov 10 15:45:24 2006
Z010999MCVT020_SVM_HER_IST_FDIR2.tcl      1.13         Wed Nov 22 12:07:11 2006
Z010999MCVT040_SVM_HER_IST_FDIR3.tcl      1.7          Thu Apr  5 09:46:32 2007
Z010999MCVT050_SVM_HER_IST_FDIR4.tcl      1.7          Sat Nov 11 13:44:04 2006
Z010999MCVT060_SVM_HER_IST_FDIR5.tcl      1.5          Fri Nov 10 14:11:46 2006
Z010999MCVT070_SVM_HER_IST_FDIR6.tcl      1.10         Sat Nov 11 12:01:23 2006
Z010999MCVT080_IST_FDIR_ASTRIUM.tcl       1.35         Wed Nov 21 16:00:11 2007
Z010999MCVT081_IST_DEGRADED_ASTRIUM.tcl   1.2          Mon Jun 11 13:34:49 2007
Z010999MCVT082_IST_LAUNCH_SEQ_ROBUST.tcl  1.15         Wed Nov 21 15:57:46 2007
Z010999MCVT083_IST_NOM_MODE_ROBUST.tcl    1.12         Wed Nov 21 16:00:50 2007
Z010999MCVT085_IST_RMS_ASTRIUM.tcl        1.18         Tue Nov  6 11:17:01 2007
Z010999MCVT086_IST_DTCP_WORST_CASE.tcl    1.6          Fri Sep 14 14:00:28 2007
Z010999MCVT087_WCS_DTCP.tcl               1.3          Thu Jul  5 15:58:29 2007
Z010999MCVT088_WCS_MTL.tcl                1.2          Tue Jun 26 19:19:05 2007
Z010999MCVT089_IST_SAT_COMMIS_ACMS.tcl    1.9          Sat Dec  1 04:29:44 2007
Z010999MCVT090_IST_DTCP_TRACE_CR.tcl      1.2          Thu Sep 27 15:29:47 2007
Z010999MCVT090_IST_DTCP_TRACE_EPH.tcl     1.4          Mon Nov  5 15:06:36 2007
Z010999MCVT091_IST_RMS_DTCP.tcl           1.6          Thu Sep 27 15:59:15 2007
Z010999MCVT092_IST_LAUNCH_CLEAN_RUN.tcl   1.3          Sat Nov 10 08:17:18 2007
Z010999MCVT093_IST_RMS_Date_Watch.tcl     1.4          Wed May 21 09:51:38 2008
Z010999MCVT094_IST_DTCP_CDMS_MANAGM.tcl   1.13         Wed Nov 21 15:58:35 2007
Z010999MCVT095_IST_CDMS_DTCP.tcl          1.7          Wed Nov 21 15:59:15 2007
Z010999MCVT096_IST_SAT_COMMISSIONING.tcl  1.4          Fri Nov 23 21:54:37 2007
Z010999MCVT097_ASDGEN_CRIT_PARS_CHECK.tcl 1.12         Sat Dec  1 04:27:53 2007
Z010999MCVT100_IST_NOMINAL_LAUNCH.tcl     1.5          Sat Oct 13 08:15:28 2007
Z010999MCVT101_IST_MODE_TRANSITIONS.tcl   1.4          Thu Aug 16 12:34:27 2007
Z010999MCVT101_SAT_HER_IST_FDIR.tcl       1.1          Wed Mar 28 08:59:15 2007
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   2007_12_03_05_00_hercdmu_hpws23_REALTIME_EMC_nois2_TestEnvironment_REALTIME
Z010999MCVT110_STATUS_SATELLITE.tcl      1.5          Wed Apr 18 08:47:43 2007
Z010999MCVT120_SAT_TIME_COUNTER.tcl      1.9          Wed Nov 21 15:43:33 2007
Z010999MCVT121_IST_LAUNCH_CNTR_LSR.tcl   1.10         Sat Nov 17 08:54:30 2007
Z010999MCVT130_IST_INSTR_COMMISSIONING.tcl 1.6          Tue Oct  2 09:17:45 2007
Z010999MCVT131_IST_INSTR_FDIR.tcl        1.4          Mon Nov 26 10:18:41 2007
Z010999MCVT132_TCprotMode_BD_AD_BD.tcl   1.2          Fri Oct 19 11:58:10 2007
Z010999MCVT133_CRYO_COVER_OPEN.tcl       1.2          Tue Oct 23 15:20:25 2007
Z010999MCVT13_CRYO_COVER_OPEN.tcl        1.1          Fri Oct 19 12:03:00 2007
Z010999MCVT153_IST_STATUS.tcl            1.23         Wed Nov 21 15:42:55 2007
Z010999MCVT154_EMC_STATUS.tcl            1.3          Tue Nov 20 18:47:39 2007
Z010999MCVT200_EMC.tcl                   1.9          Fri Nov 30 17:04:13 2007
Z010999MCVT90_IST_DTCP_TRACE_CR_EPH.tcl  1.4          Sun May 18 21:26:23 2008
Z010999MMXX002UNITS_CHECK.tcl            1.16         Mon Oct  1 13:42:41 2007
Z010999MMXX003UNITS_CHECK_PWR_OFF.tcl    1.16         Mon Oct  1 13:43:14 2007
Z102999GTVT000_GEN_COMMON_PROC_LIB.tcl   1.7          Fri Nov 30 17:03:08 2007
Z102999GTVT000_GEN_COMMON_PROC_LIB_DEBUG.tcl 1.2          Sat Nov 24 16:21:25
2007
Z102999SCVT000_SAT_COM_SREM.tcl          1.2          Fri Sep 14 14:03:38 2007
Z102999SCVT001_SREM_ON.tcl               1.13         Mon Aug 27 09:47:17 2007
Z102999SCVT002_SREM_OFF.tcl              1.6          Tue Nov 27 14:49:18 2007
Z102999SCVT003_SREM_ACQ_START.tcl        1.11         Sat Sep 15 10:57:26 2007
Z102999SCVT004_ASDGEN_SPIREPWRON_P.tcl   1.6          Thu Nov 15 17:59:30 2007
Z102999SCVT005_ASDGEN_SPIREPWROFF_P.tcl  1.6          Thu Nov 15 18:00:09 2007
Z102999SCVT006_ASDGEN_SPIREPWRON_R.tcl   1.5          Thu Nov 15 18:00:35 2007
Z102999SCVT007_ASDGEN_SPIREPWROFF_R.tcl  1.5          Thu Nov 15 18:00:59 2007
Z102999SCVT008_ASDGEN_SPIRESTBY2OPS.tcl  1.3          Mon Aug 20 13:06:30 2007
Z102999SCVT009_ASDGEN_SPIREOPS2STBY.tcl  1.3          Mon Aug 20 13:06:01 2007
Z102999SCVT010_ASDGEN_PACSPWRON_P.tcl    1.9          Sat Nov 24 08:50:06 2007
Z102999SCVT011_ASDGEN_PACSPWROFF_P.tcl   1.8          Sat Oct 27 14:50:11 2007
Z102999SCVT012_ASDGEN_PACSPWRON_R.tcl    1.6          Sat Nov 24 08:50:55 2007
Z102999SCVT013_ASDGEN_PACSPWROFF_R.tcl   1.5          Sat Oct 27 15:04:31 2007
Z102999SCVT014_ASDGEN_HIFIPWRON_P.tcl    1.3          Thu Oct 18 10:18:47 2007
Z102999SCVT015_ASDGEN_HIFIPWROFF_P.tcl   1.3          Thu Oct 18 10:18:16 2007
Z102999SCVT016_ASDGEN_HIFIPWRON_R.tcl    1.3          Thu Oct 18 10:17:53 2007
Z102999SCVT017_ASDGEN_HIFIPWROFF_R.tcl   1.3          Thu Oct 18 10:17:17 2007
Z102999SCVT018_ASDGEN_PACSBurstMode.tcl  1.2          Mon Aug 20 12:56:29 2007
Z102999SCVT019_ASDGEN_PACSNomSpect.tcl   1.2          Mon Aug 20 13:06:53 2007
acms_mdl.tcl                             1.2          Wed Apr 18 09:25:41 2007
acms_mdl_fcv.tcl                         1.5          Wed Apr 18 09:25:41 2007
acms_mdl_wheel.tcl                       1.2          Fri Oct 27 17:29:14 2006
acms_prova.tcl                           1.2          Wed Apr 18 09:25:41 2007
acms_prova_stefano.tcl                   1.2          Wed Apr 18 09:22:32 2007
acms_tslew.tcl                           1.1          Fri Oct 20 14:27:44 2006
date_watch.tcl                           1.3          Tue Jul 24 12:27:18 2007
dry_loop_commands.tcl                    1.1          Wed Apr  4 11:47:37 2007
getLogHistory.tcl                        1.1          Tue Oct 30 09:32:13 2007
mclistbox.tcl                            1.1          Thu Jun 15 14:28:32 2006
modify_NM_conf.tcl                       1.8          Sat Sep 29 14:11:47 2007
modify_SM_conf.tcl                       1.5          Sat Sep 29 14:12:11 2007
mtl_upload.tcl                           1.2          Mon Nov 27 08:48:55 2006
provavlg.tcl                             1.2          Mon Sep 10 11:45:43 2007
provola.tcl                              1.1          Wed Mar 28 17:03:31 2007
simon_test1.tcl                          1.1          Fri Oct 26 11:36:31 2007
test_jeff.tcl                            1.1          Tue Jul 10 13:36:39 2007
vlg_temp.tcl                             1.4          Mon Jul 23 15:21:53 2007
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Contents of /HPCCS/VARIABLE/ENVIRONMENTS/EXEC/USER
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CDM_only                                 1.1          Thu Jun 15 14:29:55 2006
ETM_only                                 1.1          Thu Jun 15 14:30:00 2006
IST_TEST_CONFIGURATION.txt               1.5          Wed Nov 28 09:14:27 2007
OCM_only                                 1.1          Thu Jun 15 14:30:04 2006
SAM_only                                 1.1          Thu Jun 15 14:30:09 2006
SBM_only                                 1.1          Thu Jun 15 14:30:13 2006
SCM_only                                 1.1          Thu Jun 15 14:30:18 2006
SSM_only                                 1.1          Thu Jun 15 14:30:24 2006
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Contents of /HPCCS/VARIABLE/ENVIRONMENTS/EXEC/data/ASCII

caf.dat	1.42	Sat Oct 27 07:17:36 2007
cap.dat	1.41	Sat Oct 27 07:17:36 2007
cca.dat	1.18	Thu Aug 30 04:58:31 2007
ccf.dat	1.70	Thu Nov 15 05:41:30 2007
ccs.dat	1.19	Thu Aug 30 04:58:34 2007
cdf.dat	1.61	Thu Nov 15 06:01:54 2007
cpc.dat	1.44	Thu Nov 15 06:02:20 2007
cps.dat	1.1.1.1	Fri Jun 3 05:56:08 2005
csf.dat	1.1.1.1	Fri Jun 3 05:56:08 2005
csp.dat	1.1.1.1	Fri Jun 3 05:56:08 2005
css.dat	1.1.1.1	Fri Jun 3 05:56:08 2005
cur.dat	1.13	Wed Jul 18 17:21:52 2007
cve.dat	1.15	Mon Jul 23 16:22:44 2007
cvp.dat	1.52	Thu Nov 15 05:41:47 2007
cvs.dat	1.31	Sat Oct 27 07:17:53 2007
dpc.dat	1.92	Sat Oct 27 07:17:53 2007
dpf.dat	1.67	Sat Oct 27 07:17:55 2007
dst.dat	1.58	Sat Oct 13 12:02:32 2007
gpc.dat	1.12	Sat Oct 27 07:17:55 2007
gpf.dat	1.9	Mon Jul 23 16:22:47 2007
grp.dat	1.4	Wed Feb 15 13:37:53 2006
grpa.dat	1.6	Wed Feb 15 13:37:53 2006
grpk.dat	1.1.1.1	Fri Jun 3 05:56:08 2005
lgf.dat	1.1.1.1	Fri Jun 3 05:56:08 2005
mcf.dat	1.22	Tue Jun 26 09:54:15 2007
ocf.dat	1.33	Sat Oct 27 07:17:56 2007
ocp.dat	1.34	Sat Oct 27 07:17:56 2007
paf.dat	1.36	Sat Oct 27 07:17:56 2007
pas.dat	1.37	Sat Oct 27 07:17:56 2007
pcdf.dat	1.8	Fri May 4 05:01:01 2007
pcf.dat	1.69	Thu Nov 15 05:41:47 2007
pcpc.dat	1.8	Fri May 4 05:01:05 2007
pic.dat	1.11	Fri May 4 05:01:05 2007
pid.dat	1.63	Thu Nov 15 05:41:50 2007
plf.dat	1.61	Thu Nov 15 05:41:51 2007
prf.dat	1.9	Mon Jul 23 16:22:54 2007
prv.dat	1.9	Mon Jul 23 16:22:54 2007
psm.dat	1.1.1.1	Fri Jun 3 05:56:08 2005
pst.dat	1.1.1.1	Fri Jun 3 05:56:08 2005
psv.dat	1.1.1.1	Fri Jun 3 05:56:08 2005
ptv.dat	1.9	Mon Jul 23 16:22:54 2007
pvs.dat	1.1.1.1	Fri Jun 3 05:56:08 2005
sco.dat	1.27	Sat Oct 27 07:18:02 2007
sdf.dat	1.1.1.1	Fri Jun 3 05:56:08 2005
spc.dat	1.7	Mon Jul 23 16:22:54 2007
spf.dat	1.7	Mon Jul 23 16:22:54 2007
tcd.dat	1.23	Sat Oct 13 11:56:29 2007
tcp.dat	1.11	Thu Sep 13 14:53:50 2007
tmd.dat	1.26	Sat Oct 27 07:18:02 2007
tpcf.dat	1.55	Thu Nov 15 05:41:54 2007
txf.dat	1.47	Sat Oct 27 07:18:02 2007
txp.dat	1.51	Sat Oct 27 07:18:02 2007
vdf.dat	1.51	Thu Nov 15 05:41:54 2007
vpd.dat	1.26	Sat Oct 27 07:18:03 2007

Contents of /HPCCS/VARIABLE/ENVIRONMENTS/EXEC/data/ASCII/synthetic

AD001001	1.9	Fri May 4 12:37:42 2007
AD001002	1.8	Fri May 4 12:37:42 2007
AD001091	1.6	Fri May 4 12:37:42 2007
AD001092	1.6	Fri May 4 12:37:42 2007

2007_12_03_05_00_hercdmu_hpws23_REALTIME_EMC_nois2_TestEnvironment_REALTIME

AD001093	1.6	Fri May 4 12:37:42 2007
AD001094	1.6	Fri May 4 12:37:42 2007
AD001109	1.8	Fri May 4 12:37:42 2007
AD002109	1.8	Fri May 4 12:37:42 2007
AD003109	1.8	Fri May 4 12:37:42 2007
AD004109	1.8	Fri May 4 12:37:42 2007
AD005109	1.8	Fri May 4 12:37:42 2007
AD006109	1.8	Fri May 4 12:37:42 2007
AD007109	1.8	Fri May 4 12:37:42 2007
AD008109	1.8	Fri May 4 12:37:42 2007
AD009109	1.8	Fri May 4 12:37:42 2007
AD010109	1.8	Fri May 4 12:37:42 2007
AD011109	1.8	Fri May 4 12:37:42 2007
AD700001	1.7	Fri May 4 12:37:42 2007
AD700034	1.7	Fri May 4 12:37:42 2007
AD700035	1.7	Fri May 4 12:37:42 2007
AD701001	1.7	Fri May 4 12:37:42 2007
HD087192	1.6	Mon Jul 23 16:22:59 2007
HD111190	1.1	Mon Jul 23 16:22:59 2007
HD112190	1.1	Mon Jul 23 16:22:59 2007
HD212192	1.1	Mon Jul 23 16:22:59 2007
HD247194	1.1	Mon Jul 23 16:22:59 2007
HD248194	1.1	Mon Jul 23 16:22:59 2007
HD249194	1.1	Mon Jul 23 16:22:59 2007
HD289194	1.1	Mon Jul 23 16:23:00 2007
HD291192	1.1	Mon Jul 23 16:23:00 2007
HD329192	1.1	Mon Jul 23 16:23:00 2007
HD330191	1.1	Mon Jul 23 16:23:00 2007
HD701194	1.1	Mon Jul 23 16:23:00 2007
HD716194	1.1	Mon Jul 23 16:23:00 2007
HD731194	1.1	Mon Jul 23 16:23:00 2007
HD746194	1.1	Mon Jul 23 16:23:01 2007
HD800194	1.1	Mon Jul 23 16:23:01 2007
HD801194	1.1	Mon Jul 23 16:23:01 2007
HD802194	1.1	Mon Jul 23 16:23:01 2007
HD803194	1.1	Mon Jul 23 16:23:01 2007
KD001302	1.5	Fri May 4 12:37:44 2007
KD001303	1.5	Fri May 4 12:37:44 2007
KD002302	1.5	Fri May 4 12:37:44 2007
KD002303	1.5	Fri May 4 12:37:44 2007
KD003302	1.5	Fri May 4 12:37:44 2007
KD003303	1.5	Fri May 4 12:37:44 2007
KD004302	1.5	Fri May 4 12:37:44 2007
KD004303	1.5	Fri May 4 12:37:44 2007
KD005302	1.5	Fri May 4 12:37:44 2007
KD005303	1.5	Fri May 4 12:37:44 2007
KD006302	1.5	Fri May 4 12:37:44 2007
KD006303	1.5	Fri May 4 12:37:44 2007
KD007302	1.5	Fri May 4 12:37:44 2007
KD007303	1.7	Fri May 4 12:37:44 2007
KD008302	1.5	Fri May 4 12:37:44 2007
KD008303	1.5	Fri May 4 12:37:44 2007
KD009302	1.5	Fri May 4 12:37:44 2007
KD009303	1.5	Fri May 4 12:37:44 2007
KD010302	1.5	Fri May 4 12:37:44 2007
KD010303	1.5	Fri May 4 12:37:44 2007
KD011302	1.5	Fri May 4 12:37:44 2007
KD011303	1.5	Fri May 4 12:37:44 2007
KD012302	1.5	Fri May 4 12:37:44 2007
KD012303	1.5	Fri May 4 12:37:44 2007
KD013302	1.5	Fri May 4 12:37:44 2007
KD013303	1.5	Fri May 4 12:37:44 2007
KD014302	1.5	Fri May 4 12:37:44 2007
KD014303	1.5	Fri May 4 12:37:44 2007
KD015302	1.5	Fri May 4 12:37:44 2007
KD015303	1.5	Fri May 4 12:37:44 2007
KD016302	1.5	Fri May 4 12:37:44 2007
KD016303	1.5	Fri May 4 12:37:44 2007

2007_12_03_05_00_hercdmu_hpws23_REALTIME_EMC_nois2_TestEnvironment_REALTIME

KD017302	1.5	Fri May 4 12:37:44 2007
KD017303	1.5	Fri May 4 12:37:44 2007
KD018302	1.5	Fri May 4 12:37:44 2007
KD018303	1.5	Fri May 4 12:37:44 2007
KD019302	1.5	Fri May 4 12:37:44 2007
KD019303	1.5	Fri May 4 12:37:44 2007
KD020302	1.5	Fri May 4 12:37:44 2007
KD020303	1.5	Fri May 4 12:37:44 2007
KD021302	1.5	Fri May 4 12:37:44 2007
KD021303	1.5	Fri May 4 12:37:44 2007
KD200302	1.5	Fri May 4 12:37:44 2007
KD200303	1.5	Fri May 4 12:37:44 2007
KD201302	1.5	Fri May 4 12:37:44 2007
KD201303	1.5	Fri May 4 12:37:44 2007
KD202302	1.5	Fri May 4 12:37:44 2007
KD202303	1.5	Fri May 4 12:37:44 2007
KD203302	1.5	Fri May 4 12:37:44 2007
KD203303	1.5	Fri May 4 12:37:44 2007
KD204302	1.5	Fri May 4 12:37:44 2007
KD204303	1.5	Fri May 4 12:37:44 2007
KD205302	1.5	Fri May 4 12:37:44 2007
KD205303	1.5	Fri May 4 12:37:44 2007
KD206302	1.5	Fri May 4 12:37:44 2007
KD206303	1.5	Fri May 4 12:37:44 2007
KD207302	1.5	Fri May 4 12:37:44 2007
KD207303	1.5	Fri May 4 12:37:44 2007
KD208302	1.5	Fri May 4 12:37:44 2007
KD208303	1.5	Fri May 4 12:37:44 2007
KD209302	1.5	Fri May 4 12:37:44 2007
KD209303	1.5	Fri May 4 12:37:44 2007
KD210302	1.5	Fri May 4 12:37:44 2007
KD210303	1.5	Fri May 4 12:37:44 2007
KD211302	1.5	Fri May 4 12:37:44 2007
KD211303	1.5	Fri May 4 12:37:44 2007
KD212302	1.5	Fri May 4 12:37:44 2007
KD212303	1.5	Fri May 4 12:37:44 2007
KD213302	1.5	Fri May 4 12:37:44 2007
KD213303	1.5	Fri May 4 12:37:44 2007
KD214300	1.8	Sat Oct 27 07:18:04 2007
KD214301	1.8	Sat Oct 27 07:18:05 2007
KD215300	1.8	Sat Oct 27 07:18:05 2007
KD215301	1.8	Sat Oct 27 07:18:05 2007
KD221302	1.5	Fri May 4 12:37:44 2007
KD221303	1.5	Fri May 4 12:37:44 2007
KD222302	1.5	Fri May 4 12:37:44 2007
KD222303	1.5	Fri May 4 12:37:44 2007
KD223302	1.5	Fri May 4 12:37:44 2007
KD223303	1.5	Fri May 4 12:37:44 2007
KD224302	1.5	Fri May 4 12:37:44 2007
KD224303	1.5	Fri May 4 12:37:44 2007
KD225302	1.5	Fri May 4 12:37:44 2007
KD225303	1.5	Fri May 4 12:37:44 2007
KD226302	1.5	Fri May 4 12:37:44 2007
KD226303	1.5	Fri May 4 12:37:44 2007
KD227302	1.5	Fri May 4 12:37:44 2007
KD227303	1.5	Fri May 4 12:37:44 2007
KD230300	1.8	Sat Oct 27 07:18:05 2007
KD230301	1.8	Sat Oct 27 07:18:05 2007
KD231300	1.8	Sat Oct 27 07:18:05 2007
KD231301	1.8	Sat Oct 27 07:18:05 2007
KD232302	1.5	Fri May 4 12:37:44 2007
KD232303	1.5	Fri May 4 12:37:44 2007
KD233302	1.5	Fri May 4 12:37:44 2007
KD233303	1.5	Fri May 4 12:37:44 2007
KD234302	1.5	Fri May 4 12:37:44 2007
KD234303	1.5	Fri May 4 12:37:44 2007
KD235302	1.5	Fri May 4 12:37:44 2007
KD235303	1.5	Fri May 4 12:37:45 2007

2007_12_03_05_00_hercdmu_hpws23_REALTIME_EMC_nois2_TestEnvironment_REALTIME

KD236302	1.5	Fri May 4 12:37:45 2007
KD236303	1.5	Fri May 4 12:37:45 2007
KD237302	1.5	Fri May 4 12:37:45 2007
KD237303	1.5	Fri May 4 12:37:45 2007
KD238302	1.5	Fri May 4 12:37:45 2007
KD238303	1.5	Fri May 4 12:37:45 2007
KD239302	1.5	Fri May 4 12:37:45 2007
KD239303	1.5	Fri May 4 12:37:45 2007
KD240302	1.5	Fri May 4 12:37:45 2007
KD240303	1.5	Fri May 4 12:37:45 2007
KD241302	1.5	Fri May 4 12:37:45 2007
KD241303	1.5	Fri May 4 12:37:45 2007
KD242302	1.5	Fri May 4 12:37:45 2007
KD242303	1.5	Fri May 4 12:37:45 2007
KD243302	1.5	Fri May 4 12:37:45 2007
KD243303	1.5	Fri May 4 12:37:45 2007
KD244302	1.5	Fri May 4 12:37:45 2007
KD244303	1.5	Fri May 4 12:37:45 2007
KD245302	1.5	Fri May 4 12:37:45 2007
KD245303	1.5	Fri May 4 12:37:45 2007
KD246302	1.5	Fri May 4 12:37:45 2007
KD246303	1.5	Fri May 4 12:37:45 2007
KD247300	1.8	Sat Oct 27 07:18:05 2007
KD247301	1.8	Sat Oct 27 07:18:05 2007
KD248302	1.5	Fri May 4 12:37:45 2007
KD248303	1.5	Fri May 4 12:37:45 2007
KD249302	1.5	Fri May 4 12:37:45 2007
KD249303	1.5	Fri May 4 12:37:45 2007
KD250302	1.5	Fri May 4 12:37:45 2007
KD250303	1.5	Fri May 4 12:37:45 2007
KD251302	1.5	Fri May 4 12:37:45 2007
KD251303	1.5	Fri May 4 12:37:45 2007
KD252302	1.5	Fri May 4 12:37:45 2007
KD252303	1.5	Fri May 4 12:37:45 2007
KD253302	1.5	Fri May 4 12:37:45 2007
KD253303	1.5	Fri May 4 12:37:45 2007
KD254302	1.5	Fri May 4 12:37:45 2007
KD254303	1.5	Fri May 4 12:37:45 2007
KD255302	1.5	Fri May 4 12:37:45 2007
KD255303	1.5	Fri May 4 12:37:45 2007
KD256302	1.5	Fri May 4 12:37:45 2007
KD256303	1.5	Fri May 4 12:37:45 2007
KD257302	1.5	Fri May 4 12:37:45 2007
KD257303	1.5	Fri May 4 12:37:45 2007
KD258302	1.5	Fri May 4 12:37:45 2007
KD258303	1.5	Fri May 4 12:37:45 2007
KD259302	1.5	Fri May 4 12:37:45 2007
KD259303	1.5	Fri May 4 12:37:45 2007
KD260302	1.5	Fri May 4 12:37:45 2007
KD260303	1.5	Fri May 4 12:37:45 2007
KD261302	1.5	Fri May 4 12:37:45 2007
KD261303	1.5	Fri May 4 12:37:45 2007
KD262302	1.5	Fri May 4 12:37:45 2007
KD262303	1.5	Fri May 4 12:37:45 2007
KD263300	1.8	Sat Oct 27 07:18:05 2007
KD263301	1.8	Sat Oct 27 07:18:05 2007
KD264302	1.5	Fri May 4 12:37:45 2007
KD264303	1.5	Fri May 4 12:37:45 2007
KD265302	1.5	Fri May 4 12:37:45 2007
KD265303	1.5	Fri May 4 12:37:45 2007
KD266300	1.8	Sat Oct 27 07:18:05 2007
KD266301	1.8	Sat Oct 27 07:18:05 2007
KD267300	1.8	Sat Oct 27 07:18:05 2007
KD267301	1.8	Sat Oct 27 07:18:05 2007
KD268300	1.8	Sat Oct 27 07:18:05 2007
KD268301	1.8	Sat Oct 27 07:18:05 2007
KD272300	1.5	Fri May 4 12:37:45 2007
KD272301	1.5	Fri May 4 12:37:45 2007

2007_12_03_05_00_hercdmu_hpws23_REALTIME_EMC_nois2_TestEnvironment_REALTIME

KD272302	1.8	Sat Oct 27 07:18:05 2007
KD272303	1.8	Sat Oct 27 07:18:05 2007
KD273300	1.5	Fri May 4 12:37:45 2007
KD273301	1.5	Fri May 4 12:37:45 2007
KD273302	1.8	Sat Oct 27 07:18:05 2007
KD273303	1.8	Sat Oct 27 07:18:05 2007
KD274302	1.5	Fri May 4 12:37:45 2007
KD274303	1.5	Fri May 4 12:37:45 2007
KD275302	1.5	Fri May 4 12:37:45 2007
KD275303	1.5	Fri May 4 12:37:45 2007
KD276302	1.5	Fri May 4 12:37:45 2007
KD276303	1.5	Fri May 4 12:37:45 2007
KD277302	1.5	Fri May 4 12:37:45 2007
KD277303	1.5	Fri May 4 12:37:45 2007
KD278302	1.5	Fri May 4 12:37:45 2007
KD278303	1.5	Fri May 4 12:37:45 2007
KD600300	1.5	Fri May 4 12:37:45 2007
KD600301	1.8	Sat Oct 27 07:18:05 2007
KD601300	1.5	Fri May 4 12:37:45 2007
KD601301	1.5	Fri May 4 12:37:45 2007
KD602300	1.5	Fri May 4 12:37:45 2007
KD602301	1.5	Fri May 4 12:37:45 2007
KD603300	1.5	Fri May 4 12:37:45 2007
KD603301	1.5	Fri May 4 12:37:45 2007
KD610300	1.8	Sat Oct 27 07:18:05 2007
KD610301	1.8	Sat Oct 27 07:18:05 2007
KD611300	1.8	Sat Oct 27 07:18:05 2007
KD611301	1.8	Sat Oct 27 07:18:05 2007
KD612300	1.8	Sat Oct 27 07:18:05 2007
KD612301	1.8	Sat Oct 27 07:18:05 2007
KD613300	1.8	Sat Oct 27 07:18:05 2007
KD613301	1.8	Sat Oct 27 07:18:05 2007
PD179380	1.5	Fri May 4 12:37:45 2007
PD180380	1.5	Fri May 4 12:37:45 2007
PD421410	1.5	Fri May 4 12:37:45 2007
RD003442	1.7	Fri May 4 12:37:45 2007
RD004442	1.7	Fri May 4 12:37:45 2007
SD00M510	1.5	Fri May 4 12:37:45 2007
SD00M515	1.5	Fri May 4 12:37:45 2007
SD00M520	1.5	Fri May 4 12:37:45 2007
SDB0H510	1.7	Fri May 4 12:37:45 2007
SDB1H510	1.7	Fri May 4 12:37:45 2007
SDF0H510	1.7	Fri May 4 12:37:45 2007
SDH03510	1.3	Fri May 4 12:37:45 2007
SDH03520	1.3	Fri May 4 12:37:45 2007
SDH15520	1.3	Fri May 4 12:37:46 2007
SDP0H510	1.7	Fri May 4 12:37:46 2007
SDS0P515	1.5	Fri May 4 12:37:46 2007
SD_0X510	1.5	Fri May 4 12:37:46 2007
SD_1X510	1.5	Fri May 4 12:37:46 2007
WD011565	1.7	Fri May 4 12:37:46 2007
WD012565	1.7	Fri May 4 12:37:46 2007
WD027565	1.7	Fri May 4 12:37:46 2007
WD028565	1.7	Fri May 4 12:37:46 2007
WD029565	1.7	Fri May 4 12:37:46 2007
WD030565	1.7	Fri May 4 12:37:46 2007
WD045565	1.7	Fri May 4 12:37:46 2007
WD046565	1.7	Fri May 4 12:37:46 2007
WD047565	1.7	Fri May 4 12:37:46 2007
WD048565	1.7	Fri May 4 12:37:46 2007
WD049565	1.7	Fri May 4 12:37:46 2007
WD050565	1.7	Fri May 4 12:37:46 2007
WD051565	1.7	Fri May 4 12:37:46 2007
WD052565	1.7	Fri May 4 12:37:46 2007
WD053565	1.7	Fri May 4 12:37:46 2007
WD054565	1.7	Fri May 4 12:37:46 2007
WD055565	1.7	Fri May 4 12:37:46 2007
WD056565	1.7	Fri May 4 12:37:46 2007

```
   2007_12_03_05_00_hercdmu_hpws23_REALTIME_EMC_nois2_TestEnvironment_REALTIME
WD057565                                    1.7          Fri May  4 12:37:46 2007
WD058565                                    1.7          Fri May  4 12:37:46 2007
WD059565                                    1.7          Fri May  4 12:37:46 2007
WD060565                                    1.7          Fri May  4 12:37:46 2007
WD061565                                    1.7          Fri May  4 12:37:46 2007
WD062565                                    1.7          Fri May  4 12:37:46 2007
WD063565                                    1.7          Fri May  4 12:37:46 2007
WD064565                                    1.7          Fri May  4 12:37:46 2007
WD065565                                    1.7          Fri May  4 12:37:46 2007
WD066565                                    1.7          Fri May  4 12:37:46 2007
WD067565                                    1.7          Fri May  4 12:37:46 2007
WD068565                                    1.7          Fri May  4 12:37:46 2007
WD069565                                    1.7          Fri May  4 12:37:46 2007
WD070565                                    1.7          Fri May  4 12:37:46 2007
WD100565                                    1.5          Fri May  4 12:37:46 2007
WD101565                                    1.5          Fri May  4 12:37:46 2007
WD12A565                                    1.7          Fri May  4 12:37:46 2007
WD22A565                                    1.7          Fri May  4 12:37:46 2007
WD311565                                    1.7          Fri May  4 12:37:46 2007
WD32D565                                    1.7          Fri May  4 12:37:46 2007
WD32E565                                    1.7          Fri May  4 12:37:46 2007
WD42A565                                    1.7          Fri May  4 12:37:46 2007
WD42B565                                    1.7          Fri May  4 12:37:46 2007
WD72A565                                    1.7          Fri May  4 12:37:46 2007
WD72E565                                    1.7          Fri May  4 12:37:46 2007
WD72F565                                    1.7          Fri May  4 12:37:46 2007
WD811565                                    1.7          Fri May  4 12:37:46 2007
WD82A565                                    1.7          Fri May  4 12:37:46 2007
WD82D565                                    1.7          Fri May  4 12:37:46 2007
WD82E565                                    1.7          Fri May  4 12:37:46 2007
WDA06565                                    1.7          Fri May  4 12:37:46 2007
WDA2A565                                    1.7          Fri May  4 12:37:46 2007
YD001940                                    1.11         Fri May  4 12:37:46 2007
--------------------------------------------------

--------------------------------------------------
Contents of /HPCCS/VARIABLE/ENVIRONMENTS/EXEC/data/rawTM
--------------------------------------------------
(empty)
--------------------------------------------------
```

5. Difference Configuration Report

Difference Configuration Report

```
TkDiff 3.09 report

File: 2007_12_03_05_00_hercdmu_hpws23_REALTIME_EMC_nois2
_TestEnvironment_REALTIME.txt
number of diffs: 3
         0 regions were deleted
         0 regions were added
         3 regions were changed

     1
     2   Files are from /HPCCS/VARIABLE/REPOSITORIES/USER
     3   ------------------------------------------------
     4
     5   HPCCS version is hpccs-2.0-1166
     6
     7   ------------------------------------------------
     8
     9   WARNING:- online patches are not reflected in this
    10   file unless added manually to the end of the file
    11   ------------------------------------------------
    12
    13   ------------------------------------------------------
    14   Contents of /HPCCS/VARIABLE/ENVIRONMENTS/EXEC/LOG
    15   ------------------------------------------------------
    16   ImportMIB.log                                1.1          Thu
Nov 16 08:30:19 2006
    17   consistency.log                              1.1          Thu
Nov 16 08:25:01 2006
    18 ! sessionlog                                   1.14503      Sat
Dec  1 12:58:25 2007
    19   ------------------------------------------------------
    20
    21   ------------------------------------------------------
    22   Contents of /HPCCS/VARIABLE/ENVIRONMENTS/EXEC/MISC
    23   ------------------------------------------------------
    24   (empty)
    25   ------------------------------------------------------
    26
    27   ------------------------------------------------------
    28   Contents of /HPCCS/VARIABLE/ENVIRONMENTS/EXEC/PICT
    29   ------------------------------------------------------
    30   ACMS_BLOCDIAG.ilv                            1.3          Fri
Feb 17 10:18:11 2006
    31   ACMS_H_BLOC.ilv                              1.26         Sat
Nov 10 16:05:07 2007
    32   ASTRIUM_TEST.ilv                             1.4          Tue
Mar 27 11:28:07 2007
    33   BS_PROVA.ilv                                 1.1          Tue
Sep 13 10:14:50 2005
    34   CCU.ilv                                      1.16         Sat
Nov 17 17:30:14 2007
    35   CCU_HE_TANK.ilv                              1.9          Fri
Oct 19 10:09:24 2007
    36   CCU_OPT_BENCH.ilv                            1.9          Wed
Oct 24 07:21:44 2007
    37   CDMU_BLOCDIAG.ilv                            1.15         Mon
Aug 20 12:36:52 2007
    38   EGSE_CCU.ilv                                 1.1          Thu
Nov 29 14:42:56 2007
    39   EGSE_CCU_HE_TANK.ilv                         1.1          Thu
Nov 29 14:43:19 2007
    40   EGSE_CCU_OPT_BENCH.ilv                       1.1          Thu
Nov 29 14:43:28 2007
    41   EGSE_CONN.ilv                                1.1          Tue
Sep 13 10:15:19 2005
```

```
    42   FCL.ilv                                    1.1          Tue
Sep 13 10:15:13 2005
    43   GEN_POWER.ilv                              1.7          Mon
Aug 20 12:36:33 2007
    44   HEATERS.ilv                                1.3          Wed
Apr 18 09:43:15 2007
    45   INSTRUMENTS_H.ilv                          1.12         Thu
Aug 30 05:10:25 2007
    46   LCL.ilv                                    1.1          Tue
Sep 13 10:15:07 2005
    47   LCL_HERSCHEL.ilv                           1.5          Tue
Apr 10 16:00:12 2007
    48   LCL_PLANCK.ilv                             1.1          Tue
Sep 13 10:15:34 2005
    49   RCS.ilv                                    1.3          Mon
Aug 20 12:37:10 2007
    50   RFDN.ilv                                   1.1          Tue
Sep 13 10:15:39 2005
    51   SAT.ilv                                    1.48         Sat
Nov 17 17:29:25 2007
    52   SINOTTICO.ilv                              1.1          Tue
Sep 13 10:15:02 2005
    53   TT&C_H-P.ilv                               1.3          Thu
Nov 23 11:46:21 2006
    54   TT&C_HER.ilv                               1.6          Tue
Jan 24 11:16:44 2006
    55   TT&C_HERSCHEL.ilv                          1.2          Thu
Jan 12 08:51:35 2006
    56   TT&C_HER_2.ilv                             1.9          Mon
Aug  4 08:08:14 2008
    57   TTC_H_P.ilv                                1.5          Mon
Sep 24 12:23:08 2007
    58   TWTA_1.ilv                                 1.1          Tue
Sep 13 10:15:24 2005
    59   TWTA_2.ilv                                 1.1          Tue
Sep 13 10:15:45 2005
    60   XPOND1.ilv                                 1.1          Tue
Sep 13 10:16:29 2005
    61   XPOND2.ilv                                 1.1          Tue
Sep 13 10:14:37 2005
    62   ------------------------------------------------
    63
    64   ------------------------------------------------
    65   Contents of /HPCCS/VARIABLE/ENVIRONMENTS/EXEC/TSEQ
    66   ------------------------------------------------
    67   A101109SPVT201_ACMS_STATUS.tcl             1.1          Tue
Nov  6 15:51:59 2007
    68   A102109ETVT000_ACMS_PROCESS.tcl            1.72         Sat
Nov  3 13:37:59 2007
    69   A102109SPVT003_ACMS_CONFIG25.tcl           1.20         Mon
Jul 10 11:18:26 2006
    70   A102109SPVT004_ACMS_LOADCONFIG1.tcl        1.3          Mon
May 15 13:43:03 2006
    71   A102109SPVT005_ACMS_EE_04.tcl              1.10         Wed
Feb 22 19:23:18 2006
    72   A102109SPVT006_ACMS_EE_05.tcl              1.7          Wed
Feb 22 19:23:04 2006
    73   A102109SPVT007_ACMS_PT_01.tcl              1.5          Fri
Jul 21 13:26:38 2006
    74   A102109SPVT008_ACMS_HC_04.tcl              1.12         Mon
Mar 13 13:49:55 2006
    75   A102109SPVT009_ACMS_HC_05.tcl              1.8          Wed
Mar  8 11:09:33 2006
    76   A102109SPVT010_ACMS_SCOE_CONFIG1.tcl       1.44         Fri
Oct 26 09:47:29 2007
    77   A102109SPVT011_ACMS_ON.tcl                 1.36         Sat
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Sep 29 13:51:37 2007
    78   A102109SPVT012_ACMS_OFF.tcl                1.35          Sat
Nov 10 17:07:01 2007
    79   A102109SPVT012_EMERGENCY_ACMS_OFF.tcl      1.2           Mon
Dec  4 16:56:34 2006
    80   A102109SPVT013_ACMS_EE_01.tcl              1.11          Tue
Nov 27 04:22:27 2007
    81   A102109SPVT014_ACMS_STR_Test.tcl           1.7           Mon
Jun  5 07:08:43 2006
    82   A102109SPVT015_ACMS_HC_01.tcl              1.12          Wed
Oct 31 15:45:55 2007
    83   A102109SPVT016_ACMS_EE_02.tcl              1.6           Sat
Mar  4 13:43:54 2006
    84   A102109SPVT017_ACMS_CRS_BACKGROUND.tcl     1.6           Thu
Mar 22 11:42:10 2007
    85   A102109SPVT018_ACMS_Units_Stim.tcl         1.5           Mon
Jun  5 07:09:55 2006
    86   A102109SPVT019_ACMS_HC_02.tcl              1.9           Sat
Oct 14 15:08:31 2006
    87   A102109SPVT020_ACMS_EE_06.tcl              1.4           Wed
May 10 16:51:09 2006
    88   A102109SPVT021_ACMS_ACC_SEPARA.tcl         1.29          Fri
May 11 11:58:05 2007
    89   A102109SPVT022_ACMS_SEP_DELAY.tcl          1.3           Mon
May 15 13:44:52 2006
    90   A102109SPVT028_ACMS_GYR_Test.tcl           1.2           Tue
Jun 19 07:35:45 2007
    91   A102109SPVT029_ACMS_HC_07.tcl              1.12          Tue
Mar  6 17:07:57 2007
    92   A102109SPVT030_ACMS_HC_03.tcl              1.20          Tue
Dec  5 23:17:58 2006
    93   A102109SPVT031_ACMS_EE_03.tcl              1.6           Fri
Oct 27 16:11:41 2006
    94   A102109SPVT032_DEFAULT_CONF_SEP.tcl        1.24          Tue
Nov  6 12:55:34 2007
    95   A102109SPVT034_ACMS_SAM_MON.tcl            1.12          Thu
Oct 19 11:27:46 2006
    96   A102109SPVT035_ACMS_GYRO_UFT.tcl           1.2           Mon
Jun  5 07:11:38 2006
    97   A102109SPVT035_ACMS_SEP_DELAY.tcl          1.4           Mon
May 15 13:08:41 2006
    98   A102109SPVT036_ACMS_STR_ON.tcl             1.6           Mon
Aug 28 14:45:37 2006
    99   A102109SPVT037_SUN_INER.tcl                1.5           Tue
Feb 27 16:26:09 2007
   100   A102109SPVT038_RWL_ON.tcl                  1.10          Mon
May 21 13:45:35 2007
   101   A102109SPVT039_STR_ON.tcl                  1.3           Wed
Mar 15 17:22:54 2006
   102   A102109SPVT040_STR_OFF.tcl                 1.2           Mon
May 15 13:46:25 2006
   103   A102109SPVT041_RWL_CONF.tcl                1.4           Mon
May 15 13:46:43 2006
   104   A102109SPVT042_RWL_SPINUP.tcl              1.6           Mon
Oct  2 14:56:38 2006
   105   A102109SPVT043_TRANSITION_TO_OCM.tcl       1.20          Thu
Oct 11 12:50:04 2007
   106   A102109SPVT044_RCS_MODE.tcl                1.4           Sat
Sep 23 12:25:07 2006
   107   A102109SPVT045_SCM_RASTER.tcl              1.2           Thu
Aug 23 11:55:24 2007
   108   A102109SPVT046_ROT_QUAT.tcl                1.2           Mon
May 15 13:47:38 2006
   109   A102109SPVT047_RM_DUMP.tcl                 1.2           Mon
May 15 13:37:36 2006
   110   A102109SPVT048_TEST_DW_ENA.tcl             1.1           Mon
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May 15 13:08:59 2006
   111   A102109SPVT049_ACMS_HC_09.tcl                1.14          Wed
Dec  6 11:22:50 2006
   112   A102109SPVT050_BACK_TO_PRESEP.tcl            1.6           Wed
Jul 25 08:45:06 2007
   113   A102109SPVT051_ACMS_OCM_01.tcl               1.6           Wed
Apr 18 09:22:19 2007
   114   A102109SPVT052_TRANSITION_TO_SCM.tcl         1.22          Sat
Nov  3 13:39:16 2007
   115   A102109SPVT053_ACMS_SAM_01.tcl               1.10          Tue
Nov 27 23:55:36 2007
   116   A102109SPVT056_ACMS_HC_06.tcl                1.3           Wed
Apr 18 09:22:21 2007
   117   A102109SPVT057_TRANSITION_TO_SAM.tcl         1.2           Mon
Oct  2 14:57:09 2006
   118   A102109SPVT058_ACMS_SCM_02.tcl               1.16          Wed
Jul 25 04:33:47 2007
   119   A102109SPVT059_ACC_TIMESYNC.tcl              1.1           Mon
May 15 13:10:02 2006
   120   A102109SPVT060_ACMS_SCM_05.tcl               1.12          Tue
Oct  2 14:12:36 2007
   121   A102109SPVT061_RWL_SPINDOWN.tcl              1.3           Tue
Sep 26 19:48:34 2006
   122   A102109SPVT062_ACMS_SCM_01.tcl               1.10          Fri
Jul 20 14:06:48 2007
   123   A102109SPVT063_MODIFY_CROME_REGISTER.tcl     1.1           Mon
May 22 08:28:27 2006
   124   A102109SPVT064_ACMS_CIRSIR_03.tcl            1.3           Thu
Jun  8 09:58:33 2006
   125   A102109SPVT065_ACMS_CIRSIR_04_I.tcl          1.3           Thu
Jun  8 09:59:06 2006
   126   A102109SPVT066_ACMS_CIRSIR_04_II.tcl         1.3           Thu
Jun  8 09:59:46 2006
   127   A102109SPVT067_ACMS_SCM_04.tcl               1.12          Wed
Jul 25 04:45:20 2007
   128   A102109SPVT069_ACMS_AV_02.tcl                1.3           Thu
Jun  8 10:02:23 2006
   129   A102109SPVT070_ACMS_AV_04.tcl                1.4           Thu
Jun 15 06:53:41 2006
   130   A102109SPVT071_ACMS_RECOVERY.tcl             1.2           Thu
Oct  5 10:59:51 2006
   131   A102109SPVT072_ACMS_GYRCHK_01.tcl            1.3           Wed
Apr 18 09:22:22 2007
   132   A102109SPVT073_ACMS_PT_03.tcl                1.3           Tue
Jul 25 15:46:54 2006
   133   A102109SPVT074_ACMS_PT_06.tcl                1.4           Wed
Apr 18 09:22:23 2007
   134   A102109SPVT075_ACMS_PT_07.tcl                1.3           Wed
Apr 18 09:22:24 2007
   135   A102109SPVT076_ACMS_PT_08.tcl                1.3           Wed
Apr 18 09:22:25 2007
   136   A102109SPVT077_ACMS_PT_09.tcl                1.3           Wed
Apr 18 09:22:26 2007
   137   A102109SPVT078_ACMS_PT_04.tcl                1.2           Thu
Oct 12 13:48:06 2006
   138   A102109SPVT079_ACMS_PT_05.tcl                1.3           Fri
Oct 13 11:02:31 2006
   139   A102109SPVT081_ACMS_ARAD_01.tcl              1.5           Thu
Nov 30 09:59:57 2006
   140   A102109SPVT082_ACMS_ARAD_05.tcl              1.6           Fri
Nov 10 15:21:41 2006
   141   A102109SPVT083_ACMS_FDIR_01.tcl              1.4           Mon
Nov 27 14:15:17 2006
   142   A102109SPVT084_ACMS_FDIR_02.tcl              1.3           Thu
Nov 30 09:54:44 2006
   143   A102109SPVT085_ACMS_PT_02.tcl                1.4           Mon
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Dec  4 20:01:01 2006
   144   A102109SPVT086_ACMS_TIME_SYNCH.tcl       1.1          Tue
Dec  5 17:49:39 2006
   145   A102109SPVT087_ACMS_IST_FN.tcl           1.19         Mon
Nov  5 06:02:45 2007
   146   A102109SPVT088_ACMS_IST_UNIT_CHECKOUT.tcl 1.6          Sat
Dec  1 04:53:19 2007
   147   A102109SPVT089_ACMS_IST_ACC_HEALTH.tcl   1.8          Thu
Nov 29 15:54:05 2007
   148   A102109SPVT090_ACMS_IST_TANGO.tcl        1.6          Wed
Jul 11 08:24:45 2007
   149   A102109SPVT091_ACMS_IST_RWL_HEALTH.tcl   1.5          Fri
Jul 13 13:58:58 2007
   150   A102109SPVT092_ACMS_GC_03.tcl            1.2          Wed
Nov 28 21:47:58 2007
   151   A102109SPVT0XX_ACMS_RWL_UFT.tcl          1.4          Thu
Sep 28 08:48:29 2006
   152   A102109SPVT0xx_ACMS_RWL_Test_V2.tcl      1.1          Thu
Jan 19 07:30:22 2006
   153   A102109SPVT100_ACMS_OPS.tcl              1.42         Wed
Nov 28 21:36:28 2007
   154   A102109SPVT200_ACMS_DELTA_V_IST.tcl      1.3          Thu
Aug 30 08:13:36 2007
   155   A102109SPVT201_ACMS_STATUS.tcl           1.12         Tue
Nov 21 07:25:29 2006
   156   A102109SPVT202_ACMS_STATUS_H.tcl         1.7          Tue
Nov  6 16:34:27 2007
   157   A102109SPVT203_RWL_SPINUP_IST.tcl        1.1          Fri
Nov  3 10:43:13 2006
   158   A102109SPVT204_ACMS_IST_FDIR.tcl         1.12         Thu
Aug 30 08:14:06 2007
   159   A102109SPVT204_ACMS_TRANSIT_SCM_OCM_SCM.tcl 1.3          Tue
Nov 21 07:32:07 2006
   160   A102109SPVT205_ACMS_IST_RCS_HC.tcl       1.2          Wed
Apr 18 09:22:27 2007
   161   A102109SPVT205_ACMS_IST_RCS_HC_PART1.tcl 1.3          Tue
Dec  5 15:58:25 2006
   162   A102109SPVT205_ACMS_IST_RCS_HC_PART2.tcl 1.4          Wed
Dec  6 11:27:11 2006
   163   A102109SPVT206_ACMS_RWL_EMC_SETUP.tcl    1.5          Wed
Nov 28 09:18:14 2007
   164   A102109SPVT207_ACMS_STR_EMC_SETUP.tcl    1.3          Wed
Nov 28 09:22:00 2007
   165   ACMS_ACC_CLOSE_STRAPS.tcl                1.1          Mon
May 15 13:11:18 2006
   166   ACMS_ACC_SEPARA.tcl                      1.1          Mon
May 15 13:11:28 2006
   167   ACMS_Error_Inj_AAD_curr.tcl              1.1          Mon
May 15 13:11:40 2006
   168   ACMS_FOP_RM_DISABLE.tcl                  1.1          Mon
May 15 13:11:51 2006
   169   ACMS_FOP_RM_ENABLE.tcl                   1.1          Mon
May 15 13:12:02 2006
   170   ACMS_FOP_STR_OFF.tcl                     1.1          Mon
May 15 13:12:18 2006
   171   ACMS_FOP_STR_ON.tcl                      1.1          Mon
May 15 13:11:08 2006
   172   ACMS_MON.tcl                             1.1          Mon
May 15 13:10:57 2006
   173   ACMS_QSL.tcl                             1.2          Mon
May 15 13:48:23 2006
   174   ACMS_RECOVERY.tcl                        1.2          Wed
Apr 18 09:22:28 2007
   175   ACMS_RECOVERY_from_AutoPeriod.tcl        1.2          Mon
May 19 06:33:34 2008
   176   ACMS_RWL_RUN_IN.tcl                      1.2          Wed
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Apr 18 09:22:29 2007
   177   ACMS_RWL_RUN_IN_PASSIVE.tcl                1.3          Tue
Jun  5 07:56:32 2007
   178   ACMS_SAM_MON_P.tcl                         1.1          Mon
May 15 13:10:47 2006
   179   ACMS_SCOE_checks.tcl                       1.4          Fri
Nov 18 18:48:04 2005
   180   ACMS_SEP_DELAY.tcl                         1.1          Mon
May 15 13:10:38 2006
   181   ACMS_STAR_VECTOR_TO_CCD.tcl                1.2          Wed
Apr 18 09:22:30 2007
   182   ACMS_STR_CHECK.tcl                         1.1          Tue
Jul 17 15:41:47 2007
   183   ACMS_SVT0.tcl                              1.4          Wed
Apr 18 09:25:41 2007
   184   ACMS_event_buffer_dump.tcl                 1.8          Tue
Jul 24 05:33:34 2007
   185   ACMS_get_RM_status.tcl                     1.9          Wed
Oct 11 07:26:48 2006
   186   ALL_SubscribeParams.tcl                    1.2          Thu
Apr 19 19:02:09 2007
   187   ApidTracer.tcl                             1.3          Tue
Sep 25 17:52:30 2007
   188   BARBARA_TEMPO.tcl                          1.1          Tue
Oct 25 09:08:46 2005
   189   BOLO_cooler_OBS_Shell.tcl                  1.1          Thu
Oct 18 15:48:09 2007
   190   BS_SCOE_checks.tcl                         1.1          Wed
Jul 20 10:35:01 2005
   191   Background_Adjustment_01_Shell.tcl         1.1          Thu
Oct 18 15:48:09 2007
   192   Background_Adjustment_02_Shell.tcl         1.1          Thu
Oct 18 15:48:09 2007
   193   Background_Adjustment_03_Shell.tcl         1.1          Thu
Oct 18 15:48:09 2007
   194   Background_Adjustment_04_Shell.tcl         1.1          Thu
Oct 18 15:48:09 2007
   195   Background_Adjustment_05_Shell.tcl         1.1          Thu
Oct 18 15:48:10 2007
   196   Background_Adjustment_06_Shell.tcl         1.1          Thu
Oct 18 15:48:10 2007
   197   CALIBRATION_FOR_GYRO.tcl                   1.6          Thu
Mar  8 17:44:26 2007
   198   CCS-IEGSE-IFTest-HIFI_102cmds.tcl          1.1          Thu
Apr 19 18:42:54 2007
   199   CCS-IEGSE-IFTest-HIFI_200cmds.tcl          1.1          Thu
Apr 19 18:43:02 2007
   200   CCS-IEGSE-IFTest-HIFI_20cmds.tcl           1.1          Thu
Apr 19 18:42:43 2007
   201   CCS-IEGSE-IFTest-PACS_102cmds.tcl          1.1          Thu
Apr 19 18:43:09 2007
   202   CCS-IEGSE-IFTest-PACS_200cmds.tcl          1.1          Thu
Apr 19 18:43:17 2007
   203   CCS-IEGSE-IFTest-PACS_20cmds.tcl           1.1          Thu
Apr 19 18:43:25 2007
   204   CCS-IEGSE-IFTest-SPIRE_102cmds.tcl         1.1          Thu
Apr 19 18:43:32 2007
   205   CCS-IEGSE-IFTest-SPIRE_200cmds.tcl         1.1          Thu
Apr 19 18:43:39 2007
   206   CCS-IEGSE-IFTest-SPIRE_20cmds.tcl          1.1          Thu
Apr 19 18:44:01 2007
   207   CDMU_SCOE_checks.tcl                       1.2          Fri
Aug 26 10:23:33 2005
   208   CONF_chopper_ast_OBS_Shell.tcl             1.1          Thu
Oct 18 15:48:10 2007
   209   CONF_grating_IST_OBS_Shell.tcl             1.1          Thu
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Oct 18 15:48:10 2007
   210   CONF_grating_OBS_Shell.tcl                1.1          Thu
Oct 18 15:48:10 2007
   211   CONF_grating_SFTHeII_OBS_Shell.tcl        1.1          Thu
Oct 18 15:48:10 2007
   212   CONF_grating_SFTHeII_R_OBS_Shell.tcl      1.1          Thu
Oct 18 15:48:10 2007
   213   CONF_phot_fltw_OBS_Shell.tcl              1.1          Thu
Oct 18 15:48:10 2007
   214   CONF_phot_fltw_R_OBS_Shell.tcl            1.1          Thu
Oct 18 15:48:10 2007
   215   CONF_phot_fltw_R_Warm_OBS_Shell.tcl       1.1          Thu
Oct 18 15:48:10 2007
   216   CONF_phot_fltw_Warm_OBS_Shell.tcl         1.1          Thu
Oct 18 15:48:10 2007
   217   CONF_spec_fltw_OBS_Shell.tcl              1.1          Thu
Oct 18 15:48:10 2007
   218   CONF_spec_fltw_R_OBS_Shell.tcl            1.1          Thu
Oct 18 15:48:10 2007
   219   CONF_spec_fltw_R_Warm_OBS_Shell.tcl       1.1          Thu
Oct 18 15:48:10 2007
   220   CONF_spec_fltw_Warm_OBS_Shell.tcl         1.1          Thu
Oct 18 15:48:10 2007
   221   CcsHanderTest.tcl                         1.1          Thu
Mar  8 18:04:22 2007
   222   CcsHanderTest200.tcl                      1.2          Wed
Apr 18 09:33:02 2007
   223   Check_PCDU_mini_IST.tcl                   1.1          Tue
Oct 25 23:21:04 2005
   224   Chop_mov_abs_OBS_Shell.tcl                1.1          Thu
Oct 18 15:48:10 2007
   225   D101159SCVT003DISTHERMALCONTROL.tcl       1.1          Thu
Jun 28 12:49:05 2007
   226   D101159SCVT008MEM_MAN_RD_WR_CHK.tcl       1.1          Sat
Oct 29 09:55:16 2005
   227   D101159SCVT028SSMM_OFF.tcl                1.1          Fri
Mar 10 17:14:17 2006
   228   D102159SCVT000SUBSEQ.tcl                  1.2          Mon
Nov  7 20:47:54 2005
   229   D102159SCVT000SUBSEQDOWN1.tcl             1.14         Wed
Nov  2 10:36:30 2005
   230   D102159SCVT000_SYSTEMINIT.tcl             1.2          Thu
Sep 15 12:42:21 2005
   231   D102159SCVT001PM_SELECT.tcl               1.19         Thu
Nov 15 12:14:21 2007
   232   D102159SCVT002TC_VERIF_SERVICE.tcl        1.3          Fri
Oct 28 11:15:48 2005
   233   D102159SCVT003DISTHERMALCONTROL.tcl       1.7          Mon
Oct 29 15:39:11 2007
   234   D102159SCVT003_EMERGENCY_DISTHERMALCONTROL.tcl 1.2
Wed Apr 18 08:46:13 2007
   235   D102159SCVT004DEVICE_COMMAND_DISTR.tcl    1.5          Fri
Nov 11 08:21:13 2005
   236   D102159SCVT005HK_DATA_REPORTING.tcl       1.2          Fri
Oct 28 10:19:12 2005
   237   D102159SCVT006TIMEMANAGEMENT.tcl          1.3          Tue
Oct 25 17:59:31 2005
   238   D102159SCVT007PM_RESET.tcl                1.19         Mon
Oct 29 16:46:49 2007
   239   D102159SCVT007PM_RESET_IT.tcl             1.2          Mon
Jun 11 14:03:15 2007
   240   D102159SCVT008MEM_MAN_RD_WR_CHK.tcl       1.3          Sat
Oct 29 10:03:55 2005
   241   D102159SCVT009MEMORYMANAGEMENT.tcl         1.3          Sat
Oct 29 09:54:54 2005
   242   D102159SCVT010HK_DATA_REPORTING.tcl        1.1          Fri
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Oct 28 10:22:00 2005
   243   D102159SCVT011MEM_MAN_COPY_MEM.tcl       1.4          Sat
Oct 29 09:55:59 2005
   244   D102159SCVT012_INITCOMPL_BOOTEVENT.tcl   1.3          Mon
Oct 29 16:08:07 2007
   245   D102159SCVT012_INITCOMPL_BOOTEVENT_IM2.tcl 1.2          Fri
Oct 26 09:27:01 2007
   246   D102159SCVT013_CDMSINTHKCHECK.tcl        1.3          Wed
Jan 11 09:59:28 2006
   247   D102159SCVT013_CDMSINTHKCHECK_PMB.tcl    1.4          Tue
May 16 15:36:26 2006
   248   D102159SCVT014CONNECT_TEST_SERVICE.tcl   1.2          Fri
Oct 28 10:17:23 2005
   249   D102159SCVT015_PACKET_TRASM_CONTR.tcl    1.4          Thu
Nov 10 22:18:01 2005
   250   D102159SCVT015_PACKET_TRASM_CTR_BA.tcl   1.1          Tue
Nov  8 13:42:30 2005
   251   D102159SCVT016HK_DATA_REPORTING.tcl      1.2          Fri
Oct 28 10:20:24 2005
   252   D102159SCVT017HK_DATA_REPORTING.tcl      1.2          Fri
Oct 28 10:20:55 2005
   253   D102159SCVT018HK_DATA_REPORTING.tcl      1.2          Fri
Oct 28 10:21:15 2005
   254   D102159SCVT019HK_DATA_REPORTING.tcl      1.2          Fri
Oct 28 10:21:34 2005
   255   D102159SCVT020ASW_PCDU_MANAGEMENT.tcl    1.4          Fri
Nov  3 07:34:45 2006
   256   D102159SCVT021_EVENTREPORTING.tcl        1.2          Sat
Oct 29 09:45:57 2005
   257   D102159SCVT022ASW_TT_C_MANAGEMENT.tcl    1.7          Tue
Nov  6 20:48:11 2007
   258   D102159SCVT023_TC_MODEVERIFICATION.tcl   1.3          Thu
Oct 27 19:22:29 2005
   259   D102159SCVT023_TC_MODE_VERIF_DECB.tcl    1.3          Thu
Oct 27 19:22:41 2005
   260   D102159SCVT024_THERM_CTRL_MANAG.tcl      1.5          Tue
Nov  6 17:40:00 2007
   261   D102159SCVT025ON_BOARD_MONITORING.tcl    1.4          Thu
Nov  2 09:36:28 2006
   262   D102159SCVT026BUS_COMM.tcl               1.2          Tue
Oct 25 20:06:15 2005
   263   D102159SCVT027BUS_PROF_MAN.tcl           1.5          Mon
Oct 24 18:05:46 2005
   264   D102159SCVT028SSMM_OFF.tcl               1.7          Sat
Oct 27 10:50:38 2007
   265   D102159SCVT028SSMM_OFF_IM2.tcl           1.1          Fri
Oct 26 07:50:14 2007
   266   D102159SCVT028SSMM_ON.tcl                1.8          Sat
Oct 27 10:50:51 2007
   267   D102159SCVT028SSMM_ON_IM2.tcl            1.1          Fri
Oct 26 06:44:51 2007
   268   D102159SCVT028_SSMM_MANAGEMENT.tcl       1.4          Sat
Oct 29 12:31:09 2005
   269   D102159SCVT029_ON_BOARD_STOR_RETR.tcl    1.15         Mon
Nov 14 16:15:08 2005
   270   D102159SCVT029_ON_BOARD_STOR_RETR1.tcl   1.8          Mon
Nov 14 14:03:10 2005
   271   D102159SCVT029_ON_BOARD_STOR_RETR2.tcl   1.1          Tue
Nov  8 11:54:45 2005
   272   D102159SCVT029_SEL_&_CEL.tcl             1.1          Wed
Nov  9 14:25:55 2005
   273   D102159SCVT029_SEL_AND_CEL.tcl           1.11         Tue
Nov 15 14:26:33 2005
   274   D102159SCVT029_STORING_DOWNLOAD.tcl      1.5          Wed
Oct 26 07:21:59 2005
   275   D102159SCVT029_STORING_DOWNLOAD1.tcl     1.4          Mon
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Nov 14 15:42:27 2005
   276   D102159SCVT029_STORING_DOWNLOAD2.tcl      1.5          Mon
Nov 14 08:49:05 2005
   277   D102159SCVT030_DLL1_ERR_BUSAB_HEAL.tcl    1.2          Wed
Oct 26 12:18:40 2005
   278   D102159SCVT030_DLL2_CORE.tcl              1.1          Tue
Sep  5 07:32:49 2006
   279   D102159SCVT030_DLL2_ERRA_BUSB_UNHE.tcl    1.2          Thu
Nov  2 06:12:57 2006
   280   D102159SCVT030_DLL2_ERR_A_B_UNHEAL.tcl    1.1          Tue
Sep  5 07:33:09 2006
   281   D102159SCVT030_DLL3_1_NN_VITAL_RT.tcl     1.2          Wed
Oct 26 19:22:33 2005
   282   D102159SCVT030_DLL4_VITAL_RT.tcl          1.2          Thu
Nov  2 06:12:32 2006
   283   D102159SCVT030_DLL5_NON_VITAL_RT_FAIL.tcl 1.3          Thu
Nov  2 06:11:44 2006
   284   D102159SCVT031OBCP_MANAGEMENT.tcl         1.7          Thu
Nov  2 14:38:44 2006
   285   D102159SCVT032EnNomTCSLoops.tcl           1.8          Tue
Nov  6 15:59:57 2007
   286   D102159SCVT032TIMESYNCRO.tcl              1.7          Fri
Oct 26 07:35:53 2007
   287   D102159SCVT033_TCS_END_TO_END_NR.tcl      1.1          Wed
Sep 20 15:51:59 2006
   288   D102159SCVT034_TTR_MANAGEMENT.tcl         1.13         Mon
Oct 29 18:39:30 2007
   289   D102159SCVT035_LOW_RATE_PACKET.tcl        1.4          Thu
May  3 14:27:19 2007
   290   D102159SCVT035_PAYLOAD_MANAGEMENT.tcl     1.14         Tue
Oct 30 12:19:28 2007
   291   D102159SCVT036_ASW_VAL_TEST46.tcl         1.8          Fri
Mar  3 06:38:26 2006
   292   D102159SCVT037_ASW_VAL_TEST47.tcl         1.6          Thu
Feb 23 11:17:20 2006
   293   D102159SCVT038_DECONT_HEAT_MANAG.tcl      1.2          Wed
Feb 15 19:43:48 2006
   294   D102159SCVT039_EVENT_ACTION_MANAG.tcl     1.5          Thu
Nov  2 09:25:07 2006
   295   D102159SCVT040_ASW_VAL_TEST43.tcl         1.3          Sat
Feb 18 11:56:22 2006
   296   D102159SCVT040_FAILTC_READ_BOTH_MM.tcl    1.6          Wed
Dec 21 14:56:42 2005
   297   D102159SCVT041_ASW_VAL_TEST41.tcl         1.2          Tue
Feb 21 16:21:57 2006
   298   D102159SCVT042_ASW_VAL_TEST42.tcl         1.2          Tue
Feb 21 14:57:07 2006
   299   D102159SCVT043_MODE_MANAGEMENT.tcl        1.1          Tue
Feb 28 06:50:55 2006
   300   D102159SCVT044_ASW_VAL_TEST44.tcl         1.3          Mon
Feb 20 06:01:24 2006
   301   D102159SCVT045_LAUNCH_LAUNCH.tcl          1.10         Thu
Nov  2 09:57:22 2006
   302   D102159SCVT046_LAUNCH_SUNACQ.tcl          1.5          Tue
Nov  6 19:01:15 2007
   303   D102159SCVT047_SUNACQ_SUNACQ.tcl          1.4          Tue
May 20 01:35:42 2008
   304   D102159SCVT048_SUNACQ_NOMINAL.tcl         1.6          Tue
Nov  6 20:38:54 2007
   305   D102159SCVT049_NOMINAL_NOMINAL.tcl        1.2          Tue
May 20 01:35:19 2008
   306   D102159SCVT050_NOMINAL_EARTH.tcl          1.3          Tue
Nov  6 18:55:09 2007
   307   D102159SCVT051_EARTH_EARTH.tcl            1.2          Tue
May 20 01:33:19 2008
   308   D102159SCVT052_EARTH_NOMINAL.tcl          1.2          Tue
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Nov  6 20:55:26 2007
   309   D102159SCVT053_NOMINAL_SUNACQ.tcl          1.2          Tue
Nov  6 21:05:19 2007
   310   D102159SCVT054_SUNACQ_SURV.tcl             1.7          Mon
Nov  6 19:48:11 2006
   311   D102159SCVT055_SURV_SURV.tcl               1.1          Wed
Jun 27 19:30:44 2007
   312   D102159SCVT057_NOMINAL_SURV.tcl            1.1          Tue
Nov  6 21:15:00 2007
   313   D102159SCVT059_EARTH_SURV.tcl              1.1          Wed
Jun 27 19:31:47 2007
   314   D102159SCVT060_ASW_VAL_TEST45.tcl          1.7          Fri
Feb 17 16:32:37 2006
   315   D102159SCVT062_DLL2_ERRA_BUSB_UNHE.tcl     1.3          Fri
Feb 17 15:01:57 2006
   316   D102159SCVT067_NOMINAL_SURV_FDIR4.tcl      1.2          Mon
Jul 31 08:16:45 2006
   317   D102159SCVT070_CDMS_RECONF_ANALYSIS.tcl    1.2          Wed
Feb 22 10:30:51 2006
   318   D102159SCVT071_ONBOARD_SCHEDULING.tcl      1.2          Wed
Nov  8 20:58:34 2006
   319   D102159SCVT072_SHUT_DOWN_LCL_XPND1.tcl     1.3          Tue
Nov  6 20:59:45 2007
   320   D102159SCVT073_OBCP_MAST_INST_LOAD.tcl     1.3          Tue
Nov  6 20:48:50 2007
   321   D102159SCVT076_LOAD_SHORT_MTL.tcl          1.1          Fri
Jun 30 08:41:44 2006
   322   D102159SCVT080_CEL_DOWNLINK.tcl            1.5          Tue
Oct  9 08:29:34 2007
   323   D102159SCVT081DIS_EATTABLE_ENTRIES.tcl     1.1          Sat
Jul 29 13:58:13 2006
   324   D102159SCVT081_PAP4_PM_A_NOM.tcl           1.2          Tue
Apr 24 12:40:43 2007
   325   D102159SCVT082_PAP3_PM_A_NOM.tcl           1.2          Tue
Apr 24 12:42:00 2007
   326   D102159SCVT082_SVT0_6BK_PSTORE_DEF.tcl     1.2          Wed
Aug  2 16:34:09 2006
   327   D102159SCVT083_PAP4_PM_B_NOM.tcl           1.3          Tue
Apr 24 12:42:25 2007
   328   D102159SCVT083_SVT0_2BK_PSTORE_DEF.tcl     1.2          Wed
Aug  2 16:34:20 2006
   329   D102159SCVT084_PAP3_PM_B_NOM.tcl           1.4          Tue
Apr 24 12:42:56 2007
   330   D102159SCVT084_TTEC_CHAIN2_NOMINAL.tcl     1.1          Sat
Jul 29 14:46:45 2006
   331   D102159SCVT085_FDIR_LEVEL2_AIR.tcl         1.3          Thu
Nov  9 23:43:32 2006
   332   D102159SCVT085_PAP0_PM_A_NOM.tcl           1.1          Fri
Jun 29 13:01:37 2007
   333   D102159SCVT085_PAP0_PM_B_ONLY.tcl          1.2          Tue
Apr 24 12:43:29 2007
   334   D102159SCVT086_LAUNCH_LAUNCH_IST.tcl       1.4          Tue
May 22 07:04:59 2007
   335   D102159SCVT086_PAP5_PM_B_NOM.tcl           1.2          Tue
Apr 24 12:44:16 2007
   336   D102159SCVT087_PAP1_PM_B_ONLY.tcl          1.2          Tue
Apr 24 12:44:42 2007
   337   D102159SCVT088_PAP1_PM_A_ONLY.tcl          1.2          Tue
Apr 24 12:41:25 2007
   338   D102159SCVT089_PAP2_PM_B_NOM.tcl           1.1          Fri
Jun 29 13:12:02 2007
   339   D102159SCVT090_PAP0_PM_A_NOM.tcl           1.1          Fri
Jun 29 13:09:19 2007
   340   D102159SCVT095_ASW_VAL_TEST48.tcl          1.1          Wed
Jul  4 07:04:09 2007
   341   D102159SCVT100_CHKTM_UNITHEALTH.tcl        1.1          Fri
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May 12 14:53:16 2006
   342   D102159SCVT101_CDMS_RED_CONFIG.tcl       1.2          Fri
May 12 14:41:02 2006
   343   D102159SCVT103_CDMS_NOM_CONFIG.tcl       1.1          Fri
Mar 10 15:57:38 2006
   344   D102159SCVT104_ENCODER_SELECT.tcl        1.7          Thu
Jul  5 13:40:13 2007
   345   D102159SCVT105_IST_NOMLNCH_LNCH_SAM.tcl  1.3          Mon
Sep  3 09:08:05 2007
   346   D102159SCVT105_LAUNCH_SUNACQ.tcl         1.7          Thu
Aug 30 06:51:30 2007
   347   D102159SCVT107_PACKET_STORE_DEF.tcl      1.6          Fri
May 11 10:16:36 2007
   348   D102159SCVT108_SUNACQ_NOMINAL.tcl        1.7          Mon
May 14 16:11:25 2007
   349   D102159SCVT110_MTL_RASTER_POINTING.tcl   1.8          Tue
Nov 21 08:26:42 2006
   350   D102159SCVT111_MTL_LINE_SCANNING.tcl     1.6          Tue
Nov 21 08:29:40 2006
   351   D102159SCVT112_MTL_PACS_BURST_CHECK.tcl  1.3          Fri
Nov 17 15:56:53 2006
   352   D102159SCVT113_3A_NOMINAL_PMA.tcl        1.8          Mon
Nov 13 17:10:34 2006
   353   D102159SCVT114_3B_SAM_PMA.tcl            1.6          Mon
Nov 13 17:37:22 2006
   354   D102159SCVT115_CHECK_HCS_OFF.tcl         1.3          Sun
May 18 06:25:01 2008
   355   D102159SCVT116_IST_16_OBCP_LOADING.tcl   1.1          Tue
Sep 26 15:31:12 2006
   356   D102159SCVT117_IST_OBCP_FULL_LOAD.tcl    1.1          Tue
Sep 26 15:31:54 2006
   357   D102159SCVT118_FDIR4_EARTHSURV_PMB.tcl   1.6          Wed
Nov 22 10:52:32 2006
   358   D102159SCVT119_MTL_DUMMY.tcl             1.1          Mon
Oct 16 16:24:29 2006
   359   D102159SCVT120_TM_LINK_5_KBPS.tcl        1.1          Mon
Oct 16 16:22:27 2006
   360   D102159SCVT121_P_L_SIMULATION.tcl        1.3          Fri
Nov 10 14:50:21 2006
   361   D102159SCVT122_P_L_SIMULATION_OFF.tcl    1.2          Fri
Nov 10 14:50:36 2006
   362   D102159SCVT123_TM_LINK_150_KBPS.tcl      1.2          Tue
May 20 01:29:23 2008
   363   D102159SCVT124__IST_TM_LINK_150_KBPS.tcl 1.6          Sat
Nov 11 10:04:06 2006
   364   D102159SCVT125_IST_CDMS_REDCON_PMB.tcl   1.2          Sat
Nov 11 11:23:33 2006
   365   D102159SCVT126_LCL_OFF_BEF_SC_OFF.tcl    1.3          Wed
Feb 21 15:29:14 2007
   366   D102159SCVT127_FDIR4_NOMSURV_PMA_ONLY.tcl 1.7          Thu
Apr  5 09:45:21 2007
   367   D102159SCVT128_RESTORE_FROM_SURV.tcl     1.4          Wed
Jun 27 19:28:54 2007
   368   D102159SCVT129_LANCH_SACQ_FDIR_SEP.tcl   1.2          Fri
Nov 10 14:37:05 2006
   369   D102159SCVT130_3A_NOMINAL_PMB.tcl        1.3          Wed
Nov 22 10:54:00 2006
   370   D102159SCVT131_DLL2_CORE_BUSB.tcl        1.3          Wed
Nov 22 10:54:37 2006
   371   D102159SCVT132_FDIR4_SAMSURV_PMA.tcl     1.3          Wed
Nov 22 10:55:15 2006
   372   D102159SCVT133_1553_BUS_SWITCH_OVER.tcl  1.1          Fri
Nov 17 11:00:24 2006
   373   D102159SCVT134_PCDU_1553_BUS_FAIL_RECOV.tcl 1.2          Tue
Oct 30 08:00:27 2007
   374   D102159SCVT134_RECOVERY_SGM_FAILURE.tcl  1.5          Thu
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Dec 21 13:41:12 2006
   375   D102159SCVT135_AIR_DGN_DEFINITION.tcl     1.1          Wed
Nov 22 18:35:18 2006
   376   D102159SCVT135_TTC_RX_POWER_OOL.tcl       1.2          Tue
Nov  6 18:10:52 2007
   377   D102159SCVT135_TTC_XPND_INVALID_RT.tcl    1.2          Tue
Nov  6 18:45:58 2007
   378   D102159SCVT136_FUNC_FAIL_MODE_RFDN_SWITCH.tcl 1.4
Tue Nov  6 17:49:01 2007
   379   D102159SCVT136_VERIFY_PKT_VMC.tcl         1.3          Thu
Dec 21 13:42:29 2006
   380   D102159SCVT137_IST_SUNACQ_NOM.tcl         1.5          Tue
Nov 27 17:29:15 2007
   381   D102159SCVT138_IST_LAUNCH_SUNACQ.tcl      1.4          Fri
Nov  2 13:33:26 2007
   382   D102159SCVT150_DELIBERATE_ERRORS.tcl      1.7          Sat
Nov 17 06:56:57 2007
   383   D102159SCVT151_HER_IST_PKT_STR_DEF.tcl    1.1          Wed
Feb 21 07:36:55 2007
   384   D102159SCVT152_LAUNCH_SUNACQ_IST_FDIR.tcl 1.9          Tue
May 29 07:38:09 2007
   385   D102159SCVT153_MTL_FDIR.tcl               1.11         Mon
Sep  3 14:06:29 2007
   386   D102159SCVT154_FDIR_NOM_EARTH_3a.tcl      1.10         Thu
Sep 20 14:15:01 2007
   387   D102159SCVT155_SHORT_MTL_FDIR.tcl         1.2          Fri
Jun 15 07:55:04 2007
   388   D102159SCVT156_CDMS_ANALYSIS_FDIR_IST.tcl 1.3          Thu
May  3 07:52:50 2007
   389   D102159SCVT157_FDIR_EARTH_EARTH_3b.tcl    1.15         Thu
Oct  4 12:30:57 2007
   390   D102159SCVT158_FDIR_NOMINAL_SUNACQ.tcl    1.8          Wed
Sep 26 11:27:58 2007
   391   D102159SCVT159_FDIR_CHECK_SUNACQMODE.tcl  1.4          Wed
Sep 26 11:32:44 2007
   392   D102159SCVT160_FDIR_NOM_SURV_DOD.tcl      1.5          Tue
Oct  9 11:57:20 2007
   393   D102159SCVT161_IST_MM_NOM_NOM.tcl         1.14         Fri
Sep 14 12:03:34 2007
   394   D102159SCVT162_IST_MM_SAM_SAM.tcl         1.10         Sat
Nov 10 16:31:44 2007
   395   D102159SCVT163_IST_MM_LAN_SAM.tcl         1.9          Sat
Nov 10 08:21:03 2007
   396   D102159SCVT164_IST_MM_SAM_NOM.tcl         1.9          Sat
Nov 10 09:00:57 2007
   397   D102159SCVT165_RECOVERY_PCDUA_FAST.tcl    1.3          Sat
Oct 27 11:17:45 2007
   398   D102159SCVT166_SHORT_MTL_FDIR_MM.tcl      1.6          Thu
Jun 14 08:58:15 2007
   399   D102159SCVT167_IST_MM_NOM_EAM.tcl         1.8          Fri
Jun  1 13:42:08 2007
   400   D102159SCVT168_IST_MM_EAM_EAM.tcl         1.3          Fri
Apr 20 13:12:27 2007
   401   D102159SCVT169_IST_MM_EAM_NOM.tcl         1.6          Thu
May 31 16:05:56 2007
   402   D102159SCVT170_IST_MM_NOM_SM.tcl          1.6          Fri
Jun  1 08:51:09 2007
   403   D102159SCVT171_IST_MM_SM_SM.tcl           1.5          Fri
Jun  1 08:50:39 2007
   404   D102159SCVT172_IST_MM_SM_SAM.tcl          1.3          Fri
Jun  1 10:44:00 2007
   405   D102159SCVT173_IST_MM_EAM_SAM.tcl         1.4          Fri
Jun  1 13:46:56 2007
   406   D102159SCVT174_IST_REDUNDANT_CONF.tcl     1.19         Fri
Nov  2 08:02:00 2007
   407   D102159SCVT175_SET_SURV_REG.tcl           1.16         Sat
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Nov 17 16:39:19 2007
   408 ! D102159SCVT176_WRITE_CROME.tcl           1.23          Sat
Dec  1 05:59:51 2007
   409   D102159SCVT177_IST_MM_NOM_SAM.tcl        1.3           Fri
Jun  1 14:09:55 2007
   410   D102159SCVT178_RMS_PKT_STORE_DEF.tcl     1.7           Fri
Nov 30 17:04:24 2007
   411   D102159SCVT179_DBG_PKT_STORE_DEF.tcl     1.11          Fri
Nov 30 17:04:38 2007
   412   D102159SCVT180_DUMP_PKT_STORE.tcl        1.2           Thu
May 31 16:38:41 2007
   413   D102159SCVT181_DISABLE_PKT_STORE.tcl     1.3           Thu
May 31 16:26:37 2007
   414   D102159SCVT182_DUMP_PKT_STORE_RMS_DTCP.tcl 1.3          Fri
Aug  3 18:25:46 2007
   415   D102159SCVT183_CEL_DOWNLINK_RMS_DTCP.tcl 1.2           Tue
May 20 13:25:00 2008
   416   D102159SCVT184_SWITCH_TO_PCDUB_BUS_B.tcl 1.4           Thu
Jul 19 12:36:12 2007
   417   D102159SCVT185_IST_PKT_STORE_DEF.tcl     1.13          Tue
Nov 27 08:48:02 2007
   418   D102159SCVT186_IST_SSMM_ON.tcl           1.21          Sat
Oct 27 10:58:02 2007
   419   D102159SCVT187_IST_SSMM_OFF.tcl          1.8           Fri
Nov 30 17:03:49 2007
   420   D102159SCVT188_IST_DUMP_PKT_STORE.tcl    1.10          Wed
Nov 21 15:43:44 2007
   421   D102159SCVT189_IST_PKT_STORE_DEF_2.tcl   1.10          Fri
Nov 30 17:03:34 2007
   422   D102159SCVT190_IST_WCS_PKTSTORE_DUMP_BREAK.tcl 1.3
Wed Nov  7 08:31:31 2007
   423   D102159SCVT191_TM_5_KBPS_IST.tcl          1.2           Thu
Jul 12 16:31:57 2007
   424   D102159SCVT192_GET_EAT_REPORT.tcl         1.7           Fri
Nov 30 17:04:01 2007
   425   D102159SCVT192_IST_UPLOAD_EAT.tcl         1.11          Mon
Nov 19 14:02:48 2007
   426   D102159SCVT193_IST_UPLOAD_OBCP.tcl        1.12          Sat
Nov 10 09:10:11 2007
   427   D102159SCVT196_IST_ONBOARD_SCHEDULING.tcl 1.6           Mon
Nov  5 15:01:57 2007
   428   D102159SCVT197_IST_OBCP_MANAGM.tcl        1.5           Wed
Oct 31 11:21:46 2007
   429   D102159SCVT198_IST_SSMM_MANAGM.tcl        1.12          Wed
Oct 31 09:46:29 2007
   430   D102159SCVT199_IST_OBT_MANAGM.tcl         1.8           Tue
Oct  9 11:56:09 2007
   431   D102159SCVT200_IST_CDMS_MANAGM_FDIR.tcl   1.10          Thu
Oct 25 13:12:43 2007
   432   D102159SCVT201_IST_SAT_COM_CDMS.tcl       1.7           Wed
Nov 28 20:25:56 2007
   433   D102159SCVT202_IST_MTL_PING_TEST.tcl      1.5           Wed
Nov 21 14:38:14 2007
   434   D102159SCVT203_IST_MTL_ReportCat.tcl      1.1           Thu
Oct 11 14:03:23 2007
   435   D102159SCVT204_GET_MOT.tcl                1.3           Tue
Sep 11 14:32:01 2007
   436   D102159SCVT205_SAT_COM_TCT.tcl            1.2           Mon
Nov 26 19:59:13 2007
   437   D102159SCVT206_IST_SCIENCE_DOWNLNK.tcl    1.6           Mon
Nov  5 14:49:30 2007
   438   D102159SCVT207_SAT_COM_FCCT.tcl           1.2           Fri
Sep 14 13:52:17 2007
   439 ! D102159SCVT209_START_ON_BOARD_SCHEDULE.tcl 1.8           Sat
Dec  1 07:18:17 2007
   440   D102159SCVT210_GET_ALARM_STATUS.tcl       1.2           Mon
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Nov 19 08:33:37 2007
   441   D102159SCVT211_IST_INSTR_MTL_PING.tcl     1.2          Wed
Nov  7 16:38:50 2007
   442   D102159SCVT212_EMC_LAUNCH_SUNACQ.tcl      1.2          Wed
Nov 28 13:19:04 2007
   443   D102159SCVT213_DUMP_MEM_AREAS.tcl         1.6          Wed
Nov 28 20:29:30 2007
   444   D102159SCVT214_IST_HIFI_MTL_PING.tcl      1.1          Mon
Nov 26 10:46:34 2007
   445   D102159SCVT991PM_SELECT_IM2.tcl           1.1          Thu
Oct 25 12:30:00 2007
   446   D102159SCVT993DISTHERMALCONTROL.tcl       1.1          Thu
Oct 25 12:30:38 2007
   447   D102159SCVT997PM_RESET_IM2.tcl            1.1          Thu
Oct 25 12:31:08 2007
   448   D102159SCVT998_CDMSINTHKCHECK.tcl         1.1          Thu
Oct 25 12:31:36 2007
   449   D102159SCVT999_INITCOMPL_BOOTEVENT.tcl    1.1          Thu
Oct 25 12:32:07 2007
   450   D102159SPVT001_UFT_TEST.tcl               1.3          Fri
Aug 25 10:51:57 2006
   451   D102159SPVT002_UFT_PMB_TEST.tcl           1.3          Fri
Aug 25 10:52:24 2006
   452   D201159SCVT029_ON_BOARD_STOR_RETR2.tcl    1.2          Mon
Nov  7 16:50:51 2005
   453   DisableRelTCs_RMS.tcl                     1.1          Fri
Aug  3 14:24:25 2007
   454   ENTER_SAFE_Mode_Shell.tcl                 1.1          Thu
Oct 18 15:48:10 2007
   455   H101999SCVT001_ASDDBGHIFI_PWR_ON_P.tcl    1.2          Tue
Aug 21 15:30:23 2007
   456   H101999SCVT002_ASDDBGHIFI_PWR_OFF_P.tcl   1.2          Tue
Aug 21 15:31:06 2007
   457   H102999SCVT001_ASDDBGHIFI_PWR_ON_P.tcl    1.16         Thu
Nov 15 10:41:35 2007
   458   H102999SCVT002_ASDDBGHIFI_PWR_OFF_P.tcl   1.11         Thu
Nov 15 10:46:34 2007
   459   H102999SCVT003_ASDDBGHIFI_PWR_ON_R.tcl    1.9          Thu
Nov 15 10:47:14 2007
   460   H102999SCVT004_ASDDBGHIFI_PWR_OFF_R.tcl   1.7          Thu
Nov 15 10:47:50 2007
   461   H102999SCVT005_ASDGENHIFI_PWR_ON_P.tcl    1.10         Wed
Nov 28 17:27:02 2007
   462   H102999SCVT006_ASDGENHIFI_PWR_OFF_P.tcl   1.4          Tue
Nov  6 10:50:47 2007
   463   H102999SCVT007_ASDGENHIFI_PWR_ON_R.tcl    1.9          Wed
Nov 28 06:47:58 2007
   464   H102999SCVT008_ASDGENHIFI_PWR_OFF_R.tcl   1.4          Tue
Nov  6 10:51:13 2007
   465   HIFIENG_FT_WBS_comb.tcl                   1.1          Mon
Aug 27 07:11:32 2007
   466   HIFIENG_WBS_tune.tcl                      1.1          Mon
Aug 27 07:17:39 2007
   467   HIFIENG_WBS_zero.tcl                      1.1          Mon
Aug 27 07:17:59 2007
   468   HIFIENG_config_spectro.tcl                1.1          Mon
Aug 27 07:01:13 2007
   469   HIFIENG_take_tp_spectra.tcl               1.1          Mon
Aug 27 07:16:39 2007
   470   HIFIENG_tp_spectra_only.tcl               1.1          Mon
Aug 27 07:17:13 2007
   471   HIFIST_ASED_PatchPtvChecksum.tcl          1.2          Thu
Oct 11 08:16:56 2007
   472   HIFIST_ASED_PatchTempLimits.tcl           1.3          Wed
Nov 28 13:59:38 2007
   473   HIFIST_CCS_conf_ptv_checksum.tcl          1.5          Thu
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Oct 11 16:29:50 2007
   474   HIFIST_HRScable_HRS_conf.tcl            1.1          Fri
Sep 28 09:28:40 2007
   475   HIFIST_HRScable_HRS_off.tcl             1.1          Fri
Sep 28 08:54:21 2007
   476   HIFIST_HRScable_HRS_startup.tcl         1.1          Fri
Sep 28 08:54:44 2007
   477   HIFIST_HRScable_HRS_tune.tcl            1.1          Fri
Sep 28 08:55:07 2007
   478   HIFIST_SFT_CSA.tcl                      1.4          Thu
Oct 11 16:28:29 2007
   479   HIFIST_SFT_Chopper_warm_1.tcl           1.6          Thu
Oct 11 16:28:29 2007
   480   HIFIST_SFT_FCU_INIT_1.tcl               1.6          Thu
Oct 11 16:28:29 2007
   481   HIFIST_SFT_FCU_INIT_2.tcl               1.6          Thu
Oct 11 16:28:29 2007
   482   HIFIST_SFT_FCU_INIT_3.tcl               1.6          Thu
Oct 11 16:28:29 2007
   483   HIFIST_SFT_FCU_INIT_4.tcl               1.6          Thu
Oct 11 16:28:29 2007
   484   HIFIST_SFT_FCU_INIT_5.tcl               1.6          Thu
Oct 11 16:28:29 2007
   485   HIFIST_SFT_FCU_INIT_6.tcl               1.6          Thu
Oct 11 16:28:29 2007
   486   HIFIST_SFT_FCU_INIT_7.tcl               1.6          Thu
Oct 11 16:28:29 2007
   487   HIFIST_SFT_FCU_deflux_1.tcl             1.6          Thu
Oct 11 16:28:29 2007
   488   HIFIST_SFT_FCU_deflux_2.tcl             1.6          Thu
Oct 11 16:28:29 2007
   489   HIFIST_SFT_FCU_deflux_3.tcl             1.6          Thu
Oct 11 16:28:29 2007
   490   HIFIST_SFT_FCU_deflux_4.tcl             1.6          Thu
Oct 11 16:28:29 2007
   491   HIFIST_SFT_FCU_deflux_5.tcl             1.6          Thu
Oct 11 16:28:29 2007
   492   HIFIST_SFT_FT_WBS_Laser1.tcl            1.6          Thu
Oct 11 16:28:29 2007
   493   HIFIST_SFT_FT_WBS_Laser2.tcl            1.6          Thu
Oct 11 16:28:29 2007
   494   HIFIST_SFT_HBB_test_warm.tcl            1.1          Fri
Aug 24 11:09:47 2007
   495   HIFIST_SFT_HRS_FT_1.tcl                 1.6          Thu
Oct 11 16:28:29 2007
   496   HIFIST_SFT_HRS_FT_2_Corr.tcl            1.6          Thu
Oct 11 16:28:29 2007
   497   HIFIST_SFT_HRS_FT_2_Sine.tcl            1.6          Thu
Oct 11 16:28:29 2007
   498   HIFIST_SFT_HRS_FT_2_Square_m.tcl        1.6          Thu
Oct 11 16:28:29 2007
   499   HIFIST_SFT_HRS_FT_2_Square_s.tcl        1.6          Thu
Oct 11 16:28:29 2007
   500   HIFIST_SFT_HRS_FT_4.tcl                 1.6          Thu
Oct 11 16:28:29 2007
   501   HIFIST_SFT_HRS_tune.tcl                 1.6          Thu
Oct 11 16:28:29 2007
   502   HIFIST_SFT_LCU_IV_1a.tcl                1.6          Thu
Oct 11 16:28:30 2007
   503   HIFIST_SFT_LCU_IV_1b.tcl                1.6          Thu
Oct 11 16:28:30 2007
   504   HIFIST_SFT_LCU_IV_2a.tcl                1.6          Thu
Oct 11 16:28:30 2007
   505   HIFIST_SFT_LCU_IV_2b.tcl                1.6          Thu
Oct 11 16:28:30 2007
   506   HIFIST_SFT_LCU_IV_3a.tcl                1.6          Thu
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Oct 11 16:28:30 2007
   507   HIFIST_SFT_LCU_IV_3b.tcl                1.6          Thu
Oct 11 16:28:30 2007
   508   HIFIST_SFT_LCU_IV_4a.tcl                1.6          Thu
Oct 11 16:28:30 2007
   509   HIFIST_SFT_LCU_IV_4b.tcl                1.6          Thu
Oct 11 16:28:30 2007
   510   HIFIST_SFT_LCU_IV_5a.tcl                1.6          Thu
Oct 11 16:28:30 2007
   511   HIFIST_SFT_LCU_IV_5b.tcl                1.6          Thu
Oct 11 16:28:30 2007
   512   HIFIST_SFT_LCU_IV_6a.tcl                1.6          Thu
Oct 11 16:28:30 2007
   513   HIFIST_SFT_LCU_IV_6b.tcl                1.6          Thu
Oct 11 16:28:30 2007
   514   HIFIST_SFT_LCU_IV_7a.tcl                1.6          Thu
Oct 11 16:28:30 2007
   515   HIFIST_SFT_LCU_IV_7b.tcl                1.6          Thu
Oct 11 16:28:30 2007
   516   HIFIST_SFT_LCU_switch_off.tcl           1.6          Thu
Oct 11 16:28:30 2007
   517   HIFIST_SFT_LO_SFT1a.tcl                 1.6          Thu
Oct 11 16:28:30 2007
   518   HIFIST_SFT_LO_SFT1b.tcl                 1.6          Thu
Oct 11 16:28:30 2007
   519   HIFIST_SFT_LO_SFT2a.tcl                 1.6          Thu
Oct 11 16:28:30 2007
   520   HIFIST_SFT_LO_SFT2b.tcl                 1.6          Thu
Oct 11 16:28:30 2007
   521   HIFIST_SFT_LO_SFT3a.tcl                 1.6          Thu
Oct 11 16:28:30 2007
   522   HIFIST_SFT_LO_SFT3b.tcl                 1.6          Thu
Oct 11 16:28:30 2007
   523   HIFIST_SFT_LO_SFT4a.tcl                 1.6          Thu
Oct 11 16:28:30 2007
   524   HIFIST_SFT_LO_SFT4b.tcl                 1.6          Thu
Oct 11 16:28:30 2007
   525   HIFIST_SFT_LO_SFT5a.tcl                 1.6          Thu
Oct 11 16:28:30 2007
   526   HIFIST_SFT_LO_SFT5b.tcl                 1.6          Thu
Oct 11 16:28:30 2007
   527   HIFIST_SFT_LO_SFT6a.tcl                 1.6          Thu
Oct 11 16:28:30 2007
   528   HIFIST_SFT_LO_SFT6b.tcl                 1.6          Thu
Oct 11 16:28:30 2007
   529   HIFIST_SFT_LO_SFT7a.tcl                 1.6          Thu
Oct 11 16:28:30 2007
   530   HIFIST_SFT_LO_SFT7b.tcl                 1.6          Thu
Oct 11 16:28:30 2007
   531   HIFIST_SFT_LO_heater_0.tcl              1.3          Thu
Oct 11 16:28:30 2007
   532   HIFIST_SFT_LO_heater_1.tcl              1.3          Thu
Oct 11 16:28:30 2007
   533   HIFIST_SFT_LO_heater_2.tcl              1.3          Thu
Oct 11 16:28:30 2007
   534   HIFIST_SFT_LO_heater_3.tcl              1.3          Thu
Oct 11 16:28:30 2007
   535   HIFIST_SFT_LO_heater_4.tcl              1.3          Thu
Oct 11 16:28:30 2007
   536   HIFIST_SFT_LO_heater_5.tcl              1.3          Thu
Oct 11 16:28:30 2007
   537   HIFIST_SFT_LO_heater_6.tcl              1.3          Thu
Oct 11 16:28:30 2007
   538   HIFIST_SFT_Nominal_offcold.tcl          1.1          Wed
Sep 19 09:39:46 2007
   539   HIFIST_SFT_Nominal_offwarm.tcl          1.6          Thu
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Oct 11 16:28:30 2007
   540   HIFIST_SFT_Upconv_spectra.tcl          1.1          Thu
Oct 11 16:23:30 2007
   541   HIFIST_SFT_WBS_Laser1.tcl              1.6          Thu
Oct 11 16:28:30 2007
   542   HIFIST_SFT_WBS_Laser2.tcl              1.6          Thu
Oct 11 16:28:30 2007
   543   HIFIST_SFT_WBS_Lasers_off.tcl          1.6          Thu
Oct 11 16:28:30 2007
   544   HIFIST_SFT_WBS_comb.tcl                1.4          Fri
Aug 24 11:05:03 2007
   545   HIFIST_SFT_WBS_latchup_high.tcl        1.6          Thu
Oct 11 16:28:30 2007
   546   HIFIST_SFT_WBS_latchup_low.tcl         1.6          Thu
Oct 11 16:28:30 2007
   547   HIFIST_SFT_WBS_zero.tcl                1.6          Thu
Oct 11 16:28:31 2007
   548   HIFIST_SFT_Wait5.tcl                   1.1          Wed
Sep 19 09:40:03 2007
   549   HIFIST_SFT_chopper_cold_0.tcl          1.1          Fri
Aug 24 11:08:23 2007
   550   HIFIST_SFT_chopper_cold_1.tcl          1.1          Fri
Aug 24 11:09:07 2007
   551   HIFIST_SFT_chopper_openloop_scan.tcl   1.1          Thu
Oct 11 16:23:03 2007
   552   HIFIST_SFT_chopper_warm_0.tcl          1.6          Thu
Oct 11 16:28:31 2007
   553   HIFIST_Startup_FCU_on.tcl              1.6          Fri
Oct 12 06:27:50 2007
   554   HIFIST_Startup_FPU_standby.tcl         1.6          Thu
Oct 11 16:28:48 2007
   555   HIFIST_Startup_HRS_on.tcl              1.6          Fri
Oct 12 06:27:50 2007
   556   HIFIST_Startup_HRS_standby.tcl         1.6          Thu
Oct 11 16:28:48 2007
   557   HIFIST_Startup_LCU_on.tcl              1.6          Fri
Oct 12 06:27:50 2007
   558   HIFIST_Startup_LCU_standby.tcl         1.6          Thu
Oct 11 16:28:48 2007
   559   HIFIST_Startup_LCU_table_load.tcl      1.8          Fri
Oct 12 06:27:50 2007
   560   HIFIST_Startup_LCU_table_read.tcl      1.7          Fri
Oct 12 06:27:50 2007
   561   HIFIST_Startup_LO_Nominal.tcl          1.6          Fri
Oct 12 06:27:51 2007
   562   HIFIST_Startup_OBS_SFT.tcl             1.6          Fri
Oct 12 06:27:51 2007
   563   HIFIST_Startup_WBSH_on.tcl             1.6          Fri
Oct 12 06:27:51 2007
   564   HIFIST_Startup_WBSV_on.tcl             1.6          Fri
Oct 12 06:27:51 2007
   565   HIFIST_Startup_WBS_lasertemp40.tcl     1.2          Fri
Oct 12 06:27:51 2007
   566   HIFIST_Startup_WBS_standby.tcl         1.6          Thu
Oct 11 16:28:48 2007
   567   HIFIST_Startup_all_off.tcl             1.6          Thu
Oct 11 16:28:48 2007
   568   HIFIST_Startup_chopper_set0.tcl        1.2          Fri
Oct 12 06:27:51 2007
   569   HIFIST_Startup_chopper_set1.tcl        1.2          Fri
Oct 12 06:27:51 2007
   570   HIFIST_Startup_force_boot.tcl          1.6          Fri
Oct 12 06:27:51 2007
   571   HP_OBSM_ALS.tcl                        1.1          Wed
Feb 14 14:02:18 2007
   572   INST_something1.tcl                    1.1          Mon
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Oct 29 10:36:35 2007
   573   INST_something2.tcl                          1.1          Mon
Oct 29 10:37:08 2007
   574   INST_something3.tcl                          1.1          Mon
Oct 29 10:37:30 2007
   575   K102999ECVT001_ASDGENCCUA_ChkEssTM.tcl       1.12         Thu
Mar 22 06:34:45 2007
   576   K102999ECVT001_ASDGENCCUA_DLCMPar.tcl        1.2          Wed
Apr 18 08:33:33 2007
   577   K102999ECVT001_ASDGENCCUA_DL_Close.tcl       1.2          Thu
Mar 22 10:41:24 2007
   578   K102999ECVT001_ASDGENCCUA_MnDisMd1.tcl       1.4          Thu
Mar 22 06:34:31 2007
   579   K102999ECVT001_ASDGENCCUA_MnDisMd2.tcl       1.4          Thu
Mar 22 06:34:16 2007
   580   K102999ECVT001_ASDGENCCUA_MnEnaMd1.tcl       1.4          Thu
Mar 22 06:34:02 2007
   581   K102999ECVT001_ASDGENCCUA_MnEnaMd2.tcl       1.4          Thu
Mar 22 06:33:48 2007
   582   K102999ECVT001_ASDGENCCUA_POWEROFF.tcl       1.11         Tue
May 22 14:54:07 2007
   583   K102999ECVT001_ASDGENCCUA_POWERON.tcl        1.19         Fri
Nov  2 13:02:48 2007
   584   K102999ECVT001_ASDGENCCUB_ChkEssTM.tcl       1.9          Thu
Mar 22 06:33:11 2007
   585   K102999ECVT001_ASDGENCCUB_MnDisMd1.tcl       1.4          Thu
Mar 22 06:32:57 2007
   586   K102999ECVT001_ASDGENCCUB_MnDisMd2.tcl       1.5          Thu
Mar 22 06:32:41 2007
   587   K102999ECVT001_ASDGENCCUB_MnEnaMd1.tcl       1.4          Thu
Mar 22 06:32:23 2007
   588   K102999ECVT001_ASDGENCCUB_MnEnaMd2.tcl       1.4          Thu
Mar 22 06:31:54 2007
   589   K102999ECVT001_ASDGENCCUB_POWEROFF.tcl       1.10         Tue
May 22 14:55:19 2007
   590   K102999ECVT001_ASDGENCCUB_POWERON.tcl        1.16         Fri
Nov  2 13:04:16 2007
   591   K102999ECVT001_ASDGENCCU_ABPWROFF.tcl        1.4          Thu
Mar 22 06:36:00 2007
   592   K102999ECVT001_ASDGENCCU_ABPWRON.tcl         1.9          Sat
Nov  3 09:57:19 2007
   593   K102999ECVT001_ASDGENCCU_MnDBOTH1.tcl        1.4          Thu
Mar 22 06:30:24 2007
   594   K102999ECVT001_ASDGENCCU_MnDBOTH2.tcl        1.5          Thu
Mar 22 06:35:35 2007
   595   K102999ECVT001_ASDGENCCU_MnDisDLC.tcl        1.5          Thu
Mar 22 06:35:23 2007
   596   K102999ECVT001_ASDGENCCU_MnEBOTH1.tcl        1.4          Thu
Mar 22 06:35:11 2007
   597   K102999ECVT001_ASDGENCCU_MnEBOTH2.tcl        1.5          Thu
Mar 22 06:34:57 2007
   598   K102999ECVT002_ASDGEN_CCU_GUI_VLV.tcl        1.3          Sat
Sep 29 08:40:20 2007
   599   K102999ECVT024_ASDGENCCUB_DL_Close.tcl       1.2          Thu
Mar 22 10:41:39 2007
   600   K102999ECVT025_ASDGENCCU_ABCloseDL.tcl       1.2          Thu
Mar 22 10:41:05 2007
   601   K102999ECVT026_ASDGENCCU_DL2Close.tcl        1.2          Wed
Apr 18 08:33:44 2007
   602   K102999ECVT027_ASDISTCCU_AR5DLOPENX.tcl      1.1          Wed
Apr 18 15:34:00 2007
   603   K102999ECVT028_ASTISTCCU_EmgDLCls.tcl        1.2          Wed
Apr 18 15:24:25 2007
   604   K102999ECVT029_ASDGENCCUA_MnENaDLCM.tcl      1.2          Wed
Aug 15 07:50:05 2007
   605   K102999ECVT030_ASDGENCCUB_MnENaDLCM.tcl      1.2          Wed
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Aug 15 07:50:36 2007
   606   K102999ECVT031_ASDGEN_CCU_LOG.tcl        1.2          Tue
Nov 20 15:39:13 2007
   607   LCU_PTV_patch_dummy_1.3.tcl              1.3          Tue
Aug 28 07:59:01 2007
   608   LPS_SCOE_checks.tcl                      1.1          Wed
Jul 20 10:35:13 2005
   609   Llock_Close_Shell.tcl                    1.1          Thu
Oct 18 15:48:10 2007
   610   Llock_Open_Shell.tcl                     1.1          Thu
Oct 18 15:48:10 2007
   611   MTL_PACS_restart_cata_RMS48h.tcl         1.1          Mon
May 19 11:21:26 2008
   612   MTL_Tool.tcl                             1.5          Thu
Oct 11 14:17:30 2007
   613   MTL_tclgen15b_H_IST0_MTL_D079_080_20070504_v01.tcl 1.1
Sun May 18 12:06:39 2008
   614   MTL_tclgen15b_H_IST0_MTL_D080_081_20070504_v01.tcl 1.1
Sun May 18 12:06:20 2008
   615   MTL_tclgen15b_H_IST0_MTL_D080_081_20070504_v01_1600_no1.tcl
1.1          Sun May 18 12:07:42 2008
   616   MTL_tclgen15b_H_IST0_MTL_D080_081_20070504_v01_1600_no2.tcl
1.1          Sun May 18 12:08:01 2008
   617   MTL_tclgen15b_H_IST0_MTL_D080_081_20070504_v01_1600_no3.tcl
1.1          Sun May 18 12:08:27 2008
   618   MTL_tclgen15b_H_IST0_MTL_D080_081_20070504_v01_1600_no4.tcl
1.1          Sun May 18 12:08:38 2008
   619   MTL_tclgen15b_H_IST0_MTL_D080_081_20070504_v01_1600_no5.tcl
1.1          Sun May 18 12:08:53 2008
   620   MTL_tclgen15b_H_IST0_MTL_D081_082_20070504_v01.tcl 1.1
Sun May 18 12:09:12 2008
   621   MTL_tclgen15b_H_IST0_MTL_D081_082_20070504_v01_curtailed.tcl
1.1          Wed May 23 17:20:35 2007
   622   MTL_tclgen24_H_IST0_MTL_D079_080_20070424_v02.tcl 1.1
Mon May  7 12:39:26 2007
   623   MTL_tclgen24_H_IST0_MTL_D079_080_20070424_v02_2007_127.tcl
1.1          Mon May  7 12:39:38 2007
   624   MTL_tclgen24_H_IST0_MTL_D080_081_20070424_v02.tcl 1.1
Mon May  7 12:39:54 2007
   625   MTL_tclgen24b_H_IST0_MTL_D080_081_20070424_v02_seg01_2007_
127.tcl 1.1          Mon May  7 12:40:07 2007
   626   MTL_tclgen24b_H_IST0_MTL_D080_081_20070424_v02_seg02_2007_
127.tcl 1.1          Mon May  7 12:40:19 2007
   627   MTL_tclgen24b_H_IST0_MTL_D080_081_20070424_v02_seg03_2007_
127.tcl 1.1          Mon May  7 12:40:29 2007
   628   MTL_tclgen24b_H_IST0_MTL_D080_081_20070424_v02_seg04_2007_
127.tcl 1.1          Mon May  7 12:40:41 2007
   629   MTL_tclgen24b_H_IST0_MTL_D080_081_20070424_v02_seg05_2007_
127.tcl 1.1          Mon May  7 12:40:51 2007
   630   MTL_tclgen24b_H_IST0_MTL_D080_081_20070424_v02_seg06_2007_
127.tcl 1.1          Mon May  7 12:41:04 2007
   631   MTL_tclgen24b_H_IST0_MTL_D080_081_20070424_v02_seg07_2007_
127.tcl 1.1          Mon May  7 12:41:17 2007
   632   MTL_tclgen24b_H_IST0_MTL_D080_081_20070504_v01
_cut_for_PACS_OD_debug.tcl 1.1          Wed May 23 17:21:57 2007
   633   MTL_tclgen24b_H_IST0_MTL_D081_082_20070504_v01
_AHHDA002cr.tcl 1.1          Sun May 18 10:50:48 2008
   634   MTL_tclgen24b_H_IST0_MTL_D081_082_20070504_v01
_cut_for_SPIREphot_OD_debug.tcl 1.1          Wed May 23 17:22:54 2007
   635   MTL_tclgen24b_H_IST0_MTL_D082_083_20070504_v01_curtailed.tcl
1.1          Wed May 23 17:23:52 2007
   636   MTL_tclgen24b_H_IST0_MTL_D082_083_20070504_v01_no1.tcl 1.1
Wed May 23 17:24:50 2007
   637   MTL_tclgen24b_H_IST0_MTL_D082_083_20070504_v01_no2.tcl 1.1
Wed May 23 17:26:06 2007
   638   MTL_tclgen31_H_IST0_MTL_D079_080_20070504_v01_48h_M2_M2B.tcl
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1.1          Sun May 18 07:09:52 2008
   639   MTL_tclgen31_H_IST0_MTL_D080_081_20070504_v01_48h_M1_M2
_M2B_M3_M6.tcl 1.1          Sun May 18 07:11:12 2008
   640   MTL_tclgen31_H_IST0_MTL_D080_081_20070504_v01_48h_M1_M2
_M2B_M3_M6_SEG000_1500_cnt.tcl 1.1          Sun May 18 07:58:47 2008
   641   MTL_tclgen31_H_IST0_MTL_D080_081_20070504_v01_48h_M1_M2
_M2B_M3_M6_SEG001_1500_cnt.tcl 1.1          Sun May 18 07:59:59 2008
   642   MTL_tclgen31_H_IST0_MTL_D080_081_20070504_v01_48h_M1_M2
_M2B_M3_M6_SEG002_1500_cnt.tcl 1.1          Sun May 18 08:01:11 2008
   643   MTL_tclgen31_H_IST0_MTL_D080_081_20070504_v01_48h_M1_M2
_M2B_M3_M6_SEG003_1500_cnt.tcl 1.1          Sun May 18 08:02:27 2008
   644   MTL_tclgen31_H_IST0_MTL_D080_081_20070504_v01_48h_M1_M2
_M2B_M3_M6_SEG004_1500_end.tcl 1.1          Sun May 18 08:03:42 2008
   645   MTL_tclgen31_H_IST0_MTL_D081_082_20070504_v01_48h_M2_M2B.tcl
1.1          Sun May 18 07:34:27 2008
   646   MTL_tclgen31_H_IST0_MTL_D081_082_20070504_v01_48h_M2
_M2B_SEG000_900_cnt.tcl 1.1          Sun May 18 08:04:47 2008
   647   MTL_tclgen31_H_IST0_MTL_D081_082_20070504_v01_48h_M2
_M2B_SEG001_900_end.tcl 1.1          Sun May 18 08:06:00 2008
   648   MTL_tclgen31_H_IST0_MTL_D081_082_20070504_v01_
48h_patch_after_PACS_OFF_NCR.tcl 1.1          Mon May 19 08:31:40
2008
   649   MTL_tclgen31_H_IST0_MTL_D081_082_20070504_v01_
48h_patch_after_run_time_NCR.tcl 1.1          Mon May 19 05:30:59
2008
   650   MTL_tclgen31_H_IST0_MTL_D082_083_20070504_v01_48h_M4_M5
_M7.tcl 1.1          Sun May 18 07:38:03 2008
   651   MTL_tclgen31_H_IST0_MTL_D082_083_20070504_v01_48h_M4_M5_M7
_SEG000_1200_cnt.tcl 1.1          Sun May 18 08:07:15 2008
   652   MTL_tclgen31_H_IST0_MTL_D082_083_20070504_v01_48h_M4_M5_M7
_SEG000_1200_unbugged_cnt.tcl 1.1          Mon May 19 12:48:10 2008
   653   MTL_tclgen31_H_IST0_MTL_D082_083_20070504_v01_48h_M4_M5_M7
_SEG001_1200_end.tcl 1.1          Sun May 18 08:08:18 2008
   654   MTL_tclgen31_H_IST0_MTL_Dummy_D083_084_20070504_v01_48h_M1
_M2_M3.tcl 1.1          Sun May 18 07:40:44 2008
   655   MTL_tclgen31_H_IST0_MTL_Dummy_D083_084_20070504_v01_48h_M1
_M2_M3_SEG000_1500_cnt.tcl 1.1          Sun May 18 08:09:43 2008
   656   MTL_tclgen31_H_IST0_MTL_Dummy_D083_084_20070504_v01_48h_M1
_M2_M3_SEG001_1500_cnt.tcl 1.1          Sun May 18 08:10:52 2008
   657   MTL_tclgen31_H_IST0_MTL_Dummy_D083_084_20070504_v01_48h_M1
_M2_M3_SEG002_1500_cnt.tcl 1.1          Sun May 18 08:12:03 2008
   658   MTL_tclgen31_H_IST0_MTL_Dummy_D083_084_20070504_v01_48h_M1
_M2_M3_SEG003_1500_cnt.tcl 1.1          Sun May 18 08:13:17 2008
   659   MTL_tclgen31_H_IST0_MTL_Dummy_D083_084_20070504_v01_48h_M1
_M2_M3_SEG004_1500_end.tcl 1.1          Sun May 18 08:14:29 2008
   660   OBCP_chop_scan_phot_OBS_Shell.tcl        1.1          Thu
Oct 18 15:48:10 2007
   661   P102999SCVT901_ASDDBGPACS_PWR_ON.tcl     1.11         Thu
Aug 16 11:06:28 2007
   662   P102999SCVT902_ASDDBGPACS_PWR_OFF.tcl    1.8          Thu
Aug 16 11:07:04 2007
   663   P102999SCVT903_SSMM_FILL_IN.tcl          1.5          Wed
Jun  6 07:12:29 2007
   664   P102999SCVT904_ASDGENPACS_NomSpect.tcl   1.3          Fri
Oct 26 14:37:07 2007
   665   P102999SCVT905_ASDISTPACS_PWR_ON_N.tcl   1.9          Fri
Oct 19 08:47:40 2007
   666   P102999SCVT906_ASDISTPACS_PWR_OFF_N.tcl  1.6          Mon
Oct 15 10:48:04 2007
   667   P102999SCVT907_ASDISTPACS_PWR_ON_R.tcl   1.9          Fri
Oct 19 07:47:44 2007
   668   P102999SCVT908_ASDISTPACS_PWR_OFF_R.tcl  1.6          Mon
Oct 15 10:48:27 2007
   669   P102999SCVT909_ASDGENPACS_PWR_ON_N.tcl   1.11         Wed
Oct 31 15:10:14 2007
   670   P102999SCVT910_ASDGENPACS_PWR_OFF_N.tcl  1.8          Mon
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Oct 15 10:45:54 2007
   671   P102999SCVT911_ASDGENPACS_PWR_ON_R.tcl   1.12          Wed
Oct 31 15:10:54 2007
   672   P102999SCVT912_ASDGENPACS_PWR_OFF_R.tcl  1.7           Mon
Oct 15 10:47:39 2007
   673   P102999SCVT913_ASDGENPACS_BurstMode.tcl  1.2           Fri
Oct 26 10:29:59 2007
   674   P102999SCVT914_ASDGENPACS_DisSciDwnLnk.tcl 1.3          Sat
Oct 27 14:19:26 2007
   675   P102999SCVT915_ASDGENPACS_EnSciDwnLnk.tcl 1.3           Sat
Oct 27 14:20:17 2007
   676   PACS_Burstmode_Reset_OBS_Shell.tcl       1.1          Thu
Oct 18 15:48:10 2007
   677   PACS_Burstmode_Setup_OBS_Shell.tcl       1.1          Thu
Oct 18 15:48:10 2007
   678   PACS_CS_SFT_Cold_OBS_Shell.tcl           1.1          Thu
Oct 18 15:48:10 2007
   679   PACS_CS_SFT_Warm_OBS_Shell.tcl           1.1          Thu
Oct 18 15:48:10 2007
   680   PACS_Chopper_EnDis_PlateauTest_ast_OBS_Shell.tcl 1.1
Thu Oct 18 15:48:10 2007
   681   PACS_Chopper_EnDis_Test_ast1_OBS_Shell.tcl 1.1          Thu
Oct 18 15:48:10 2007
   682   PACS_Chopper_EnDis_Test_ast300_OBS_Shell.tcl 1.1
Thu Oct 18 15:48:10 2007
   683   PACS_Chopper_SFT_Cold_OpenLoop_OBS_Shell.tcl 1.1
Thu Oct 18 15:48:10 2007
   684   PACS_Chopper_SFT_Warm_OpenLoop_OBS_Shell.tcl 1.1
Thu Oct 18 15:48:10 2007
   685   PACS_Chopper_uk_move_12000_ast_OBS_Shell.tcl 1.1
Thu Oct 18 15:48:10 2007
   686   PACS_Chopper_uk_move_18000_ast_OBS_Shell.tcl 1.1
Thu Oct 18 15:48:10 2007
   687   PACS_Chopper_uk_move_21000_ast_OBS_Shell.tcl 1.1
Thu Oct 18 15:48:10 2007
   688   PACS_Chopper_uk_move_3000_ast_OBS_Shell.tcl 1.1          Thu
Oct 18 15:48:10 2007
   689   PACS_Close_Launch_Lock_OBS_Shell.tcl     1.1          Thu
Oct 18 15:48:10 2007
   690   PACS_DPU_Fill_SSMM_from_SAFE.tcl         1.3          Sun
May 18 07:30:42 2008
   691   PACS_DPU_Nominal_Science_Flow.tcl        1.2          Sun
May 18 07:32:29 2008
   692   PACS_Diaghk_Reset_OBS_Shell.tcl          1.1          Thu
Oct 18 15:48:10 2007
   693   PACS_Diagnostic_Check.tcl                1.1          Wed
Aug 15 07:16:03 2007
   694   PACS_Dis_chopper_ast_OBS_Shell.tcl       1.1          Thu
Oct 18 15:48:10 2007
   695   PACS_Disable_HK_OBS_Shell.tcl            1.1          Thu
Oct 18 15:48:10 2007
   696   PACS_En_chopper_ast_OBS_Shell.tcl        1.1          Thu
Oct 18 15:48:10 2007
   697   PACS_GeGa_SFT_Init_Cold_He2_OBS_Shell.tcl 1.1          Thu
Oct 18 15:48:10 2007
   698   PACS_GeGa_SFT_Init_Cold_OBS_Shell.tcl    1.1          Thu
Oct 18 15:48:10 2007
   699   PACS_GeGa_SFT_Init_Warm_OBS_Shell.tcl    1.1          Thu
Oct 18 15:48:10 2007
   700   PACS_Open_Launch_Lock_OBS_Shell.tcl      1.1          Thu
Oct 18 15:48:10 2007
   701   PACS_Phot_Cold_Startinputsignal_OBS_Shell.tcl 1.1
Thu Oct 18 15:48:10 2007
   702   PACS_Phot_DPU_DMC_Setup_OBS_Shell.tcl    1.1          Thu
Oct 18 15:48:10 2007
   703   PACS_Phot_Fil_Diaghk_Setup_OBS_Shell.tcl 1.1          Thu
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Oct 18 15:48:10 2007
   704   PACS_Phot_Fil_Testseq_OBS_Shell.tcl      1.1          Thu
Oct 18 15:48:10 2007
   705   PACS_Phot_Fil_nturns_OBS_15_Shell.tcl    1.1          Thu
Oct 18 15:48:10 2007
   706   PACS_Phot_HK_Setup_OBS_Shell.tcl         1.1          Thu
Oct 18 15:48:10 2007
   707   PACS_Phot_SFT_Cooler_OBS_Shell.tcl       1.1          Thu
Oct 18 15:48:10 2007
   708   PACS_Phot_SFT_Cooler_Warm_OBS_Shell.tcl  1.1          Thu
Oct 18 15:48:10 2007
   709   PACS_Phot_SPU_Reset_OBS_Shell.tcl        1.1          Thu
Oct 18 15:48:10 2007
   710   PACS_Phot_SPU_Setup.tcl                  1.2          Wed
Feb 14 16:50:26 2007
   711   PACS_Phot_SPU_Setup_OBS_Shell.tcl        1.1          Thu
Oct 18 15:48:10 2007
   712   PACS_Phot_Sequencer_Setup_OBS_Shell.tcl  1.1          Thu
Oct 18 15:48:10 2007
   713   PACS_Phot_Switchoff_OBS_Shell.tcl        1.1          Thu
Oct 18 15:48:10 2007
   714   PACS_Phot_Switchon_OBS_Shell.tcl         1.1          Thu
Oct 18 15:48:10 2007
   715   PACS_Phot_Warm_Startinputsignal_OBS_Shell.tcl 1.1
Thu Oct 18 15:48:11 2007
   716   PACS_SAFE_Mode.tcl                       1.1          Mon
May  7 13:41:48 2007
   717   PACS_Spec_CRE_01pF0bias_Setup_Warm_OBS_Shell.tcl 1.1
Thu Oct 18 15:48:11 2007
   718   PACS_Spec_CRE_1pF0bias_Setup_Warm_OBS_Shell.tcl 1.1
Thu Oct 18 15:48:11 2007
   719   PACS_Spec_CRE_Setup_Cold_HeI_OBS_Shell.tcl 1.1          Thu
Oct 18 15:48:11 2007
   720   PACS_Spec_CRE_Setup_Cold_OBS_Shell.tcl   1.1          Thu
Oct 18 15:48:11 2007
   721   PACS_Spec_CRE_Setup_Warm_OBS_Shell.tcl   1.1          Thu
Oct 18 15:48:11 2007
   722   PACS_Spec_Curing_OBS_Shell.tcl           1.1          Thu
Oct 18 15:48:11 2007
   723   PACS_Spec_Fil_Diaghk_Setup_OBS_Shell.tcl 1.1          Thu
Oct 18 15:48:11 2007
   724   PACS_Spec_Fil_Testseq_OBS_Shell.tcl      1.1          Thu
Oct 18 15:48:11 2007
   725   PACS_Spec_Fil_nturns_OBS_15_Shell.tcl    1.1          Thu
Oct 18 15:48:11 2007
   726   PACS_Spec_FlashHeat_Diaghk_Setup_OBS_Shell.tcl 1.1
Thu Oct 18 15:48:11 2007
   727   PACS_Spec_Flash_IST_Shell.tcl            1.1          Thu
Oct 18 15:48:11 2007
   728   PACS_Spec_Flash_SFT_Shell.tcl            1.1          Thu
Oct 18 15:48:11 2007
   729   PACS_Spec_Flash_SFT_Warm_Shell.tcl       1.1          Thu
Oct 18 15:48:11 2007
   730   PACS_Spec_Flash_SWOF_OBS_Shell.tcl       1.1          Thu
Oct 18 15:48:11 2007
   731   PACS_Spec_Flash_SWON_OBS_Shell.tcl       1.1          Thu
Oct 18 15:48:11 2007
   732   PACS_Spec_Gra_DEG_MODE_Diaghk_Setup_OBS_Shell.tcl 1.1
Thu Oct 18 15:48:11 2007
   733   PACS_Spec_Gra_Diaghk_SINCOS_Setup_OBS_Shell.tcl 1.1
Thu Oct 18 15:48:11 2007
   734   PACS_Spec_Gra_Diaghk_Setup_OBS_Shell.tcl 1.1          Thu
Oct 18 15:48:11 2007
   735   PACS_Spec_Gra_Healthcheck_OBS_Shell.tcl  1.1          Thu
Oct 18 15:48:11 2007
   736   PACS_Spec_Gra_IST_Ampl_OBS_Shell.tcl     1.1          Thu
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Oct 18 15:48:11 2007
   737   PACS_Spec_Gra_IST_Check_1_OBS_Shell.tcl  1.1          Thu
Oct 18 15:48:11 2007
   738   PACS_Spec_Gra_IST_Check_2_OBS_Shell.tcl  1.1          Thu
Oct 18 15:48:11 2007
   739   PACS_Spec_Gra_IST_Check_4_OBS_Shell.tcl  1.1          Thu
Oct 18 15:48:11 2007
   740   PACS_Spec_Gra_IST_Disable_OBS_Shell.tcl  1.1          Thu
Oct 18 15:48:11 2007
   741   PACS_Spec_Gra_IST_Enable_OBS_Shell.tcl   1.1          Thu
Oct 18 15:48:11 2007
   742   PACS_Spec_Gra_IST_Full_Charac_OBS_Shell.tcl 1.1          Thu
Oct 18 15:48:11 2007
   743   PACS_Spec_Gra_IST_Home_OBS_Shell.tcl     1.1          Thu
Oct 18 15:48:11 2007
   744   PACS_Spec_Gra_IST_Move_Abs_OBS_Shell.tcl 1.1          Thu
Oct 18 15:48:11 2007
   745   PACS_Spec_Gra_IST_Move_Rel_OBS_Shell.tcl 1.1          Thu
Oct 18 15:48:11 2007
   746   PACS_Spec_Gra_IST_PID_Loop_Less_OBS_Shell.tcl 1.1
Thu Oct 18 15:48:11 2007
   747   PACS_Spec_Gra_IST_PID_Loop_OBS_Shell.tcl 1.1          Thu
Oct 18 15:48:11 2007
   748   PACS_Spec_Gra_IST_SWOF_OBS_Shell.tcl     1.1          Thu
Oct 18 15:48:11 2007
   749   PACS_Spec_Gra_IST_SWON_OBS_Shell.tcl     1.1          Thu
Oct 18 15:48:11 2007
   750   PACS_Spec_Gra_IST_Set_Ctrl_Par_OBS_Shell.tcl 1.1
Thu Oct 18 15:48:11 2007
   751   PACS_Spec_Gra_Mec_Setup_Cold_OBS_Shell.tcl 1.1          Thu
Oct 18 15:48:11 2007
   752   PACS_Spec_Gra_Mec_Setup_Cold_Redun_OBS_Shell.tcl 1.1
Thu Oct 18 15:48:11 2007
   753   PACS_Spec_Gra_SFT_Warm_OBS_Shell.tcl     1.1          Thu
Oct 18 15:48:11 2007
   754   PACS_Spec_Gra_Slew_Time_Cal_Shell.tcl    1.1          Thu
Oct 18 15:48:11 2007
   755   PACS_Spec_HK_Setup_OBS_Shell.tcl         1.1          Thu
Oct 18 15:48:11 2007
   756   PACS_Spec_Heat_FFT_Shell.tcl             1.1          Thu
Oct 18 15:48:11 2007
   757   PACS_Spec_Heat_SFT_Shell.tcl             1.1          Thu
Oct 18 15:48:11 2007
   758   PACS_Spec_Heat_SWOF_OBS_Shell.tcl        1.1          Thu
Oct 18 15:48:11 2007
   759   PACS_Spec_Heat_SWON_OBS_Shell.tcl        1.1          Thu
Oct 18 15:48:11 2007
   760   PACS_Spec_QuickFullSpectrum_OBS_CS1_Shell.tcl 1.1
Thu Oct 18 15:48:11 2007
   761   PACS_Spec_QuickFullSpectrum_OBS_CS2_Shell.tcl 1.1
Thu Oct 18 15:48:11 2007
   762   PACS_Spec_SPU_Buffer_Setup_OBS_Shell.tcl 1.1          Thu
Oct 18 15:48:11 2007
   763   PACS_Spec_SPU_Reset_OBS_Shell.tcl        1.1          Thu
Oct 18 15:48:12 2007
   764   PACS_Spec_SPU_Setup.tcl                  1.2          Wed
Feb 14 16:52:22 2007
   765   PACS_Spec_Time_Constant_IST_OBS_Shell.tcl 1.1          Thu
Oct 18 15:48:12 2007
   766   PACS_StartAutonomy_Function_14_OBS_Shell.tcl 1.1
Thu Oct 18 15:48:12 2007
   767   PACS_StartAutonomy_Function_17_OBS_Shell.tcl 1.1
Thu Oct 18 15:48:12 2007
   768   PACS_SwOff_chopper_ast_OBS_Shell.tcl     1.1          Thu
Oct 18 15:48:12 2007
   769   PACS_SwOn_chopper_ast_OBS_Shell.tcl      1.1          Thu
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Oct 18 15:48:12 2007
   770   PACS_Switch_Off.tcl                          1.1          Wed
Feb 14 07:33:54 2007
   771   PACS_Switch_Off_CCS.tcl                      1.2          Wed
Feb 14 16:55:43 2007
   772   PACS_Switch_Off_CCS_Nominal.tcl              1.1          Mon
Oct 15 09:54:10 2007
   773   PACS_Switch_Off_CCS_Redundant.tcl            1.1          Mon
Oct 15 09:55:00 2007
   774   PACS_Switch_On.tcl                           1.1          Wed
Feb 14 07:34:31 2007
   775   PACS_Switch_On_CCS.tcl                       1.2          Wed
Feb 14 16:56:24 2007
   776   PACS_Switch_On_CCS_Nominal.tcl               1.1          Mon
Oct 15 09:53:52 2007
   777   PACS_Switch_On_CCS_Redundant.tcl             1.1          Mon
Oct 15 09:54:35 2007
   778   PACS_Switch_On_CCS_SPU_shifted.tcl           1.1          Tue
Jul 31 12:50:38 2007
   779   PACS_Switch_On_DPU_SPULLSW.tcl               1.3          Sat
Aug  4 10:09:29 2007
   780   PACS_Wave_Cal_Shell.tcl                      1.1          Thu
Oct 18 15:48:12 2007
   781   PACS_pre_test.tcl                            1.1          Wed
Feb 14 10:34:59 2007
   782   PCS__mini_IST.tcl                            1.1          Thu
Oct 27 00:56:50 2005
   783   PHOT_TestPattern_obs_Shell.tcl               1.1          Thu
Oct 18 15:48:12 2007
   784   PHOT_VrlVhBlind_OBS_Shell.tcl                1.1          Thu
Oct 18 15:48:12 2007
   785   PHOT_all_aots_OBS_Shell.tcl                  1.1          Thu
Oct 18 15:48:12 2007
   786   PHOT_cal_recipes_OBS_Shell.tcl               1.1          Thu
Oct 18 15:48:12 2007
   787   PHOT_chopped_photometry_OBS_Shell.tcl        1.1          Thu
Oct 18 15:48:12 2007
   788   PHOT_low_freq_OBS_Shell.tcl                  1.1          Thu
Oct 18 15:48:12 2007
   789   PHOT_low_freq_direct_OBS_Shell.tcl           1.1          Thu
Oct 18 15:48:12 2007
   790   PHOT_redArray_DDCS_IST_OBS_Shell.tcl         1.1          Thu
Oct 18 15:48:12 2007
   791   PHOT_redArray_alternatePolarisation_DDCS_IST_OBS_Shell.tcl
1.1          Thu Oct 18 15:48:12 2007
   792   PHOT_redArray_alternatePolarisation_direct_IST_OBS_Shell.tcl
1.1          Thu Oct 18 15:48:12 2007
   793   PHOT_redArray_direct_IST_OBS_Shell.tcl       1.1          Thu
Oct 18 15:48:12 2007
   794   PHOT_saturation_OBS_Shell.tcl                1.1          Thu
Oct 18 15:48:12 2007
   795   PHOT_setup_OBS_EMC_Shell.tcl                 1.1          Thu
Oct 18 15:48:12 2007
   796   PHOT_setup_OBS_Shell.tcl                     1.1          Thu
Oct 18 15:48:12 2007
   797   PHOT_setup_redundant_OBS_Shell.tcl           1.1          Thu
Oct 18 15:48:12 2007
   798   PHOT_spu_data_rate_obs_Shell.tcl             1.1          Thu
Oct 18 15:48:12 2007
   799   PHOT_spu_reset_OBS_Shell.tcl                 1.1          Thu
Oct 18 15:48:12 2007
   800   PHOT_spu_setup_OBS_Shell.tcl                 1.1          Thu
Oct 18 15:48:12 2007
   801   PHOT_thermal_OBS_Shell.tcl                   1.1          Thu
Oct 18 15:48:12 2007
   802   PHOT_timeconst_fluxchange_OBS_Shell.tcl      1.1          Thu
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Oct 18 15:48:12 2007
   803   PIERO PROVA RM DISABLE.tcl                1.1          Tue
Feb 28 11:14:01 2006
   804   PIERO_PROVA_RM_DISABLE.tcl                1.5          Thu
Mar  2 17:26:28 2006
   805   PROVA ALE.tcl                             1.2          Mon
Nov  7 16:22:54 2005
   806   PROVA_ALESSIO.tcl                         1.4          Tue
May 22 14:24:10 2007
   807   PROVA_luigi.tcl                           1.6          Thu
Nov 22 19:44:41 2007
   808   Phot_prepareEMC_DDCS_OBS_Shell.tcl        1.1          Thu
Oct 18 15:48:12 2007
   809   Phot_prepareEMC_direct_OBS_Shell.tcl      1.1          Thu
Oct 18 15:48:12 2007
   810   PhotometrytoNonprime.tcl                  1.2          Wed
Feb 14 16:57:56 2007
   811   R102479ECVT001_TWTA_CFG.tcl               1.5          Wed
Aug 30 07:55:15 2006
   812   R102479ECVT002_XPND_LCL.tcl               1.7          Fri
Sep 15 07:09:56 2006
   813   R102479ECVT003_RX_BIT_RATE_SET.tcl        1.6          Tue
May 29 21:21:47 2007
   814   R102479ECVT004_XPND_TX.tcl                1.4          Wed
Aug 30 08:02:12 2006
   815   R102479ECVT005_XPND1_TC_1553.tcl          1.5          Thu
Jun  1 13:43:25 2006
   816   R102479ECVT006_XPND2_TC_1553.tcl          1.5          Thu
Jun  1 13:47:13 2006
   817   R102479ECVT008_CDMU_TM_RATE.tcl           1.6          Tue
Nov  7 11:46:15 2006
   818   R102479ECVT009_UNITS_SELECTION.tcl        1.10         Mon
May 21 22:22:32 2007
   819   R102479ECVT011_TWTA_CFG_ASW.tcl           1.2          Thu
Sep 21 10:34:06 2006
   820   R102479ECVT012_XPND_CFG_ASW.tcl           1.3          Thu
Sep 21 16:24:15 2006
   821   R102479EMVT011_ANALOGUE_TM.tcl            1.9          Thu
May 24 23:15:54 2007
   822   R102479EMVT012_DIGITAL_TM.tcl             1.7          Thu
May 24 23:17:34 2007
   823   R102479ETVT007_REPEAT_TC.tcl              1.8          Wed
Sep 20 15:50:51 2006
   824   R102479ETVT009_CHECK_RX_LOCK.tcl          1.5          Thu
May 24 23:19:45 2007
   825   R102479ETVT010_RFDN_CONF.tcl              1.4          Wed
Aug 30 08:01:35 2006
   826   R102479ETVT011_RFDN_CONF_CHECK_TWTA.tcl   1.3          Wed
May 23 19:39:09 2007
   827   R102479ETVT013_REPEAT_TC_FAST.tcl         1.1          Thu
Sep 21 16:26:19 2006
   828   R102479SMXX001_XPND_HUM_TXT.tcl           1.2          Wed
Sep 19 07:08:23 2007
   829   R102479SPVT001_RX1_LOCK_ACQ.tcl           1.9          Mon
Sep 25 16:32:54 2006
   830   R102479SPVT002_RX1_LOCK_MNT.tcl           1.10         Wed
Sep 27 07:13:14 2006
   831   R102479SPVT003_RX1_TC_LOW_BIT_RATE.tcl    1.6          Mon
Sep 25 15:39:13 2006
   832   R102479SPVT004_RX1_TC_HIGH_BIT_RATE.tcl   1.6          Tue
Sep 26 11:36:47 2006
   833   R102479SPVT005_TX1_DL_CARRIER.tcl         1.9          Wed
Sep 27 12:09:22 2006
   834   R102479SPVT006_TX1_DL_TM_BAND.tcl         1.6          Thu
Sep 28 09:16:52 2006
   835   R102479SPVT008_RX1_TC_LBR_RNG.tcl         1.5          Thu
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Sep 28 10:57:20 2006
   836   R102479SPVT009_RX1_TC_MBR_RNG.tcl          1.5          Thu
Sep 28 11:02:37 2006
   837   R102479SPVT010_TX1_DL_RNG_SIGNAL.tcl       1.4          Mon
Sep 25 15:55:45 2006
   838   R102479SPVT011_TX1_DL_RNG_PERF.tcl         1.5          Mon
Sep 25 16:00:24 2006
   839   R102479SPVT012_TX1_TM+RG.tcl               1.3          Mon
Sep 25 16:09:56 2006
   840   R102479SPVT012_TX1_TM_RG_SIGNAL.tcl        1.2          Tue
Jan 24 08:33:43 2006
   841   R102479SPVT014_TC1_LBR_LOW_DOPPLER.tcl     1.4          Mon
Sep 25 16:14:40 2006
   842   R102479SPVT015_TC1_MBR_HIGH_DOPPLER.tcl    1.4          Mon
Sep 25 16:15:58 2006
   843   R102479SPVT016_TC1_MBR_LOW_DOPPLER.tcl     1.4          Mon
Sep 25 16:21:28 2006
   844   R102479SPVT017_TC+TM+RNG_END2END1.tcl      1.4          Wed
Oct  4 05:24:50 2006
   845   R102479SPVT017_TC_TM_RNG_END2END1.tcl      1.3          Wed
Aug 30 08:03:06 2006
   846   R102479SPVT020_RFDN_UFT.tcl                1.4          Sat
Sep 30 09:42:24 2006
   847   R102479SPVT031_RX2_LOCK_ACQ.tcl            1.7          Tue
May 29 20:36:28 2007
   848   R102479SPVT032_RX2_LOCK_MNT.tcl            1.8          Wed
May 30 15:20:16 2007
   849   R102479SPVT033_RX2_TC_LOW_BIT_RATE.tcl     1.7          Wed
May 30 15:18:52 2007
   850   R102479SPVT034_RX2_TC_HIGH_BIT_RATE.tcl    1.5          Wed
May 30 15:19:29 2007
   851   R102479SPVT035_TX2_DL_CARRIER.tcl          1.6          Wed
May 30 15:19:52 2007
   852   R102479SPVT036_TX2_DL_TM_BAND.tcl          1.5          Fri
May 25 03:02:24 2007
   853   R102479SPVT038_RX2_TC_LBR_RNG.tcl          1.4          Wed
May 30 23:26:52 2007
   854   R102479SPVT039_RX2_TC_MBR_RNG.tcl          1.4          Wed
May 30 23:18:54 2007
   855   R102479SPVT040_TX2_DL_RNG_SIGNAL.tcl       1.5          Fri
Jun  1 22:05:28 2007
   856   R102479SPVT041_TX2_DL_RNG_PERF.tcl         1.5          Wed
Sep 27 14:07:33 2006
   857   R102479SPVT042_TX2_TM+RG.tcl               1.3          Wed
May 30 23:26:42 2007
   858   R102479SPVT042_TX2_TM_RG_SIGNAL.tcl        1.2          Wed
Aug 30 08:03:20 2006
   859   R102479SPVT044_TC2_LBR_LOW_DOPPLER.tcl     1.4          Fri
Jun  1 16:09:31 2007
   860   R102479SPVT045_TC2_MBR_HIGH_DOPPLER.tcl    1.5          Wed
May 30 16:00:22 2007
   861   R102479SPVT046_TC2_MBR_LOW_DOPPLER.tcl     1.4          Mon
Jun  4 06:27:37 2007
   862   R102479SPVT047_TC_TM_RNG_END2END2.tcl      1.4          Mon
Jun  4 06:46:30 2007
   863   R102479SPVT048_TTC_MNGM.tcl                1.3          Thu
May 31 01:52:16 2007
   864   R102479SPVT100_TTC_IST_HEALTH_CHECK.tcl    1.6          Wed
Dec  6 07:28:52 2006
   865   R102479SPVT101_IST_RFDN_UFT.tcl            1.2          Thu
Jul 20 07:30:32 2006
   866   R102479SPVT103_IST_LOCK_ACQ.tcl            1.8          Thu
May 24 23:24:53 2007
   867   R102479SPVT104_IST_END2END.tcl             1.7          Thu
May 24 23:27:21 2007
   868   R102479SPVT105_IST_DL_VERIFICATION.tcl     1.4          Wed
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Nov 29 17:32:32 2006
   869   R102479SPVT107_IST_LAUNCH_2_SUN.tcl      1.9          Tue
Nov  6 16:06:54 2007
   870   R102479SPVT108_IST_SUN_2_NOM.tcl         1.5          Tue
Nov  6 16:11:27 2007
   871   R102479SPVT109_IST_NOM_2_SURV.tcl        1.5          Fri
Apr 13 07:47:44 2007
   872   R102479SPVT110_IST_BEFORE_SAM.tcl        1.3          Wed
Nov 15 11:56:38 2006
   873   R102479SPVT111_IST_FDIR_2_SAM.tcl        1.2          Wed
Nov 15 11:59:30 2006
   874   R102479SPVT113_IST_LOCK_ACQ2.tcl         1.7          Tue
Oct 24 09:32:31 2006
   875   R102479SPVT114_IST_END2END2.tcl          1.4          Thu
Oct 26 14:55:22 2006
   876   R102479SPVT115_IST_DL_VERIFICATION2.tcl  1.5          Thu
Oct 26 14:29:50 2006
   877   R102479SPVT116_IST_DUMP_ON.tcl           1.4          Tue
Nov  6 16:14:52 2007
   878   R102479SPVT117_IST_GMSK_2_NOM.tcl        1.1          Tue
Feb 20 18:12:33 2007
   879   R102479SPVT117_IST_NOM_2_5k.tcl          1.5          Thu
Jul 12 17:30:42 2007
   880   R102479SPVT120_IST_CONF_TTC_HC.tcl       1.3          Wed
Apr 18 09:36:56 2007
   881   R102479SPVT121_IST_CONF_TTC2_HC_OFF.tcl  1.2          Wed
Apr 18 09:38:02 2007
   882   R102479SPVT122_IST_TTC_COMMISSIONING.tcl 1.6          Thu
Nov 22 18:24:22 2007
   883   RMS_DTCP_ASTRIUM.tcl                     1.6          Tue
May 20 05:39:20 2008
   884   S102999SCVT005_ASDSFTSPIR_PWR_ON_P.tcl   1.6          Wed
Oct 31 15:04:29 2007
   885   S102999SCVT006_ASDSFTSPIR_PWR_ON_R.tcl   1.7          Wed
Oct 31 15:04:54 2007
   886   S102999SCVT007_ASDSFTSPIR_PWR_OFF_P.tcl  1.4          Sat
Nov 24 11:39:39 2007
   887   S102999SCVT008_ASDSFTSPIR_PWR_OFF_R.tcl  1.5          Sat
Nov 24 11:40:04 2007
   888   S102999SCVT009_ASDWFTSPIR_PWR_ON_P.tcl   1.6          Thu
Nov 15 11:09:46 2007
   889   S102999SCVT010_ASDWFTSPIR_PWR_ON_R.tcl   1.7          Thu
Nov 15 11:10:15 2007
   890   S102999SCVT011_ASDWFTSPIR_PWR_OFF_P.tcl  1.5          Sat
Nov 24 11:41:22 2007
   891   S102999SCVT012_ASDWFTSPIR_PWR_OFF_R.tcl  1.5          Sat
Nov 24 11:41:47 2007
   892   S102999SCVT013_ASDWFTSPIR_SAFE_OFF_P.tcl 1.2          Mon
Oct 22 11:32:03 2007
   893   S102999SCVT014_ASDWFTSPIR_SAFE_OFF_R.tcl 1.2          Mon
Oct 22 11:32:44 2007
   894   S102999SCVT015_ASDISTSPIR_STBY2PHOT.tcl  1.2          Thu
Nov 15 17:19:41 2007
   895   S102999SCVT016_ASDISTSPIR_PHOT2STBY.tcl  1.2          Thu
Nov 15 17:20:14 2007
   896   S102999SCVT017_ASDGENSPIR_PWR_ON_P.tcl   1.2          Thu
Nov 15 17:33:55 2007
   897   S102999SCVT018_ASDGENSPIR_PWR_ON_R.tcl   1.1          Thu
Nov 15 17:31:07 2007
   898   S102999SCVT019_ASDGENSPIR_PWR_OFF_P.tcl  1.2          Sat
Nov 24 11:42:41 2007
   899   S102999SCVT020_ASDGENSPIR_PWR_OFF_R.tcl  1.2          Sat
Nov 24 11:45:02 2007
   900   S102999SCVT9013_ASDDBGSPIR_PWR_ON_P_INITMD.tcl 1.2
Thu Jun 28 06:54:30 2007
   901   S102999SCVT9014_ASDDBGSPIR_SW_UPL.tcl    1.3          Mon
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Jul  2 16:40:01 2007
   902   S102999SCVT901_ASDDBGSPIR_PWR_ON_P.tcl   1.20          Thu
Nov 15 17:58:47 2007
   903   S102999SCVT902_ASDDBGSPIR_PWR_ON_R.tcl   1.16          Thu
Nov 15 17:58:25 2007
   904   S102999SCVT903_ASDDBGSPIR_PWR_OFF_P.tcl  1.17          Sat
Nov 24 11:45:30 2007
   905   S102999SCVT904_ASDDBGSPIR_PWR_OFF_R.tcl  1.10          Sat
Nov 24 11:45:53 2007
   906   S102999SCVT907_ASDDBGSPIR_STBY2PHTSTBY.tcl 1.2           Wed
Jun  6 07:01:01 2007
   907   S102999SCVT908_ASDDBGSPIR_PHTSTBY2STBY.tcl 1.2           Wed
Jun  6 07:01:34 2007
   908   S102999SCVT909_ASDDBGSPIR_STBY2SPECSTBY.tcl 1.2           Wed
Jun  6 07:02:03 2007
   909   S102999SCVT910_ASDDBGSPIR_SPECSTBY2STBY.tcl 1.2           Wed
Jun  6 07:02:32 2007
   910   S102999SCVT911_ASDDBGSPIR_STBY2OPS.tcl   1.3           Sat
Oct 27 15:03:08 2007
   911   S102999SCVT912_ASDDBGSPIR_OPS2STBY.tcl   1.3           Sat
Oct 27 15:03:44 2007
   912   S102999SCVT915_ASDDBGSPIR_PWR_OFF_P_INITMD.tcl 1.1
Thu Jun 28 11:20:12 2007
   913   SPEC_CRE_setup_OBS_Shell.tcl              1.1           Thu
Oct 18 15:48:12 2007
   914   SPEC_CS_imt511_OBS_Shell.tcl              1.1           Thu
Oct 18 15:48:12 2007
   915   SPEC_Chopper_dhk_5hk_1khz_OBS_Shell.tcl   1.1           Thu
Oct 18 15:48:12 2007
   916   SPEC_Chopper_dhk_imt_OBS_Shell.tcl        1.1           Thu
Oct 18 15:48:12 2007
   917   SPEC_Chopper_dhk_stop_OBS_Shell.tcl       1.1           Thu
Oct 18 15:48:12 2007
   918   SPEC_Chopper_fmilt076spec_detsel_llc_ast_V1_OBS_Shell.tcl
1.1           Thu Oct 18 15:48:12 2007
   919   SPEC_Chopper_fmilt076spec_detsel_llc_ast_V2_OBS_Shell.tcl
1.1           Thu Oct 18 15:48:12 2007
   920   SPEC_Chopper_imt504_OBS_Shell.tcl         1.1           Thu
Oct 18 15:48:12 2007
   921   SPEC_Chopper_imt504_ast_OBS_Shell.tcl     1.1           Thu
Oct 18 15:48:13 2007
   922   SPEC_Gra_move_abs_raw_obs_500_Shell.tcl   1.1           Thu
Oct 18 15:48:13 2007
   923   SPEC_Gra_move_abs_raw_obs_775_Shell.tcl   1.1           Thu
Oct 18 15:48:13 2007
   924   SPEC_Prepare_EMC_Shell.tcl                1.1           Thu
Oct 18 15:48:13 2007
   925   SPEC_Readouts_per_Ramp_Test_01_Shell.tcl  1.1           Thu
Oct 18 15:48:13 2007
   926   SPEC_Readouts_per_Ramp_Test_02_Shell.tcl  1.1           Thu
Oct 18 15:48:13 2007
   927   SPEC_Readouts_per_Ramp_Test_03_Shell.tcl  1.1           Thu
Oct 18 15:48:13 2007
   928   SPEC_Readouts_per_Ramp_Test_04_Shell.tcl  1.1           Thu
Oct 18 15:48:13 2007
   929   SPEC_Readouts_per_Ramp_Test_05_Shell.tcl  1.1           Thu
Oct 18 15:48:13 2007
   930   SPEC_Readouts_per_Ramp_Test_06_Shell.tcl  1.1           Thu
Oct 18 15:48:13 2007
   931   SPEC_all_aots_OBS_Shell.tcl               1.1           Thu
Oct 18 15:48:13 2007
   932   SPEC_chopped_SED_OBS_Shell.tcl            1.1           Thu
Oct 18 15:48:13 2007
   933   SPEC_cre_setup_OBS_capa00_Shell.tcl       1.1           Thu
Oct 18 15:48:13 2007
   934   SPEC_cre_setup_OBS_capa1212_Shell.tcl     1.1           Thu
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Oct 18 15:48:13 2007
   935   SPEC_cre_setup_OBS_capa12_Shell.tcl      1.1          Thu
Oct 18 15:48:13 2007
   936   SPEC_dark_current_imt502_OBS_Shell.tcl   1.1          Thu
Oct 18 15:48:13 2007
   937   SPEC_detector_imt509_OBS_Shell.tcl       1.1          Thu
Oct 18 15:48:13 2007
   938   SPEC_fov_scan_imt409_OBS_Shell.tcl       1.1          Thu
Oct 18 15:48:13 2007
   939   SPEC_setup_CSOFF_OBS_Shell.tcl           1.1          Thu
Oct 18 15:48:13 2007
   940   SPEC_setup_OBS_Shell.tcl                 1.1          Thu
Oct 18 15:48:13 2007
   941   SPEC_setup_redundant_OBS_Shell.tcl       1.1          Thu
Oct 18 15:48:13 2007
   942   SPEC_spu_data_rate_obs_Shell.tcl         1.1          Thu
Oct 18 15:48:13 2007
   943   SPEC_spu_reset_OBS_Shell.tcl             1.1          Thu
Oct 18 15:48:13 2007
   944   SPEC_spu_setup_OBS_Shell.tcl             1.1          Thu
Oct 18 15:48:13 2007
   945   SPEC_spu_setup_OBS_Shell_025s.tcl        1.1          Thu
Oct 18 15:48:13 2007
   946   SPEC_thermal_OBS_Shell.tcl               1.1          Thu
Oct 18 15:48:13 2007
   947   SPIRE-FM-SFT-BSM-OFF-P.tcl               1.1          Tue
Sep 11 17:12:25 2007
   948   SPIRE-FM-SFT-BSM-OFF-R.tcl               1.1          Tue
Sep 11 17:12:25 2007
   949   SPIRE-FM-SFT-DPU-START-P-PP.tcl          1.1          Tue
Sep 11 17:12:25 2007
   950   SPIRE-FM-SFT-DPU-START-P-SP.tcl          1.1          Tue
Sep 11 17:12:26 2007
   951   SPIRE-FM-SFT-DPU-START-R-PP.tcl          1.1          Tue
Sep 11 17:12:26 2007
   952   SPIRE-FM-SFT-DPU-START-R-SP.tcl          1.1          Tue
Sep 11 17:12:26 2007
   953   SPIRE-FM-SFT-DRCU-OFF-P.tcl              1.1          Tue
Sep 11 17:12:26 2007
   954   SPIRE-FM-SFT-DRCU-OFF-R.tcl              1.1          Tue
Sep 11 17:12:26 2007
   955   SPIRE-FM-SFT-DRCU-START-P-STEP1.tcl      1.1          Tue
Sep 11 17:12:26 2007
   956   SPIRE-FM-SFT-DRCU-START-P-STEP2.tcl      1.1          Tue
Sep 11 17:12:26 2007
   957   SPIRE-FM-SFT-DRCU-START-R-STEP1.tcl      1.1          Tue
Sep 11 17:12:26 2007
   958   SPIRE-FM-SFT-DRCU-START-R-STEP2.tcl      1.1          Tue
Sep 11 17:12:26 2007
   959   SPIRE-FM-SFT-FUNC-BSM-01-P.tcl           1.1          Tue
Sep 11 17:12:26 2007
   960   SPIRE-FM-SFT-FUNC-BSM-01-R.tcl           1.1          Tue
Sep 11 17:12:26 2007
   961   SPIRE-FM-SFT-FUNC-DCU-01-P.tcl           1.1          Tue
Sep 11 17:12:26 2007
   962   SPIRE-FM-SFT-FUNC-DCU-01-R.tcl           1.1          Tue
Sep 11 17:12:26 2007
   963   SPIRE-FM-SFT-FUNC-DCU-04-PHOT-P.tcl      1.1          Tue
Sep 11 17:12:26 2007
   964   SPIRE-FM-SFT-FUNC-DCU-04-PHOT-R.tcl      1.1          Tue
Sep 11 17:12:26 2007
   965   SPIRE-FM-SFT-FUNC-DCU-04-SPEC-P.tcl      1.1          Tue
Sep 11 17:12:26 2007
   966   SPIRE-FM-SFT-FUNC-DCU-04-SPEC-R.tcl      1.1          Tue
Sep 11 17:12:26 2007
   967   SPIRE-FM-SFT-FUNC-MCU-01-P.tcl           1.1          Tue
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Sep 11 17:12:26 2007
   968   SPIRE-FM-SFT-FUNC-MCU-01-R.tcl          1.1          Tue
Sep 11 17:12:26 2007
   969   SPIRE-FM-SFT-FUNC-MCU-02-P.tcl          1.1          Tue
Sep 11 17:12:26 2007
   970   SPIRE-FM-SFT-FUNC-MCU-02-R.tcl          1.1          Tue
Sep 11 17:12:27 2007
   971   SPIRE-FM-SFT-FUNC-SCU-01-P.tcl          1.1          Tue
Sep 11 17:12:27 2007
   972   SPIRE-FM-SFT-FUNC-SCU-01-R.tcl          1.1          Tue
Sep 11 17:12:27 2007
   973   SPIRE-FM-SFT-FUNC-SCU-03-P.tcl          1.1          Tue
Sep 11 17:12:27 2007
   974   SPIRE-FM-SFT-FUNC-SCU-03-R.tcl          1.1          Tue
Sep 11 17:12:27 2007
   975   SPIRE-FM-SFT-FUNC-SCU-04-P.tcl          1.1          Tue
Sep 11 17:12:27 2007
   976   SPIRE-FM-SFT-FUNC-SCU-04-R.tcl          1.1          Tue
Sep 11 17:12:27 2007
   977   SPIRE-FM-SFT-FUNC-SCU-05-P.tcl          1.1          Tue
Sep 11 17:12:27 2007
   978   SPIRE-FM-SFT-FUNC-SCU-05-R.tcl          1.1          Tue
Sep 11 17:12:27 2007
   979   SPIRE-FM-SFT-FUNC-SCU-06-P.tcl          1.1          Tue
Sep 11 17:12:27 2007
   980   SPIRE-FM-SFT-FUNC-SCU-06-R.tcl          1.1          Tue
Sep 11 17:12:27 2007
   981   SPIRE-FM-SFT-FUNC-SCU-07-P.tcl          1.1          Tue
Sep 11 17:12:27 2007
   982   SPIRE-FM-SFT-FUNC-SCU-07-R.tcl          1.1          Tue
Sep 11 17:12:27 2007
   983   SPIRE-FM-SFT-FUNC-SMEC-01-P.tcl         1.1          Tue
Sep 11 17:12:27 2007
   984   SPIRE-FM-SFT-FUNC-SMEC-01-R.tcl         1.1          Tue
Sep 11 17:12:27 2007
   985   SPIRE-FM-SFT-MCU-OFF-P.tcl              1.1          Tue
Sep 11 17:12:27 2007
   986   SPIRE-FM-SFT-MCU-OFF-R.tcl              1.1          Tue
Sep 11 17:12:27 2007
   987   SPIRE-FM-SFT-PHOT-JFET-OFF.tcl          1.1          Tue
Sep 11 17:12:27 2007
   988   SPIRE-FM-SFT-PHOT-JFET-ON-01.tcl        1.1          Tue
Sep 11 17:12:27 2007
   989   SPIRE-FM-SFT-PHOT-JFET-ON-02.tcl        1.1          Tue
Sep 11 17:12:27 2007
   990   SPIRE-FM-SFT-PLIA-OFF-P.tcl             1.1          Tue
Sep 11 17:12:27 2007
   991   SPIRE-FM-SFT-PLIA-OFF-R.tcl             1.1          Tue
Sep 11 17:12:28 2007
   992   SPIRE-FM-SFT-SCU-OFF-P.tcl              1.1          Tue
Sep 11 17:12:28 2007
   993   SPIRE-FM-SFT-SCU-OFF-R.tcl              1.1          Tue
Sep 11 17:12:28 2007
   994   SPIRE-FM-SFT-SLIA-OFF-P.tcl             1.1          Tue
Sep 11 17:12:28 2007
   995   SPIRE-FM-SFT-SLIA-OFF-R.tcl             1.1          Tue
Sep 11 17:12:28 2007
   996   SPIRE-FM-SFT-SMEC-OFF-P.tcl             1.1          Tue
Sep 11 17:12:28 2007
   997   SPIRE-FM-SFT-SMEC-OFF-R.tcl             1.1          Tue
Sep 11 17:12:28 2007
   998   SPIRE-FM-SFT-SPEC-JFET-OFF.tcl          1.1          Tue
Sep 11 17:12:28 2007
   999   SPIRE-FM-SFT-SPEC-JFET-ON-01.tcl        1.1          Tue
Sep 11 17:12:28 2007
  1000   SPIRE-FM-SFT-SPEC-JFET-ON-02.tcl        1.1          Tue
```

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Sep 11 17:12:28 2007
  1001   SPIRE-FM-WFT-BSM-INIT-P.tcl                1.4          Mon
Oct 22 12:13:59 2007
  1002   SPIRE-FM-WFT-BSM-INIT-R.tcl                1.4          Mon
Oct 22 12:13:59 2007
  1003   SPIRE-FM-WFT-BSM-OFF-P.tcl                 1.4          Mon
Oct 22 12:13:59 2007
  1004   SPIRE-FM-WFT-BSM-OFF-R.tcl                 1.4          Mon
Oct 22 12:13:59 2007
  1005   SPIRE-FM-WFT-DPU-START-P-SP.tcl            1.4          Mon
Oct 22 12:13:59 2007
  1006   SPIRE-FM-WFT-DPU-START-R-PP.tcl            1.4          Mon
Oct 22 12:13:59 2007
  1007   SPIRE-FM-WFT-DRCU-OFF-P.tcl                1.4          Mon
Oct 22 12:14:00 2007
  1008   SPIRE-FM-WFT-DRCU-OFF-R.tcl                1.4          Mon
Oct 22 12:14:00 2007
  1009   SPIRE-FM-WFT-DRCU-START-P-STEP1.tcl        1.4          Mon
Oct 22 12:14:00 2007
  1010   SPIRE-FM-WFT-DRCU-START-P-STEP2.tcl        1.4          Mon
Oct 22 12:14:00 2007
  1011   SPIRE-FM-WFT-DRCU-START-R-STEP1.tcl        1.4          Mon
Oct 22 12:14:00 2007
  1012   SPIRE-FM-WFT-DRCU-START-R-STEP2.tcl        1.4          Mon
Oct 22 12:14:00 2007
  1013   SPIRE-FM-WFT-FUNC-BSM-01-P.tcl             1.4          Mon
Oct 22 12:14:00 2007
  1014   SPIRE-FM-WFT-FUNC-BSM-01-R.tcl             1.4          Mon
Oct 22 12:14:00 2007
  1015   SPIRE-FM-WFT-FUNC-BSM-02C-P.tcl            1.4          Mon
Oct 22 12:14:00 2007
  1016   SPIRE-FM-WFT-FUNC-BSM-02C-R.tcl            1.4          Mon
Oct 22 12:14:00 2007
  1017   SPIRE-FM-WFT-FUNC-BSM-02J-P.tcl            1.4          Mon
Oct 22 12:14:00 2007
  1018   SPIRE-FM-WFT-FUNC-BSM-02J-R.tcl            1.4          Mon
Oct 22 12:14:00 2007
  1019   SPIRE-FM-WFT-FUNC-BSM-03-P.tcl             1.4          Mon
Oct 22 12:14:00 2007
  1020   SPIRE-FM-WFT-FUNC-BSM-03-R.tcl             1.4          Mon
Oct 22 12:14:00 2007
  1021   SPIRE-FM-WFT-FUNC-BSM-05A-P.tcl            1.4          Mon
Oct 22 12:14:00 2007
  1022   SPIRE-FM-WFT-FUNC-BSM-05A-R.tcl            1.4          Mon
Oct 22 12:14:00 2007
  1023   SPIRE-FM-WFT-FUNC-BSM-05B-P.tcl            1.4          Mon
Oct 22 12:14:00 2007
  1024   SPIRE-FM-WFT-FUNC-BSM-05B-R.tcl            1.4          Mon
Oct 22 12:14:00 2007
  1025   SPIRE-FM-WFT-FUNC-BSM-06-P.tcl             1.4          Mon
Oct 22 12:14:00 2007
  1026   SPIRE-FM-WFT-FUNC-BSM-06-R.tcl             1.4          Mon
Oct 22 12:14:00 2007
  1027   SPIRE-FM-WFT-FUNC-DCU-01-P.tcl             1.4          Mon
Oct 22 12:14:00 2007
  1028   SPIRE-FM-WFT-FUNC-DCU-01-R.tcl             1.4          Mon
Oct 22 12:14:00 2007
  1029   SPIRE-FM-WFT-FUNC-DCU-02-P.tcl             1.4          Mon
Oct 22 12:14:00 2007
  1030   SPIRE-FM-WFT-FUNC-DCU-02-R.tcl             1.4          Mon
Oct 22 12:14:00 2007
  1031   SPIRE-FM-WFT-FUNC-DCU-03-P.tcl             1.4          Mon
Oct 22 12:14:00 2007
  1032   SPIRE-FM-WFT-FUNC-DCU-03-R.tcl             1.4          Mon
Oct 22 12:14:00 2007
  1033   SPIRE-FM-WFT-FUNC-DCU-04-PHOT-P.tcl        1.4          Mon
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Oct 22 12:14:01 2007
  1034   SPIRE-FM-WFT-FUNC-DCU-04-PHOT-R.tcl      1.4          Mon
Oct 22 12:14:01 2007
  1035   SPIRE-FM-WFT-FUNC-DCU-04-SPEC-P.tcl      1.4          Mon
Oct 22 12:14:01 2007
  1036   SPIRE-FM-WFT-FUNC-DCU-04-SPEC-R.tcl      1.4          Mon
Oct 22 12:14:01 2007
  1037   SPIRE-FM-WFT-FUNC-DCU-11-PHOT-P.tcl      1.4          Mon
Oct 22 12:14:01 2007
  1038   SPIRE-FM-WFT-FUNC-DCU-11-PHOT-R.tcl      1.4          Mon
Oct 22 12:14:01 2007
  1039   SPIRE-FM-WFT-FUNC-DCU-11-SPEC-P.tcl      1.4          Mon
Oct 22 12:14:01 2007
  1040   SPIRE-FM-WFT-FUNC-DCU-11-SPEC-R.tcl      1.4          Mon
Oct 22 12:14:01 2007
  1041   SPIRE-FM-WFT-FUNC-DCU-13-PHOT-P.tcl      1.4          Mon
Oct 22 12:14:01 2007
  1042   SPIRE-FM-WFT-FUNC-DCU-13-PHOT-R.tcl      1.4          Mon
Oct 22 12:14:01 2007
  1043   SPIRE-FM-WFT-FUNC-DCU-13-SPEC-P.tcl      1.4          Mon
Oct 22 12:14:01 2007
  1044   SPIRE-FM-WFT-FUNC-DCU-13-SPEC-R.tcl      1.4          Mon
Oct 22 12:14:01 2007
  1045   SPIRE-FM-WFT-FUNC-DCU-14-PHOT-P.tcl      1.4          Mon
Oct 22 12:14:01 2007
  1046   SPIRE-FM-WFT-FUNC-DCU-14-PHOT-R.tcl      1.4          Mon
Oct 22 12:14:01 2007
  1047   SPIRE-FM-WFT-FUNC-DCU-14-SPEC-P.tcl      1.4          Mon
Oct 22 12:14:01 2007
  1048   SPIRE-FM-WFT-FUNC-DCU-14-SPEC-R.tcl      1.4          Mon
Oct 22 12:14:01 2007
  1049   SPIRE-FM-WFT-FUNC-MCU-01-P.tcl           1.4          Mon
Oct 22 12:14:01 2007
  1050   SPIRE-FM-WFT-FUNC-MCU-01-R.tcl           1.4          Mon
Oct 22 12:14:01 2007
  1051   SPIRE-FM-WFT-FUNC-MCU-02-P.tcl           1.4          Mon
Oct 22 12:14:01 2007
  1052   SPIRE-FM-WFT-FUNC-MCU-02-R.tcl           1.4          Mon
Oct 22 12:14:01 2007
  1053   SPIRE-FM-WFT-FUNC-MCU-03-P.tcl           1.4          Mon
Oct 22 12:14:01 2007
  1054   SPIRE-FM-WFT-FUNC-MCU-03-R.tcl           1.4          Mon
Oct 22 12:14:01 2007
  1055   SPIRE-FM-WFT-FUNC-MCU-04-P.tcl           1.4          Mon
Oct 22 12:14:02 2007
  1056   SPIRE-FM-WFT-FUNC-MCU-04-R.tcl           1.4          Mon
Oct 22 12:14:02 2007
  1057   SPIRE-FM-WFT-FUNC-SCU-01-P.tcl           1.4          Mon
Oct 22 12:14:02 2007
  1058   SPIRE-FM-WFT-FUNC-SCU-01-R.tcl           1.4          Mon
Oct 22 12:14:02 2007
  1059   SPIRE-FM-WFT-FUNC-SCU-02-P.tcl           1.4          Mon
Oct 22 12:14:02 2007
  1060   SPIRE-FM-WFT-FUNC-SCU-02-R.tcl           1.4          Mon
Oct 22 12:14:02 2007
  1061   SPIRE-FM-WFT-FUNC-SCU-03-P.tcl           1.4          Mon
Oct 22 12:14:02 2007
  1062   SPIRE-FM-WFT-FUNC-SCU-03-R.tcl           1.4          Mon
Oct 22 12:14:02 2007
  1063   SPIRE-FM-WFT-FUNC-SCU-04-P.tcl           1.4          Mon
Oct 22 12:14:02 2007
  1064   SPIRE-FM-WFT-FUNC-SCU-04-R.tcl           1.4          Mon
Oct 22 12:14:02 2007
  1065   SPIRE-FM-WFT-FUNC-SCU-05-P.tcl           1.4          Mon
Oct 22 12:14:02 2007
  1066   SPIRE-FM-WFT-FUNC-SCU-05-R.tcl           1.4          Mon
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Oct 22 12:14:02 2007
  1067   SPIRE-FM-WFT-FUNC-SCU-06-P.tcl          1.4          Mon
Oct 22 12:14:02 2007
  1068   SPIRE-FM-WFT-FUNC-SCU-06-R.tcl          1.4          Mon
Oct 22 12:14:02 2007
  1069   SPIRE-FM-WFT-FUNC-SCU-07-P.tcl          1.4          Mon
Oct 22 12:14:02 2007
  1070   SPIRE-FM-WFT-FUNC-SCU-07-R.tcl          1.4          Mon
Oct 22 12:14:02 2007
  1071   SPIRE-FM-WFT-FUNC-SCU-08-P.tcl          1.4          Mon
Oct 22 12:14:02 2007
  1072   SPIRE-FM-WFT-FUNC-SCU-08-R.tcl          1.4          Mon
Oct 22 12:14:02 2007
  1073   SPIRE-FM-WFT-FUNC-SMEC-01-P.tcl         1.4          Mon
Oct 22 12:14:02 2007
  1074   SPIRE-FM-WFT-FUNC-SMEC-01-R.tcl         1.4          Mon
Oct 22 12:14:02 2007
  1075   SPIRE-FM-WFT-FUNC-SMEC-02A-P.tcl        1.4          Mon
Oct 22 12:14:02 2007
  1076   SPIRE-FM-WFT-FUNC-SMEC-02A-R.tcl        1.4          Mon
Oct 22 12:14:02 2007
  1077   SPIRE-FM-WFT-FUNC-SMEC-02B-P.tcl        1.4          Mon
Oct 22 12:14:02 2007
  1078   SPIRE-FM-WFT-FUNC-SMEC-02B-R.tcl        1.4          Mon
Oct 22 12:14:02 2007
  1079   SPIRE-FM-WFT-FUNC-SMEC-03-P.tcl         1.4          Mon
Oct 22 12:14:02 2007
  1080   SPIRE-FM-WFT-FUNC-SMEC-03-R.tcl         1.4          Mon
Oct 22 12:14:02 2007
  1081   SPIRE-FM-WFT-FUNC-SMEC-04A-P.tcl        1.4          Mon
Oct 22 12:14:02 2007
  1082   SPIRE-FM-WFT-FUNC-SMEC-04A-R.tcl        1.4          Mon
Oct 22 12:14:02 2007
  1083   SPIRE-FM-WFT-FUNC-SMEC-07-P.tcl         1.4          Mon
Oct 22 12:14:02 2007
  1084   SPIRE-FM-WFT-FUNC-SMEC-07-R.tcl         1.4          Mon
Oct 22 12:14:02 2007
  1085   SPIRE-FM-WFT-FUNC-SMEC-09-P.tcl         1.4          Mon
Oct 22 12:14:03 2007
  1086   SPIRE-FM-WFT-FUNC-SMEC-09-R.tcl         1.4          Mon
Oct 22 12:14:03 2007
  1087   SPIRE-FM-WFT-MCU-OFF-P.tcl              1.4          Mon
Oct 22 12:14:03 2007
  1088   SPIRE-FM-WFT-MCU-OFF-R.tcl              1.4          Mon
Oct 22 12:14:03 2007
  1089   SPIRE-FM-WFT-PDET-OFF-P.tcl             1.4          Mon
Oct 22 12:14:03 2007
  1090   SPIRE-FM-WFT-PDET-OFF-R.tcl             1.4          Mon
Oct 22 12:14:03 2007
  1091   SPIRE-FM-WFT-SCU-OFF-P.tcl              1.4          Mon
Oct 22 12:14:03 2007
  1092   SPIRE-FM-WFT-SCU-OFF-R.tcl              1.4          Mon
Oct 22 12:14:03 2007
  1093   SPIRE-FM-WFT-SDET-OFF-P.tcl             1.4          Mon
Oct 22 12:14:03 2007
  1094   SPIRE-FM-WFT-SDET-OFF-R.tcl             1.4          Mon
Oct 22 12:14:03 2007
  1095   SPIRE-FM-WFT-SMEC-INIT-P.tcl            1.4          Mon
Oct 22 12:14:03 2007
  1096   SPIRE-FM-WFT-SMEC-INIT-R.tcl            1.4          Mon
Oct 22 12:14:03 2007
  1097   SPIRE-FM-WFT-SMEC-OFF-P.tcl             1.4          Mon
Oct 22 12:14:03 2007
  1098   SPIRE-FM-WFT-SMEC-OFF-R.tcl             1.4          Mon
Oct 22 12:14:03 2007
  1099   SPIRE-IST-DBG-DPUON2STBY.tcl            1.6          Thu
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Sep 13 09:25:20 2007
  1100   SPIRE-IST-DBG-LOAD-VM-TABLES.tcl          1.1          Sat
Aug  4 11:04:24 2007
  1101   SPIRE-IST-DBG-OFF2DPUON-SP.tcl            1.3          Sat
Aug  4 11:03:21 2007
  1102   SPIRE-IST-DBG-OFF2DPUON.tcl               1.6          Fri
Aug 24 11:18:17 2007
  1103   SPIRE-IST-DBG-OPS2STBY.tcl                1.6          Sat
Aug  4 11:05:34 2007
  1104   SPIRE-IST-DBG-PHOT2STBY.tcl               1.1          Wed
May 23 09:24:16 2007
  1105   SPIRE-IST-DBG-SPEC2STBY.tcl               1.1          Wed
May 23 09:24:45 2007
  1106   SPIRE-IST-DBG-STBY-DCU-02.tcl             1.1          Fri
Apr 13 14:45:25 2007
  1107   SPIRE-IST-DBG-STBY2OFF.tcl                1.4          Sat
Aug  4 11:06:12 2007
  1108   SPIRE-IST-DBG-STBY2OPS.tcl                1.5          Sat
Aug  4 11:06:41 2007
  1109   SPIRE-IST-DBG-STBY2PHOT.tcl               1.4          Sat
Aug  4 11:26:49 2007
  1110   SPIRE-IST-DBG-STBY2SPEC.tcl               1.4          Sat
Aug  4 11:57:59 2007
  1111   SPIRE-IST-WU-EMC-CE-PHOTOPS2STBY.tcl      1.2          Thu
Sep 13 09:35:38 2007
  1112   SPIRE-IST-WU-EMC-CE-STBY2PHOTOPS.tcl      1.1          Fri
Aug 24 11:14:18 2007
  1113   SPIRE-WU-INT-DCU-01-P.tcl                 1.4          Tue
Mar 27 13:19:51 2007
  1114   SPIRE-WU-INT-DCU-01-R.tcl                 1.4          Tue
Mar 27 13:24:56 2007
  1115   SPIRE-WU-INT-DCU-02-P.tcl                 1.4          Tue
Mar 27 13:20:25 2007
  1116   SPIRE-WU-INT-DCU-02-R.tcl                 1.4          Tue
Mar 27 13:25:16 2007
  1117   SPIRE-WU-INT-DPU-START-P-SP.tcl           1.1          Wed
Apr 11 16:09:51 2007
  1118   SPIRE-WU-INT-DPU-START-P.tcl              1.5          Tue
Mar 27 13:20:48 2007
  1119   SPIRE-WU-INT-DPU-START-P__new.tcl         1.1          Tue
Mar 27 12:29:15 2007
  1120   SPIRE-WU-INT-DPU-START-R.tcl              1.4          Tue
Mar 27 13:25:40 2007
  1121   SPIRE-WU-INT-DRCU-OFF-P.tcl               1.4          Tue
Mar 27 13:21:09 2007
  1122   SPIRE-WU-INT-DRCU-OFF-R.tcl               1.4          Tue
Mar 27 13:26:04 2007
  1123   SPIRE-WU-INT-DRCU-START-P-STEP1.tcl       1.4          Tue
Mar 27 13:21:31 2007
  1124   SPIRE-WU-INT-DRCU-START-P-STEP2.tcl       1.4          Tue
Mar 27 13:21:50 2007
  1125   SPIRE-WU-INT-DRCU-START-R-STEP1.tcl       1.4          Tue
Mar 27 13:26:24 2007
  1126   SPIRE-WU-INT-DRCU-START-R-STEP2.tcl       1.4          Tue
Mar 27 13:26:33 2007
  1127   SPIRE-WU-INT-MCU-01-P.tcl                 1.4          Tue
Mar 27 13:22:57 2007
  1128   SPIRE-WU-INT-MCU-01-R.tcl                 1.4          Tue
Mar 27 13:27:02 2007
  1129   SPIRE-WU-INT-MCU-02-P.tcl                 1.4          Tue
Mar 27 13:23:27 2007
  1130   SPIRE-WU-INT-MCU-02-R.tcl                 1.4          Tue
Mar 27 13:27:24 2007
  1131   SPIRE-WU-INT-MCU-OFF-P.tcl                1.4          Tue
Mar 27 13:23:48 2007
  1132   SPIRE-WU-INT-MCU-OFF-R.tcl                1.4          Tue
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Mar 27 13:27:46 2007
  1133   SPIRE-WU-INT-SCU-01-P.tcl                  1.4          Tue
Mar 27 13:24:13 2007
  1134   SPIRE-WU-INT-SCU-01-R.tcl                  1.4          Tue
Mar 27 13:28:05 2007
  1135   SPIRE-WU-INT-SCU-02-P.tcl                  1.4          Tue
Mar 27 13:24:37 2007
  1136   SPIRE-WU-INT-SCU-02-R.tcl                  1.4          Tue
Mar 27 13:28:25 2007
  1137   SPIRE_OBS_2-2-G_Upload2DM_CCS.tcl          1.2          Wed
May  2 17:50:26 2007
  1138   SPIREx_SubscribeParams.tcl                 1.2          Thu
Apr 19 18:59:12 2007
  1139   SPU_MemCheck_All_12_81.tcl                 1.1          Thu
Feb 15 09:07:43 2007
  1140   SPU_MemCheck_All_12_81_0x10000.tcl         1.1          Tue
Jul 31 12:53:03 2007
  1141   SPU_MemCheck_All_12_81_0x10000_new.tcl     1.1          Sat
Aug  4 10:18:37 2007
  1142   SWITCH_BUS.tcl                             1.2          Sat
Jan 14 13:20:26 2006
  1143   SetupPhotometrySimulation.tcl              1.4          Thu
Jul 19 08:34:41 2007
  1144   SetupSpectroscopySimulation.tcl            1.4          Thu
Jul 19 08:36:52 2007
  1145   SpectroscopytoNonPrime_OBS_Shell.tcl       1.1          Thu
Oct 18 15:48:13 2007
  1146   SpectroscopytoNonprime.tcl                 1.2          Wed
Feb 14 17:00:35 2007
  1147   T001899MTXX001EPS_DUTY_CYCLE_TM.tcl        1.2          Fri
Sep 29 07:48:59 2006
  1148   T001899MTXX001EPS_IHMThermListeLignes.tcl 1.2            Fri
Sep 29 07:49:26 2006
  1149   T001899MTXX001EPS_Load_Thermal_Table.tcl 1.2             Fri
Sep 29 07:49:44 2006
  1150   T001899MTXX001EPS_TC_IMAGE.tcl             1.2          Fri
Sep 29 07:50:12 2006
  1151   T001899MTXX001EPS_Thermal_Line.tcl         1.2          Fri
Sep 29 07:50:42 2006
  1152   TC_aggregation_disable.tcl                 1.1          Mon
May  7 12:15:10 2007
  1153   TC_aggregation_enable.tcl                  1.1          Mon
May  7 12:23:53 2007
  1154   TMTC_DFE_checks.tcl                        1.1          Wed
Jul 20 10:35:19 2005
  1155   TM_data_new.tcl                            1.2          Fri
Jun 16 07:04:07 2006
  1156   TTC_SCOE_checks.tcl                        1.2          Wed
Nov  2 20:03:08 2005
  1157   W010584SPVT001_PCS_HEALTH_CHECK_RT5_B.tcl 1.5            Sat
Oct 14 11:34:40 2006
  1158   W010584SPVT002_PCS_HEALTH_CHECK_RT6_B.tcl 1.5            Sat
Oct 14 11:34:49 2006
  1159   W102584EPVT007_CHECK_PCDU.tcl              1.13         Mon
May 19 06:46:35 2008
  1160   W102584EPVT007_CHECK_PCDU_LAUNCH.tcl       1.2          Mon
Nov 27 15:56:16 2006
  1161   W102584EPVT007_CHECK_PCS_AIT_LAUNCH.tcl    1.3          Mon
May 19 06:49:32 2008
  1162   W102584EPVT007_IST_CHECK_PCDU.tcl          1.3          Wed
Sep 26 15:42:31 2007
  1163   W102584EPVT008_Acq_Data_PCDU.tcl           1.1          Sat
Oct 22 08:13:25 2005
  1164   W102584SPVT001_BAT_EOC_VERIF.tcl           1.3          Tue
Jun 19 09:54:25 2007
  1165   W102584SPVT002_BDR_DOD_MANAG_VER.tcl       1.5          Thu
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Jun 21 15:17:30 2007
  1166   W102584SPVT003_COM_AND_MON.tcl            1.13          Thu
Jun 21 12:21:18 2007
  1167   W102584SPVT004_DNEL_MANAG_VERIF.tcl       1.3           Thu
Jun 14 12:37:59 2007
  1168   W102584SPVT005_TWO_DOMAINS.tcl            1.7           Tue
Jun 19 09:31:05 2007
  1169   W102584SPVT010_NCA_VERIFICATION.tcl       1.11          Tue
Jun 19 14:33:29 2007
  1170   W102584SPVT011_EOC_ECLIPSE_4_SVT0.tcl     1.3           Fri
Jul 28 06:01:52 2006
  1171   W102584SPVT012_HCS_SWITCH_ON_OFF.tcl      1.2           Tue
Oct 10 06:20:08 2006
  1172   W102584SPVT013_TRANS_SUN_ECL_SUN.tcl      1.3           Sat
Nov 11 10:11:37 2006
  1173   W102584SPVT100_PCDU_TRANSITION.tcl        1.9           Mon
Jul 30 12:05:54 2007
  1174   W102584SPVT101_PCDU_TRANSITION_FDIR.tcl   1.5           Tue
May 15 15:53:24 2007
  1175   W102584SPVT102_PCDU_TRANSITION_EMC.tcl    1.4           Sat
Nov 24 14:50:44 2007
  1176   W102584SPVT102_PCDU_TRANSITION_MODE_TR.tcl 1.5           Wed
Oct 31 20:11:24 2007
  1177   W102584SPVT110_PCS_COMMISSIONING.tcl      1.3           Tue
Nov 13 10:51:11 2007
  1178   WHEELS_WATCH.tcl                          1.1           Wed
Sep  5 14:01:56 2007
  1179   Wave_Cal_FilA_Shell.tcl                   1.1           Thu
Oct 18 15:48:13 2007
  1180   Wave_Cal_FilB_Shell.tcl                   1.1           Thu
Oct 18 15:48:13 2007
  1181   Y102989ECVT001_TMTC_LINK.tcl              1.4           Thu
Nov 16 15:10:27 2006
  1182   Y102989ECVT003_TC_DFE_OUT_2_TTC.tcl       1.4           Wed
Aug 30 07:59:58 2006
  1183   Y102989ECVT004_TTC_RNG_SET.tcl            1.4           Wed
Aug 30 07:59:47 2006
  1184   Y102989ECVT005_TM_DFE_IN_FROM_TTC.tcl     1.5           Mon
Jun  4 06:42:36 2007
  1185   Y102989ECVT006_TTC_DL_PORT_SET.tcl        1.6           Wed
Aug 30 07:59:32 2006
  1186   Y102989ECVT007_TTC_UL_PARAM_SET.tcl       1.4           Wed
Aug 30 07:58:52 2006
  1187   Y102989ECVT008_TCRG_MI_SET_TTC.tcl        1.6           Thu
Nov 16 14:59:04 2006
  1188   Y102989ECVT009_TCRG_MI_SET_SSBV_RX.tcl    1.3           Wed
Aug 30 07:58:41 2006
  1189   Y102989ECVT018_TTC_TC_OP_METHOD.tcl       1.3           Wed
Aug 30 07:58:35 2006
  1190   Y102989EMVT010_PHASE_NOISE_MEAS.tcl       1.4           Sat
Sep  2 12:54:53 2006
  1191   Y102989EMVT011_MEAS_FREQ.tcl              1.4           Wed
Sep 27 11:17:54 2006
  1192   Y102989EMVT012_MEAS_POWER.tcl             1.3           Wed
Aug 30 07:58:16 2006
  1193   Y102989EMVT013_MEAS_MOD_INDEX.tcl         1.4           Wed
Aug 30 07:57:56 2006
  1194   Y102989EMVT014_MEAS_ANA_SPEC.tcl          1.3           Wed
Aug 30 07:58:10 2006
  1195   Y102989EMVT015_AMPLITUDE_RES.tcl          1.4           Thu
Sep 21 16:41:07 2006
  1196   Y102989EMVT016_MEAS_GROUP_DELAY.tcl       1.3           Wed
Aug 30 07:57:49 2006
  1197   Y102989EMVT023_TTC_ASA_COPY.tcl           1.3           Wed
Aug 30 07:56:23 2006
  1198   Y102989EPVT001_EMERGENCY_PWR_SCOE_ON.tcl  1.2           Fri
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Jan 13 10:00:48 2006
  1199   Y102989EPVT001_PWR_SCOE_ON.tcl            1.23          Mon
May 19 06:47:32 2008
  1200   Y102989EPVT001_PWR_SCOE_ON_AIT_LNC.tcl    1.6           Mon
May 19 13:19:03 2008
  1201   Y102989EPVT001_PWR_SCOE_ON_EMC.tcl        1.2           Wed
Nov 14 15:18:09 2007
  1202   Y102989EPVT001_PWR_SCOE_ON_LAUNCH.tcl     1.3           Thu
Nov 30 14:52:26 2006
  1203   Y102989EPVT002_EMERG_PWR_SCOE_OFF.tcl     1.1           Fri
Dec  1 16:21:23 2006
  1204   Y102989EPVT002_PWR_SCOE_OFF.tcl           1.17          Mon
May 19 06:55:46 2008
  1205   Y102989EPVT002_PWR_SCOE_OFF_AIT_LNC.tcl   1.2           Wed
Feb 14 07:56:35 2007
  1206   Y102989EPVT002_PWR_SCOE_OFF_CLN_LNCH.tcl  1.2           Thu
Jul 26 12:36:19 2007
  1207   Y102989EPVT002_PWR_SCOE_OFF_LAUNCH.tcl    1.3           Thu
Nov 30 14:58:47 2006
  1208   Y102989EPVT003_BS_CONN_REM_ONL.tcl         1.1           Sat
Oct 22 08:17:42 2005
  1209   Y102989EPVT004_SAS_CONN_REM_ONL.tcl        1.1           Sat
Oct 22 08:18:07 2005
  1210   Y102989EPVT005_Acq_Data_SCOE_PCDU.tcl      1.2           Tue
Jun 12 16:41:07 2007
  1211   Y102989EPVT006_Acq_Data_SCOE.tcl           1.1           Sat
Oct 22 08:18:42 2005
  1212   Y102989EPVT007_IST_PWR_SCOE_ON.tcl         1.7           Mon
Nov 26 13:41:43 2007
  1213   Y102989ETVT002_TM_DFE_STATISTICS.tcl       1.5           Wed
Aug 30 07:56:17 2006
  1214   Y102989ETVT017_TTC_CHECK_ROUTINE.tcl       1.5           Wed
Oct 25 14:06:12 2006
  1215   Y102989ETVT019_TTC_SCOE_ACTIVITY.tcl       1.5           Sat
Sep  2 08:34:19 2006
  1216   Y102989ETVT020_TTC_SCOE_OFF.tcl            1.4           Wed
Jul 25 17:48:15 2007
  1217   Y102989ETVT021_TTC_SCOE_ON.tcl             1.3           Thu
Jul 20 08:21:38 2006
  1218   Y102989ETVT022_TTC_UL_OFFLINE.tcl          1.4           Wed
Aug 30 07:55:52 2006
  1219   Y102989ETVT030_ASDGEN_PFM_VLV_GUI.tcl      1.5           Wed
Oct 17 20:50:42 2007
  1220   Y102989ETVT031_ASDGEN_PFM_HEATER_GUI.tcl   1.3           Wed
Oct 17 20:46:12 2007
  1221   Y102989ETVT032_ASDGEN_PFM_TBTV_LOG.tcl     1.7           Mon
Nov 26 16:23:22 2007
  1222   Y102989ETVT033_ASDGEN_PFM_SCOE_GUI.tcl     1.6           Mon
Nov 26 16:24:12 2007
  1223   Y102989ETVT034_ASDGEN_RPFM_LOG.tcl         1.2           Tue
Nov 20 15:43:41 2007
  1224   Y102989SCVT023_ECL_TO_SUN.tcl              1.1           Wed
Jun 28 14:21:32 2006
  1225   Z010999MCVT001_POWER_ON.tcl                1.21          Wed
Oct 31 16:28:25 2007
  1226   Z010999MCVT001_POWER_ON_FAST.tcl           1.5           Fri
Oct 27 13:48:09 2006
  1227   Z010999MCVT001_POWER_ON_HER_IST.tcl        1.23          Tue
Nov 20 18:46:36 2007
  1228   Z010999MCVT001_POWER_ON_IM2.tcl            1.1           Thu
Oct 25 12:55:00 2007
  1229   Z010999MCVT001_POWER_ON_LAUNCH_CONF.tcl    1.3           Thu
Nov 30 14:39:58 2006
  1230   Z010999MCVT001_POWER_ON_PMAI2.tcl          1.2           Fri
Oct 27 14:11:11 2006
  1231   Z010999MCVT001_POWER_ON_WITH_LPS.tcl       1.4           Mon
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Jun 11 15:07:13 2007
  1232   Z010999MCVT001__EMERGENCY_POWER_ON.tcl   1.2          Wed
Apr 18 08:49:19 2007
  1233   Z010999MCVT002_EMERGENCY_POWER_OFF.tcl   1.2          Fri
Dec  1 16:20:33 2006
  1234   Z010999MCVT002_EMERGENCY_SWITCH_OFF_ALL.tcl 1.4          Thu
Feb  1 16:17:36 2007
  1235   Z010999MCVT002_POWER_OFF.tcl             1.12         Wed
Oct 17 09:43:09 2007
  1236   Z010999MCVT002_POWER_OFF_CLN_LNCH.tcl    1.2          Thu
Jul 26 12:30:48 2007
  1237   Z010999MCVT002_POWER_OFF_FAST.tcl        1.4          Tue
Nov 20 03:25:52 2007
  1238   Z010999MCVT002_POWER_OFF_HER_IST.tcl     1.2          Wed
Feb 14 07:51:09 2007
  1239   Z010999MCVT002_POWER_OFF_IM2.tcl         1.3          Fri
Oct 26 10:00:42 2007
  1240   Z010999MCVT002_POWER_OFF_LAUNCH_CONF.tcl 1.2          Thu
Nov 30 14:39:48 2006
  1241   Z010999MCVT002_POWER_OFF_PMAI2.tcl       1.2          Fri
Oct 27 14:17:20 2006
  1242   Z010999MCVT002_POWER_OFF_WITH_LPS.tcl    1.4          Mon
Jun 11 15:07:41 2007
  1243   Z010999MCVT003_IST_START.tcl             1.44         Wed
Nov 21 15:43:55 2007
  1244   Z010999MCVT004_IST_END.tcl               1.20         Sat
Dec  1 04:45:48 2007
  1245   Z010999MCVT005_IST_START_SSMM.tcl        1.7          Tue
Nov 27 08:57:31 2007
  1246   Z010999MCVT010_SVM_SFT_HERSCHEL_IST.tcl  1.16         Tue
Dec  5 20:04:11 2006
  1247   Z010999MCVT011_STATUS_SPACECRAFT.tcl     1.18         Tue
Nov  6 16:30:43 2007
  1248   Z010999MCVT011_STATUS_SPACECRAFT_FDIR.tcl 1.2          Wed
Apr 18 08:43:37 2007
  1249   Z010999MCVT011_SVM_HERSCHEL_IST_1.tcl    1.14         Tue
Nov  6 16:35:52 2007
  1250   Z010999MCVT015_SVM_HER_IST_FDIR1.tcl     1.9          Fri
Nov 10 15:45:24 2006
  1251   Z010999MCVT020_SVM_HER_IST_FDIR2.tcl     1.13         Wed
Nov 22 12:07:11 2006
  1252   Z010999MCVT040_SVM_HER_IST_FDIR3.tcl     1.7          Thu
Apr  5 09:46:32 2007
  1253   Z010999MCVT050_SVM_HER_IST_FDIR4.tcl     1.7          Sat
Nov 11 13:44:04 2006
  1254   Z010999MCVT060_SVM_HER_IST_FDIR5.tcl     1.5          Fri
Nov 10 14:11:46 2006
  1255   Z010999MCVT070_SVM_HER_IST_FDIR6.tcl     1.10         Sat
Nov 11 12:01:23 2006
  1256   Z010999MCVT080_IST_FDIR_ASTRIUM.tcl      1.35         Wed
Nov 21 16:00:11 2007
  1257   Z010999MCVT081_IST_DEGRADED_ASTRIUM.tcl  1.2          Mon
Jun 11 13:34:49 2007
  1258   Z010999MCVT082_IST_LAUNCH_SEQ_ROBUST.tcl 1.15         Wed
Nov 21 15:57:46 2007
  1259   Z010999MCVT083_IST_NOM_MODE_ROBUST.tcl   1.12         Wed
Nov 21 16:00:50 2007
  1260   Z010999MCVT085_IST_RMS_ASTRIUM.tcl       1.18         Tue
Nov  6 11:17:01 2007
  1261   Z010999MCVT086_IST_DTCP_WORST_CASE.tcl   1.6          Fri
Sep 14 14:00:28 2007
  1262   Z010999MCVT087_WCS_DTCP.tcl              1.3          Thu
Jul  5 15:58:29 2007
  1263   Z010999MCVT088_WCS_MTL.tcl               1.2          Tue
Jun 26 19:19:05 2007
  1264   Z010999MCVT089_IST_SAT_COMMIS_ACMS.tcl   1.9          Sat
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Dec  1 04:29:44 2007
  1265   Z010999MCVT090_IST_DTCP_TRACE_CR.tcl      1.2          Thu
Sep 27 15:29:47 2007
  1266   Z010999MCVT090_IST_DTCP_TRACE_EPH.tcl     1.4          Mon
Nov  5 15:06:36 2007
  1267   Z010999MCVT091_IST_RMS_DTCP.tcl           1.6          Thu
Sep 27 15:59:15 2007
  1268   Z010999MCVT092_IST_LAUNCH_CLEAN_RUN.tcl   1.3          Sat
Nov 10 08:17:18 2007
  1269   Z010999MCVT093_IST_RMS_Date_Watch.tcl     1.4          Wed
May 21 09:51:38 2008
  1270   Z010999MCVT094_IST_DTCP_CDMS_MANAGM.tcl   1.13         Wed
Nov 21 15:58:35 2007
  1271   Z010999MCVT095_IST_CDMS_DTCP.tcl          1.7          Wed
Nov 21 15:59:15 2007
  1272   Z010999MCVT096_IST_SAT_COMMISSIONING.tcl  1.4          Fri
Nov 23 21:54:37 2007
  1273   Z010999MCVT097_ASDGEN_CRIT_PARS_CHECK.tcl 1.12         Sat
Dec  1 04:27:53 2007
  1274   Z010999MCVT100_IST_NOMINAL_LAUNCH.tcl     1.5          Sat
Oct 13 08:15:28 2007
  1275   Z010999MCVT101_IST_MODE_TRANSITIONS.tcl   1.4          Thu
Aug 16 12:34:27 2007
  1276   Z010999MCVT101_SAT_HER_IST_FDIR.tcl       1.1          Wed
Mar 28 08:59:15 2007
  1277   Z010999MCVT110_STATUS_SATELLITE.tcl       1.5          Wed
Apr 18 08:47:43 2007
  1278   Z010999MCVT120_SAT_TIME_COUNTER.tcl       1.9          Wed
Nov 21 15:43:33 2007
  1279   Z010999MCVT121_IST_LAUNCH_CNTR_LSR.tcl    1.10         Sat
Nov 17 08:54:30 2007
  1280   Z010999MCVT130_IST_INSTR_COMMISSIONING.tcl 1.6          Tue
Oct  2 09:17:45 2007
  1281   Z010999MCVT131_IST_INSTR_FDIR.tcl         1.4          Mon
Nov 26 10:18:41 2007
  1282   Z010999MCVT132_TCprotMode_BD_AD_BD.tcl    1.2          Fri
Oct 19 11:58:10 2007
  1283   Z010999MCVT133_CRYO_COVER_OPEN.tcl        1.2          Tue
Oct 23 15:20:25 2007
  1284   Z010999MCVT13_CRYO_COVER_OPEN.tcl         1.1          Fri
Oct 19 12:03:00 2007
  1285   Z010999MCVT153_IST_STATUS.tcl             1.23         Wed
Nov 21 15:42:55 2007
  1286   Z010999MCVT154_EMC_STATUS.tcl             1.3          Tue
Nov 20 18:47:39 2007
  1287   Z010999MCVT200_EMC.tcl                    1.9          Fri
Nov 30 17:04:13 2007
  1288   Z010999MCVT90_IST_DTCP_TRACE_CR_EPH.tcl   1.4          Sun
May 18 21:26:23 2008
  1289   Z010999MMXX002UNITS_CHECK.tcl             1.16         Mon
Oct  1 13:42:41 2007
  1290   Z010999MMXX003UNITS_CHECK_PWR_OFF.tcl     1.16         Mon
Oct  1 13:43:14 2007
  1291   Z102999GTVT000_GEN_COMMON_PROC_LIB.tcl    1.7          Fri
Nov 30 17:03:08 2007
  1292   Z102999GTVT000_GEN_COMMON_PROC_LIB_DEBUG.tcl 1.2
Sat Nov 24 16:21:25 2007
  1293   Z102999SCVT000_SAT_COM_SREM.tcl           1.2          Fri
Sep 14 14:03:38 2007
  1294   Z102999SCVT001_SREM_ON.tcl                1.13         Mon
Aug 27 09:47:17 2007
  1295   Z102999SCVT002_SREM_OFF.tcl               1.6          Tue
Nov 27 14:49:18 2007
  1296   Z102999SCVT003_SREM_ACQ_START.tcl         1.11         Sat
Sep 15 10:57:26 2007
  1297   Z102999SCVT004_ASDGEN_SPIREPWRON_P.tcl    1.6          Thu
```

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Nov 15 17:59:30 2007
  1298   Z102999SCVT005_ASDGEN_SPIREPWROFF_P.tcl  1.6          Thu
Nov 15 18:00:09 2007
  1299   Z102999SCVT006_ASDGEN_SPIREPWRON_R.tcl   1.5          Thu
Nov 15 18:00:35 2007
  1300   Z102999SCVT007_ASDGEN_SPIREPWROFF_R.tcl  1.5          Thu
Nov 15 18:00:59 2007
  1301   Z102999SCVT008_ASDGEN_SPIRESTBY2OPS.tcl  1.3          Mon
Aug 20 13:06:30 2007
  1302   Z102999SCVT009_ASDGEN_SPIREOPS2STBY.tcl  1.3          Mon
Aug 20 13:06:01 2007
  1303   Z102999SCVT010_ASDGEN_PACSPWRON_P.tcl    1.9          Sat
Nov 24 08:50:06 2007
  1304   Z102999SCVT011_ASDGEN_PACSPWROFF_P.tcl   1.8          Sat
Oct 27 14:50:11 2007
  1305   Z102999SCVT012_ASDGEN_PACSPWRON_R.tcl    1.6          Sat
Nov 24 08:50:55 2007
  1306   Z102999SCVT013_ASDGEN_PACSPWROFF_R.tcl   1.5          Sat
Oct 27 15:04:31 2007
  1307   Z102999SCVT014_ASDGEN_HIFIPWRON_P.tcl    1.3          Thu
Oct 18 10:18:47 2007
  1308   Z102999SCVT015_ASDGEN_HIFIPWROFF_P.tcl   1.3          Thu
Oct 18 10:18:16 2007
  1309   Z102999SCVT016_ASDGEN_HIFIPWRON_R.tcl    1.3          Thu
Oct 18 10:17:53 2007
  1310   Z102999SCVT017_ASDGEN_HIFIPWROFF_R.tcl   1.3          Thu
Oct 18 10:17:17 2007
  1311   Z102999SCVT018_ASDGEN_PACSBurstMode.tcl  1.2          Mon
Aug 20 12:56:29 2007
  1312   Z102999SCVT019_ASDGEN_PACSNomSpect.tcl   1.2          Mon
Aug 20 13:06:53 2007
  1313   acms_mdl.tcl                             1.2          Wed
Apr 18 09:25:41 2007
  1314   acms_mdl_fcv.tcl                         1.5          Wed
Apr 18 09:25:41 2007
  1315   acms_mdl_wheel.tcl                       1.2          Fri
Oct 27 17:29:14 2006
  1316   acms_prova.tcl                           1.2          Wed
Apr 18 09:25:41 2007
  1317   acms_prova_stefano.tcl                   1.2          Wed
Apr 18 09:22:32 2007
  1318   acms_tslew.tcl                           1.1          Fri
Oct 20 14:27:44 2006
  1319   date_watch.tcl                           1.3          Tue
Jul 24 12:27:18 2007
  1320   dry_loop_commands.tcl                    1.1          Wed
Apr  4 11:47:37 2007
  1321   getLogHistory.tcl                        1.1          Tue
Oct 30 09:32:13 2007
  1322   mclistbox.tcl                            1.1          Thu
Jun 15 14:28:32 2006
  1323   modify_NM_conf.tcl                       1.8          Sat
Sep 29 14:11:47 2007
  1324   modify_SM_conf.tcl                       1.5          Sat
Sep 29 14:12:11 2007
  1325   mtl_upload.tcl                           1.2          Mon
Nov 27 08:48:55 2006
  1326   provavlg.tcl                             1.2          Mon
Sep 10 11:45:43 2007
  1327   provola.tcl                              1.1          Wed
Mar 28 17:03:31 2007
  1328   simon_test1.tcl                          1.1          Fri
Oct 26 11:36:31 2007
  1329   test_jeff.tcl                            1.1          Tue
Jul 10 13:36:39 2007
  1330   vlg_temp.tcl                             1.4          Mon
```

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Jul 23 15:21:53 2007
  1331   ---------------------------------------------------
  1332
  1333   ---------------------------------------------------
  1334   Contents of /HPCCS/VARIABLE/ENVIRONMENTS/EXEC/USER
  1335   ---------------------------------------------------
  1336   CDM_only                                  1.1          Thu
Jun 15 14:29:55 2006
  1337   ETM_only                                  1.1          Thu
Jun 15 14:30:00 2006
  1338   IST_TEST_CONFIGURATION.txt                1.5          Wed
Nov 28 09:14:27 2007
  1339   OCM_only                                  1.1          Thu
Jun 15 14:30:04 2006
  1340   SAM_only                                  1.1          Thu
Jun 15 14:30:09 2006
  1341   SBM_only                                  1.1          Thu
Jun 15 14:30:13 2006
  1342   SCM_only                                  1.1          Thu
Jun 15 14:30:18 2006
  1343   SSM_only                                  1.1          Thu
Jun 15 14:30:24 2006
  1344   ---------------------------------------------------
  1345
  1346   ---------------------------------------------------
  1347   Contents of /HPCCS/VARIABLE/ENVIRONMENTS/EXEC/data/ASCII
  1348   ---------------------------------------------------
  1349   caf.dat                                   1.42         Sat
Oct 27 07:17:36 2007
  1350   cap.dat                                   1.41         Sat
Oct 27 07:17:36 2007
  1351   cca.dat                                   1.18         Thu
Aug 30 04:58:31 2007
  1352   ccf.dat                                   1.70         Thu
Nov 15 05:41:30 2007
  1353   ccs.dat                                   1.19         Thu
Aug 30 04:58:34 2007
  1354   cdf.dat                                   1.61         Thu
Nov 15 06:01:54 2007
  1355   cpc.dat                                   1.44         Thu
Nov 15 06:02:20 2007
  1356   cps.dat                                   1.1.1.1      Fri
Jun  3 05:56:08 2005
  1357   csf.dat                                   1.1.1.1      Fri
Jun  3 05:56:08 2005
  1358   csp.dat                                   1.1.1.1      Fri
Jun  3 05:56:08 2005
  1359   css.dat                                   1.1.1.1      Fri
Jun  3 05:56:08 2005
  1360   cur.dat                                   1.13         Wed
Jul 18 17:21:52 2007
  1361   cve.dat                                   1.15         Mon
Jul 23 16:22:44 2007
  1362   cvp.dat                                   1.52         Thu
Nov 15 05:41:47 2007
  1363   cvs.dat                                   1.31         Sat
Oct 27 07:17:53 2007
  1364   dpc.dat                                   1.92         Sat
Oct 27 07:17:53 2007
  1365   dpf.dat                                   1.67         Sat
Oct 27 07:17:55 2007
  1366   dst.dat                                   1.58         Sat
Oct 13 12:02:32 2007
  1367   gpc.dat                                   1.12         Sat
Oct 27 07:17:55 2007
  1368   gpf.dat                                   1.9          Mon
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Jul 23 16:22:47 2007			
1369	grp.dat	1.4	Wed
Feb 15 13:37:53 2006			
1370	grpa.dat	1.6	Wed
Feb 15 13:37:53 2006			
1371	grpk.dat	1.1.1.1	Fri
Jun 3 05:56:08 2005			
1372	lgf.dat	1.1.1.1	Fri
Jun 3 05:56:08 2005			
1373	mcf.dat	1.22	Tue
Jun 26 09:54:15 2007			
1374	ocf.dat	1.33	Sat
Oct 27 07:17:56 2007			
1375	ocp.dat	1.34	Sat
Oct 27 07:17:56 2007			
1376	paf.dat	1.36	Sat
Oct 27 07:17:56 2007			
1377	pas.dat	1.37	Sat
Oct 27 07:17:56 2007			
1378	pcdf.dat	1.8	Fri
May 4 05:01:01 2007			
1379	pcf.dat	1.69	Thu
Nov 15 05:41:47 2007			
1380	pcpc.dat	1.8	Fri
May 4 05:01:05 2007			
1381	pic.dat	1.11	Fri
May 4 05:01:05 2007			
1382	pid.dat	1.63	Thu
Nov 15 05:41:50 2007			
1383	plf.dat	1.61	Thu
Nov 15 05:41:51 2007			
1384	prf.dat	1.9	Mon
Jul 23 16:22:54 2007			
1385	prv.dat	1.9	Mon
Jul 23 16:22:54 2007			
1386	psm.dat	1.1.1.1	Fri
Jun 3 05:56:08 2005			
1387	pst.dat	1.1.1.1	Fri
Jun 3 05:56:08 2005			
1388	psv.dat	1.1.1.1	Fri
Jun 3 05:56:08 2005			
1389	ptv.dat	1.9	Mon
Jul 23 16:22:54 2007			
1390	pvs.dat	1.1.1.1	Fri
Jun 3 05:56:08 2005			
1391	sco.dat	1.27	Sat
Oct 27 07:18:02 2007			
1392	sdf.dat	1.1.1.1	Fri
Jun 3 05:56:08 2005			
1393	spc.dat	1.7	Mon
Jul 23 16:22:54 2007			
1394	spf.dat	1.7	Mon
Jul 23 16:22:54 2007			
1395	tcd.dat	1.23	Sat
Oct 13 11:56:29 2007			
1396	tcp.dat	1.11	Thu
Sep 13 14:53:50 2007			
1397	tmd.dat	1.26	Sat
Oct 27 07:18:02 2007			
1398	tpcf.dat	1.55	Thu
Nov 15 05:41:54 2007			
1399	txf.dat	1.47	Sat
Oct 27 07:18:02 2007			
1400	txp.dat	1.51	Sat
Oct 27 07:18:02 2007			
1401	vdf.dat	1.51	Thu

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Nov 15 05:41:54 2007
  1402   vpd.dat                                          1.26          Sat
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  1403   ------------------------------------------------
  1404
  1405   ------------------------------------------------
  1406   Contents of
/HPCCS/VARIABLE/ENVIRONMENTS/EXEC/data/ASCII/synthetic
  1407   ------------------------------------------------
  1408   AD001001                                         1.9           Fri
May  4 12:37:42 2007
  1409   AD001002                                         1.8           Fri
May  4 12:37:42 2007
  1410   AD001091                                         1.6           Fri
May  4 12:37:42 2007
  1411   AD001092                                         1.6           Fri
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  1412   AD001093                                         1.6           Fri
May  4 12:37:42 2007
  1413   AD001094                                         1.6           Fri
May  4 12:37:42 2007
  1414   AD001109                                         1.8           Fri
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  1415   AD002109                                         1.8           Fri
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  1416   AD003109                                         1.8           Fri
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  1417   AD004109                                         1.8           Fri
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  1418   AD005109                                         1.8           Fri
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  1419   AD006109                                         1.8           Fri
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  1421   AD008109                                         1.8           Fri
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  1422   AD009109                                         1.8           Fri
May  4 12:37:42 2007
  1423   AD010109                                         1.8           Fri
May  4 12:37:42 2007
  1424   AD011109                                         1.8           Fri
May  4 12:37:42 2007
  1425   AD700001                                         1.7           Fri
May  4 12:37:42 2007
  1426   AD700034                                         1.7           Fri
May  4 12:37:42 2007
  1427   AD700035                                         1.7           Fri
May  4 12:37:42 2007
  1428   AD701001                                         1.7           Fri
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  1429   HD087192                                         1.6           Mon
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  1430   HD111190                                         1.1           Mon
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  1431   HD112190                                         1.1           Mon
Jul 23 16:22:59 2007
  1432   HD212192                                         1.1           Mon
Jul 23 16:22:59 2007
  1433   HD247194                                         1.1           Mon
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  1434   HD248194                                         1.1           Mon
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  1435   HD249194                                         1.1           Mon
Jul 23 16:22:59 2007
  1436   HD289194                                         1.1           Mon
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1437	HD291192	1.1	Mon
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1438	HD329192	1.1	Mon
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1439	HD330191	1.1	Mon
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1440	HD701194	1.1	Mon
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1441	HD716194	1.1	Mon
Jul 23 16:23:00 2007			
1442	HD731194	1.1	Mon
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1443	HD746194	1.1	Mon
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1444	HD800194	1.1	Mon
Jul 23 16:23:01 2007			
1445	HD801194	1.1	Mon
Jul 23 16:23:01 2007			
1446	HD802194	1.1	Mon
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1448	KD001302	1.5	Fri
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1451	KD002303	1.5	Fri
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1452	KD003302	1.5	Fri
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1469	KD011303	1.5	Fri

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1474	KD014302	1.5	Fri
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1476	KD015302	1.5	Fri
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1478	KD016302	1.5	Fri
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1479	KD016303	1.5	Fri
May 4 12:37:44 2007			
1480	KD017302	1.5	Fri
May 4 12:37:44 2007			
1481	KD017303	1.5	Fri
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1482	KD018302	1.5	Fri
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1489	KD021303	1.5	Fri
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1495	KD202303	1.5	Fri
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1496	KD203302	1.5	Fri
May 4 12:37:44 2007			
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1506	KD208302	1.5	Fri
May 4 12:37:44 2007			
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1514	KD212302	1.5	Fri
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1515	KD212303	1.5	Fri
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1516	KD213302	1.5	Fri
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1517	KD213303	1.5	Fri
May 4 12:37:44 2007			
1518	KD214300	1.8	Sat
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1519	KD214301	1.8	Sat
Oct 27 07:18:05 2007			
1520	KD215300	1.8	Sat
Oct 27 07:18:05 2007			
1521	KD215301	1.8	Sat
Oct 27 07:18:05 2007			
1522	KD221302	1.5	Fri
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1523	KD221303	1.5	Fri
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1524	KD222302	1.5	Fri
May 4 12:37:44 2007			
1525	KD222303	1.5	Fri
May 4 12:37:44 2007			
1526	KD223302	1.5	Fri
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May 4 12:37:44 2007			
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May 4 12:37:44 2007			
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Oct 27 07:18:05 2007			
1539	KD231301	1.8	Sat
Oct 27 07:18:05 2007			
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May 4 12:37:44 2007			
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May 4 12:37:44 2007			
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May 4 12:37:45 2007			
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May 4 12:37:45 2007			
1562	KD243302	1.5	Fri
May 4 12:37:45 2007			
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1572	KD248302	1.5	Fri
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May 4 12:37:45 2007			
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May 4 12:37:45 2007			
1578	KD251302	1.5	Fri
May 4 12:37:45 2007			
1579	KD251303	1.5	Fri
May 4 12:37:45 2007			
1580	KD252302	1.5	Fri
May 4 12:37:45 2007			
1581	KD252303	1.5	Fri
May 4 12:37:45 2007			
1582	KD253302	1.5	Fri
May 4 12:37:45 2007			
1583	KD253303	1.5	Fri
May 4 12:37:45 2007			
1584	KD254302	1.5	Fri
May 4 12:37:45 2007			
1585	KD254303	1.5	Fri
May 4 12:37:45 2007			
1586	KD255302	1.5	Fri
May 4 12:37:45 2007			
1587	KD255303	1.5	Fri
May 4 12:37:45 2007			
1588	KD256302	1.5	Fri
May 4 12:37:45 2007			
1589	KD256303	1.5	Fri
May 4 12:37:45 2007			
1590	KD257302	1.5	Fri
May 4 12:37:45 2007			
1591	KD257303	1.5	Fri
May 4 12:37:45 2007			
1592	KD258302	1.5	Fri
May 4 12:37:45 2007			
1593	KD258303	1.5	Fri
May 4 12:37:45 2007			
1594	KD259302	1.5	Fri
May 4 12:37:45 2007			
1595	KD259303	1.5	Fri
May 4 12:37:45 2007			
1596	KD260302	1.5	Fri
May 4 12:37:45 2007			
1597	KD260303	1.5	Fri
May 4 12:37:45 2007			
1598	KD261302	1.5	Fri
May 4 12:37:45 2007			
1599	KD261303	1.5	Fri
May 4 12:37:45 2007			
1600	KD262302	1.5	Fri
May 4 12:37:45 2007			
1601	KD262303	1.5	Fri

May 4 12:37:45 2007			
1602	KD263300	1.8	Sat
Oct 27 07:18:05 2007			
1603	KD263301	1.8	Sat
Oct 27 07:18:05 2007			
1604	KD264302	1.5	Fri
May 4 12:37:45 2007			
1605	KD264303	1.5	Fri
May 4 12:37:45 2007			
1606	KD265302	1.5	Fri
May 4 12:37:45 2007			
1607	KD265303	1.5	Fri
May 4 12:37:45 2007			
1608	KD266300	1.8	Sat
Oct 27 07:18:05 2007			
1609	KD266301	1.8	Sat
Oct 27 07:18:05 2007			
1610	KD267300	1.8	Sat
Oct 27 07:18:05 2007			
1611	KD267301	1.8	Sat
Oct 27 07:18:05 2007			
1612	KD268300	1.8	Sat
Oct 27 07:18:05 2007			
1613	KD268301	1.8	Sat
Oct 27 07:18:05 2007			
1614	KD272300	1.5	Fri
May 4 12:37:45 2007			
1615	KD272301	1.5	Fri
May 4 12:37:45 2007			
1616	KD272302	1.8	Sat
Oct 27 07:18:05 2007			
1617	KD272303	1.8	Sat
Oct 27 07:18:05 2007			
1618	KD273300	1.5	Fri
May 4 12:37:45 2007			
1619	KD273301	1.5	Fri
May 4 12:37:45 2007			
1620	KD273302	1.8	Sat
Oct 27 07:18:05 2007			
1621	KD273303	1.8	Sat
Oct 27 07:18:05 2007			
1622	KD274302	1.5	Fri
May 4 12:37:45 2007			
1623	KD274303	1.5	Fri
May 4 12:37:45 2007			
1624	KD275302	1.5	Fri
May 4 12:37:45 2007			
1625	KD275303	1.5	Fri
May 4 12:37:45 2007			
1626	KD276302	1.5	Fri
May 4 12:37:45 2007			
1627	KD276303	1.5	Fri
May 4 12:37:45 2007			
1628	KD277302	1.5	Fri
May 4 12:37:45 2007			
1629	KD277303	1.5	Fri
May 4 12:37:45 2007			
1630	KD278302	1.5	Fri
May 4 12:37:45 2007			
1631	KD278303	1.5	Fri
May 4 12:37:45 2007			
1632	KD600300	1.5	Fri
May 4 12:37:45 2007			
1633	KD600301	1.8	Sat
Oct 27 07:18:05 2007			
1634	KD601300	1.5	Fri

May 4 12:37:45 2007			
1635	KD601301	1.5	Fri
May 4 12:37:45 2007			
1636	KD602300	1.5	Fri
May 4 12:37:45 2007			
1637	KD602301	1.5	Fri
May 4 12:37:45 2007			
1638	KD603300	1.5	Fri
May 4 12:37:45 2007			
1639	KD603301	1.5	Fri
May 4 12:37:45 2007			
1640	KD610300	1.8	Sat
Oct 27 07:18:05 2007			
1641	KD610301	1.8	Sat
Oct 27 07:18:05 2007			
1642	KD611300	1.8	Sat
Oct 27 07:18:05 2007			
1643	KD611301	1.8	Sat
Oct 27 07:18:05 2007			
1644	KD612300	1.8	Sat
Oct 27 07:18:05 2007			
1645	KD612301	1.8	Sat
Oct 27 07:18:05 2007			
1646	KD613300	1.8	Sat
Oct 27 07:18:05 2007			
1647	KD613301	1.8	Sat
Oct 27 07:18:05 2007			
1648	PD179380	1.5	Fri
May 4 12:37:45 2007			
1649	PD180380	1.5	Fri
May 4 12:37:45 2007			
1650	PD421410	1.5	Fri
May 4 12:37:45 2007			
1651	RD003442	1.7	Fri
May 4 12:37:45 2007			
1652	RD004442	1.7	Fri
May 4 12:37:45 2007			
1653	SD00M510	1.5	Fri
May 4 12:37:45 2007			
1654	SD00M515	1.5	Fri
May 4 12:37:45 2007			
1655	SD00M520	1.5	Fri
May 4 12:37:45 2007			
1656	SDB0H510	1.7	Fri
May 4 12:37:45 2007			
1657	SDB1H510	1.7	Fri
May 4 12:37:45 2007			
1658	SDF0H510	1.7	Fri
May 4 12:37:45 2007			
1659	SDH03510	1.3	Fri
May 4 12:37:45 2007			
1660	SDH03520	1.3	Fri
May 4 12:37:45 2007			
1661	SDH15520	1.3	Fri
May 4 12:37:46 2007			
1662	SDP0H510	1.7	Fri
May 4 12:37:46 2007			
1663	SDS0P515	1.5	Fri
May 4 12:37:46 2007			
1664	SD_0X510	1.5	Fri
May 4 12:37:46 2007			
1665	SD_1X510	1.5	Fri
May 4 12:37:46 2007			
1666	WD011565	1.7	Fri
May 4 12:37:46 2007			
1667	WD012565	1.7	Fri

May 4 12:37:46 2007			
1668	WD027565	1.7	Fri
May 4 12:37:46 2007			
1669	WD028565	1.7	Fri
May 4 12:37:46 2007			
1670	WD029565	1.7	Fri
May 4 12:37:46 2007			
1671	WD030565	1.7	Fri
May 4 12:37:46 2007			
1672	WD045565	1.7	Fri
May 4 12:37:46 2007			
1673	WD046565	1.7	Fri
May 4 12:37:46 2007			
1674	WD047565	1.7	Fri
May 4 12:37:46 2007			
1675	WD048565	1.7	Fri
May 4 12:37:46 2007			
1676	WD049565	1.7	Fri
May 4 12:37:46 2007			
1677	WD050565	1.7	Fri
May 4 12:37:46 2007			
1678	WD051565	1.7	Fri
May 4 12:37:46 2007			
1679	WD052565	1.7	Fri
May 4 12:37:46 2007			
1680	WD053565	1.7	Fri
May 4 12:37:46 2007			
1681	WD054565	1.7	Fri
May 4 12:37:46 2007			
1682	WD055565	1.7	Fri
May 4 12:37:46 2007			
1683	WD056565	1.7	Fri
May 4 12:37:46 2007			
1684	WD057565	1.7	Fri
May 4 12:37:46 2007			
1685	WD058565	1.7	Fri
May 4 12:37:46 2007			
1686	WD059565	1.7	Fri
May 4 12:37:46 2007			
1687	WD060565	1.7	Fri
May 4 12:37:46 2007			
1688	WD061565	1.7	Fri
May 4 12:37:46 2007			
1689	WD062565	1.7	Fri
May 4 12:37:46 2007			
1690	WD063565	1.7	Fri
May 4 12:37:46 2007			
1691	WD064565	1.7	Fri
May 4 12:37:46 2007			
1692	WD065565	1.7	Fri
May 4 12:37:46 2007			
1693	WD066565	1.7	Fri
May 4 12:37:46 2007			
1694	WD067565	1.7	Fri
May 4 12:37:46 2007			
1695	WD068565	1.7	Fri
May 4 12:37:46 2007			
1696	WD069565	1.7	Fri
May 4 12:37:46 2007			
1697	WD070565	1.7	Fri
May 4 12:37:46 2007			
1698	WD100565	1.5	Fri
May 4 12:37:46 2007			
1699	WD101565	1.5	Fri
May 4 12:37:46 2007			
1700	WD12A565	1.7	Fri

```
May  4 12:37:46 2007
  1701   WD22A565                                    1.7          Fri
May  4 12:37:46 2007
  1702   WD311565                                    1.7          Fri
May  4 12:37:46 2007
  1703   WD32D565                                    1.7          Fri
May  4 12:37:46 2007
  1704   WD32E565                                    1.7          Fri
May  4 12:37:46 2007
  1705   WD42A565                                    1.7          Fri
May  4 12:37:46 2007
  1706   WD42B565                                    1.7          Fri
May  4 12:37:46 2007
  1707   WD72A565                                    1.7          Fri
May  4 12:37:46 2007
  1708   WD72E565                                    1.7          Fri
May  4 12:37:46 2007
  1709   WD72F565                                    1.7          Fri
May  4 12:37:46 2007
  1710   WD811565                                    1.7          Fri
May  4 12:37:46 2007
  1711   WD82A565                                    1.7          Fri
May  4 12:37:46 2007
  1712   WD82D565                                    1.7          Fri
May  4 12:37:46 2007
  1713   WD82E565                                    1.7          Fri
May  4 12:37:46 2007
  1714   WDA06565                                    1.7          Fri
May  4 12:37:46 2007
  1715   WDA2A565                                    1.7          Fri
May  4 12:37:46 2007
  1716   YD001940                                    1.11         Fri
May  4 12:37:46 2007
  1717   ----------------------------------------------------
  1718
  1719   ----------------------------------------------------
  1720   Contents of /HPCCS/VARIABLE/ENVIRONMENTS/EXEC/data/rawTM
  1721   ----------------------------------------------------
  1722   (empty)
  1723   ----------------------------------------------------
  1724
```

6. Functional AIT Log Book

Date	**1.12.2007**	
Operator	**O.Martin, C.Much, S.Elsley continued by J. Maukisch, P. Modesto, D. Salvatore**	
Test Case	**EMC**	
OBSW	**CDMS 3.1.0.2, ACMS 3.7**	
HPSDB	**HPSDB 3.3.1.24 R_TM_HERSCH_FM9_711071940 (RN: H-P-2-ASP-LI-1441 issue 4 (with patch for NC-3673)**	
HPCCS Release	**hpccs_2_0_1166**	
Test Environment / Version	**HEAD**	
Session ID	**2007_12_1_05_16_hercdmu_hpws23_REALTIME_EMC_nois**	
Purpose of test	Debugging	X
	Formal run	
	NCR investigation	
	Unit integration testing	

Time (UTC)	Test Procedure / Step / Script / Command / Event / Anomaly	Remarks / Cause of anomaly / Corrective action	C/A type (T/P)	NCR ref. (PA)
	For Config Control: Generate Tag with "EMC Nois"			
05:30	**S/C Switch on (EMC Noisiest)**			
6:50	**Packet store definition failed in "storage sel def report" during rerun , 2 Tcs were rejected (allocate pkt store)**			
	TM check failed: No Telemetry packet received when TQD display correct value with 4sc update of TM SM_0V520 (SCUP5V)	NCR probably already raised about fetch		NCR-3798
10:36	During execution of HIFI_PWRON_P	Alarm:"HIFI OBS runtime error" - Known NCR on ASW 5.2 - sih		
10:36	Out Of limit of H_ptv_checksum (HD247194) value 28166 instead of 0	Expected at this point - sih		
10:38	During HIFI Power ON, Script HIFIST_Startup_LCU_table_load blocked due to wrong timestamp returned (value 0 instead of correct one)	This problem looks like NCR 3140 even if the workaround was set (script All_SubscribeParams was performed and still present in Test sequence console)		NCR-3799
11:44	Decision made to kill script HIFIST_Startup_LCU_table_load and restart it			
	Due to wrong comment in the EMC Procedure PACS_PWR_ON a			

Time (UTC)	Test Procedure / Step / Script / Command / Event / Anomaly	Remarks / Cause of anomaly / Corrective action	C/A type (T/P)	NCR ref. (PA)
	delay of 1,5 hours was caused			
12:23	HM061289 Value is jumping between 20 and 30 dgrs and is then decreasing slowly	HM061289 doesn't exist!!! - to be clarified by whoever wrote this - sih		
	PACS_PWR_ON does not show enough detail to follow the test	Please clarify whoever wrote this - sih?		
	Unclear how the EMC TCL configuration control is performed			
13:17	Shift handover from team C. Much to team J. Maukisch			
13:30 - 13:44	Execute TP-0155, chapter 8.6, step 16.1, B.4.4 => PR-0100, chapter 8.2.5.2 to switch HIFI to NOISIEST mode	**Note**: All HIFI scripts called up in chapter 8.2.5.2 do not come up with a script window (=> no operator information on script steps executed are displayed). As a consequence no script log is found on CCS (issued command however can be found in the command history log). Don't understand this comment HIFI script logs ARE present on CCS - sih		
14:24 - 15:20	Execute TP-0155, chapter 7.1, B.2.1. => PR-0100, chapter 8.2.7 switch STR1 to dumping mode and STR2 in standby mode			
15:24 - 16:20	Execute TP-0155, chapter 7.1, B.2.1. => PR-0100, chapter 8.2.7 switch STR1 to dumping mode and STR2 in standby mode			
16:20 - 17:16	Execute TP-0155, chapter 7.1, B.2.1. => PR-0100, chapter 8.2.7 switch STR1 to dumping mode and STR2 in standby mode			
16:45	Data from Test conductor store on USB stick			

Time (UTC)	Test Procedure / Step / Script / Command / Event / Anomaly	Remarks / Cause of anomaly / Corrective action	C/A type (T/P)	NCR ref. (PA)
	Test break			
17:33 18:29	Execute TP-0155, chapter 7.1, B.2.1. => PR-0100, chapter 8.2.7 switch STR1 to dumping mode and STR2 in standby mode			
18:30 - 19:26	Execute TP-0155, chapter 7.1, B.2.1. => PR-0100, chapter 8.2.7 switch STR1 to dumping mode and STR2 in standby mode			
19:27 -	Execute TP-0155, chapter 7.1, B.2.1. => TP-0100, chapter 8.2.7 switch STR1 to dumping mode and STR2 in standby mode			
19:50	End of EMC testing, HIFI=NOISIEST mode, PACS & SPIRE=STANDBY			
19:52	Start Switch OFF instruments, SVM & EGSE.			
19:52	Switch HIFI from NOISIEST -> STANDBY Execute PR-0100, chapter 8.2.5.3			
20:04 - 20:07	Switch SPIRE from STANDBY -> OFF Execute PR-0100, chapter 8.2.4.4	Note: SPIRE I-EGSE was disconnected but not switched off as it was unclear how and where to do this. SPIRE I-EGSE shouldn't be switched off only disconnected - sih		
20:14 - 20:20	Switch HIFI from STANDBY -> OFF Execute PR-0100, chapter 8.2.5.4			
20:21 - 20:26	Switch PACS from STANDBY -> OFF Execute PR-0100, chapter 8.2.3.4			
20:28 -	Switch SVM & CCU OFF Execute PR-0100, chapter 8.4.1.1			
20:38	SREM OFF	DEF4M160 expected NO, actual YES DEF4N160 expected NO, actual YES		

Time (UTC)	Test Procedure / Step / Script / Command / Event / Anomaly	Remarks / Cause of anomaly / Corrective action	C/A type (T/P)	NCR ref. (PA)
		DEF4P160 expected OFF, actual ON DEF4R160 expected NO, actual YES DEF4S160 expected NO, actual YES		
21:02	Turn off SSMM	DEH89170 expected off, actual ON. TM correctly checked in TQD		

Date	**Date** **3.12.2007**	
Operator	**O.Martin, C.Much, S.Elsley**	
Test Case	**EMC**	
OBSW	**CDMS 3.1.0.2, ACMS 3.7**	
HPSDB	**HPSDB 3.3.1.24 R_TM_HERSCH_FM9_711071940 (RN: H-P-2-ASP-LI-1441 issue 4 (with patch for NC-3673)**	
HPCCS Release	**hpccs_2_0_1166**	
Test Environment / Version	**HEAD**	
Session ID	**2007_12_3_05_00_hercdmu_hpws23_REALTIME_EMC_nois2**	
Purpose of test	Debugging	Formal run EMC 2
	NCR investigation	
	NCR investigation	
	Unit integration testing	

Time (UTC)	Test Procedure / Step / Script / Command / Event / Anomaly	Remarks / Cause of anomaly / Corrective action	C/A type (T/P)	NCR ref. (PA)
5:23	**S/C switch on started**			
5:30	**DFE TC "YM101946 ==online" failed**	TM/TC FE was left off from last shift		
	Restarted TM/TC FE and resend **"YM101946 ==online"**	0.k.		
6:36	Warning from Apid 2016 New TM 251002939			
7:18	TM failure in OnBoardSchedule Initialisation	Pb known in previous session		NCR 3650
8:31	SVM in noisiest mode			
8:44	No TM received by checking DPU LCL	2 retry necessary to get correct value		
8:45	Lot of Out Of Limit occurs after TC FORCE_BOOT_SECONDARY	NCR?? - No this is expected during power on of SPIRE (as explained to team on Saturday) - sih		
9:20	HIFI switch ON: TC ForceBootDEfault HC162289 sent but acceptance rejected	Old NCR which should be solved in HPSDB but reoccur. Not an error to be expected BSW cannot acknowledge command and ASW is not running at this point - sih		
9:22	HIFI_OBS_runtime_Error OutOfLimit of TM HM258194	Known NCR on ASW 5.2 - sih To be checked but probably expected - sih		
9:25	Sequence blocked by HIFIST_Startup_LCU_table_load: TM YM3FF962 returns always a timestamp 2007.337.09.22.31 without update. But the TQD return a value (29) last updated on 2007.337.09.25.45	Kill sequence and restart		NCR-3799
9:47	Out of Limit of TM HM188192, HM187192, HM062193	Problem in TM read		
10:04	Sequence blocked by HIFIST_Startup_LO_Nominal: TM YM3FF962 returns always a timestamp 2007.337.09.43.27 without update. But the TQD return a value (29) last updated	Kill sequence and restart		NCR-3799
13:27	**********shift handover **********	**********		

Date	**Date** **3.12.2007**	
Operator	**J. Maukisch, P. Modesto, D. Salvatore**	
Test Case	**EMC**	
OBSW	**CDMS 3.1.0.2, ACMS 3.7**	
HPSDB	**HPSDB 3.3.1.24 R_TM_HERSCH_FM9_711071940 (RN: H-P-2-ASP-LI-1441 issue 4 (with patch for NC-3673)**	
HPCCS Release	**hpccs_2_0_1166**	
Test Environment / Version	**HEAD**	
Session ID	**2007_12_3_05_00_hercdmu_hpws23_REALTIME_EMC_nois2**	
Purpose of test	Debugging	X
	Formal run	
	NCR investigation	
	Unit integration testing	

Time (UTC)	Test Procedure / Step / Script / Command / Event / Anomaly	Remarks / Cause of anomaly / Corrective action	C/A type (T/P)	NCR ref. (PA)
13:59 - 14:02	Switch **HIFI** from NOISEST -> STANDBY Execute PR-0100, chapter 8.2.5.3			
14:04 - 14:05	Switch **SPIRE** from STANDBY -> NOISEST Execute PR-0100, chapter 8.2.4.2			
14:05	**RWLs** are still running			
14:11- 14:20	**RWLs** are switched off (45)			
- 14:10	Execute TP-0155, chapter 7.1, B.2.1. => PR-0100, chapter 8.2.7 switch **STR1** to dumping mode and **STR2** in standby mode			
14:11 - 15:10	Execute TP-0155, chapter 7.1, B.2.1. => PR-0100, chapter 8.2.7 switch **STR1** to dumping mode and **STR2** in standby mode			
14:56 - 14:57	Switch **SPIRE** from NOISEST -> STANDBY Execute PR-0100, chapter 8.2.4.3			
15:09 - 15:39	Test Break			

Time (UTC)	Test Procedure / Step / Script / Command / Event / Anomaly	Remarks / Cause of anomaly / Corrective action	C/A type (T/P)	NCR ref. (PA)
15:39 - 16:36	Execute TP-0155, chapter 7.1, B.2.1. => PR-0100, chapter 8.2.7 switch **STR1** to dumping mode and **STR2** in standby mode			
15:55	**Instruments** (in SAT synoptic): SPIRE = STANDBY ON:REDY HIFI = STANDBY STDBY / PRIME PACS = STANDBY (SAFE) STDBY/NON - PRIME			
16:37 - 17:34	Execute TP-0155, chapter 7.1, B.2.1. => PR-0100, chapter 8.2.7 switch **STR1** to dumping mode and **STR2** in standby mode			
17:34 - 18:31	Execute TP-0155, chapter 7.1, B.2.1. => PR-0100, chapter 8.2.7 switch **STR1** to dumping mode and **STR2** in standby mode			
18:39 -	Execute TP-0155, chapter 7.1, B.2.1. => PR-0100, chapter 8.2.7 switch **STR1** to dumping mode and **STR2** in standby mode			
19:00	End of EMC test, start switch off Instruments, SVM & EGSE			
19:03 - 19:09	Switch **SPIRE** from STANDBY -> OFF Execute PR-0100, chapter 8.2.4.4			
19:10 - 19:17	Switch **HIFI** from STANDBY -> OFF Execute PR-0100, chapter 8.2.5.4	Out of limit: HM062193 & HM259194		
19:19 - 19:25	witch **PACS** from STANDBY -> OFF Execute PR-0100, chapter 8.2.3.4			
19:28 - 20:00	Switch SVM & CCU OFF Execute PR-0100, chapter 8.4.1.1			
19:59	Z102999SCVT002_SREM_OFF	DEF4M160 expected NO, actual YES DEF4N160 expected NO, actual YES DEF4P160 expected OFF, actual ON DEF4R160 expected NO, actual YES DEF4S160 expected NO, actual YES		
20:07	IST_END	No Crome report received		

Time (UTC)	Test Procedure / Step / Script / Command / Event / Anomaly	Remarks / Cause of anomaly / Corrective action	C/A type (T/P)	NCR ref. (PA)
		Script is not stopping !!		
20:10	WRITE_CROME	No Crome report received Script is not stopping !!		
20:19	IST_END	DEH89170 expected off, actual ON. TM correctly checked in TQD After repeat value OK		

Date		
Operator		
Test Case		
OBSW		
HPSDB		
HPCCS Release		
Test Environment / Version		
Session ID		
Purpose of test	Debugging	X
	Formal run	
	NCR investigation	
	Unit integration testing	

Time (UTC)	Test Procedure / Step / Script / Command / Event / Anomaly	Remarks / Cause of anomaly / Corrective action	C/A type (T/P)	NCR ref. (PA)

Date	**Date** **4.12.2007**	
Operator	**O.Martin, C.Much, S.Elsley continued by J. Maukisch, D. Liberatore, Ian Luck**	
Test Case	**EMC & ACS HP-2-ASED-SD-0205, issue 1B**	
OBSW	**CDMS 3.1.0.2, ACMS 3.7**	
HPSDB	**HPSDB 3.3.1.24 R_TM_HERSCH_FM9_711071940 (RN: H-P-2-ASP-LI-1441 issue 4 (with patch for NC-3673)**	
HPCCS Release	**hpccs_2_0_1166**	
Test Environment / Version	**HEAD**	
Session ID	**2007_12_4_06_39_hercdmu_hpws22_REALTIME_EMC_nois3**	
Purpose of test	Debugging	X
	Formal run	Formal run EMC 3
	NCR investigation	
	Unit integration testing	

Time (UTC)	Test Procedure / Step / Script / Command / Event / Anomaly	Remarks / Cause of anomaly / Corrective action	C/A type (T/P)	NCR ref. (PA)
06:45	**Reference Mode reached**			
12:45	**Measurement finished End of Test**			
	Start execution of Comparison of Cryo SCOE & S/C CCU Sensor Readings			
15:00	Execute ACS Step-by-step procedure, step 36-38			
15:03 - 15:49	Execute ACS Step-by-step procedure, step 39, chapter 7.1 – 7.5 Note: Started with chapter 7.5 of appendix 11 as CCS, TM/TC & SCOEs were already switched on Bus Monitor cannot be switched on	Bus Monitor cannot be switched on After Reset at CDMU SCOE rack -> OK		
15:50	Execute ACS Step-by-step procedure, step 40			
16:00	**No step to switch on (off) CCU A&B into mode 2 in procedure of the ACS.**	Update procedure in ACS		
16:23 - 16:30	Switch on CCU A&B K102999ECVT001_ASDGENCCU_ABPWRON	Missing step in procedure		

END OF DOCUMENT

	Name	Dep./Comp.		Name	Dep./Comp.
	Alberti von Mathias Dr.	ASG23		Schmidt Thomas	AED15
	Baldock Richard	FAE12		Schuler Günter	ASA42
	Barlage Bernhard	AED13		Schweickert Gunn	ASG23
	Bayer Thomas	ASA42	x	Sonn Nico	ASG51
	Brune Holger	ASA45		Steininger Eric	AED32
	Edelhoff Dirk	AED2	x	Stritter Rene	AED11
	Fehringer Alexander	ASG13		Suess Rudi	OTN/ASA44
x	Fricke Wolfgang Dr.	AED 65	x	Theunissen Martiin	DSSA
	Geiger Hermann	ASA42	x	Vascotto Riccardo	AED11
	Grasl Andreas	OTN/ASA44		Wagner Klaus	ASG23
	Grasshoff Brigitte	AET12	x	Wietbrock Walter	AET12
x	Hamer Simon	Terma		Wöhler Hans	ASG23
	Hendrikse Jeffrey	HE Space		Wössner Ulrich	ASE252
	Hendry David	Terma		Zumstein Armin	ASQ42
	Hengstler Reinhold	ASA42			
	Hinger Jürgen	ASG23			
x	Hohn Rüdiger	AED65			
	Hölzle Edgar Dr.	AED32			
x	Hopfgarten Michael				
	Huber Johann	ASA42			
	Hund Walter	ASE252			
x	Idler Siegmund	AED312			
	Iványi von András	FAE12			
	Jahn Gerd Dr.	ASG23			
x	Kalde Clemens	ASM2			
	Kameter Rudolf	OTN/ASA42	x	ESA/ESTEC	ESA
	Kettner Bernhard	AET42	x	Thales Alenia Space Cannes	TAS-F
	Knoblauch August	AET32	x	Thales Alenia Space Torino	TAS-I
x	Koelle Markus	ASA43			
x	Koppe Axel	AED312		**Instruments:**	
x	Kroeker Jürgen	AED65	x	MPE (PACS)	MPE
x	La Gioia Valentina	Terma	x	RAL (SPIRE)	RAL
	Lang Jürgen	ASE252	x	SRON (HIFI)	SRON
	Langenstein Rolf	AED15			
	Langfermann Michael	ASA41			
	Martin Olivier	ASA43		**Subcontractors:**	
x	Maukisch Jan	ASA43		Austrian Aerospace	AAE
x	Much Christoph	ASA43		Austrian Aerospace	AAEM
	Müller Jörg	ASA42		BOC Edwards	BOCE
x	Müller Martin	ASA43		Dutch Space Solar Arrays	DSSA
	Peltz Heinz-Willi	ASG13		EADS Astrium Sub-Subsyst. &	ASSE
	Pietroboni Karin	AED65		EADS CASA Espacio	CASA
	Platzer Wilhelm	AED2		EADS CASA Espacio	ECAS
	Reichle Konrad	ASA42		European Test Services	ETS
	Runge Axel	OTN/ASA44		Patria New Technologies Oy	PANT
	Sauer Maximilian Dr.	AED65		SENER Ingenieria SA	SEN
x	Schink Dietmar	AED32		Thales Alenia Space, Antwerp	TAS-ETCA