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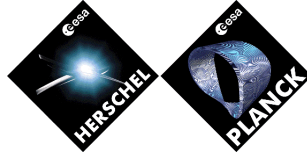
**PLANCK SYSTEM Electrical Interface
Control Document EICD**

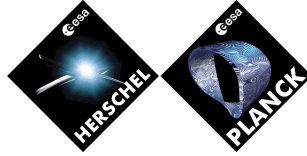
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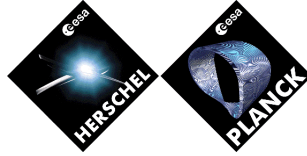
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2	17/05/2004	Document revised considering the implementation details as provided by subcontractors	K.R.Hibberd

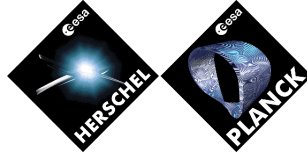
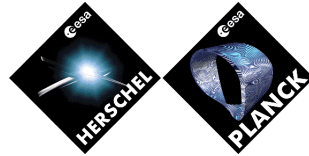


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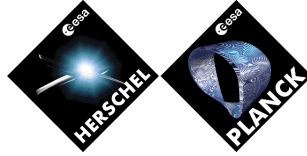
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1. SCOPE

This document describes all of the interfaces as seen and required for test and integration at system level. Hence all SVM and PLM level interfaces are described along with all internal SVM interfaces. Instrument interfaces upto their connections with the system are described. Internal instrument interfaces are not detailed in this document.

1.1 Input Documents

ALENIA SVM EICD H-P-IC-AL-0003 issue 4.0
HFI IID-B SCI-PT-IIDB/HFI-04141 issue 3.1
LFI IID-B SCI-PT-IIDB/LFI-04142 issue 3.1
SCS Interface Control Document PL-LFI-PST-ID-002 Issue 2.2
Planck PLM EICD H-P-3-ASP-ID-0550 issue 2.0

2. DOCUMENT DESCRIPTION

This chapter describes how this document is organised and how the reader may navigate this EICD.

This document considers the satellite as several blocks, the interface of each block at the block boundary is fully described.

For the SVM, PLM and Miscellaneous blocks, the elements constituting that block are also described in terms of electrical interfaces. It should be highlighted that this document considers each instrument as a "black box", only electrical interfaces at instrument level are detailed, internal interfaces are described in the appropriate instrument EICD.

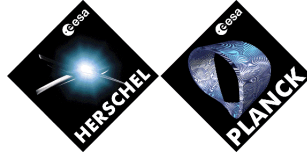
This document describes the electrical interfaces in terms of

- Electrical Block Diagrams (interconnection drawings) – these show the elements of the block, the connectors and harness
- List of Connections – a table providing information for each pin of each connector of the block boundary. For each signal a reference is provided for the associated circuit diagram and signal characteristic.
- Interface Signal Characteristics – a table of electrical parameters for each specific interface as referenced in the List of Connections. The values for each parameter should be those as actually implemented in the interface, not as specified, e.g. the specification may call for an input impedance of $1M\Omega$, the actual interface may satisfy this requirement with an input impedance of $10M\Omega$, in which case $10M\Omega$ should be reported in the Interface Signal Characteristic table. Note that each interface shall provide specific interface characteristics even if the interface is "generic" and used elsewhere.
- Interface Circuit Diagrams – an electrical schematic representation for each specific interface as referenced in the List of Connections. The values for each component should be given and shall be the same as those as actually implemented in the interface. Note that each interface shall provide specific interface circuit diagram even if the interface is "generic" and used elsewhere.

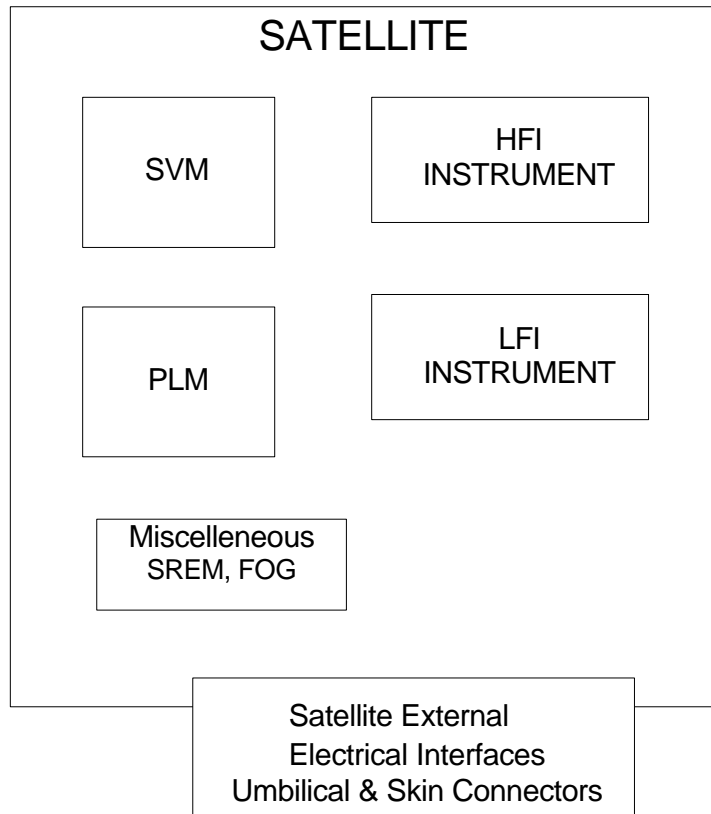
For each of the above topics the satellite is divided as:

Satellite level – all electrical interfaces which are at the boundary of the satellite, e.g. Umbilical connectors and skin connectors.

SVM – all electrical interfaces at the boundary and between the SVM units, this will include the power outputs, the TM/TC interfaces both to the instruments, PLM and miscellaneous units.



Instruments – all electrical interfaces at the boundary of the instrument as described in the relevant IID. Interfaces which are totally within the instrument i.e. those between instrument units, will not be detailed within this EICD.
 Miscellaneous – all electrical interfaces which are not covered by one of the above, these include the SREM and the FOG.



3. INTERCONNECTION DRAWINGS

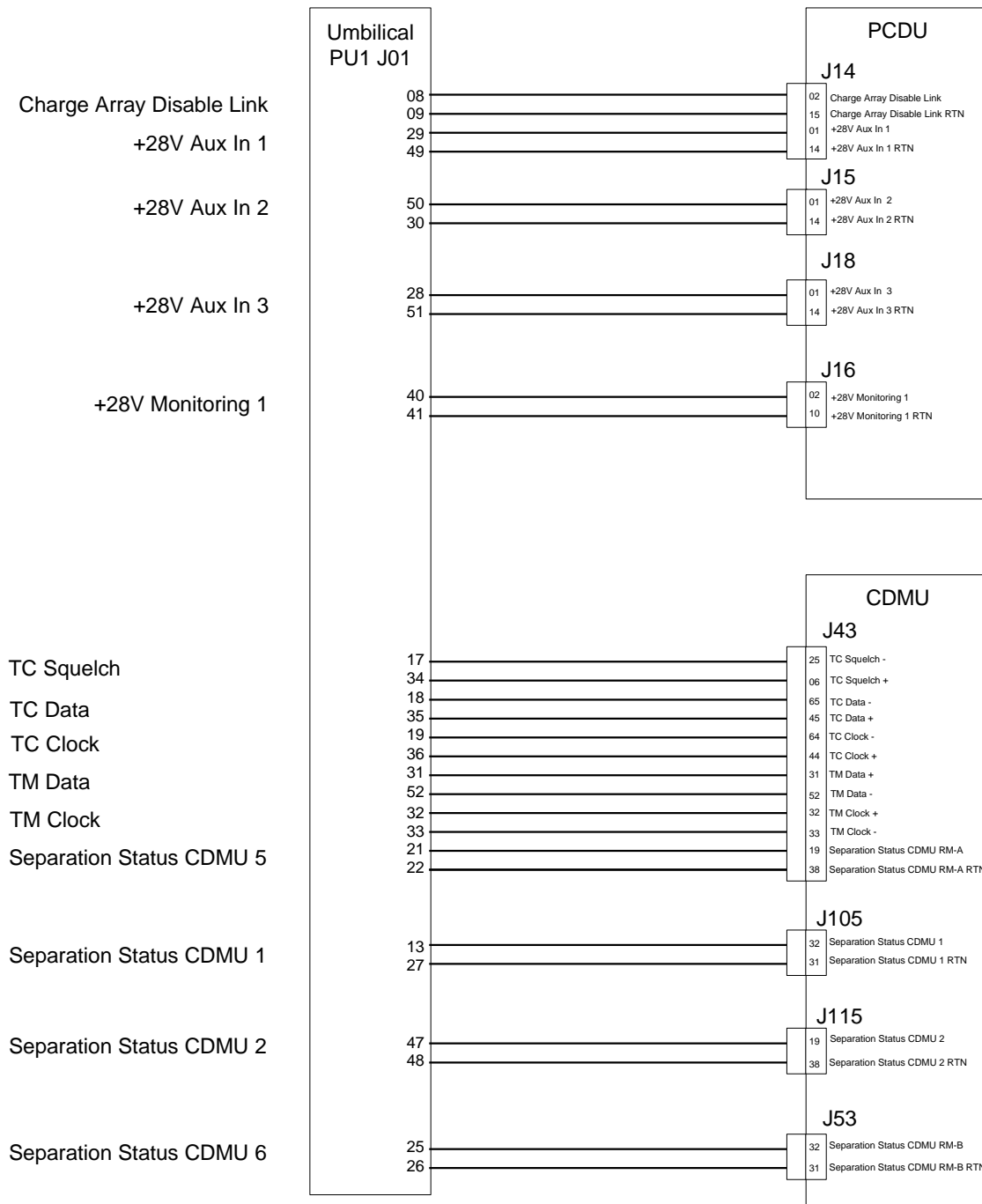
The following drawings show how the external interfaces, the PLM interfaces and the Instrument interfaces are connected to the SVM units.

For the sake of clarity any intermediate connectors (dismountability connectors) have not been shown.

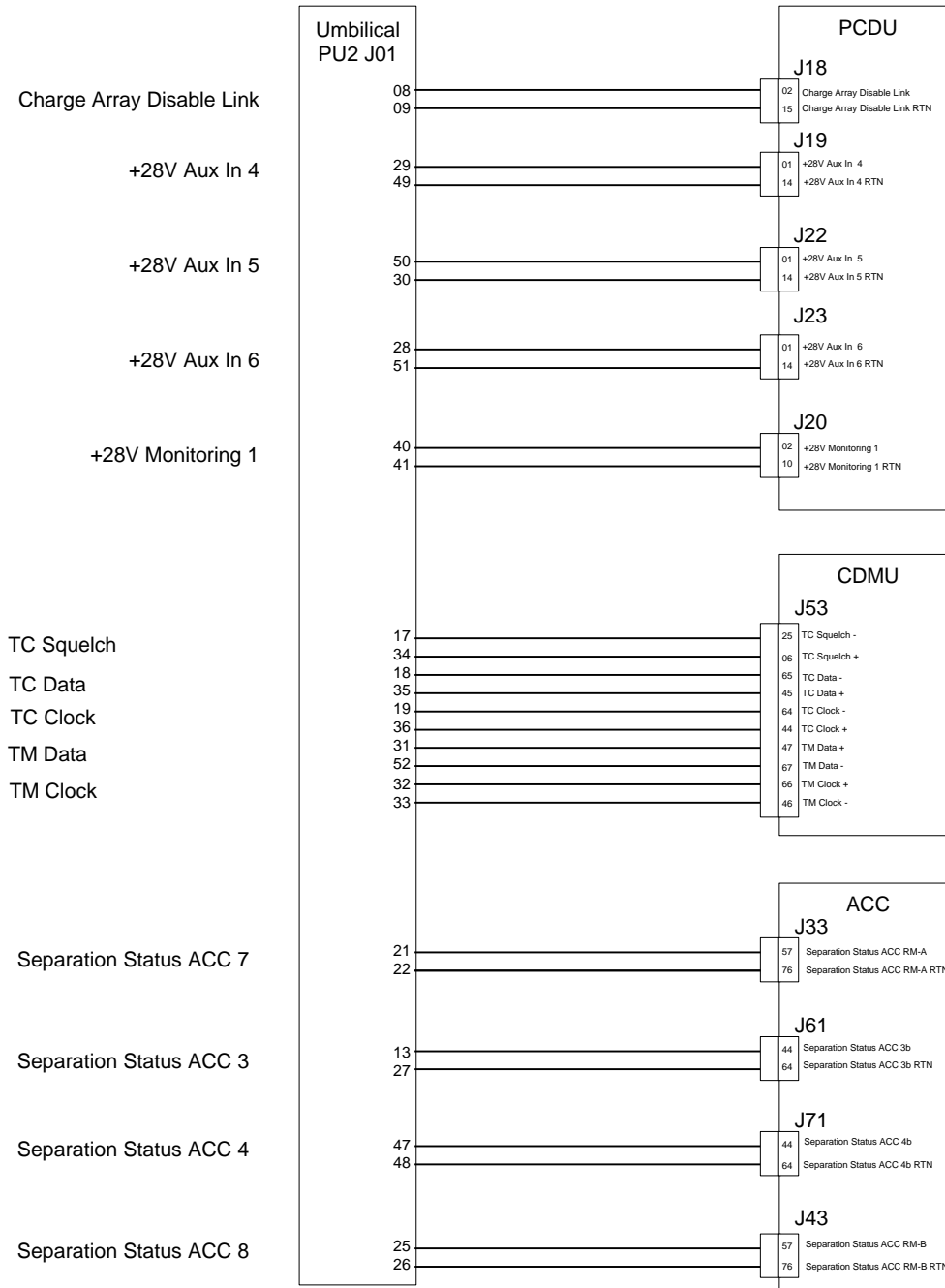
Full information regarding types of connectors are listed in chapter 7.

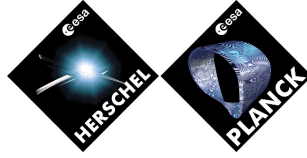
3.1 Satellite Level Umbilical Connectors

3.1.1 Satellite Level Umbilical Connectors PU1 J01

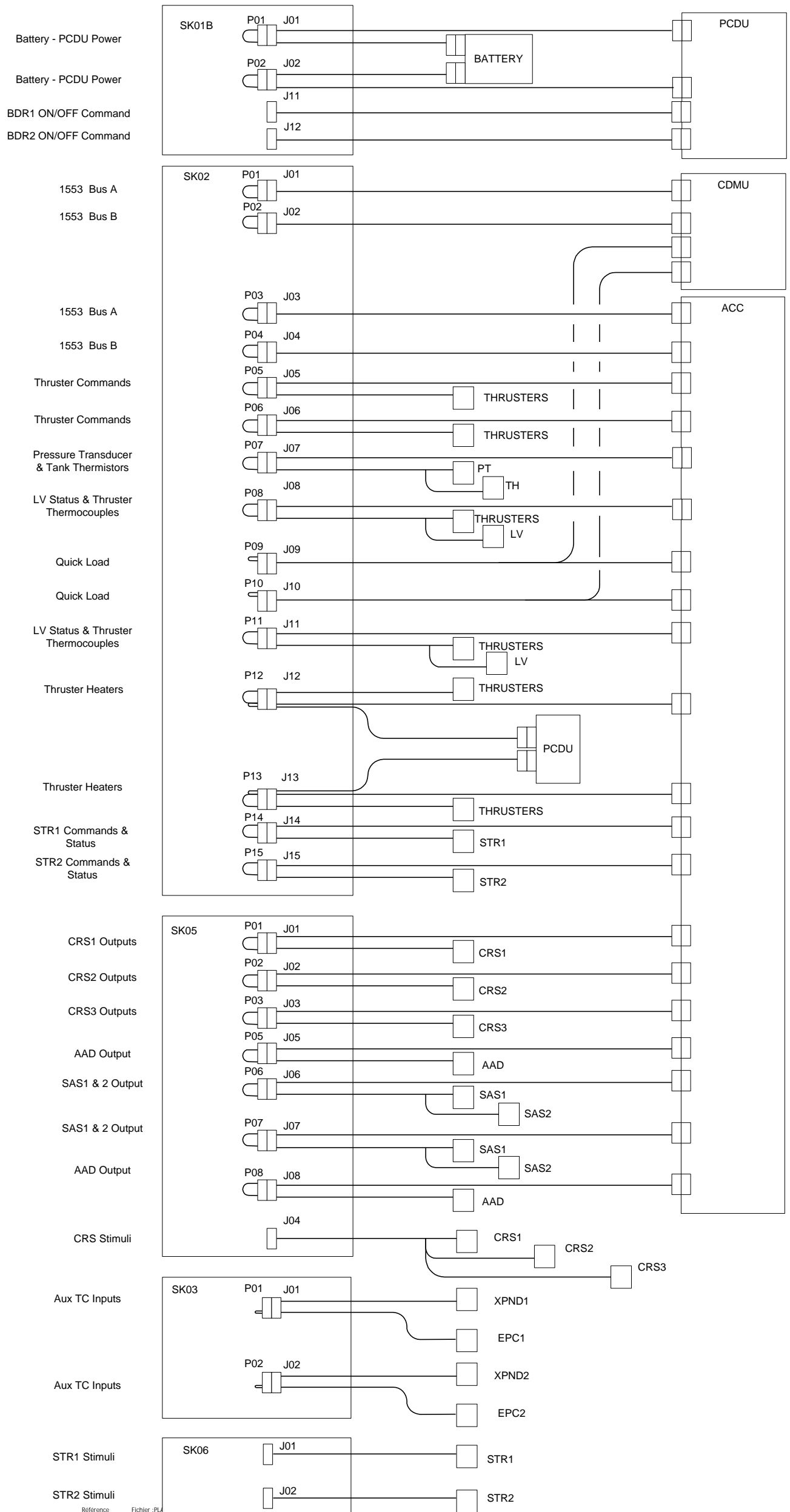


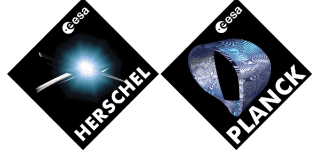
3.1.2 Satellite Level Umbilical Connectors PU2 J01



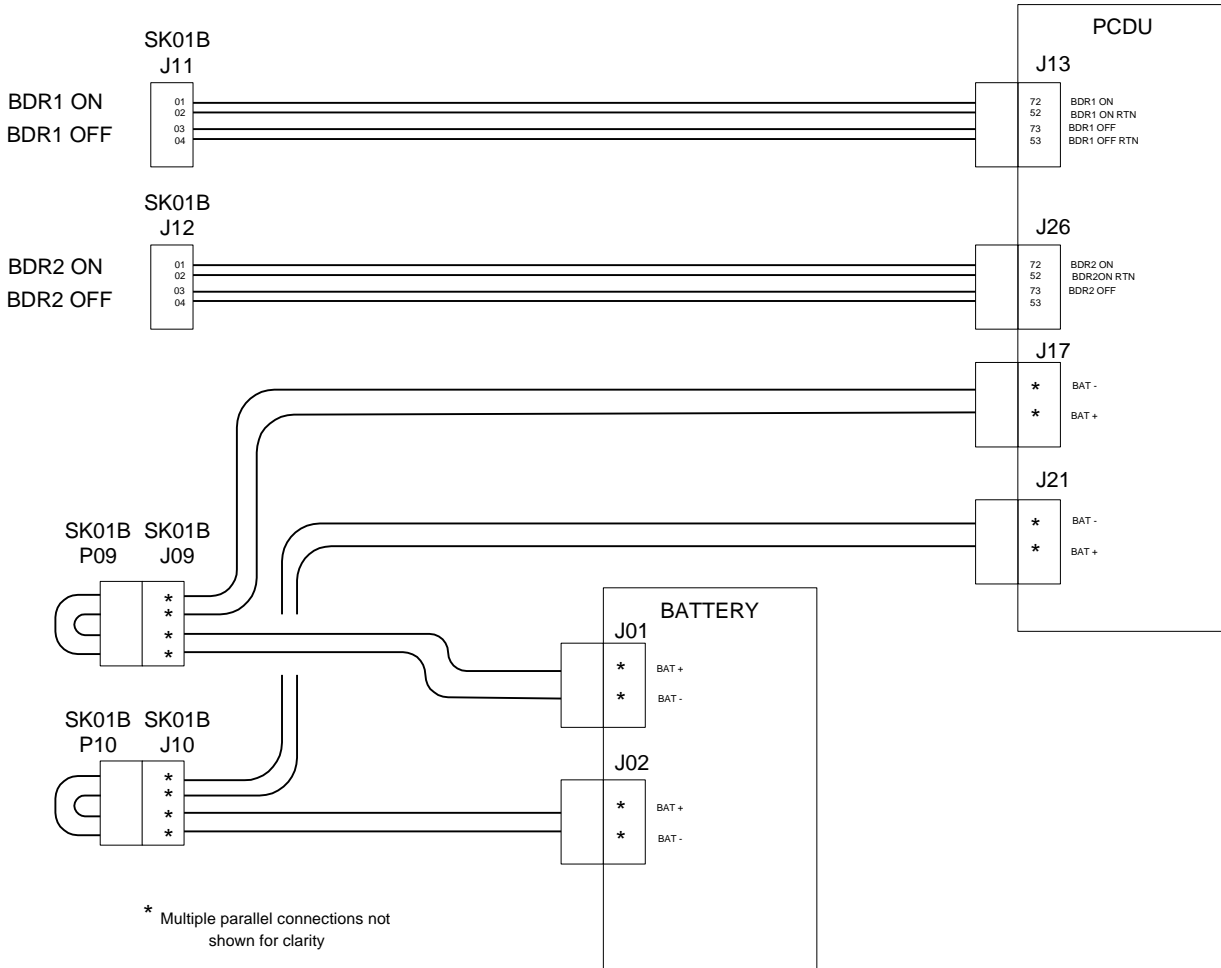


3.2 Satellite Level Skin Connectors Overview



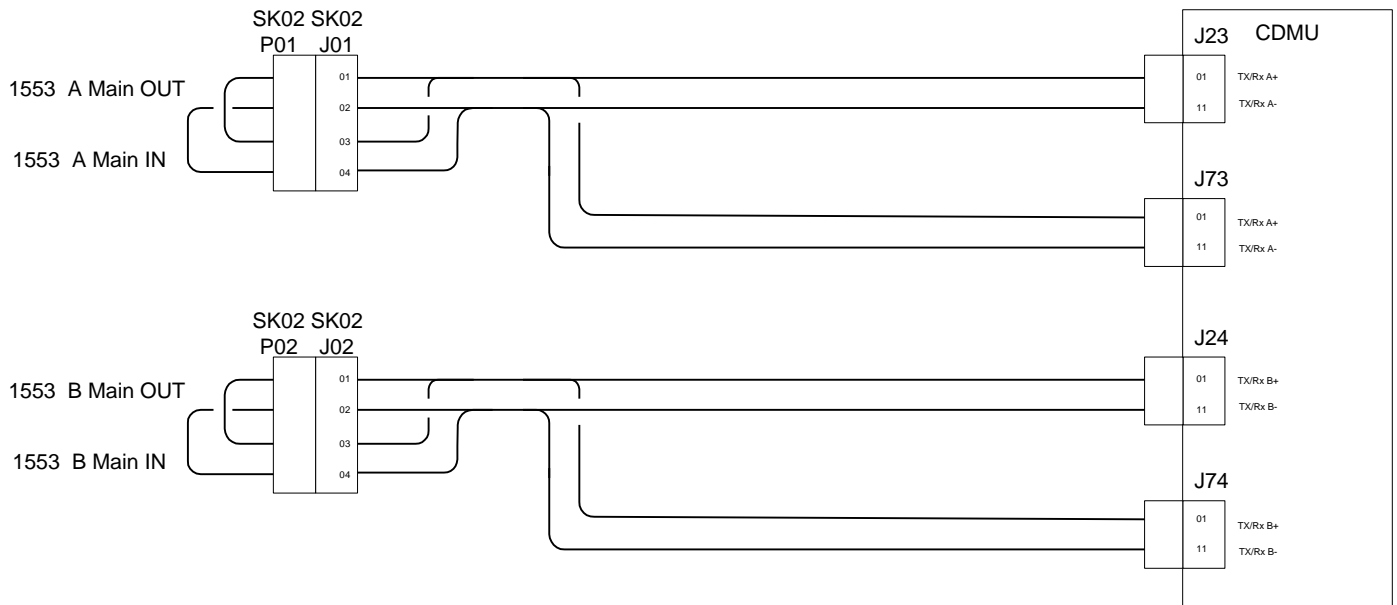


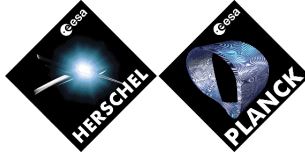
3.2.1 Satellite Level Skin Connector SK01B J09, J10, J11 & J12 Battery - PCDU & BDR ON/OFF



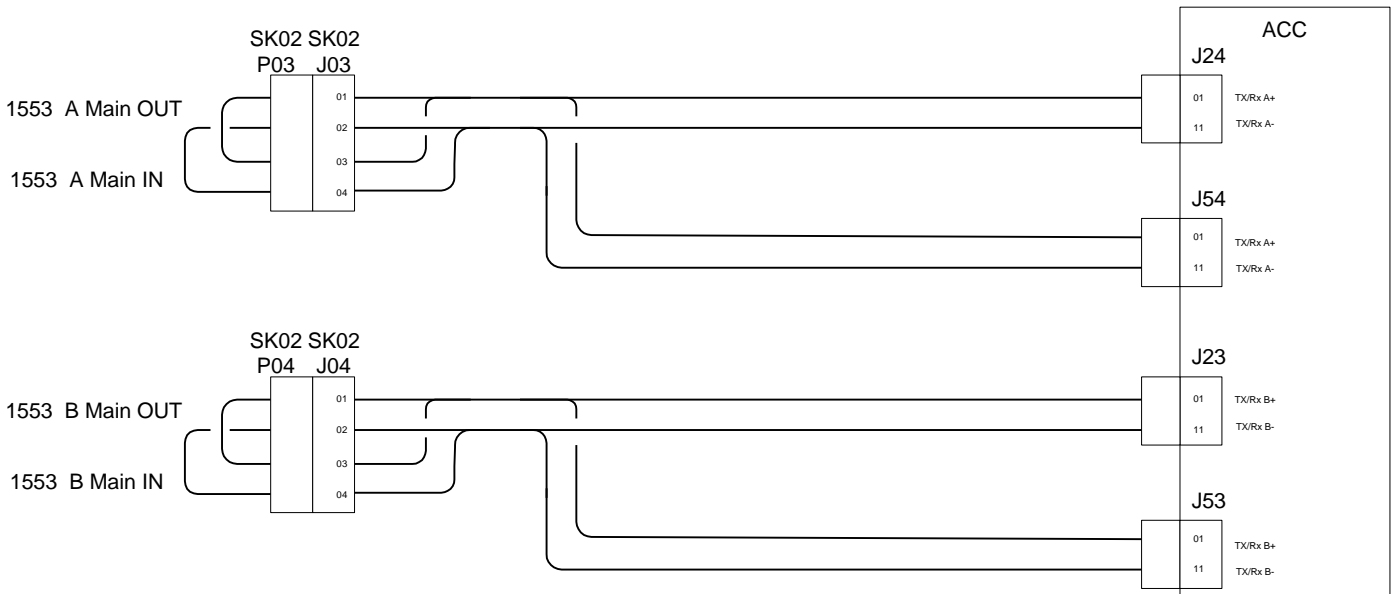


3.2.2 Satellite Level Skin Connector SK02 J01 & J02 CDMU 1553 Bus

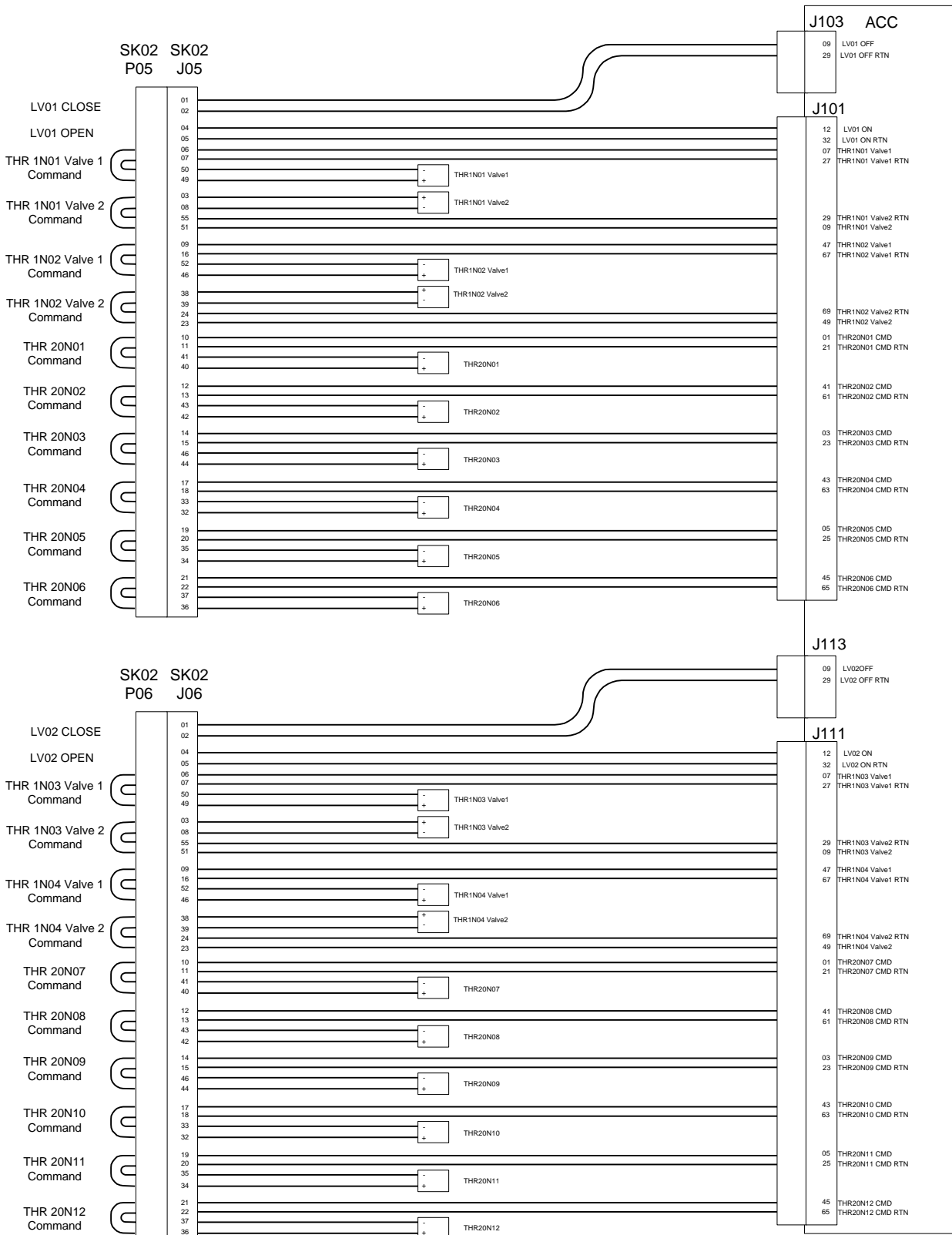




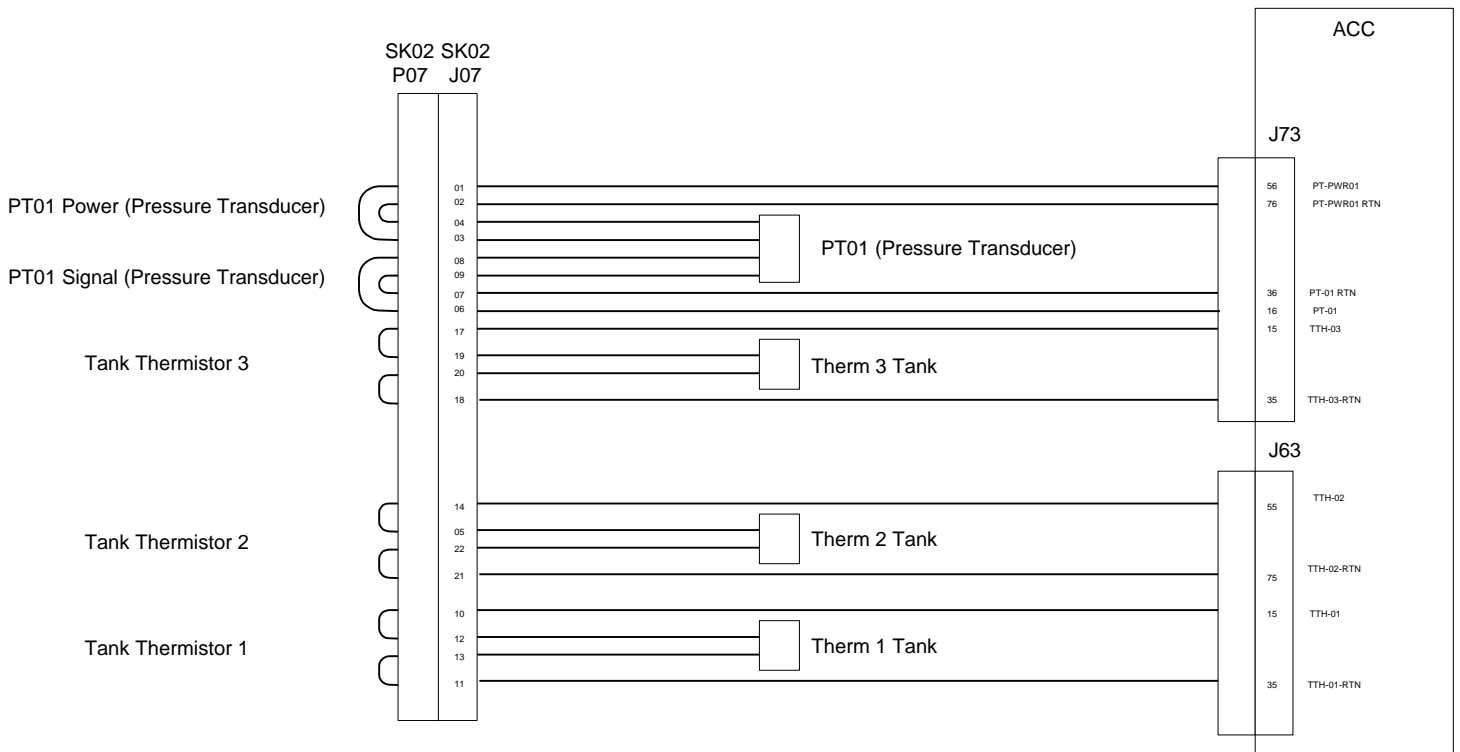
3.2.3 Satellite Level Skin Connector SK02 J03 & J04 ACC 1553 Bus



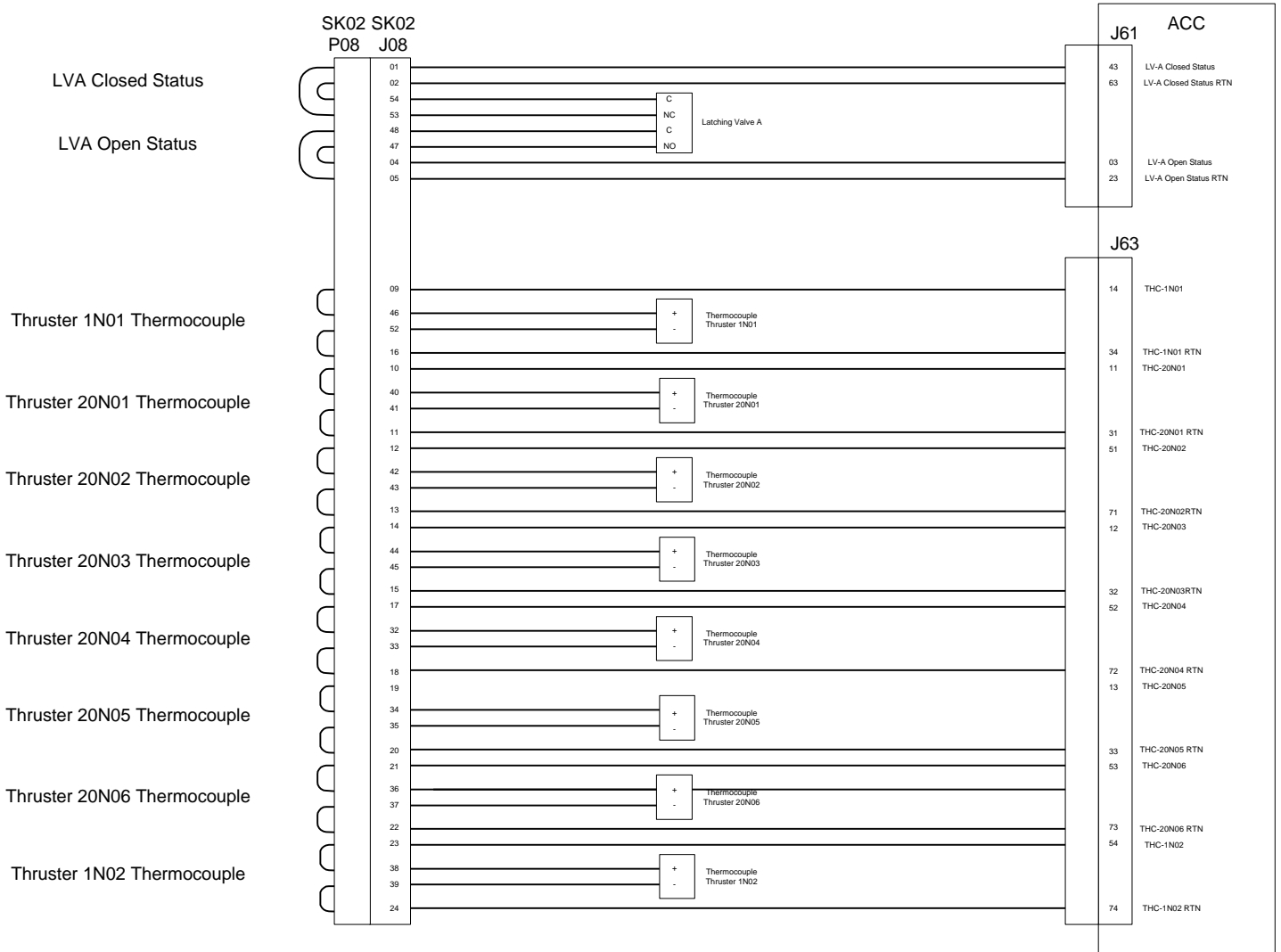
3.2.4 Satellite Level Skin Connector SK02 J05 & J06 ACC - Thruster Commands



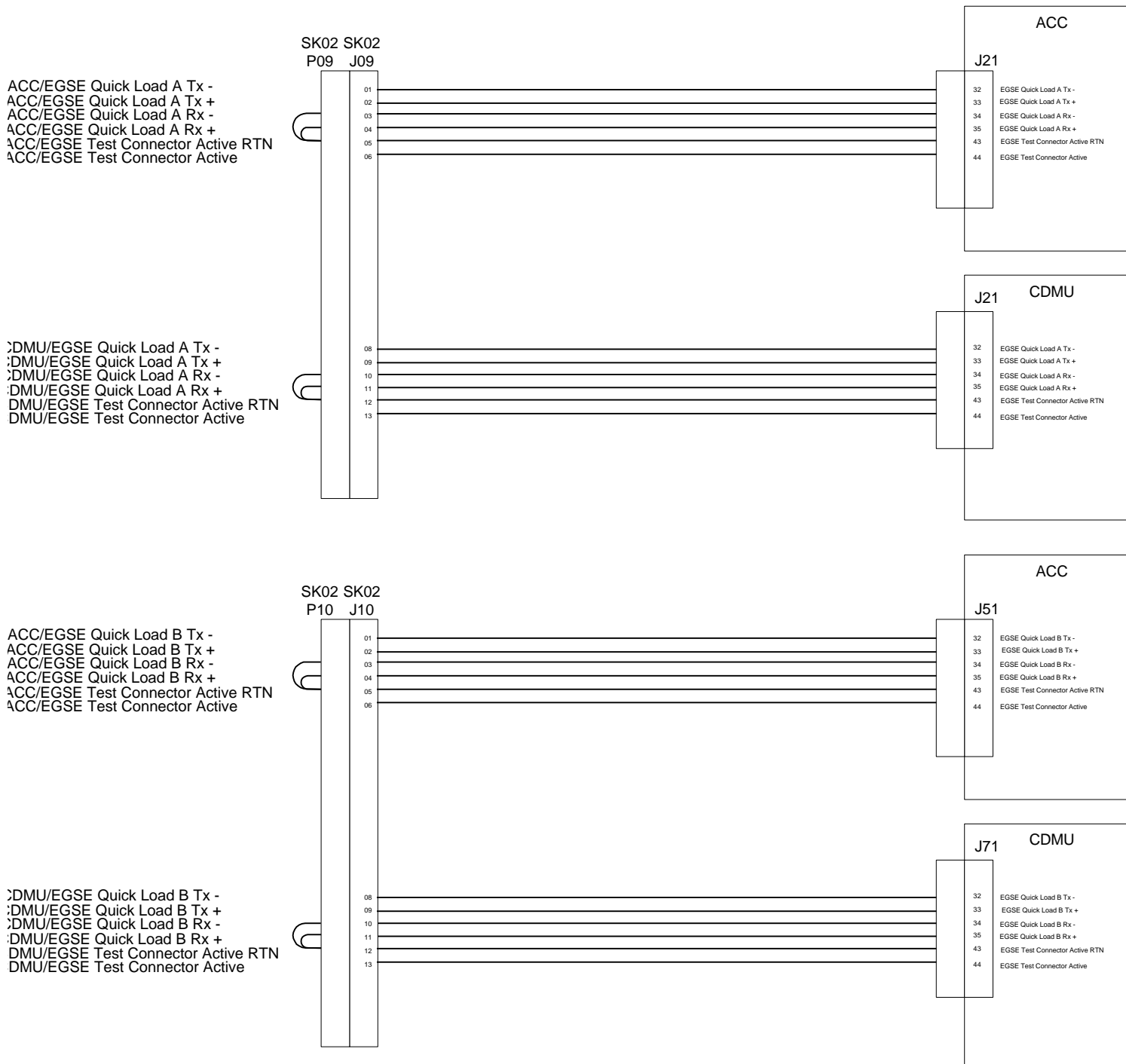
3.2.5 Satellite Level Skin Connector SK02 J07 ACC - PT+Tank Thermistors



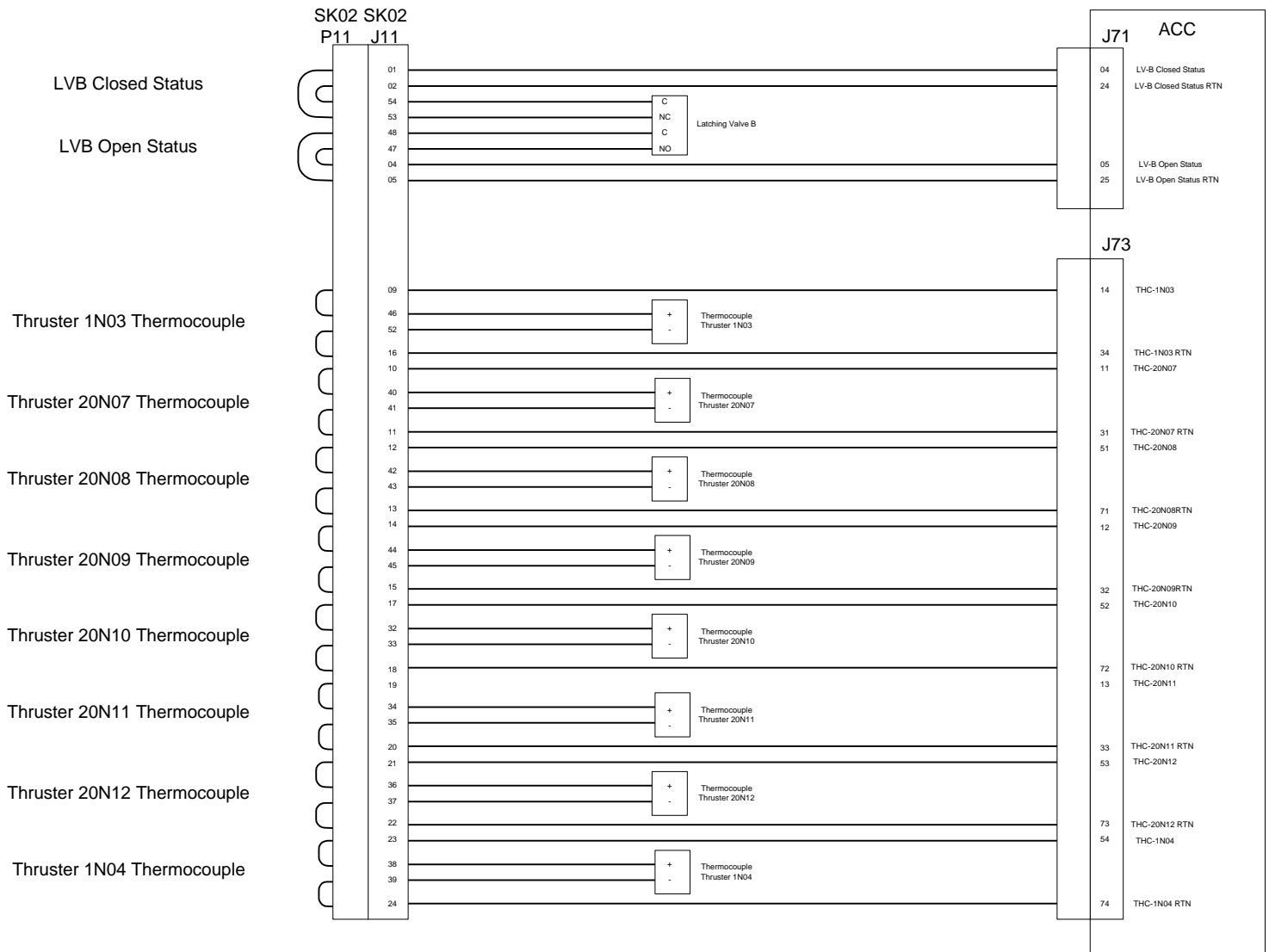
3.2.6 Satellite Level Skin Connector SK02 J08 ACC - Thruster Thermocouples



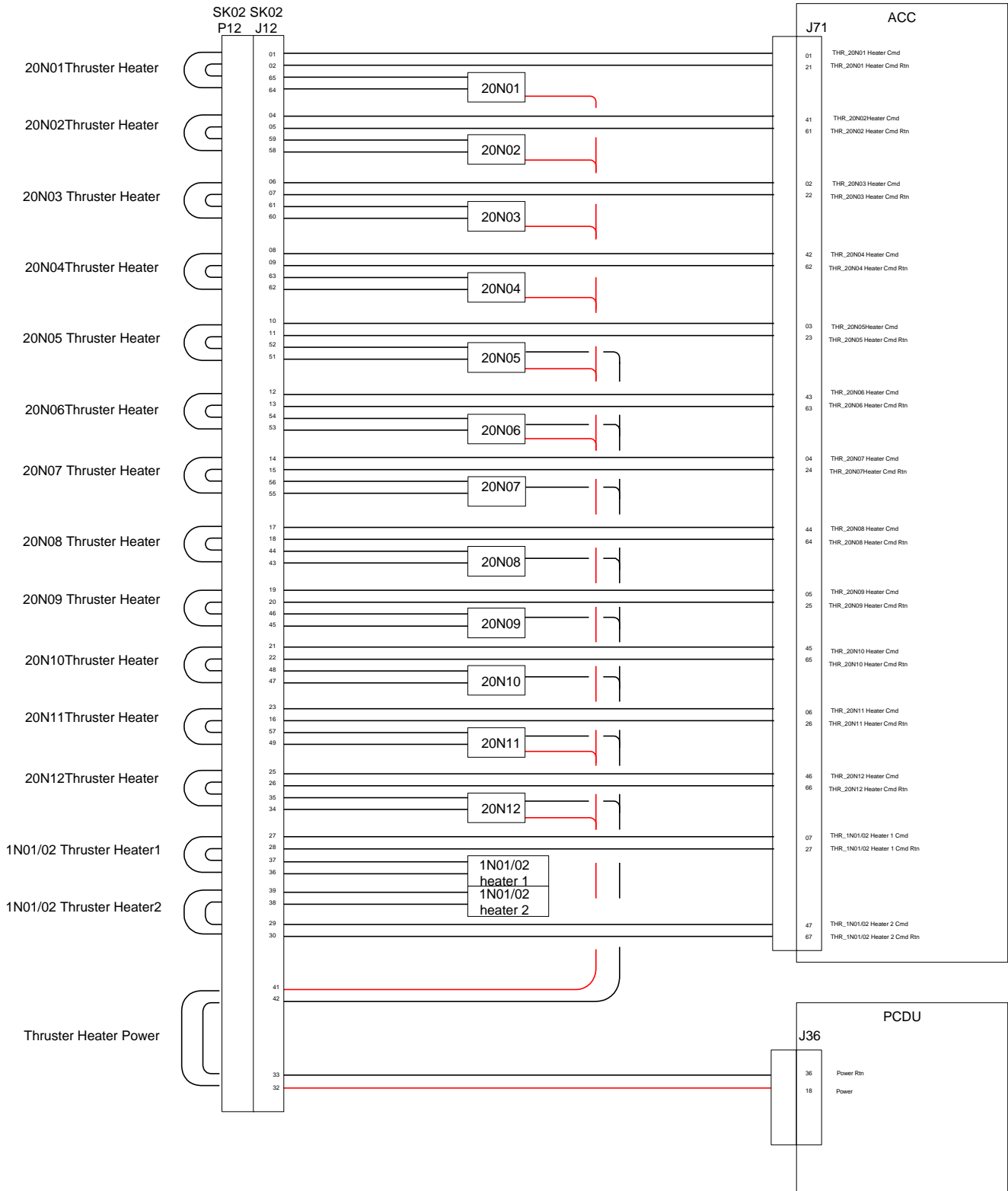
3.2.7 Satellite Level Skin Connector SK02 J09 & J10 ACC & CDMU Quick Load Interfaces



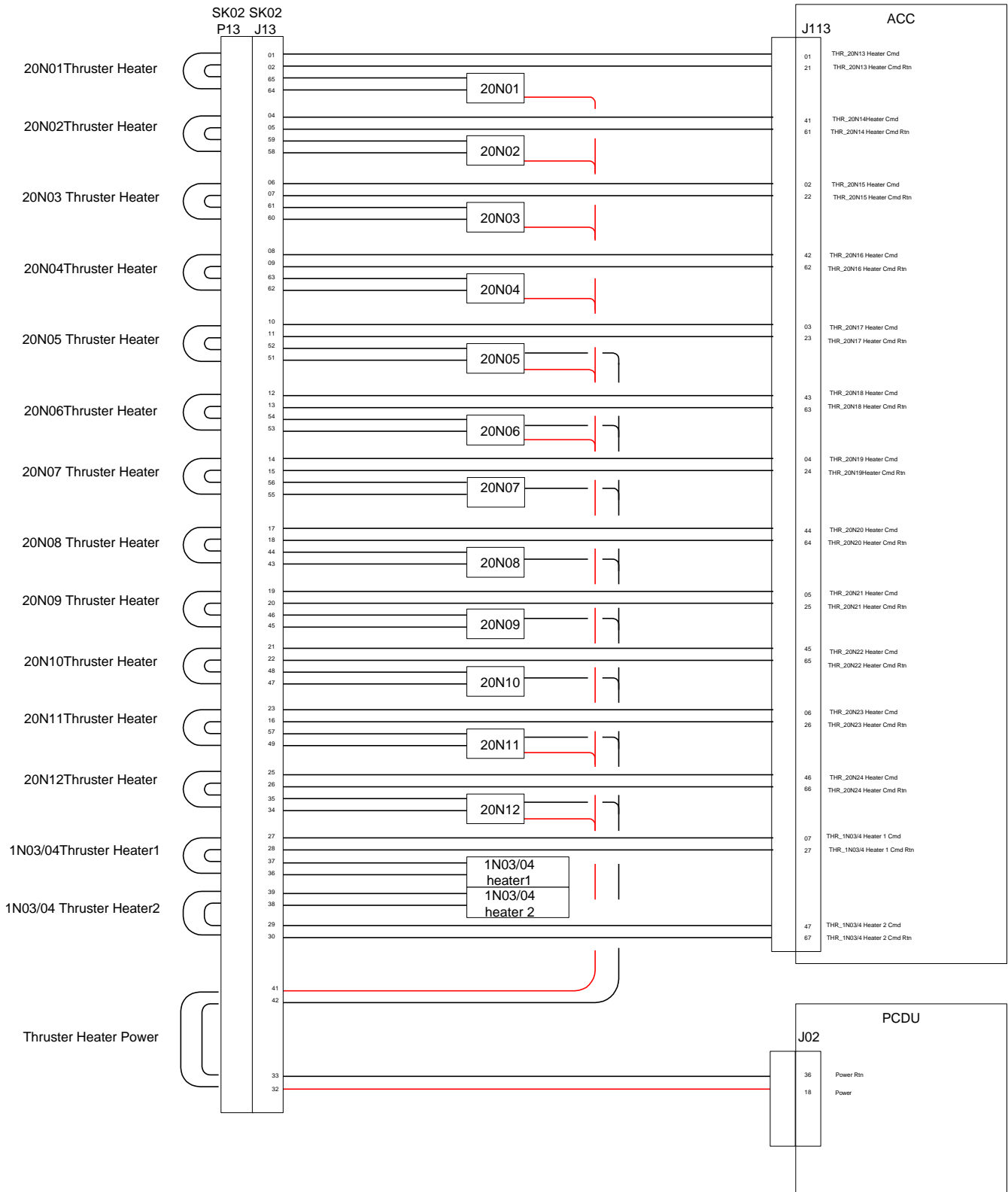
3.2.8 Satellite Level Skin Connector SK02 J11 ACC - Thruster Thermocouples



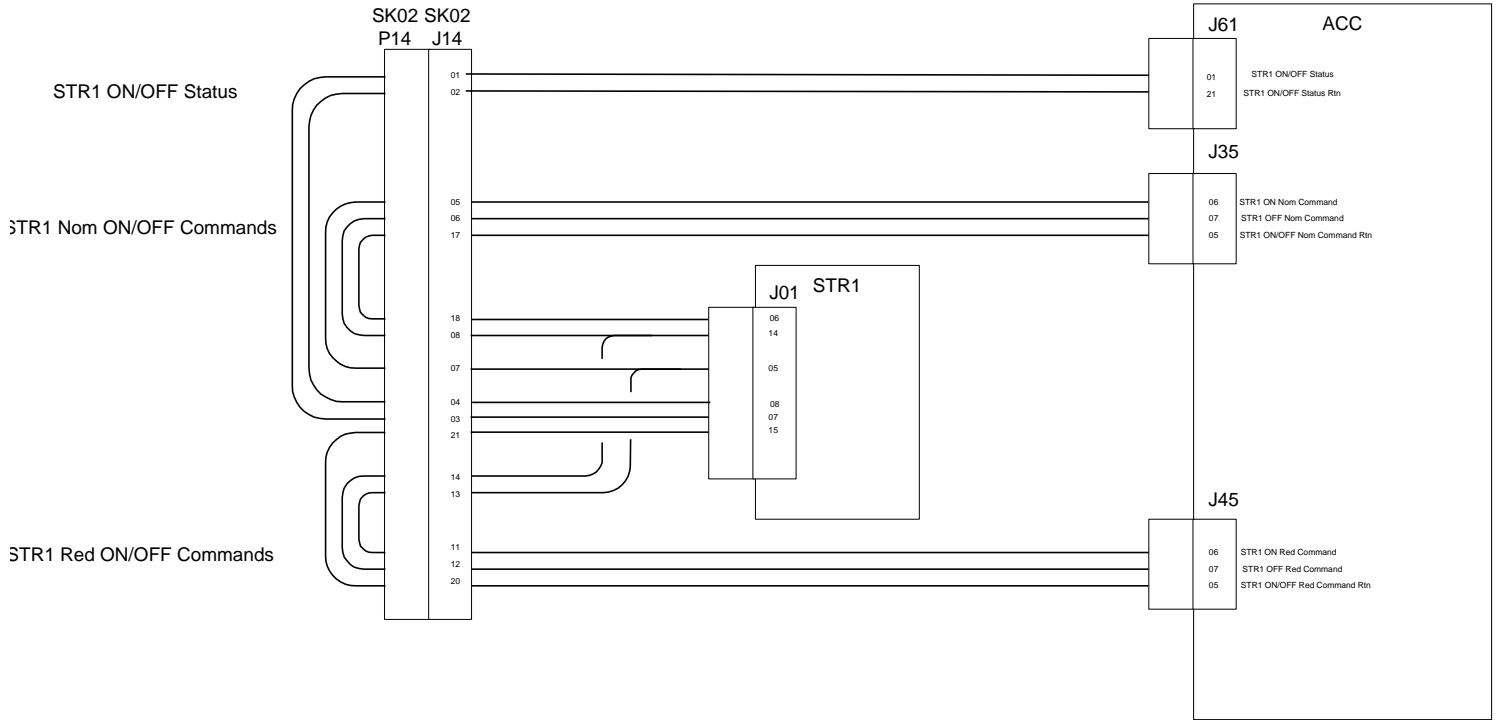
3.2.9 Satellite Level Skin Connector SK02 J12 ACC + PCDU - Thruster Heaters



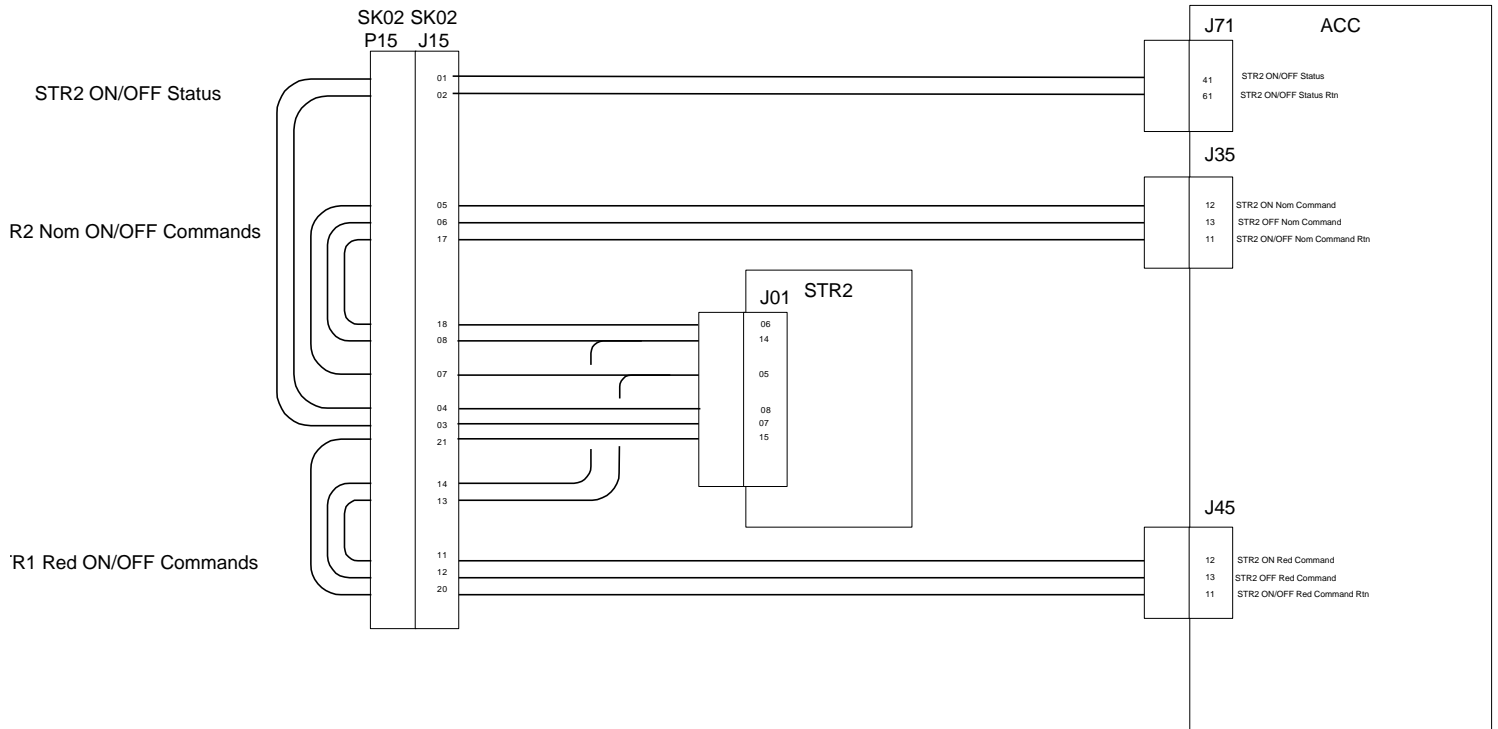
3.2.10 Satellite Level Skin Connector SK02 J13 ACC + PCDU - Thruster Heaters

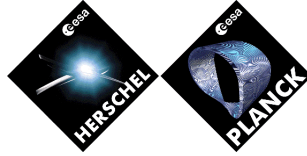


3.2.11 Satellite Level Skin Connector SK02 J14 ACC – STR1

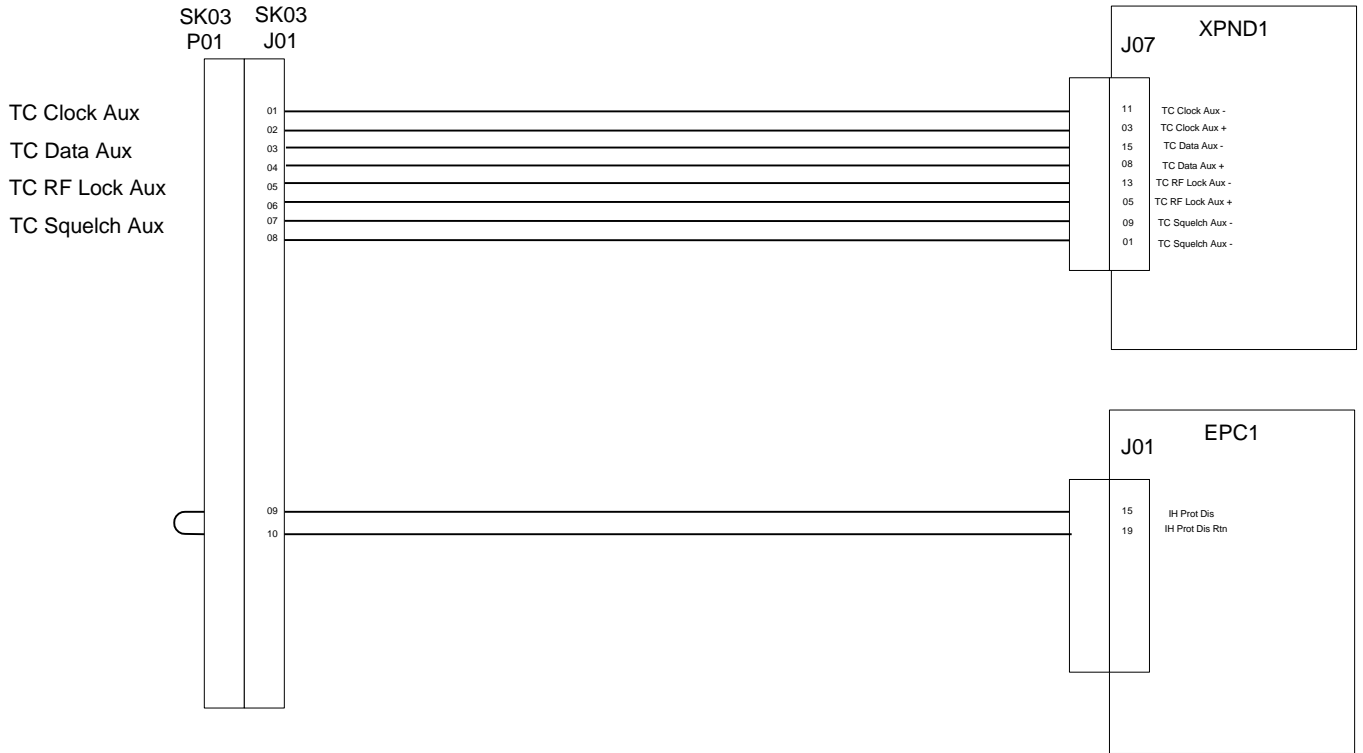


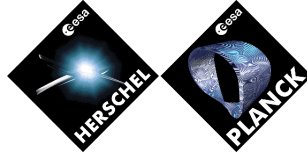
3.2.12 Satellite Level Skin Connector SK02 J15 ACC – STR2



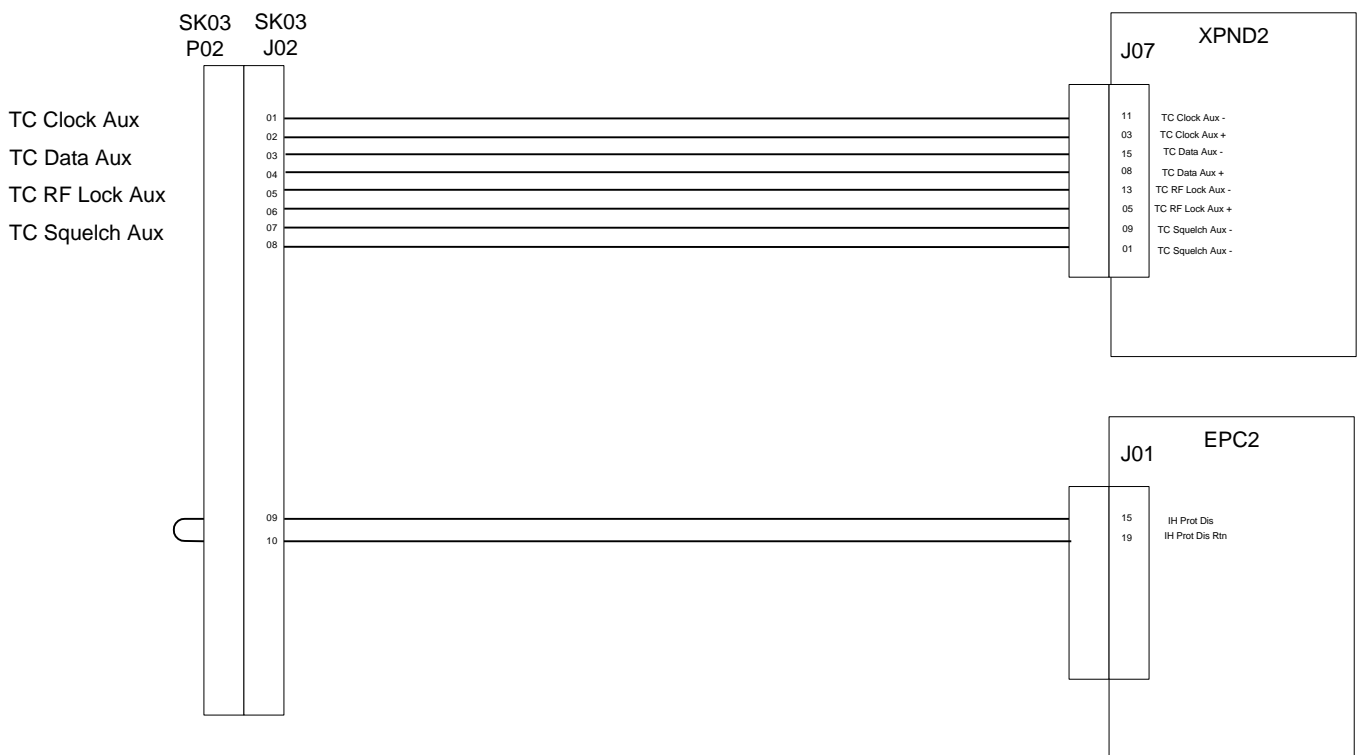


3.2.13 Satellite Level Skin Connector SK03 J01 Xponder 1 Aux Inputs

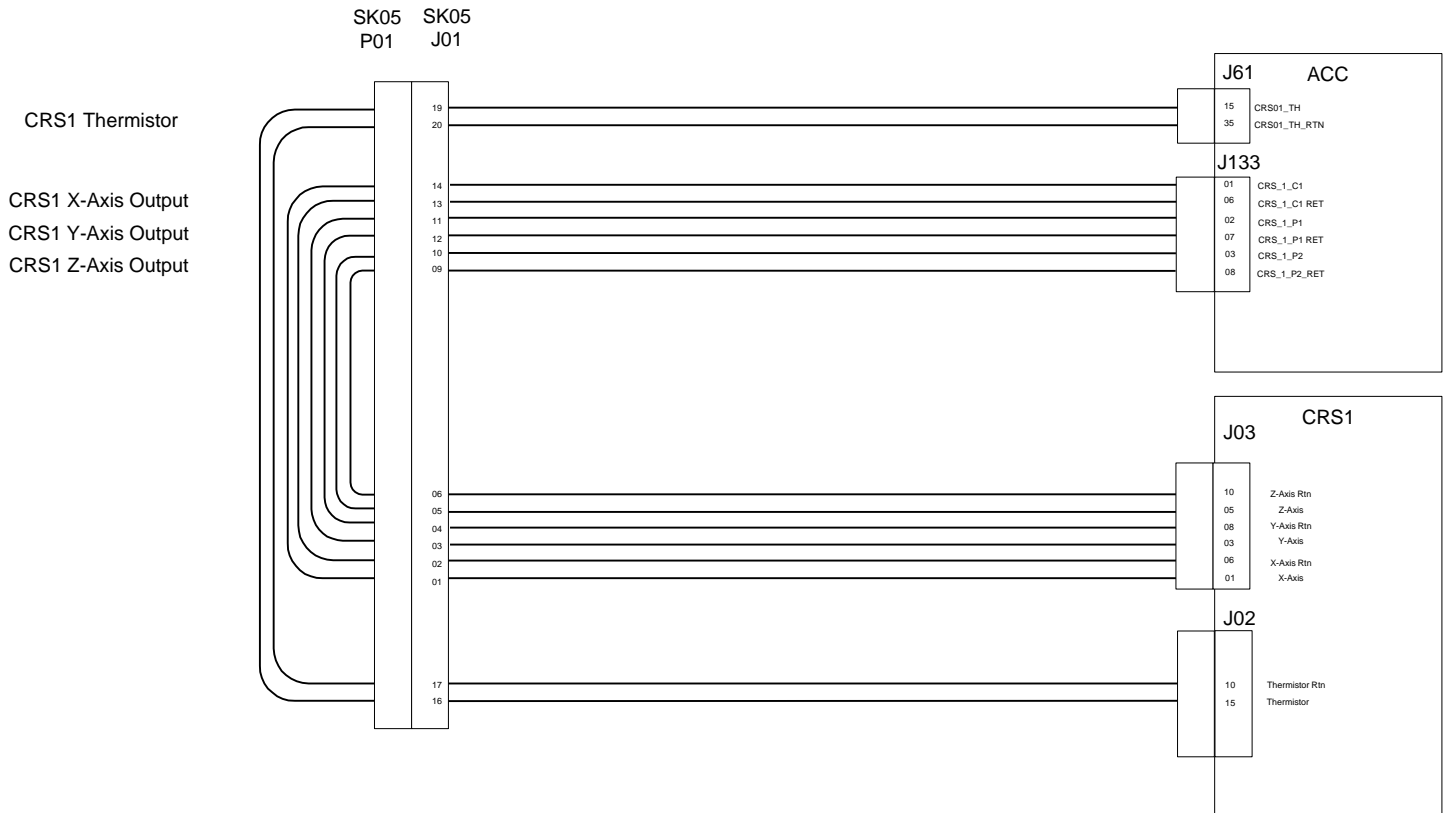




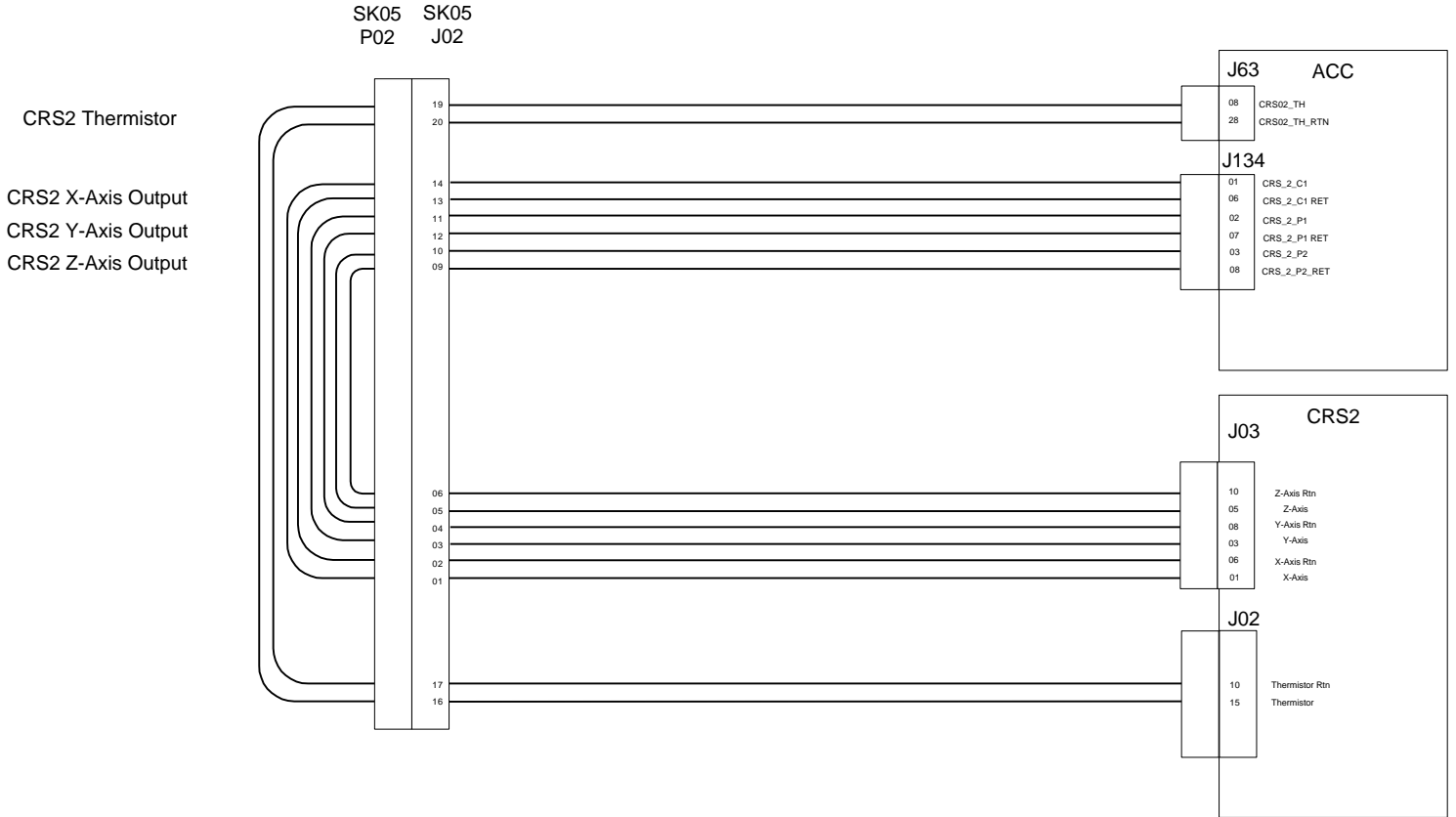
3.2.14 Satellite Level Skin Connector SK03 J02 Xponder 2 Aux Inputs

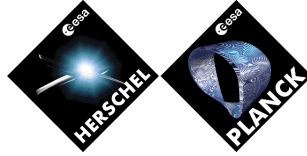


3.2.15 Satellite Level Skin Connector SK05 J01 CRS1 - ACC

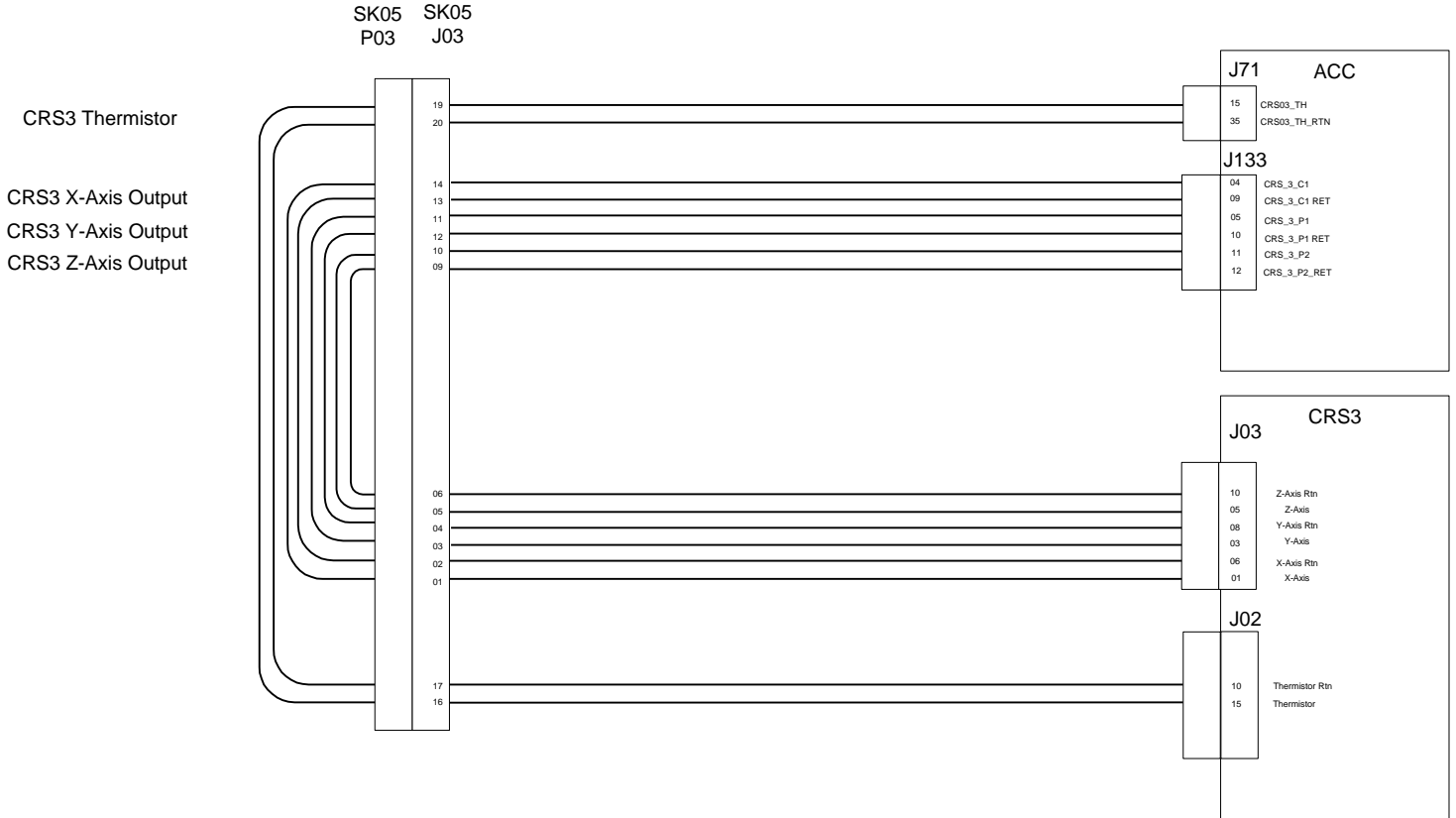


3.2.16 Satellite Level Skin Connector SK05 J02 CRS2 - ACC

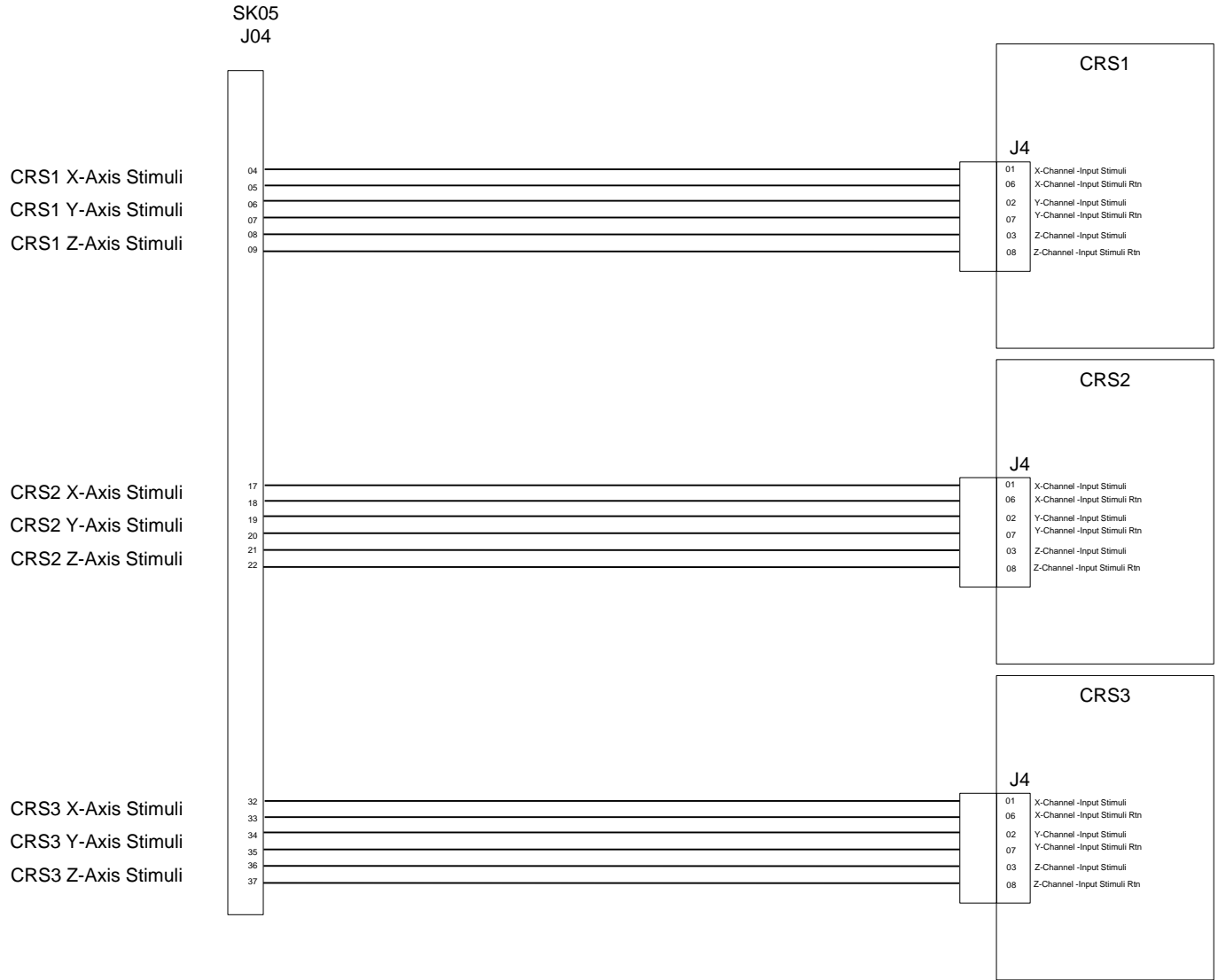


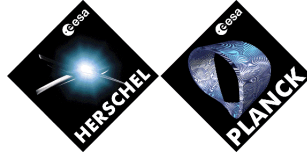


3.2.17 Satellite Level Skin Connector SK05 J03 CRS3 - ACC

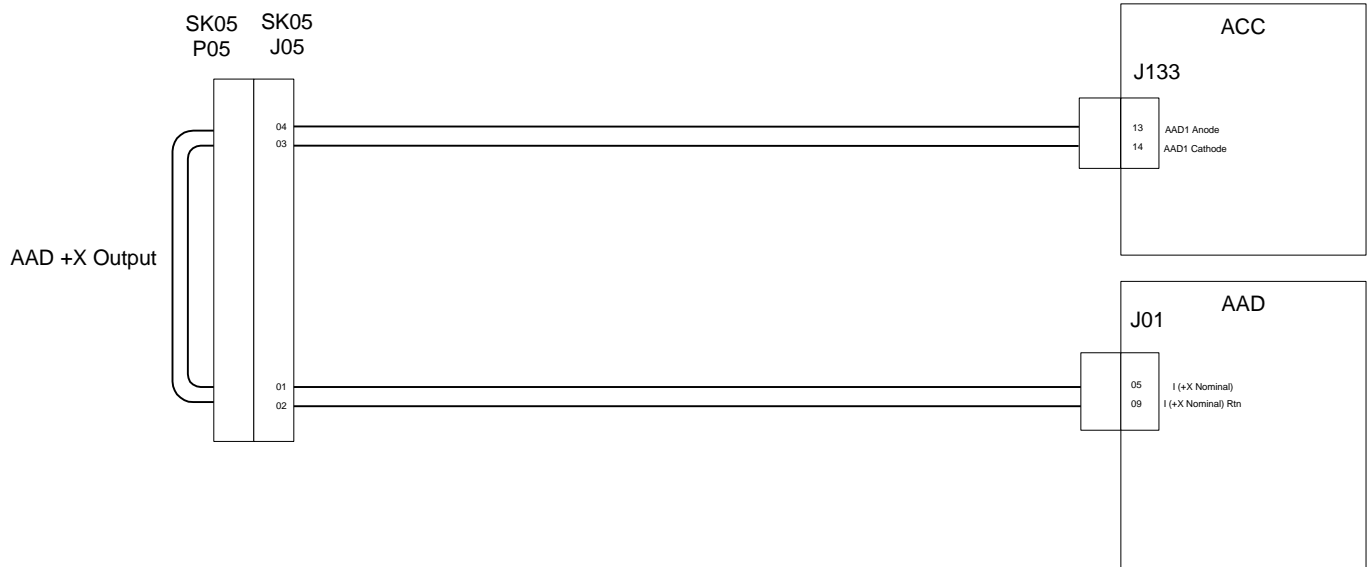


3.2.18 Satellite Level Skin Connector SK05 J04 CRS1 Stimuli

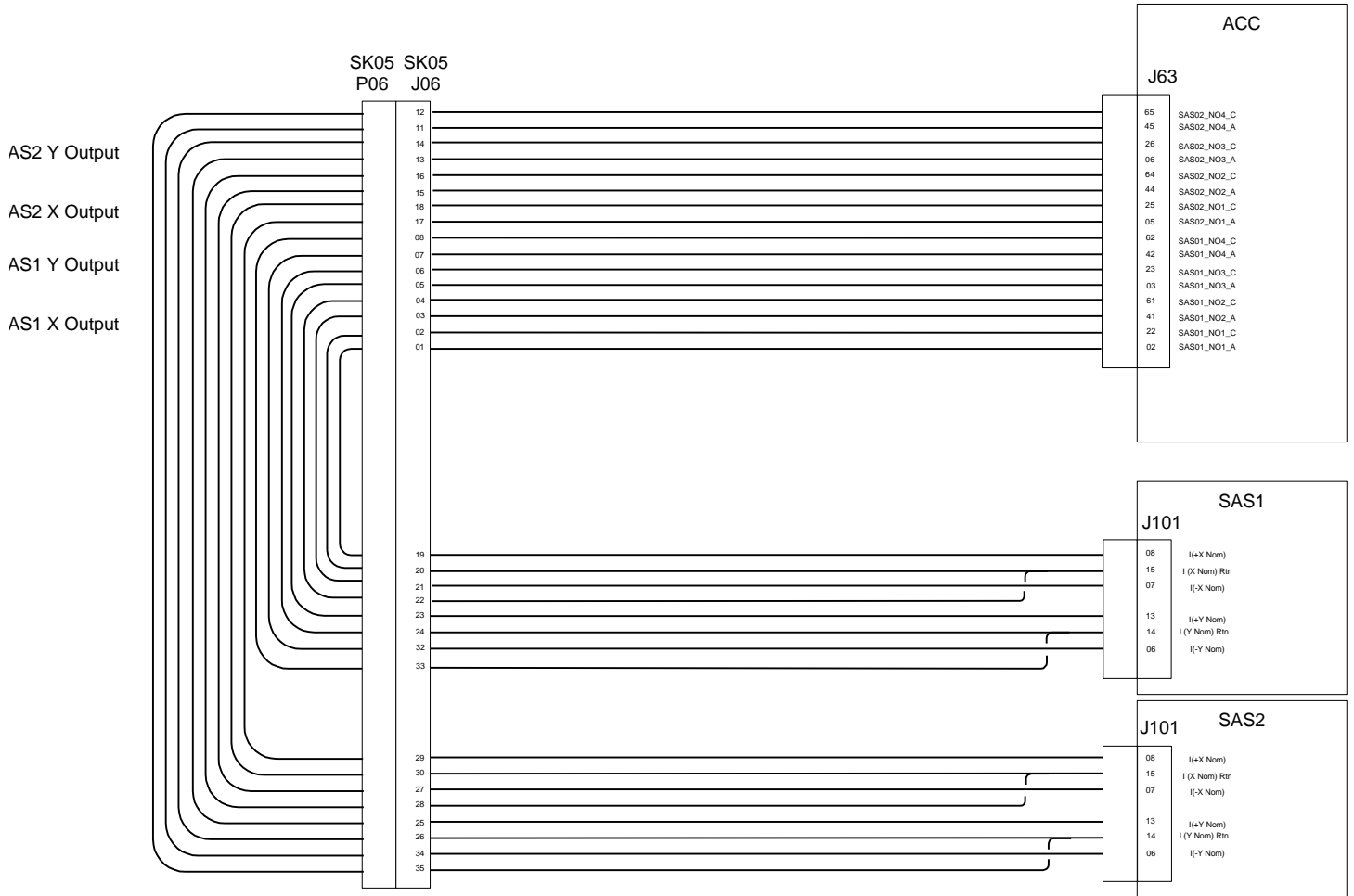




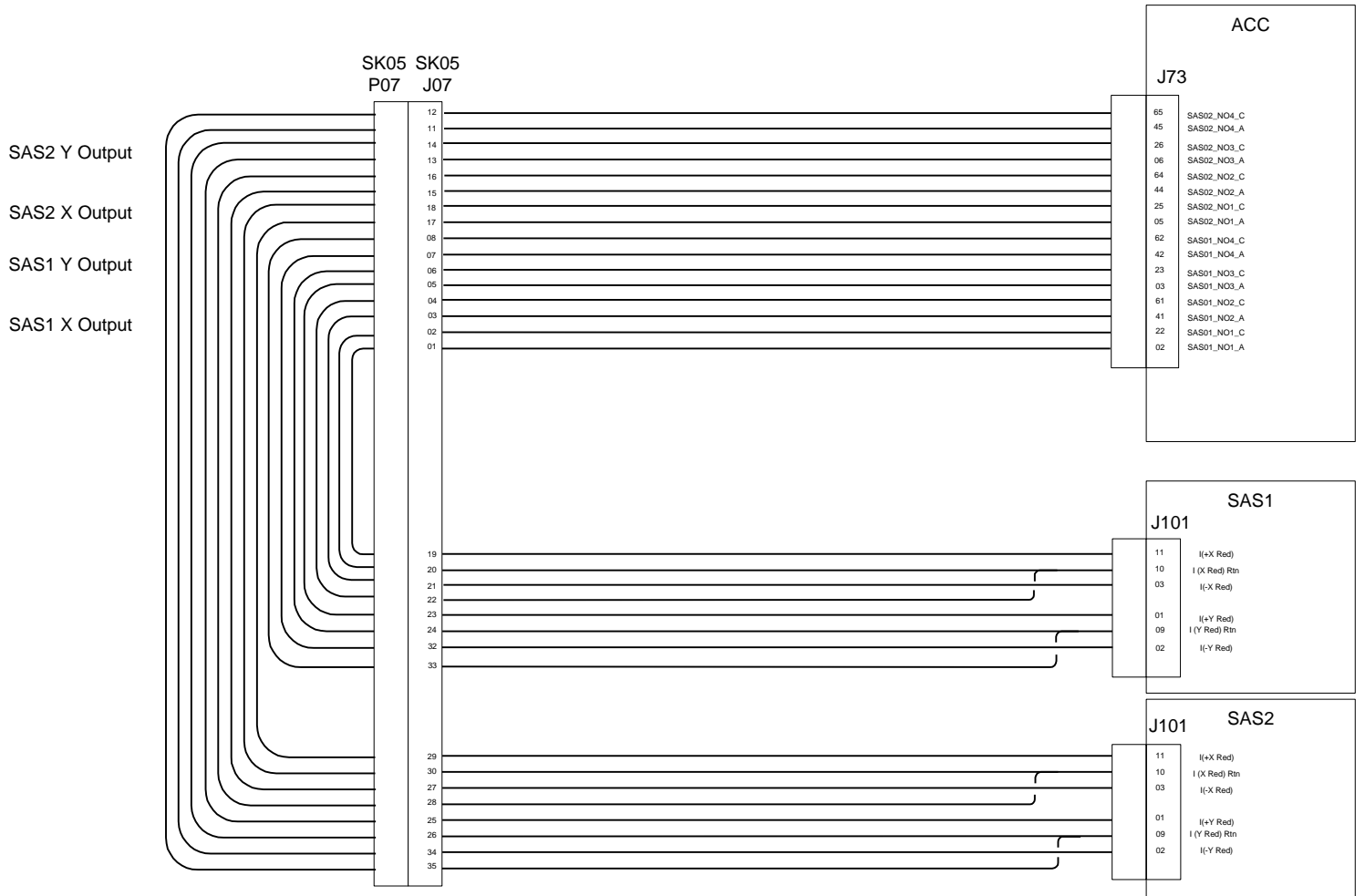
3.2.19 Satellite Level Skin Connector SK05 J05 AAD – ACC

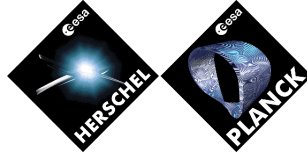


3.2.20 Satellite Level Skin Connector SK05 J06 SAS1 & SAS2 - ACC

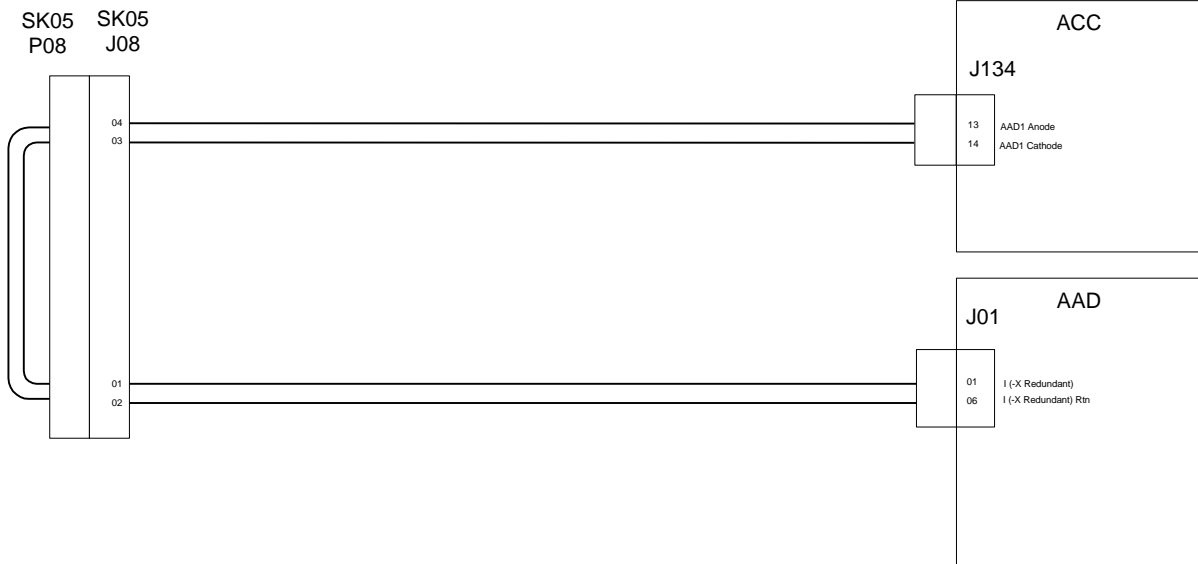


3.2.21 Satellite Level Skin Connector SK05 J07 SAS1 & SAS2 - ACC



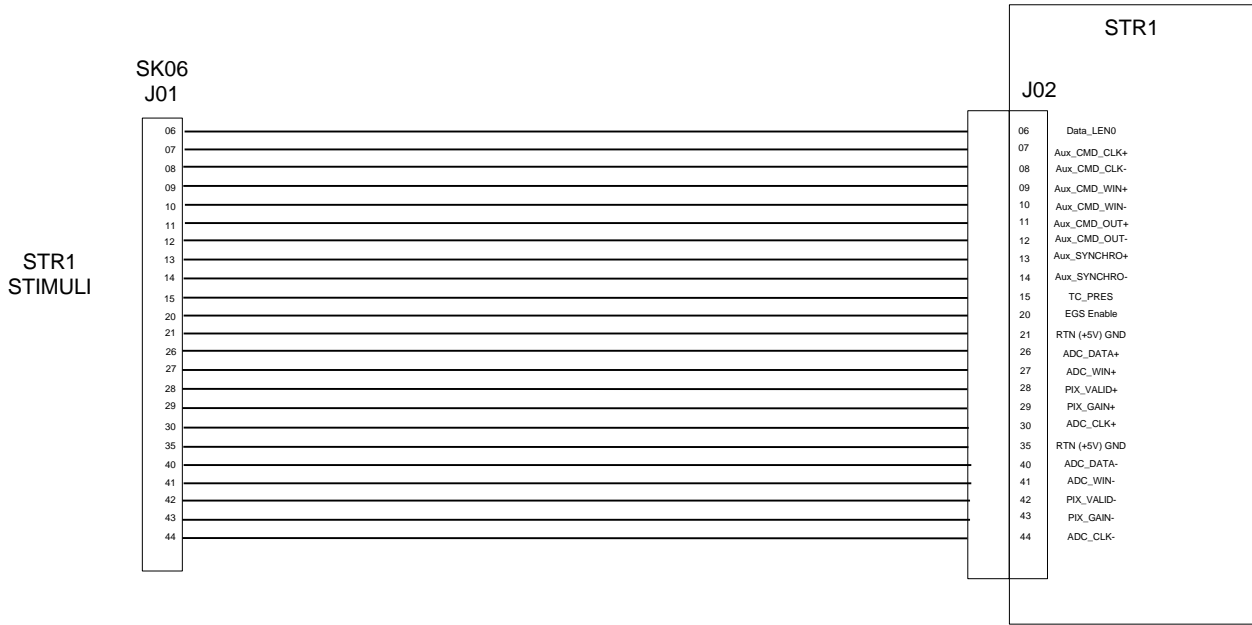


3.2.22 Satellite Level Skin Connector SK05 J08 AAD – ACC

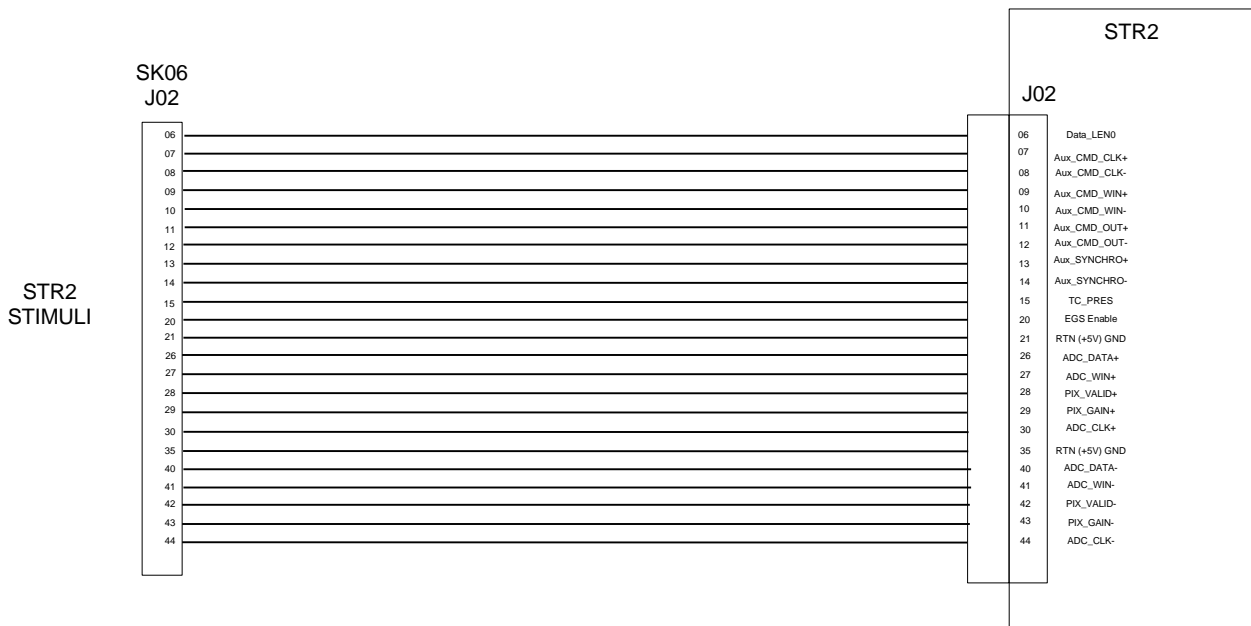




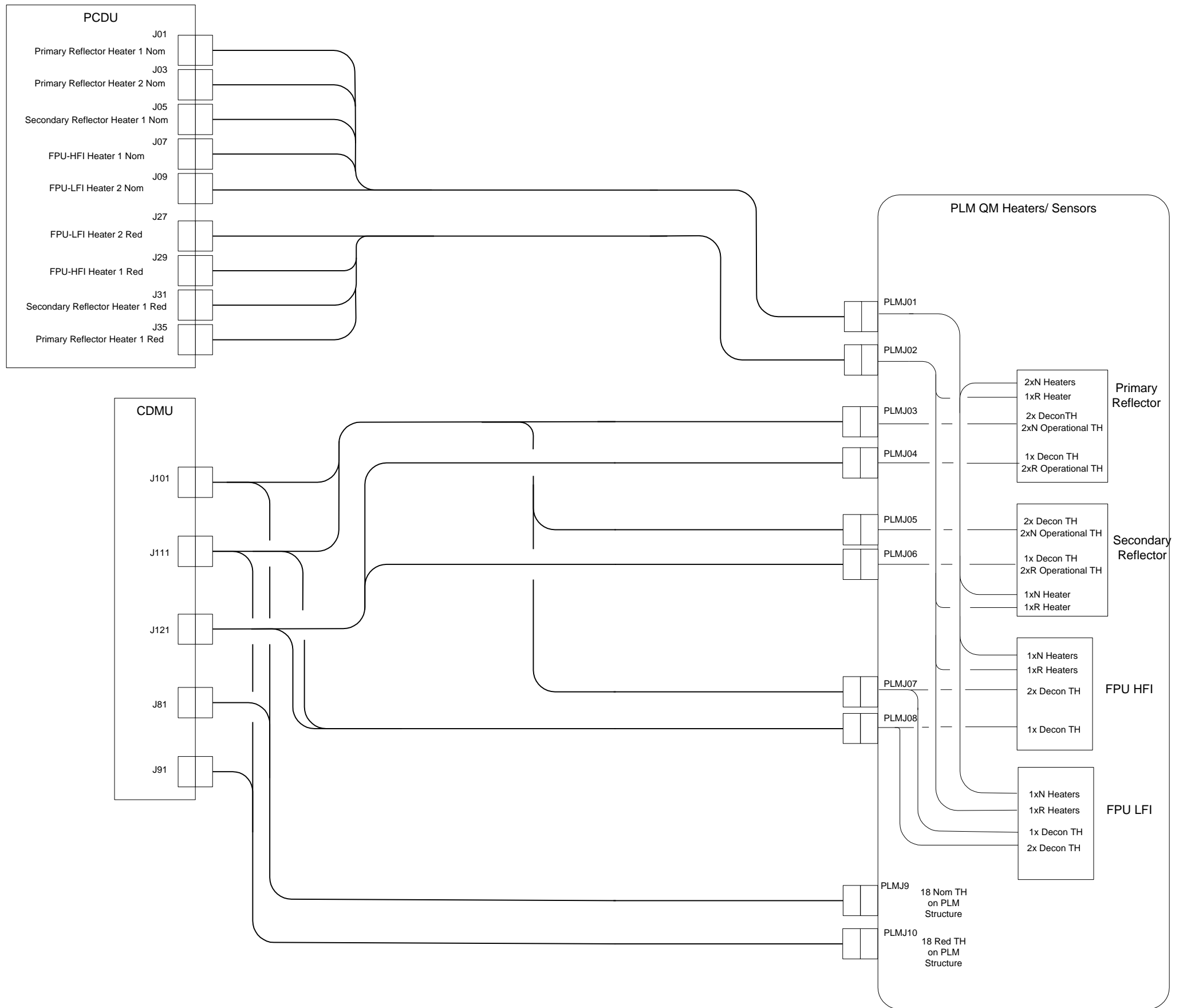
3.2.23 Satellite Level Skin Connector SK06 J01 STR1 Stimuli



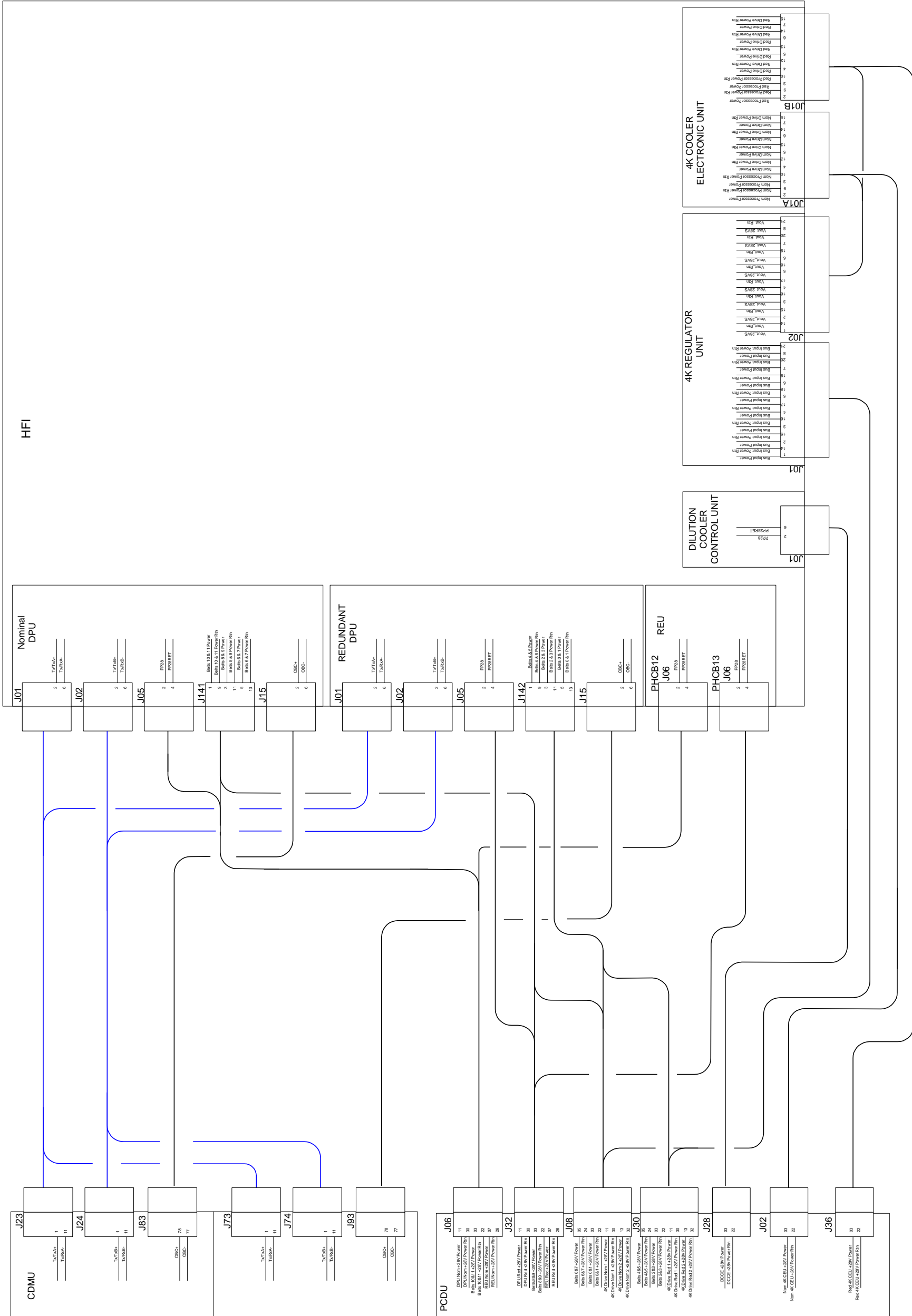
3.2.24 Satellite Level Skin Connector SK06 J02 STR2 Stimuli



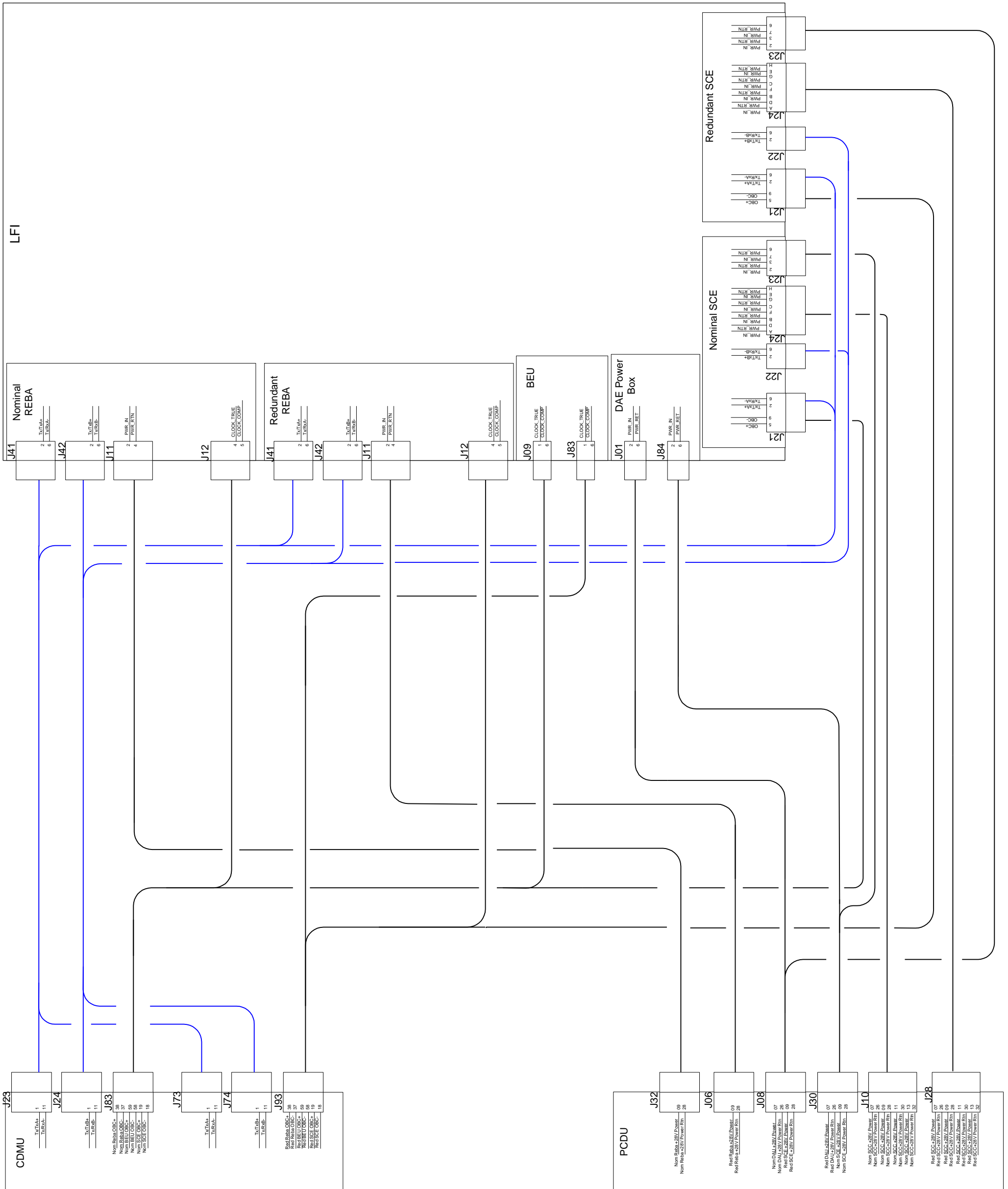
3.3 Satellite Level PLM - SVM Connections



3.4 Satellite Level HFI - SVM Connections

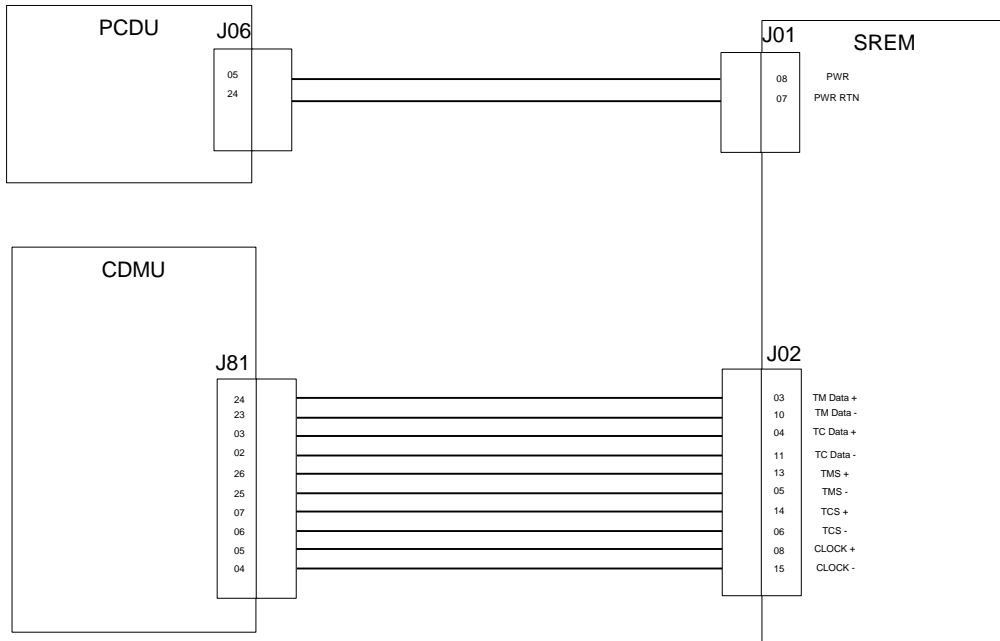


3.5 Satellite Level LFI - SVM Connections

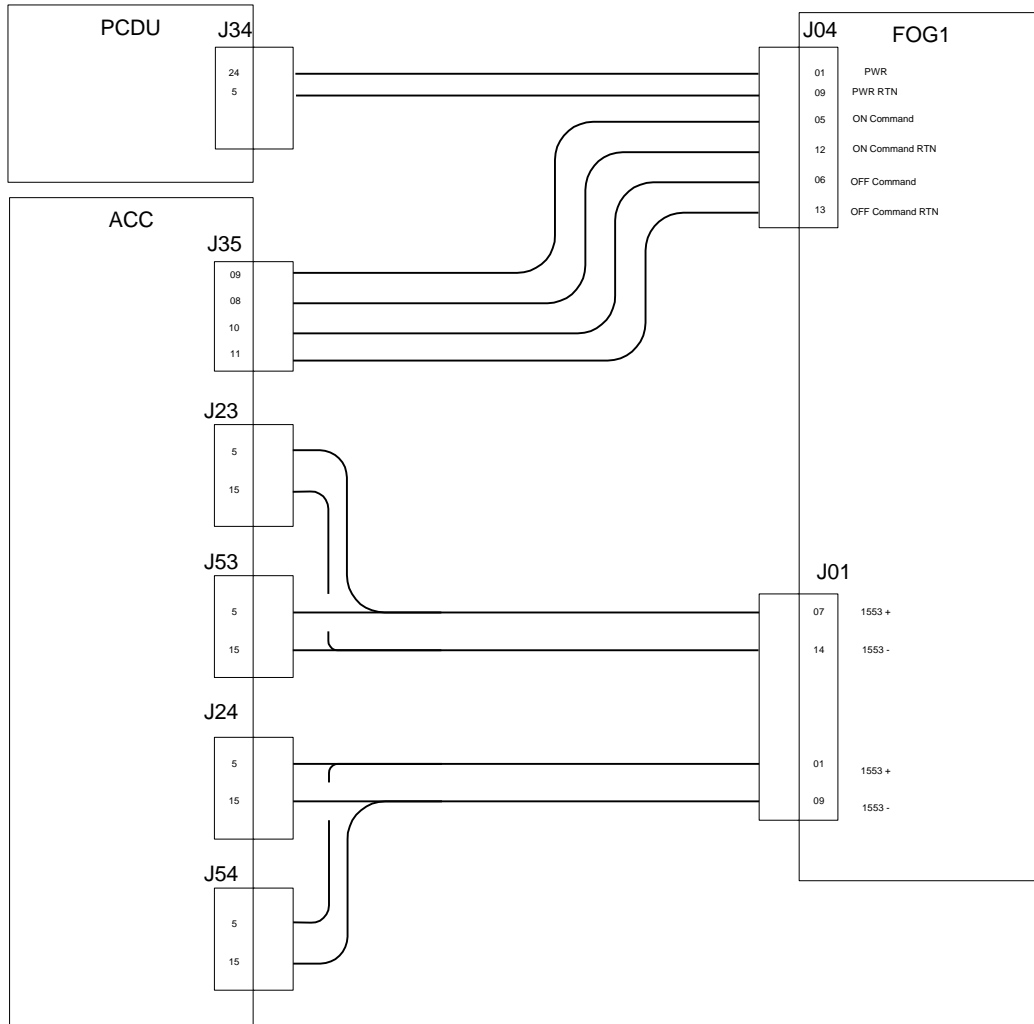


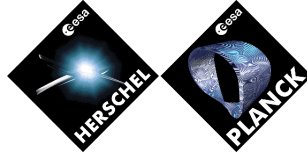
3.6 Miscellaneous Connections

3.6.1 Miscellaneous Connections, SREM

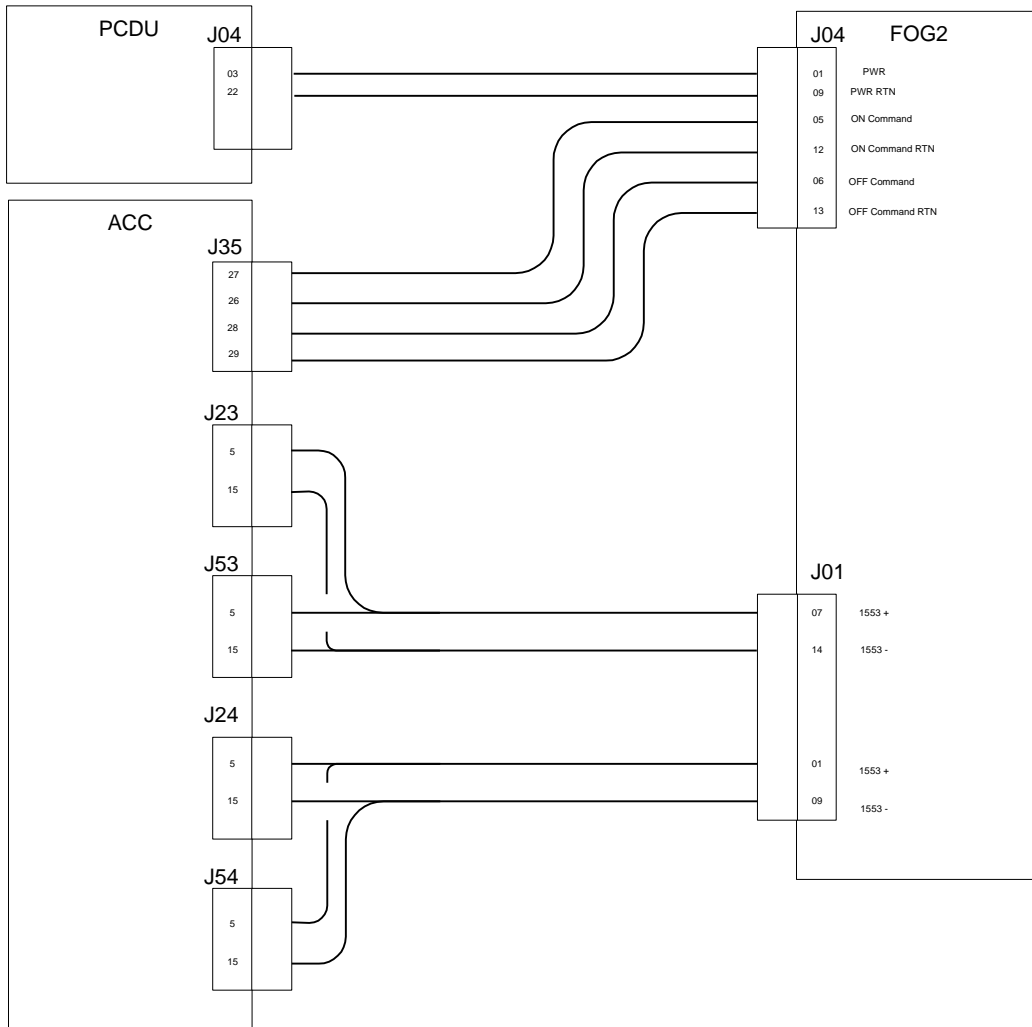


3.6.2 Miscellaneous Connections, FOG1



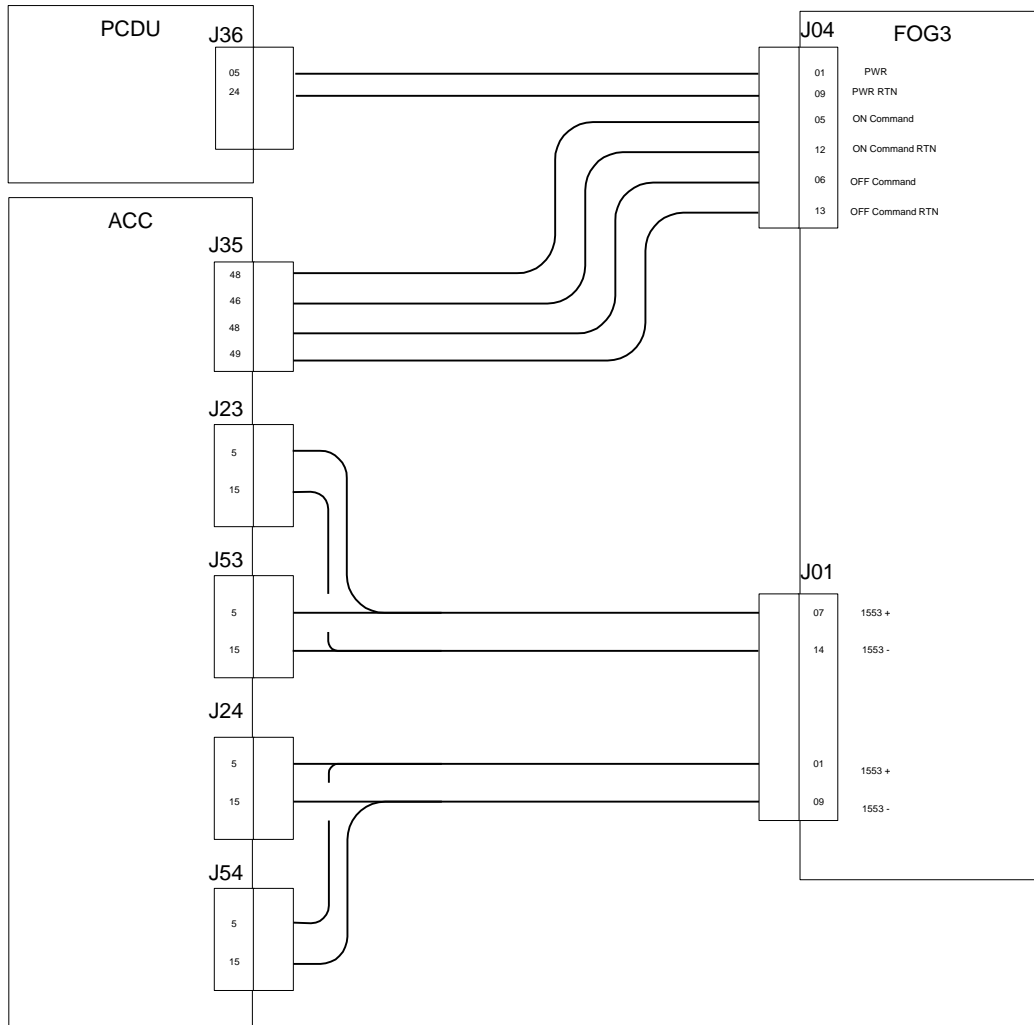


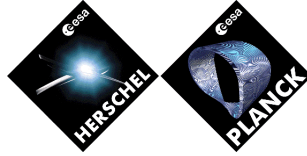
3.6.3 Miscellaneous Connections, FOG2



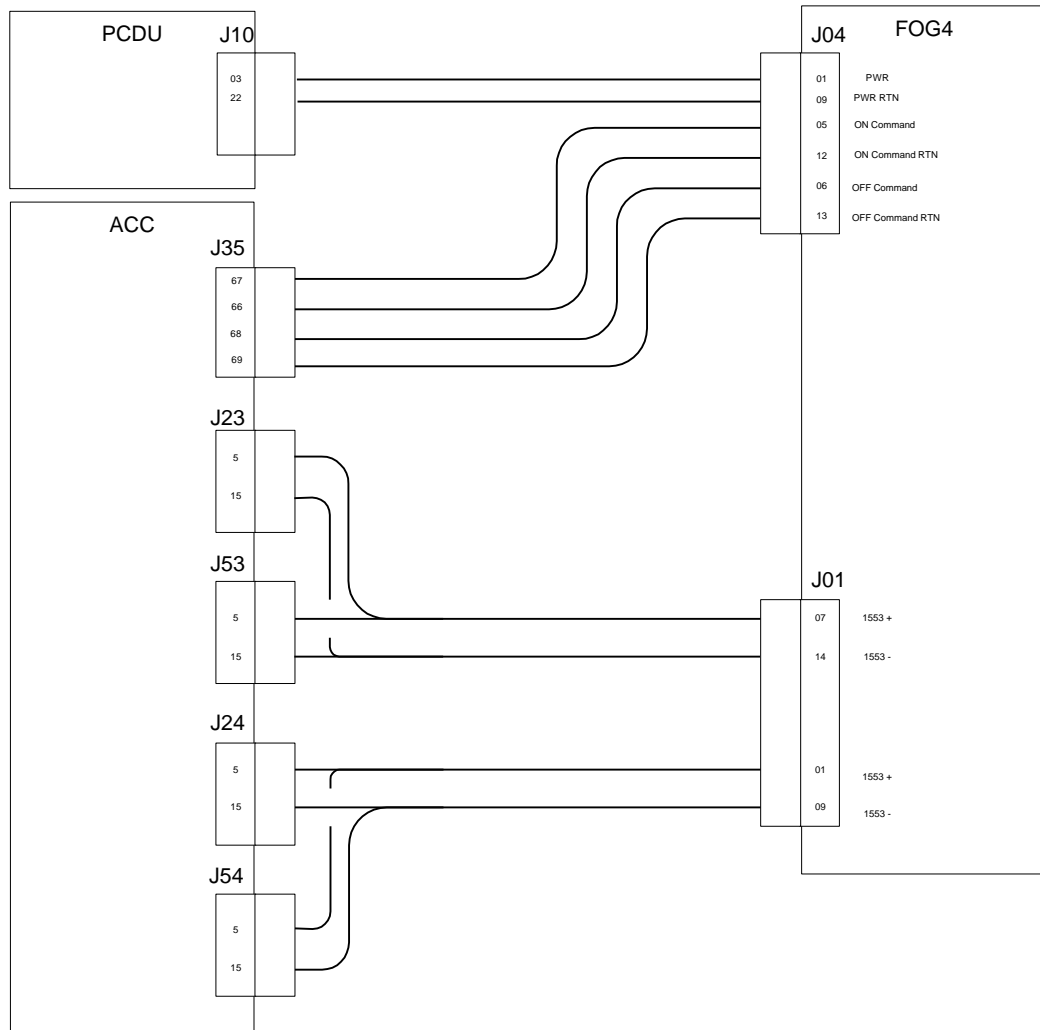


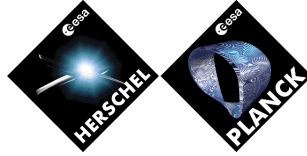
3.6.4 Miscellaneous Connections, FOG3





3.6.5 Miscellaneous Connections, FOG4





4. LIST OF CONNECTIONS

This paragraph provides an overview of all connections within the satellite for each interface.

A table is provided for each connector shown in the previous drawings, the "Source" columns pertaining to the connector and the "Destination" columns refer to the unit/connector/pin to which the source pin is connected.

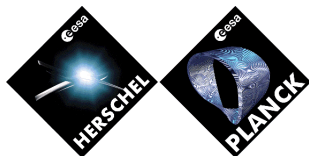
For each Source and Destination the relevant interface type is listed (except for skin connectors which either have EGSE as inputs or links, in which case only the destination interface type is given). For each interface type reference the electrical schematic and the electrical characteristics are defined in subsequent chapters.



4.1 Satellite Level Umbilical Connectors

4.1.1 Satellite Level Umbilical Connector PU1 J01

SOURCE		SignalName	DESTINATION		Interface_of_Source	Interface of Destination
Connector Reference	Pin		Connected to Unit/Connector	Pin		
PU1J01	8	PU1/PCDU_Charge_Array_Disable_Cmd +	PCDUJ14	2	Link	HL_Cmd
PU1J01	9	PU1/PCDU_Charge_Array_Disable_Cmd RTN	PCDUJ14	15	Link	HL_Cmd
PU1J01	13	PU1/CDMU_Sep_Strap1_Sts	CDMUJ105	32	Link	DR_Mnt
PU1J01	17	PU1/CDMU_TC_Squelch _L	CDMUJ043	25	SBDL_Receiver	SBDL_Driver
PU1J01	18	PU1/CDMU_TC_Data _L	CDMUJ043	65	SBDL_Receiver	SBDL_Driver
PU1J01	19	PU1/CDMU_TC_Clock _L	CDMUJ043	64	SBDL_Receiver	SBDL_Driver
PU1J01	21	PU1/CDMU_RM_A_Sep_Strap5a_Alarm_Sts _H	CDMUJ043	19	Link	DR_Mnt
PU1J01	22	PU1/CDMU_RM_A_Sep_Strap5a_Alarm_Sts _L	CDMUJ043	38	Link	DR_Mnt
PU1J01	25	PU1/CDMU_RM_B_Sep_Strap6a_Alarm_Sts _H	CDMUJ053	19	Link	DR_Mnt
PU1J01	26	PU1/CDMU_RM_B_Sep_Strap6a_Alarm_Sts _L	CDMUJ053	38	Link	DR_Mnt
PU1J01	27	PU1/CDMU_Sep_Strap1_Sts RTN	CDMUJ105	31	Link	DR_Mnt
PU1J01	28	PU1/PCDU_+28V_Aux_IN-3_Pwr +	PCDUJ18	1	EGSE_PWR	SA_Aux_PWR_in
PU1J01	29	PU1/PCDU_+28V_Aux_IN-1_Pwr +	PCDUJ14	1	EGSE_PWR	SA_Aux_PWR_in
PU1J01	30	PU1/PCDU_+28V_Aux_IN-2_Pwr RTN	PCDUJ15	14	EGSE_PWR	SA_Aux_PWR_in
PU1J01	31	CDMU/PU1_TM_Data _H	CDMUJ043	47	SBDL_Driver	SBDL_Receiver
PU1J01	32	CDMU/PU1_TM_Clock _L	CDMUJ043	66	SBDL_Driver	SBDL_Receiver
PU1J01	33	CDMU/PU1_TM_Clock _H	CDMUJ043	46	SBDL_Driver	SBDL_Receiver
PU1J01	34	PU1/CDMU_TC_Squelch _H	CDMUJ043	6	SBDL_Receiver	SBDL_Driver
PU1J01	35	PU1/CDMU_TC_Data _H	CDMUJ043	45	SBDL_Receiver	SBDL_Driver
PU1J01	36	PU1/CDMU_TC_Clock _H	CDMUJ043	44	SBDL_Receiver	SBDL_Driver
PU1J01	40	PCDU/PU1_+28V_Mnt +	PCDUJ16	2	+28V_TM	EGSE
PU1J01	41	PCDU/PU1_+28V_Mnt -	PCDUJ16	10	+28V_TM	EGSE
PU1J01	47	PU1/CDMU_Sep_Strap2_Sts	CDMUJ115	32	Link	DR_Mnt
PU1J01	48	PU1/CDMU_Sep_Strap2_Sts RTN	CDMUJ115	31	Link	DR_Mnt



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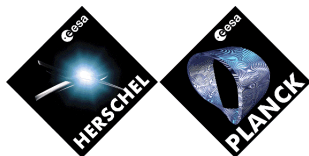
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PU1J01	49	PU1/PCDU_+28V_Aux_IN-1_Pwr RTN	PCDUJ14	14	EGSE_PWR	SA_Aux_PWR_in
PU1J01	50	PU1/PCDU_+28V_Aux_IN-2_Pwr +	PCDUJ15	1	EGSE_PWR	SA_Aux_PWR_in
PU1J01	51	PU1/PCDU_+28V_Aux_IN-3_Pwr RTN	PCDUJ18	14	EGSE_PWR	SA_Aux_PWR_in
PU1J01	52	CDMU/PU1_TM_Data_L	CDMUJ043	67	SBDL_Driver	SBDL_Receiver
PU1J01	BKSH	Shield_for_CDMU/PU1_TM_Clock	CDMUJ043	BKSH	Shield	Shield
PU1J01	BKSH	Shield_for_CDMU/PU1_TM_Data	CDMUJ043	BKSH	Shield	Shield
PU1J01	BKSH	Shield_for_PCDU/PU1_+28V_Mnt	PCDUJ16	BKSH	Shield	Shield
PU1J01	BKSH	Shield_for_PU1/CDMU_RM_A_Sep_Strap5a_Alarm_Sts	CDMUJ043	BKSH	Shield	Shield
PU1J01	BKSH	Shield_for_PU1/CDMU_RM_B_Sep_Strap6a_Alarm_Sts	CDMUJ053	BKSH	Shield	Shield
PU1J01	BKSH	Shield_for_PU1/CDMU_Sep_Strap1_Sts	CDMUJ105	BKSH	Shield	Shield
PU1J01	BKSH	Shield_for_PU1/CDMU_Sep_Strap2_Sts	CDMUJ115	BKSH	Shield	Shield
PU1J01	BKSH	Shield_for_PU1/CDMU_TC_Clock	CDMUJ043	BKSH	Shield	Shield
PU1J01	BKSH	Shield_for_PU1/CDMU_TC_Data	CDMUJ043	BKSH	Shield	Shield
PU1J01	BKSH	Shield_for_PU1/CDMU_TC_Squelch	CDMUJ043	BKSH	Shield	Shield



4.1.2 Satellite Level Umbilical Connector PU2 J01

SOURCE		SignalName	DESTINATION		Interface_of_Source	Interface of Destination
Connector Reference	Pin		Connected to Unit/Connector	Pin		
PU2J01						
PU2J01	8	PU2/PCDU_Charge_Array_Disable_Cmd +	PCDUJ18	2	Link	HL_Cmd
PU2J01	9	PU2/PCDU_Charge_Array_Disable_Cmd RTN	PCDUJ18	15	Link	HL_Cmd
PU2J01	13	PU2/ACC_Sep_Strap3b_Sts	ACCJ061	44	Link	DR_Mnt
PU2J01	17	PU2/CDMU_TC_Squelch_L	CDMUJ053	25	SBDL_Receiver	SBDL_Driver
PU2J01	18	PU2/CDMU_TC_Data_L	CDMUJ053	65	SBDL_Receiver	SBDL_Driver
PU2J01	19	PU2/CDMU_TC_Clock_L	CDMUJ053	64	SBDL_Receiver	SBDL_Driver
PU2J01	21	PU2/ACC_RM_A_Sep_Strap7a_Alarm_Sts_H	ACCJ033	57	Link	DR_Mnt
PU2J01	22	PU2/ACC_RM_A_Sep_Strap7a_Alarm_Sts_L	ACCJ033	76	Link	DR_Mnt
PU2J01	25	PU2/ACC_RM_B_Sep_Strap8a_Alarm_Sts_H	ACCJ043	57	Link	DR_Mnt
PU2J01	26	PU2/ACC_RM_B_Sep_Strap8a_Alarm_Sts_L	ACCJ043	76	Link	DR_Mnt
PU2J01	27	PU2/ACC_Sep_Strap3b_Sts RTN	ACCJ061	64	Link	DR_Mnt
PU2J01	28	PU2/PCDU_+28V_Aux_IN-6_Pwr +	PCDUJ23	1	EGSE_PWR	SA_Aux_PWR_in
PU2J01	29	PU2/PCDU_+28V_Aux_IN-4_Pwr +	PCDUJ19	1	EGSE_PWR	SA_Aux_PWR_in
PU2J01	30	PU2/PCDU_+28V_Aux_IN-5_Pwr RTN	PCDUJ22	14	EGSE_PWR	SA_Aux_PWR_in
PU2J01	31	CDMU/PU2_TM_Data_H	CDMUJ053	47	SBDL_Driver	SBDL_Receiver
PU2J01	32	CDMU/PU2_TM_Clock_L	CDMUJ053	66	SBDL_Driver	SBDL_Receiver
PU2J01	33	CDMU/PU2_TM_Clock_H	CDMUJ053	46	SBDL_Driver	SBDL_Receiver
PU2J01	34	PU2/CDMU_TC_Squelch_H	CDMUJ053	6	SBDL_Receiver	SBDL_Driver
PU2J01	35	PU2/CDMU_TC_Data_H	CDMUJ053	45	SBDL_Receiver	SBDL_Driver
PU2J01	36	PU2/CDMU_TC_Clock_H	CDMUJ053	44	SBDL_Receiver	SBDL_Driver
PU2J01	40	PCDU/PU2_+28V_Mnt +	PCDUJ20	2	+28V_TM	EGSE
PU2J01	41	PCDU/PU2_+28V_Mnt -	PCDUJ20	10	+28V_TM	EGSE
PU2J01	47	PU2/ACC_Sep_Strap4b_Sts	ACCJ071	44	Link	DR_Mnt
PU2J01	48	PU2/ACC_Sep_Strap4b_Sts RTN	ACCJ071	64	Link	DR_Mnt
PU2J01	49	PU2/PCDU_+28V_Aux_IN-4_Pwr RTN	PCDUJ19	14	EGSE_PWR	SA_Aux_PWR_in
PU2J01	50	PU2/PCDU_+28V_Aux_IN-5_Pwr +	PCDUJ22	1	EGSE_PWR	SA_Aux_PWR_in

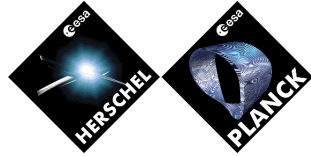


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PU2J01	51	PU2/PCDU_+28V_Aux_IN-6_Pwr RTN	PCDUJ23	14	EGSE_PWR	SA_Aux_PWR_in
PU2J01	52	CDMU/PU2_TM_Data_L	CDMUJ053	67	SBDL_Driver	SBDL_Receiver
PU2J01	BKSH	Shield_for_CDMU/PU2_TM_Clock	CDMUJ053	BKSH	Shield	Shield
PU2J01	BKSH	Shield_for_CDMU/PU2_TM_Data	CDMUJ053	BKSH	Shield	Shield
PU2J01	BKSH	Shield_for_PCDU/PU2_+28V_Mnt	PCDUJ20	BKSH	Shield	Shield
PU2J01	BKSH	Shield_for_PU2/ACC_RM_A_Sep_Strap7a_Alarm_Sts	ACCJ033	BKSH	Shield	Shield
PU2J01	BKSH	Shield_for_PU2/ACC_RM_B_Sep_Strap8a_Alarm_Sts	ACCJ043	BKSH	Shield	Shield
PU2J01	BKSH	Shield_for_PU2/ACC_Sep_Strap3b_Sts	ACCJ061	BKSH	Shield	Shield
PU2J01	BKSH	Shield_for_PU2/ACC_Sep_Strap4b_Sts	ACCJ071	BKSH	Shield	Shield
PU2J01	BKSH	Shield_for_PU2/CDMU_TC_Clock	CDMUJ053	BKSH	Shield	Shield
PU2J01	BKSH	Shield_for_PU2/CDMU_TC_Data	CDMUJ053	BKSH	Shield	Shield
PU2J01	BKSH	Shield_for_PU2/CDMU_TC_Squelch	CDMUJ053	BKSH	Shield	Shield

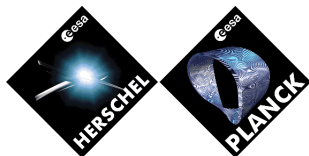


4.1.3 Satellite Level Skin Connector SK01B J09, J10, J11 & J12 Battery - PCDU & BDR ON/

SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
SK01BJ09	A	BATT/EGSE_Line1_Nom_Pwr-1 POSITIVE	BATTJ01	2		BatteryPower
SK01BJ09	a	PCDU/EGSE_Line1_Nom_Pwr-7 +	PCDUJ17	19		PCDU_Battery_Input
SK01BJ09	AA	BATT/EGSE_Line2_Nom_Pwr-6 NEGATIVE	BATTJ01	35		BatteryPower
SK01BJ09	B	BATT/EGSE_Line1_Nom_Pwr-1 NEGATIVE	BATTJ01	11		BatteryPower
SK01BJ09	b	PCDU/EGSE_Line1_Nom_Pwr-7 -	PCDUJ17	10		PCDU_Battery_Input
SK01BJ09	C	BATT/EGSE_Line1_Nom_Pwr-2 POSITIVE	BATTJ01	3		BatteryPower
SK01BJ09	c	BATT/EGSE_Line1_Nom_Pwr-7 POSITIVE	BATTJ01	8		BatteryPower
SK01BJ09	D	BATT/EGSE_Line1_Nom_Pwr-2 NEGATIVE	BATTJ01	12		BatteryPower
SK01BJ09	d	BATT/EGSE_Line1_Nom_Pwr-7 NEGATIVE	BATTJ01	17		BatteryPower
SK01BJ09	DD	PCDU/EGSE_Line2_Nom_Pwr-6 -	PCDUJ17	27		PCDU_Battery_Input
SK01BJ09	E	BATT/EGSE_Line1_Nom_Pwr-3 POSITIVE	BATTJ01	4		BatteryPower
SK01BJ09	e	BATT/EGSE_Line2_Nom_Pwr-1 POSITIVE	BATTJ01	21		BatteryPower
SK01BJ09	EE	PCDU/EGSE_Line2_Nom_Pwr-6 +	PCDUJ17	36		PCDU_Battery_Input
SK01BJ09	F	BATT/EGSE_Line1_Nom_Pwr-3 NEGATIVE	BATTJ01	13		BatteryPower
SK01BJ09	f	BATT/EGSE_Line2_Nom_Pwr-1 NEGATIVE	BATTJ01	30		BatteryPower
SK01BJ09	FF	PCDU/EGSE_Line2_Nom_Pwr-4 -	PCDUJ17	25		PCDU_Battery_Input
SK01BJ09	G	BATT/EGSE_Line1_Nom_Pwr-4 POSITIVE	BATTJ01	5		BatteryPower
SK01BJ09	g	BATT/EGSE_Line2_Nom_Pwr-2 POSITIVE	BATTJ01	22		BatteryPower
SK01BJ09	GG	PCDU/EGSE_Line2_Nom_Pwr-4 +	PCDUJ17	34		PCDU_Battery_Input
SK01BJ09	H	BATT/EGSE_Line1_Nom_Pwr-4 NEGATIVE	BATTJ01	14		BatteryPower
SK01BJ09	h	BATT/EGSE_Line2_Nom_Pwr-2 NEGATIVE	BATTJ01	31		BatteryPower
SK01BJ09	i	BATT/EGSE_Line2_Nom_Pwr-3 POSITIVE	BATTJ01	23		BatteryPower
SK01BJ09	J	BATT/EGSE_Line1_Nom_Pwr-5 POSITIVE	BATTJ01	6		BatteryPower
SK01BJ09	j	BATT/EGSE_Line2_Nom_Pwr-3 NEGATIVE	BATTJ01	32		BatteryPower
SK01BJ09	K	BATT/EGSE_Line1_Nom_Pwr-5 NEGATIVE	BATTJ01	15		BatteryPower
SK01BJ09	KK	BATT/EGSE_Line2_Nom_Pwr-6 POSITIVE	BATTJ01	26		BatteryPower
SK01BJ09	L	BATT/EGSE_Line1_Nom_Pwr-6 POSITIVE	BATTJ01	7		BatteryPower
SK01BJ09	M	BATT/EGSE_Line1_Nom_Pwr-6 NEGATIVE	BATTJ01	16		BatteryPower



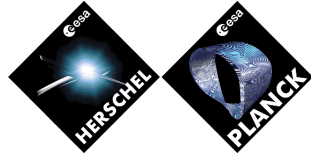
SK01BJ09	m	PCDU/EGSE_Line1_Nom_Pwr-6 +	PCDUJ17	18	PCDU_Battery_Input
SK01BJ09	MM	PCDU/EGSE_Line2_Nom_Pwr-5 -	PCDUJ17	26	PCDU_Battery_Input
SK01BJ09	n	PCDU/EGSE_Line1_Nom_Pwr-6 -	PCDUJ17	9	PCDU_Battery_Input
SK01BJ09	NN	PCDU/EGSE_Line2_Nom_Pwr-5 +	PCDUJ17	35	PCDU_Battery_Input
SK01BJ09	P	PCDU/EGSE_Line1_Nom_Pwr-5 -	PCDUJ17	8	PCDU_Battery_Input
SK01BJ09	p	PCDU/EGSE_Line2_Nom_Pwr-3 -	PCDUJ17	24	PCDU_Battery_Input
SK01BJ09	q	PCDU/EGSE_Line2_Nom_Pwr-3 +	PCDUJ17	33	PCDU_Battery_Input
SK01BJ09	R	PCDU/EGSE_Line1_Nom_Pwr-5 +	PCDUJ17	17	PCDU_Battery_Input
SK01BJ09	r	PCDU/EGSE_Line2_Nom_Pwr-2 -	PCDUJ17	23	PCDU_Battery_Input
SK01BJ09	S	PCDU/EGSE_Line1_Nom_Pwr-4 -	PCDUJ17	7	PCDU_Battery_Input
SK01BJ09	s	PCDU/EGSE_Line2_Nom_Pwr-2 +	PCDUJ17	32	PCDU_Battery_Input
SK01BJ09	T	PCDU/EGSE_Line1_Nom_Pwr-4 +	PCDUJ17	16	PCDU_Battery_Input
SK01BJ09	t	PCDU/EGSE_Line2_Nom_Pwr-1 -	PCDUJ17	22	PCDU_Battery_Input
SK01BJ09	U	PCDU/EGSE_Line1_Nom_Pwr-3 -	PCDUJ17	6	PCDU_Battery_Input
SK01BJ09	u	PCDU/EGSE_Line2_Nom_Pwr-1 +	PCDUJ17	31	PCDU_Battery_Input
SK01BJ09	V	PCDU/EGSE_Line1_Nom_Pwr-3 +	PCDUJ17	15	PCDU_Battery_Input
SK01BJ09	w	BATT/EGSE_Line2_Nom_Pwr-4 POSITIVE	BATTJ01	24	BatteryPower
SK01BJ09	W	PCDU/EGSE_Line1_Nom_Pwr-2 -	PCDUJ17	5	PCDU_Battery_Input
SK01BJ09	x	BATT/EGSE_Line2_Nom_Pwr-4 NEGATIVE	BATTJ01	33	BatteryPower
SK01BJ09	X	PCDU/EGSE_Line1_Nom_Pwr-2 +	PCDUJ17	14	PCDU_Battery_Input
SK01BJ09	y	BATT/EGSE_Line2_Nom_Pwr-5 POSITIVE	BATTJ01	25	BatteryPower
SK01BJ09	Y	PCDU/EGSE_Line1_Nom_Pwr-1 -	PCDUJ17	4	PCDU_Battery_Input
SK01BJ09	z	BATT/EGSE_Line2_Nom_Pwr-5 NEGATIVE	BATTJ01	34	BatteryPower
SK01BJ09	Z	PCDU/EGSE_Line1_Nom_Pwr-1 +	PCDUJ17	13	PCDU_Battery_Input



SOURCE		SignalName	DESTINATION		Interface_of_Source	Interface of Destination
Connector Reference	Pin		Connected to Unit/Connector	Pin		
SK01BJ10	A	BATT/EGSE_Line1_Red_Pwr-1 POSITIVE	BATTJ02	2		BatteryPower
SK01BJ10	a	PCDU/EGSE_Line1_Red_Pwr-7 +	PCDUJ21	19		PCDU_Battery_Input
SK01BJ10	AA	BATT/EGSE_Line2_Red_Pwr-6 NEGATIVE	BATTJ02	35		BatteryPower
SK01BJ10	B	BATT/EGSE_Line1_Red_Pwr-1 NEGATIVE	BATTJ02	11		BatteryPower
SK01BJ10	b	PCDU/EGSE_Line1_Red_Pwr-7 -	PCDUJ21	10		PCDU_Battery_Input
SK01BJ10	C	BATT/EGSE_Line1_Red_Pwr-2 POSITIVE	BATTJ02	3		BatteryPower
SK01BJ10	c	BATT/EGSE_Line1_Red_Pwr-7 POSITIVE	BATTJ02	8		BatteryPower
SK01BJ10	D	BATT/EGSE_Line1_Red_Pwr-2 NEGATIVE	BATTJ02	12		BatteryPower
SK01BJ10	d	BATT/EGSE_Line1_Red_Pwr-7 NEGATIVE	BATTJ02	17		BatteryPower
SK01BJ10	DD	PCDU/EGSE_Line2_Red_Pwr-6 -	PCDUJ21	27		PCDU_Battery_Input
SK01BJ10	E	BATT/EGSE_Line1_Red_Pwr-3 POSITIVE	BATTJ02	4		BatteryPower
SK01BJ10	e	BATT/EGSE_Line2_Red_Pwr-1 POSITIVE	BATTJ02	21		BatteryPower
SK01BJ10	EE	PCDU/EGSE_Line2_Red_Pwr-6 +	PCDUJ21	36		PCDU_Battery_Input
SK01BJ10	F	BATT/EGSE_Line1_Red_Pwr-3 NEGATIVE	BATTJ02	13		BatteryPower
SK01BJ10	f	BATT/EGSE_Line2_Red_Pwr-1 NEGATIVE	BATTJ02	30		BatteryPower
SK01BJ10	FF	PCDU/EGSE_Line2_Red_Pwr-4 -	PCDUJ21	25		PCDU_Battery_Input
SK01BJ10	G	BATT/EGSE_Line1_Red_Pwr-4 POSITIVE	BATTJ02	5		BatteryPower
SK01BJ10	g	BATT/EGSE_Line2_Red_Pwr-2 POSITIVE	BATTJ02	22		BatteryPower
SK01BJ10	GG	PCDU/EGSE_Line2_Red_Pwr-4 +	PCDUJ21	34		PCDU_Battery_Input
SK01BJ10	H	BATT/EGSE_Line1_Red_Pwr-4 NEGATIVE	BATTJ02	14		BatteryPower
SK01BJ10	h	BATT/EGSE_Line2_Red_Pwr-2 NEGATIVE	BATTJ02	31		BatteryPower
SK01BJ10	i	BATT/EGSE_Line2_Red_Pwr-3 POSITIVE	BATTJ02	23		BatteryPower
SK01BJ10	J	BATT/EGSE_Line1_Red_Pwr-5 POSITIVE	BATTJ02	6		BatteryPower
SK01BJ10	j	BATT/EGSE_Line2_Red_Pwr-3 NEGATIVE	BATTJ02	32		BatteryPower
SK01BJ10	K	BATT/EGSE_Line1_Red_Pwr-5 NEGATIVE	BATTJ02	15		BatteryPower
SK01BJ10	KK	BATT/EGSE_Line2_Red_Pwr-6 POSITIVE	BATTJ02	26		BatteryPower
SK01BJ10	L	BATT/EGSE_Line1_Red_Pwr-6 POSITIVE	BATTJ02	7		BatteryPower
SK01BJ10	M	BATT/EGSE_Line1_Red_Pwr-6 NEGATIVE	BATTJ02	16		BatteryPower
SK01BJ10	m	PCDU/EGSE_Line1_Red_Pwr-6 +	PCDUJ21	18		PCDU_Battery_Input
SK01BJ10	MM	PCDU/EGSE_Line2_Red_Pwr-5 -	PCDUJ21	26		PCDU_Battery_Input
SK01BJ10	n	PCDU/EGSE_Line1_Red_Pwr-6 -	PCDUJ21	9		PCDU_Battery_Input
SK01BJ10	NN	PCDU/EGSE_Line2_Red_Pwr-5 +	PCDUJ21	35		PCDU_Battery_Input



SK01BJ10	P	PCDU/EGSE_Line1_Red_Pwr-5 -	PCDUJ21	8	PCDU_Battery_Input
SK01BJ10	p	PCDU/EGSE_Line2_Red_Pwr-3 -	PCDUJ21	24	PCDU_Battery_Input
SK01BJ10	q	PCDU/EGSE_Line2_Red_Pwr-3 +	PCDUJ21	33	PCDU_Battery_Input
SK01BJ10	R	PCDU/EGSE_Line1_Red_Pwr-5 +	PCDUJ21	17	PCDU_Battery_Input
SK01BJ10	r	PCDU/EGSE_Line2_Red_Pwr-2 -	PCDUJ21	23	PCDU_Battery_Input
SK01BJ10	S	PCDU/EGSE_Line1_Red_Pwr-4 -	PCDUJ21	7	PCDU_Battery_Input
SK01BJ10	s	PCDU/EGSE_Line2_Red_Pwr-2 +	PCDUJ21	32	PCDU_Battery_Input
SK01BJ10	T	PCDU/EGSE_Line1_Red_Pwr-4 +	PCDUJ21	16	PCDU_Battery_Input
SK01BJ10	t	PCDU/EGSE_Line2_Red_Pwr-1 -	PCDUJ21	22	PCDU_Battery_Input
SK01BJ10	U	PCDU/EGSE_Line1_Red_Pwr-3 -	PCDUJ21	6	PCDU_Battery_Input
SK01BJ10	u	PCDU/EGSE_Line2_Red_Pwr-1 +	PCDUJ21	31	PCDU_Battery_Input
SK01BJ10	V	PCDU/EGSE_Line1_Red_Pwr-3 +	PCDUJ21	15	PCDU_Battery_Input
SK01BJ10	w	BATT/EGSE_Line2_Red_Pwr-4 POSITIVE	BATTJ02	24	BatteryPower
SK01BJ10	W	PCDU/EGSE_Line1_Red_Pwr-2 -	PCDUJ21	5	PCDU_Battery_Input
SK01BJ10	x	BATT/EGSE_Line2_Red_Pwr-4 NEGATIVE	BATTJ02	33	BatteryPower
SK01BJ10	X	PCDU/EGSE_Line1_Red_Pwr-2 +	PCDUJ21	14	PCDU_Battery_Input
SK01BJ10	y	BATT/EGSE_Line2_Red_Pwr-5 POSITIVE	BATTJ02	25	BatteryPower
SK01BJ10	Y	PCDU/EGSE_Line1_Red_Pwr-1 -	PCDUJ21	4	PCDU_Battery_Input
SK01BJ10	z	BATT/EGSE_Line2_Red_Pwr-5 NEGATIVE	BATTJ02	34	BatteryPower
SK01BJ10	Z	PCDU/EGSE_Line1_Red_Pwr-1 +	PCDUJ21	13	PCDU_Battery_Input



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SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
SK01BJ11	1	EGSE/PCDU_BDR1_AIT_ON_Cmd	PCDUJ13	72	EGSE__HL_Cmd	HL_Cmd
SK01BJ11	2	EGSE/PCDU_BDR1_AIT_ON_Cmd RTN	PCDUJ13	52	EGSE__HL_Cmd	HL_Cmd
SK01BJ11	3	EGSE/PCDU_BDR1_AIT_OFF_Cmd	PCDUJ13	73	EGSE__HL_Cmd	HL_Cmd
SK01BJ11	4	EGSE/PCDU_BDR1_AIT_OFF_Cmd RTN	PCDUJ13	53	EGSE__HL_Cmd	HL_Cmd

SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
SK01BJ12	1	EGSE/PCDU_BDR2_AIT_ON_Cmd	PCDUJ26	72	EGSE__HL_Cmd	HL_Cmd
SK01BJ12	2	EGSE/PCDU_BDR2_AIT_ON_Cmd RTN	PCDUJ26	52	EGSE__HL_Cmd	HL_Cmd
SK01BJ12	3	EGSE/PCDU_BDR2_AIT_OFF_Cmd	PCDUJ26	73	EGSE__HL_Cmd	HL_Cmd
SK01BJ12	4	EGSE/PCDU_BDR2_AIT_OFF_Cmd RTN	PCDUJ26	53	EGSE__HL_Cmd	HL_Cmd



4.1.4 Satellite Level Skin Connector SK02 J01 & J02 CDMU 1553 Bus

SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
SK02J01	1	DMS_1553A	CDMUJ023	1	1553_Link_or_RT	1553_BC
SK02J01	1	DMS_1553A	CDMUJ073	1	1553_Link_or_RT	1553_BC
SK02J01	2	DMS_1553A RTN	CDMUJ023	11	1553_Link_or_RT	1553_BC
SK02J01	2	DMS_1553A RTN	CDMUJ073	11	1553_Link_or_RT	1553_BC
SK02J01	3	DMS_1553A	CDMUJ023	1	1553_Link_or_RT	1553_BC
SK02J01	3	DMS_1553A	CDMUJ073	1	1553_Link_or_RT	1553_BC
SK02J01	4	DMS_1553A RTN	CDMUJ023	11	1553_Link_or_RT	1553_BC
SK02J01	4	DMS_1553A RTN	CDMUJ073	11	1553_Link_or_RT	1553_BC
SK02J01	BKSH	Shield_for_DMS_1553A	CDMUJ023	BKSH	Shield	Shield
SK02J01	BKSH	Shield_for_DMS_1553A	CDMUJ073	BKSH	Shield	Shield

SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
SK02J02	1	DMS_1553B	CDMUJ024	1	1553_Link_or_RT	1553_BC
SK02J02	1	DMS_1553B	CDMUJ074	1	1553_Link_or_RT	1553_BC
SK02J02	2	DMS_1553B RTN	CDMUJ024	11	1553_Link_or_RT	1553_BC
SK02J02	2	DMS_1553B RTN	CDMUJ074	11	1553_Link_or_RT	1553_BC
SK02J02	3	DMS_1553B	CDMUJ024	1	1553_Link_or_RT	1553_BC
SK02J02	3	DMS_1553B	CDMUJ074	1	1553_Link_or_RT	1553_BC
SK02J02	4	DMS_1553B RTN	CDMUJ024	11	1553_Link_or_RT	1553_BC
SK02J02	4	DMS_1553B RTN	CDMUJ074	11	1553_Link_or_RT	1553_BC
SK02J02	BKSH	Shield_for_DMS_1553B	CDMUJ024	BKSH	Shield	Shield
SK02J02	BKSH	Shield_for_DMS_1553B	CDMUJ074	BKSH	Shield	Shield



4.1.5 Satellite Level Skin Connector SK02 J03 & J04 ACC 1553 Bus

SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
SK02J03	1	ACMS_1553A	ACCJ024	5	1553_Link_or_RT	1553_BC
SK02J03	1	ACMS_1553A	ACCJ054	5	1553_Link_or_RT	1553_BC
SK02J03	2	ACMS_1553A RTN	ACCJ024	15	1553_Link_or_RT	1553_BC
SK02J03	2	ACMS_1553A RTN	ACCJ054	15	1553_Link_or_RT	1553_BC
SK02J03	3	ACMS_1553A	ACCJ024	5	1553_Link_or_RT	1553_BC
SK02J03	3	ACMS_1553A	ACCJ054	5	1553_Link_or_RT	1553_BC
SK02J03	4	ACMS_1553A RTN	ACCJ024	15	1553_Link_or_RT	1553_BC
SK02J03	4	ACMS_1553A RTN	ACCJ054	15	1553_Link_or_RT	1553_BC
SK02J03	BKSH	Shield_for_ACMS_1553A	ACCJ024	BKSH	Shield	Shield
SK02J03	BKSH	Shield_for_ACMS_1553A	ACCJ054	BKSH	Shield	Shield

SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
SK02J04	1	ACMS_1553B	ACCJ023	5	1553_Link_or_RT	1553_BC
SK02J04	1	ACMS_1553B	ACCJ053	5	1553_Link_or_RT	1553_BC
SK02J04	2	ACMS_1553B RTN	ACCJ023	15	1553_Link_or_RT	1553_BC
SK02J04	2	ACMS_1553B RTN	ACCJ053	15	1553_Link_or_RT	1553_BC
SK02J04	3	ACMS_1553B	ACCJ023	5	1553_Link_or_RT	1553_BC
SK02J04	3	ACMS_1553B	ACCJ053	5	1553_Link_or_RT	1553_BC
SK02J04	4	ACMS_1553B RTN	ACCJ023	15	1553_Link_or_RT	1553_BC
SK02J04	4	ACMS_1553B RTN	ACCJ053	15	1553_Link_or_RT	1553BC
SK02J04	BKSH	Shield_for_ACMS_1553B	ACCJ023	BKSH	Shield	Shield
SK02J04	BKSH	Shield_for_ACMS_1553B	ACCJ053	BKSH	Shield	Shield

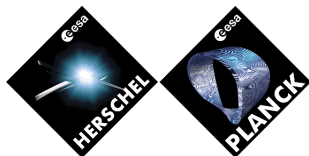


4.1.6 Satellite Level Skin Connector SK02 J05 & J06 ACC - Thruster Commands

SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
SK02J05	1	ACC/EGSE_LV_A_Close_Cmd	ACCJ103	9		HL_Cmd
SK02J05	2	ACC/EGSE_LV_A_Close_Cmd RTN	ACCJ103	29		HL_Cmd
SK02J05	3	ACC/EGSE_THR_1N01_Vlv2_Cmd	ACCJ101	9		THR1N_Vlv-Cmd
SK02J05	4	ACC/EGSE_LV_A_Open_Cmd	ACCJ101	12		HL_Cmd
SK02J05	5	ACC/EGSE_LV_A_Open_Cmd RTN	ACCJ101	32		HL_Cmd
SK02J05	6	ACC/EGSE_THR_1N01_Vlv1_Cmd	ACCJ101	7		THR1N_Vlv-Cmd
SK02J05	7	ACC/EGSE_THR_1N01_Vlv1_Cmd RTN	ACCJ101	27		THR1N_Vlv-Cmd
SK02J05	8	ACC/EGSE_THR_1N01_Vlv2_Cmd RTN	ACCJ101	29		THR1N_Vlv-Cmd
SK02J05	9	ACC/EGSE_THR_1N02_Vlv1_Cmd	ACCJ101	47		THR1N_Vlv-Cmd
SK02J05	10	ACC/EGSE_THR_20N01_Vlv_Cmd	ACCJ101	1		THRDV_Vlv-Cmd
SK02J05	11	ACC/EGSE_THR_20N01_Vlv_Cmd RTN	ACCJ101	21		THRDV_Vlv-Cmd
SK02J05	12	ACC/EGSE_THR_20N02_Vlv_Cmd	ACCJ101	41		THRDV_Vlv-Cmd
SK02J05	13	ACC/EGSE_THR_20N02_Vlv_Cmd RTN	ACCJ101	61		THRDV_Vlv-Cmd
SK02J05	14	ACC/EGSE_THR_20N03_Vlv_Cmd	ACCJ101	3		THRDV_Vlv-Cmd
SK02J05	15	ACC/EGSE_THR_20N03_Vlv_Cmd RTN	ACCJ101	23		THRDV_Vlv-Cmd
SK02J05	16	ACC/EGSE_THR_1N02_Vlv1_Cmd RTN	ACCJ101	67		THR1N_Vlv-Cmd
SK02J05	17	ACC/EGSE_THR_20N04_Vlv_Cmd	ACCJ101	43		THRDV_Vlv-Cmd
SK02J05	18	ACC/EGSE_THR_20N04_Vlv_Cmd RTN	ACCJ101	63		THRDV_Vlv-Cmd
SK02J05	19	ACC/EGSE_THR_20N05_Vlv_Cmd	ACCJ101	5		THRDV_Vlv-Cmd
SK02J05	20	ACC/EGSE_THR_20N05_Vlv_Cmd RTN	ACCJ101	25		THRDV_Vlv-Cmd
SK02J05	21	ACC/EGSE_THR_20N06_Vlv_Cmd	ACCJ101	45		THRDV_Vlv-Cmd
SK02J05	22	ACC/EGSE_THR_20N06_Vlv_Cmd RTN	ACCJ101	65		THRDV_Vlv-Cmd
SK02J05	23	ACC/EGSE_THR_1N02_Vlv2_Cmd	ACCJ101	49		THR1N_Vlv-Cmd
SK02J05	24	ACC/EGSE_THR_1N02_Vlv2_Cmd RTN	ACCJ101	69		THR1N_Vlv-Cmd
SK02J05	32	EGSE/THR_20N04_Vlv_Cmd Positive	THR_20N04J01	Yellow		THR20_Vlv-Cmd_Input
SK02J05	33	EGSE/THR_20N04_Vlv_Cmd Negative	THR_20N04J01	Red		THR20_Vlv-Cmd_Input
SK02J05	34	EGSE/THR_20N05_Vlv_Cmd Positive	THR_20N05J01	Yellow		THR20_Vlv-Cmd_Input
SK02J05	35	EGSE/THR_20N05_Vlv_Cmd Negative	THR_20N05J01	Red		THR20_Vlv-Cmd_Input



SK02J05	36	EGSE/THR_20N06_Vlv_Cmd Positive	THR_20N06J01	Yellow		THR20_Vlv-Cmd_Input
SK02J05	37	EGSE/THR_20N06_Vlv_Cmd Negative	THR_20N06J01	Red		THR20_Vlv-Cmd_Input
SK02J05	38	EGSE/THR_1N02_Vlv2_Cmd Positive	THR_1N02J01	Blue		THR1_Vlv-Cmd_Input
SK02J05	39	EGSE/THR_1N02_Vlv2_Cmd Negative	THR_1N02J01	White		THR1_Vlv-Cmd_Input
SK02J05	40	EGSE/THR_20N01_Vlv_Cmd Positive	THR_20N01J01	Yellow		THR20_Vlv-Cmd_Input
SK02J05	41	EGSE/THR_20N01_Vlv_Cmd Negative	THR_20N01J01	Red		THR20_Vlv-Cmd_Input
SK02J05	42	EGSE/THR_20N02_Vlv_Cmd Positive	THR_20N02J01	Yellow		THR20_Vlv-Cmd_Input
SK02J05	43	EGSE/THR_20N02_Vlv_Cmd Negative	THR_20N02J01	Red		THR20_Vlv-Cmd_Input
SK02J05	44	EGSE/THR_20N03_Vlv_Cmd Positive	THR_20N03J01	Yellow		THR20_Vlv-Cmd_Input
SK02J05	45	EGSE/THR_20N03_Vlv_Cmd Negative	THR_20N03J01	Red		THR20_Vlv-Cmd_Input
SK02J05	46	EGSE/THR_1N02_Vlv1_Cmd Positive	THR_1N02J01	Red		THR1_Vlv-Cmd_Input
SK02J05	47	EGSE/LVA_ON_Cmd Positive	LVAJ01	White		LV_Cmd_Input
SK02J05	48	EGSE/LVA_ON_Cmd Negative	LVAJ01	Black/White		LV_Cmd_Input
SK02J05	49	EGSE/THR_1N01_Vlv1_Cmd Positive	THR_1N01J01	Red		THR1_Vlv-Cmd_Input
SK02J05	50	EGSE/THR_1N01_Vlv1_Cmd Negative	THR_1N01J01	Black		THR1_Vlv-Cmd_Input
SK02J05	51	EGSE/THR_1N01_Vlv2_Cmd Positive	THR_1N01J01	Blue		THR1_Vlv-Cmd_Input
SK02J05	52	EGSE/THR_1N02_Vlv1_Cmd Negative	THR_1N02J01	Black		THR1_Vlv-Cmd_Input
SK02J05	53	EGSE/LVA_OFF_Cmd Positive	LVAJ01	Red		LV_Cmd_Input
SK02J05	54	EGSE/LVA_OFF_Cmd Negative	LVAJ01	Black		LV_Cmd_Input
SK02J05	55	EGSE/THR_1N01_Vlv2_Cmd Negative	THR_1N01J01	White		THR1_Vlv-Cmd_Input
SK02J05	BKSH	Shield_for_ACC/EGSE_LV_A_Close_Cmd	ACCJ103	BKSH		Shield
SK02J05	BKSH	Shield_for_ACC/EGSE_LV_A_Open_Cmd	ACCJ101	BKSH		Shield
SK02J05	BKSH	Shield_for_ACC/EGSE_THR_1N01_Vlv1_Cmd	ACCJ101	BKSH		Shield
SK02J05	BKSH	Shield_for_ACC/EGSE_THR_1N01_Vlv2_Cmd	ACCJ101	BKSH		Shield
SK02J05	BKSH	Shield_for_ACC/EGSE_THR_1N02_Vlv1_Cmd	ACCJ101	BKSH		Shield
SK02J05	BKSH	Shield_for_ACC/EGSE_THR_1N02_Vlv2_Cmd	ACCJ101	BKSH		Shield
SK02J05	BKSH	Shield_for_ACC/EGSE_THR_20N01_Vlv_Cmd	ACCJ101	BKSH		Shield
SK02J05	BKSH	Shield_for_ACC/EGSE_THR_20N02_Vlv_Cmd	ACCJ101	BKSH		Shield
SK02J05	BKSH	Shield_for_ACC/EGSE_THR_20N03_Vlv_Cmd	ACCJ101	BKSH		Shield
SK02J05	BKSH	Shield_for_ACC/EGSE_THR_20N04_Vlv_Cmd	ACCJ101	BKSH		Shield
SK02J05	BKSH	Shield_for_ACC/EGSE_THR_20N05_Vlv_Cmd	ACCJ101	BKSH		Shield
SK02J05	BKSH	Shield_for_ACC/EGSE_THR_20N06_Vlv_Cmd	ACCJ101	BKSH		Shield
SK02J05	BKSH	Shield_for_EGSE/LVA_OFF_Cmd	LVAJ01	BKSH		Shield
SK02J05	BKSH	Shield_for_EGSE/LVA_ON_Cmd	LVAJ01	BKSH		Shield
SK02J05	BKSH	Shield_for_EGSE/THR_1N01_Vlv1_Cmd	THR_1N01J01	BKSH		Shield
SK02J05	BKSH	Shield_for_EGSE/THR_1N01_Vlv2_Cmd	THR_1N01J01	BKSH		Shield
SK02J05	BKSH	Shield_for_EGSE/THR_1N02_Vlv1_Cmd	THR_1N02J01	BKSH		Shield



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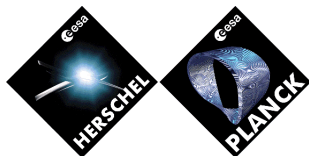
SK02J05	BKSH	Shield_for_EGSE/THR_1N02_Vlv2_Cmd	THR_1N02J01	BKSH		Shield
SK02J05	BKSH	Shield_for_EGSE/THR_20N01_Vlv_Cmd	THR_20N01J01	BKSH		Shield
SK02J05	BKSH	Shield_for_EGSE/THR_20N02_Vlv_Cmd	THR_20N02J01	BKSH		Shield
SK02J05	BKSH	Shield_for_EGSE/THR_20N03_Vlv_Cmd	THR_20N03J01	BKSH		Shield
SK02J05	BKSH	Shield_for_EGSE/THR_20N04_Vlv_Cmd	THR_20N04J01	BKSH		Shield
SK02J05	BKSH	Shield_for_EGSE/THR_20N05_Vlv_Cmd	THR_20N05J01	BKSH		Shield
SK02J05	BKSH	Shield_for_EGSE/THR_20N06_Vlv_Cmd	THR_20N06J01	BKSH		Shield



SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
SK02J06	1	ACC/EGSE_LV_B_OFF_Cmd	ACCJ113	9		HL_Cmd
SK02J06	2	ACC/EGSE_LV_B_OFF_Cmd RTN	ACCJ113	29		HL_Cmd
SK02J06	3	ACC/EGSE_THR_1N03_Vlv2_Cmd	ACCJ111	9		THR1N_Vlv-Cmd
SK02J06	4	ACC/EGSE_LV_B_ON_Cmd	ACCJ111	12		HL_Cmd
SK02J06	5	ACC/EGSE_LV_B_ON_Cmd RTN	ACCJ111	32		HL_Cmd
SK02J06	6	ACC/EGSE_THR_1N03_Vlv1_Cmd	ACCJ111	7		THR1N_Vlv-Cmd
SK02J06	7	ACC/EGSE_THR_1N03_Vlv1_Cmd RTN	ACCJ111	27		THR1N_Vlv-Cmd
SK02J06	8	ACC/EGSE_THR_1N03_Vlv2_Cmd RTN	ACCJ111	29		THR1N_Vlv-Cmd
SK02J06	9	ACC/EGSE_THR_1N04_Vlv1_Cmd	ACCJ111	47		THR1N_Vlv-Cmd
SK02J06	10	ACC/EGSE_THR_20N07_Vlv_Cmd	ACCJ111	1		THRDV_Vlv-Cmd
SK02J06	11	ACC/EGSE_THR_20N07_Vlv_Cmd RTN	ACCJ111	21		THRDV_Vlv-Cmd
SK02J06	12	ACC/EGSE_THR_20N08_Vlv_Cmd	ACCJ111	41		THRDV_Vlv-Cmd
SK02J06	13	ACC/EGSE_THR_20N08_Vlv_Cmd RTN	ACCJ111	61		THRDV_Vlv-Cmd
SK02J06	14	ACC/EGSE_THR_20N09_Vlv_Cmd	ACCJ111	3		THRDV_Vlv-Cmd
SK02J06	15	ACC/EGSE_THR_20N09_Vlv_Cmd RTN	ACCJ111	23		THRDV_Vlv-Cmd
SK02J06	16	ACC/EGSE_THR_1N04_Vlv1_Cmd RTN	ACCJ111	67		THR1N_Vlv-Cmd
SK02J06	17	ACC/EGSE_THR_20N10_Vlv_Cmd	ACCJ111	43		THRDV_Vlv-Cmd
SK02J06	18	ACC/EGSE_THR_20N10_Vlv_Cmd RTN	ACCJ111	63		THRDV_Vlv-Cmd
SK02J06	19	ACC/EGSE_THR_20N11_Vlv_Cmd	ACCJ111	5		THRDV_Vlv-Cmd
SK02J06	20	ACC/EGSE_THR_20N11_Vlv_Cmd RTN	ACCJ111	25		THRDV_Vlv-Cmd
SK02J06	21	ACC/EGSE_THR_20N12_Vlv_Cmd	ACCJ111	45		THRDV_Vlv-Cmd
SK02J06	22	ACC/EGSE_THR_20N12_Vlv_Cmd RTN	ACCJ111	65		THRDV_Vlv-Cmd
SK02J06	23	ACC/EGSE_THR_1N04_Vlv2_Cmd	ACCJ111	49		THR1N_Vlv-Cmd
SK02J06	24	ACC/EGSE_THR_1N04_Vlv2_Cmd RTN	ACCJ111	69		THR1N_Vlv-Cmd
SK02J06	32	EGSE/THR_20N10_Vlv_Cmd Positive	THR_20N10J01	Yellow		THR20_Vlv-Cmd_Input
SK02J06	33	EGSE/THR_20N10_Vlv_Cmd Negative	THR_20N10J01	Red		THR20_Vlv-Cmd_Input
SK02J06	34	EGSE/THR_20N11_Vlv_Cmd Positive	THR_20N11J01	Yellow		THR20_Vlv-Cmd_Input
SK02J06	35	EGSE/THR_20N11_Vlv_Cmd Negative	THR_20N11J01	Red		THR20_Vlv-Cmd_Input
SK02J06	36	EGSE/THR_20N12_Vlv_Cmd Positive	THR_20N12J01	Yellow		THR20_Vlv-Cmd_Input
SK02J06	37	EGSE/THR_20N12_Vlv_Cmd Negative	THR_20N12J01	Red		THR20_Vlv-Cmd_Input
SK02J06	38	EGSE/THR_1N04_Vlv2_Cmd Positive	THR_1N04J01	Blue		THR1_Vlv-Cmd_Input
SK02J06	39	EGSE/THR_1N04_Vlv2_Cmd Negative	THR_1N04J01	White		THR1_Vlv-Cmd_Input



SK02J06	40	EGSE/THR_20N07_Vlv_Cmd Positive	THR_20N07J01	Yellow		THR20_Vlv-Cmd_Input
SK02J06	41	EGSE/THR_20N07_Vlv_Cmd Negative	THR_20N07J01	Red		THR20_Vlv-Cmd_Input
SK02J06	42	EGSE/THR_20N08_Vlv_Cmd Positive	THR_20N08J01	Yellow		THR20_Vlv-Cmd_Input
SK02J06	43	EGSE/THR_20N08_Vlv_Cmd Negative	THR_20N08J01	Red		THR20_Vlv-Cmd_Input
SK02J06	44	EGSE/THR_20N09_Vlv_Cmd Positive	THR_20N09J01	Yellow		THR20_Vlv-Cmd_Input
SK02J06	45	EGSE/THR_20N09_Vlv_Cmd Negative	THR_20N09J01	Red		THR20_Vlv-Cmd_Input
SK02J06	46	EGSE/THR_1N04_Vlv1_Cmd Positive	THR_1N04J01	Red		THR1_Vlv-Cmd_Input
SK02J06	47	EGSE/LVB_ON_Cmd Positive	LVBj01	White		LV_Cmd_Input
SK02J06	48	EGSE/LVB_ON_Cmd Negative	LVBj01	Black/White		LV_Cmd_Input
SK02J06	49	EGSE/THR_1N03_Vlv1_Cmd Positive	THR_1N03J01	Red		THR1_Vlv-Cmd_Input
SK02J06	50	EGSE/THR_1N03_Vlv1_Cmd Negative	THR_1N03J01	Black		THR1_Vlv-Cmd_Input
SK02J06	51	EGSE/THR_1N03_Vlv2_Cmd Positive	THR_1N03J01	Blue		THR1_Vlv-Cmd_Input
SK02J06	53	EGSE/LVB_OFF_Cmd Positive	LVBj01	Red		THR1_Vlv-Cmd_Input
SK02J06	54	EGSE/LVB_OFF_Cmd Negative	LVBj01	Black		LV_Cmd_Input
SK02J06	55	EGSE/THR_1N03_Vlv2_Cmd Negative	THR_1N03J01	White		LV_Cmd_Input
SK02J06	BKSH	Shield_for_ACC/EGSE_LV_B_OFF_Cmd	ACCJ113	BKSH		THR1_Vlv-Cmd_Input
SK02J06	BKSH	Shield_for_ACC/EGSE_LV_B_ON_Cmd	ACCJ111	BKSH		Shield
SK02J06	BKSH	Shield_for_ACC/EGSE_THR_1N03_Vlv1_Cmd	ACCJ111	BKSH		Shield
SK02J06	BKSH	Shield_for_ACC/EGSE_THR_1N03_Vlv2_Cmd	ACCJ111	BKSH		Shield
SK02J06	BKSH	Shield_for_ACC/EGSE_THR_1N04_Vlv1_Cmd	ACCJ111	BKSH		Shield
SK02J06	BKSH	Shield_for_ACC/EGSE_THR_1N04_Vlv2_Cmd	ACCJ111	BKSH		Shield
SK02J06	BKSH	Shield_for_ACC/EGSE_THR_20N07_Vlv_Cmd	ACCJ111	BKSH		Shield
SK02J06	BKSH	Shield_for_ACC/EGSE_THR_20N08_Vlv_Cmd	ACCJ111	BKSH		Shield
SK02J06	BKSH	Shield_for_ACC/EGSE_THR_20N09_Vlv_Cmd	ACCJ111	BKSH		Shield
SK02J06	BKSH	Shield_for_ACC/EGSE_THR_20N10_Vlv_Cmd	ACCJ111	BKSH		Shield
SK02J06	BKSH	Shield_for_ACC/EGSE_THR_20N11_Vlv_Cmd	ACCJ111	BKSH		Shield
SK02J06	BKSH	Shield_for_ACC/EGSE_THR_20N12_Vlv_Cmd	ACCJ111	BKSH		Shield
SK02J06	BKSH	Shield_for_EGSE/LVB_OFF_Cmd	LVBj01	BKSH		Shield
SK02J06	BKSH	Shield_for_EGSE/LVB_ON_Cmd	LVBj01	BKSH		Shield
SK02J06	BKSH	Shield_for_EGSE/THR_1N03_Vlv1_Cmd	THR_1N03J01	BKSH		Shield
SK02J06	BKSH	Shield_for_EGSE/THR_1N03_Vlv2_Cmd	THR_1N03J01	BKSH		Shield
SK02J06	BKSH	Shield_for_EGSE/THR_1N04_Vlv1_Cmd	THR_1N04J01	BKSH		Shield
SK02J06	BKSH	Shield_for_EGSE/THR_1N04_Vlv2_Cmd	THR_1N04J01	BKSH		Shield
SK02J06	BKSH	Shield_for_EGSE/THR_20N07_Vlv_Cmd	THR_20N07J01	BKSH		Shield
SK02J06	BKSH	Shield_for_EGSE/THR_20N08_Vlv_Cmd	THR_20N08J01	BKSH		Shield
SK02J06	BKSH	Shield_for_EGSE/THR_20N09_Vlv_Cmd	THR_20N09J01	BKSH		Shield
SK02J06	BKSH	Shield_for_EGSE/THR_20N10_Vlv_Cmd	THR_20N10J01	BKSH		Shield

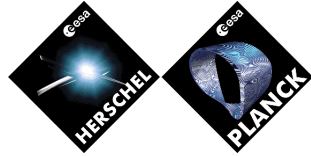


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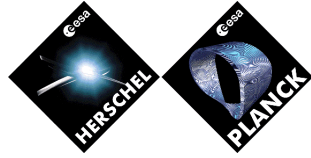
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SK02J06	BKSH	Shield_for_EGSE/THR_20N11_Vlv_Cmd	THR_20N11J01	BKSH		Shield
SK02J06	BKSH	Shield_for_EGSE/THR_20N12_Vlv_Cmd	THR_20N12J01	BKSH		Shield



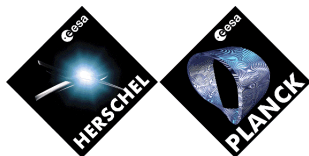
4.1.7 Satellite Level Skin Connector SK02 J07 ACC - PT+Tank Thermistors

SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
SK02J07	1	ACC/EGSE_PT_Sensor_Pwr	ACCJ073	56		PT_Pwr
SK02J07	2	ACC/EGSE_PT_Sensor_Pwr RTN	ACCJ073	76		PT_Pwr
SK02J07	3	EGSE/PT_Sensor_Pwr Positive	PTJ01	A		PT_Input_Pwr
SK02J07	4	EGSE/PT_Sensor_Pwr RTN	PTJ01	B		PT_Input_Pwr
SK02J07	5	THERM/EGSE_Therm2_Tank_Mnt	N/A	FL1		Thermistor
SK02J07	6	EGSE/ACC_PT_Sensor_Meas	ACCJ073	16		PT_Meas
SK02J07	7	EGSE/ACC_PT_Sensor_Meas RTN	ACCJ073	36		PT_Meas
SK02J07	8	PT/EGSE_Sensor_Meas Positive	PTJ01	E		PT_Meas_Output
SK02J07	9	PT/EGSE_Sensor_Meas RTN	PTJ01	D		PT_Meas_Output
SK02J07	10	EGSE/ACC_Therm1_Tank_Mnt	ACCJ063	15		Therm_Mnt
SK02J07	11	EGSE/ACC_Therm1_Tank_Mnt RTN	ACCJ063	35		Therm_Mnt
SK02J07	12	THERM/EGSE_Therm1_Tank_Mnt	N/A	FL1		Thermistor
SK02J07	13	THERM/EGSE_Therm1_Tank_Mnt	N/A	FL2		Thermistor
SK02J07	14	EGSE/ACC_Therm2_Tank_Mnt	ACCJ063	55		Therm_Mnt
SK02J07	17	EGSE/ACC_Therm3_Tank_Mnt	ACCJ073	15		Therm_Mnt
SK02J07	18	EGSE/ACC_Therm3_Tank_Mnt RTN	ACCJ073	35		Therm_Mnt
SK02J07	19	THERM/EGSE_Therm3_Tank_Mnt	N/A	FL1		Thermistor
SK02J07	20	THERM/EGSE_Therm3_Tank_Mnt	N/A	FL2		Thermistor
SK02J07	21	EGSE/ACC_Therm2_Tank_Mnt RTN	ACCJ063	75		Therm_Mnt
SK02J07	22	THERM/EGSE_Therm2_Tank_Mnt	N/A	FL2		Thermistor
SK02J07	BKSH	Shield_for_EGSE/ACC_PT_Sensor_Meas	ACCJ073	BKSH		Shield
SK02J07	BKSH	Shield_for_EGSE/ACC_Therm1_Tank_Mnt	ACCJ063	BKSH		Shield
SK02J07	BKSH	Shield_for_EGSE/ACC_Therm2_Tank_Mnt	ACCJ063	BKSH		Shield
SK02J07	BKSH	Shield_for_EGSE/ACC_Therm3_Tank_Mnt	ACCJ073	BKSH		Shield
SK02J07	BKSH	Shield_for_PT/EGSE_Sensor_Meas	PTJ01	BKSH		Shield
SK02J07	BKSH	Shield_for_THERM/EGSE_Therm1_Tank_Mnt	N/A	BKSH		Shield
SK02J07	BKSH	Shield_for_THERM/EGSE_Therm2_Tank_Mnt	N/A	BKSH		Shield
SK02J07	BKSH	Shield_for_THERM/EGSE_Therm3_Tank_Mnt	N/A	BKSH		Shield

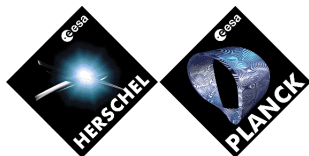


4.1.8 Satellite Level Skin Connector SK02 J08 ACC - Thruster Thermocouples

SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
SK02J08	1	EGSE/ACC_LV_A_Closed_Sts	ACCJ061	43		DR_Mnt
SK02J08	2	EGSE/ACC_LV_A_Closed_Sts RTN	ACCJ061	63		DR_Mnt
SK02J08	4	EGSE/ACC_LV_A_Open_Sts	ACCJ061	3		DR_Mnt
SK02J08	5	EGSE/ACC_LV_A_Open_Sts RTN	ACCJ061	23		DR_Mnt
SK02J08	9	EGSE/ACC_THR_1N01_TS_Mnt	ACCJ063	14		THR_TS
SK02J08	10	EGSE/ACC_THR_20N01_TS_Mnt	ACCJ063	11		THR_TS
SK02J08	11	EGSE/ACC_THR_20N01_TS_Mnt RTN	ACCJ063	31		THR_TS
SK02J08	12	EGSE/ACC_THR_20N02_TS_Mnt	ACCJ063	51		THR_TS
SK02J08	13	EGSE/ACC_THR_20N02_TS_Mnt RTN	ACCJ063	71		THR_TS
SK02J08	14	EGSE/ACC_THR_20N03_TS_Mnt	ACCJ063	12		THR_TS
SK02J08	15	EGSE/ACC_THR_20N03_TS_Mnt RTN	ACCJ063	32		THR_TS
SK02J08	16	EGSE/ACC_THR_1N01_TS_Mnt RTN	ACCJ063	34		THR_TS
SK02J08	17	EGSE/ACC_THR_20N04_TS_Mnt	ACCJ063	52		THR_TS
SK02J08	18	EGSE/ACC_THR_20N04_TS_Mnt RTN	ACCJ063	72		THR_TS
SK02J08	19	EGSE/ACC_THR_20N05_TS_Mnt	ACCJ063	13		THR_TS
SK02J08	20	EGSE/ACC_THR_20N05_TS_Mnt RTN	ACCJ063	33		THR_TS
SK02J08	21	EGSE/ACC_THR_20N06_TS_Mnt	ACCJ063	53		THR_TS
SK02J08	22	EGSE/ACC_THR_20N06_TS_Mnt RTN	ACCJ063	73		THR_TS
SK02J08	23	EGSE/ACC_THR_1N02_TS_Mnt	ACCJ063	54		THR_TS
SK02J08	24	EGSE/ACC_THR_1N02_TS_Mnt RTN	ACCJ063	74		THR_TS
SK02J08	32	THR_20N04/EGSE_TS_Mnt +	THR_20N04J03	Green		THR_TS_Output
SK02J08	33	THR_20N04/EGSE_TS_Mnt -	THR_20N04J03	White		THR_TS_Output
SK02J08	34	THR_20N05/EGSE_TS_Mnt +	THR_20N05J03	Green		THR_TS_Output
SK02J08	35	THR_20N05/EGSE_TS_Mnt -	THR_20N05J03	White		THR_TS_Output
SK02J08	36	THR_20N06/EGSE_TS_Mnt +	THR_20N06J03	Green		THR_TS_Output
SK02J08	37	THR_20N06/EGSE_TS_Mnt -	THR_20N06J03	White		THR_TS_Output
SK02J08	38	THR_1N02/EGSE_TS_Mnt +	THR_1N02J03	Green		THR_TS_Output
SK02J08	39	THR_1N02/EGSE_TS_Mnt -	THR_1N02J03	White		THR_TS_Output



SK02J08	40	THR_20N01/EGSE_TS_Mnt +	THR_20N01J03	Green	THR_TS_Output
SK02J08	41	THR_20N01/EGSE_TS_Mnt -	THR_20N01J03	White	THR_TS_Output
SK02J08	42	THR_20N02/EGSE_TS_Mnt +	THR_20N02J03	Green	THR_TS_Output
SK02J08	43	THR_20N02/EGSE_TS_Mnt -	THR_20N02J03	White	THR_TS_Output
SK02J08	44	THR_20N03/EGSE_TS_Mnt +	THR_20N03J03	Green	THR_TS_Output
SK02J08	45	THR_20N03/EGSE_TS_Mnt -	THR_20N03J03	White	THR_TS_Output
SK02J08	46	THR_1N01/EGSE_TS_Mnt +	THR_1N01J03	Green	THR_TS_Output
SK02J08	47	LVA/EGSE_Open_Sts	LVAJ01	White	LVA_DR_Output
SK02J08	48	LVA/EGSE_Open_Sts	LVAJ01	Red/White	LVA_DR_Output
SK02J08	52	THR_1N01/EGSE_TS_Mnt -	THR_1N01J03	White	THR_TS_Output
SK02J08	53	LVA/EGSE_Closed_Sts	LVAJ01	Green/White	LVA_DR_Output
SK02J08	54	LVA/EGSE_Closed_Sts	LVAJ01	Red/White	LVA_DR_Output
SK02J08	BKSH	Shield_for_EGSE/ACC_LV_A_Closed_Sts	ACCJ061	BKSH	Shield
SK02J08	BKSH	Shield_for_EGSE/ACC_LV_A_Open_Sts	ACCJ061	BKSH	Shield
SK02J08	BKSH	Shield_for_EGSE/ACC_THR_1N01_TS_Mnt	ACCJ063	BKSH	Shield
SK02J08	BKSH	Shield_for_EGSE/ACC_THR_1N02_TS_Mnt	ACCJ063	BKSH	Shield
SK02J08	BKSH	Shield_for_EGSE/ACC_THR_20N01_TS_Mnt	ACCJ063	BKSH	Shield
SK02J08	BKSH	Shield_for_EGSE/ACC_THR_20N02_TS_Mnt	ACCJ063	BKSH	Shield
SK02J08	BKSH	Shield_for_EGSE/ACC_THR_20N03_TS_Mnt	ACCJ063	BKSH	Shield
SK02J08	BKSH	Shield_for_EGSE/ACC_THR_20N04_TS_Mnt	ACCJ063	BKSH	Shield
SK02J08	BKSH	Shield_for_EGSE/ACC_THR_20N05_TS_Mnt	ACCJ063	BKSH	Shield
SK02J08	BKSH	Shield_for_EGSE/ACC_THR_20N06_TS_Mnt	ACCJ063	BKSH	Shield
SK02J08	BKSH	Shield_for_LVA/EGSE_Closed_Sts	LVAJ01	BKSH	Shield
SK02J08	BKSH	Shield_for_LVA/EGSE_Open_Sts	LVAJ01	BKSH	Shield
SK02J08	BKSH	Shield_for_THR_1N01/EGSE_TS_Mnt	THR_1N01J03	BKSH	Shield
SK02J08	BKSH	Shield_for_THR_1N02/EGSE_TS_Mnt	THR_1N02J03	BKSH	Shield
SK02J08	BKSH	Shield_for_THR_20N01/EGSE_TS_Mnt	THR_20N01J03	BKSH	Shield
SK02J08	BKSH	Shield_for_THR_20N02/EGSE_TS_Mnt	THR_20N02J03	BKSH	Shield
SK02J08	BKSH	Shield_for_THR_20N03/EGSE_TS_Mnt	THR_20N03J03	BKSH	Shield
SK02J08	BKSH	Shield_for_THR_20N04/EGSE_TS_Mnt	THR_20N04J03	BKSH	Shield
SK02J08	BKSH	Shield_for_THR_20N05/EGSE_TS_Mnt	THR_20N05J03	BKSH	Shield
SK02J08	BKSH	Shield_for_THR_20N06/EGSE_TS_Mnt	THR_20N06J03	BKSH	Shield

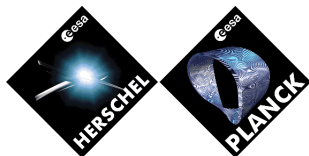


4.1.9 Satellite Level Skin Connector SK02 J09 & J10 ACC & CDMU Quick Load Interfaces

SOURCE		SignalName	DESTINATION		Interface_of_Source	Interface of Destination
Connector Reference	Pin		Connected to Unit/Connector	Pin		
SK02J09	1	ACC/EGSE_Quick_SW_Load_A_Tx_L	ACCJ021	32	EGSE_SBDL_Receiver	SBDL_Driver
SK02J09	2	ACC/EGSE_Quick_SW_Load_A_Tx_H	ACCJ021	33	EGSE_SBDL_Receiver	SBDL_Driver
SK02J09	3	EGSE/ACC_Quick_SW_Load_A_Rx_L	ACCJ021	34	EGSE_SBDL_Driver	SBDL_Receiver
SK02J09	4	EGSE/ACC_Quick_SW_Load_A_Rx_H	ACCJ021	35	EGSE_SBDL_Driver	SBDL_Receiver
SK02J09	5	ACC/EGSE_ACC_A_Test_Connector_Active GND	ACCJ021	43	Link	
SK02J09	6	ACC/EGSE_ACC_A_Test_Connector_Active	ACCJ021	44	Link	
SK02J09	8	CDMU/EGSE_Quick_SW_Load_A_Tx_L	CDMUJ021	32	EGSE_SBDL_Receiver	SBDL_Driver
SK02J09	9	CDMU/EGSE_Quick_SW_Load_A_Tx_H	CDMUJ021	33	EGSE_SBDL_Receiver	SBDL_Driver
SK02J09	10	EGSE/CDMU_Quick_SW_Load_A_Rx_L	CDMUJ021	34	EGSE_SBDL_Driver	SBDL_Receiver
SK02J09	11	EGSE/CDMU_Quick_SW_Load_A_Rx_H	CDMUJ021	35	EGSE_SBDL_Driver	SBDL_Receiver
SK02J09	12	ACC/EGSE_ACC_B_Test_Connector_Active GND	ACCJ051	43	Link	
SK02J09	13	ACC/EGSE_ACC_B_Test_Connector_Active	ACCJ051	44	Link	
SK02J09	BKSH	Shield_for_ACC/EGSE_Quick_SW_Load_A_Tx	ACCJ021	BKSH	Shield	Shield
SK02J09	BKSH	Shield_for_CDMU/EGSE_Quick_SW_Load_A_Tx	CDMUJ021	BKSH	Shield	Shield
SK02J09	BKSH	Shield_for_EGSE/ACC_Quick_SW_Load_A_Rx	ACCJ021	BKSH	Shield	Shield
SK02J09	BKSH	Shield_for_EGSE/CDMU_Quick_SW_Load_A_Rx	CDMUJ021	BKSH	Shield	Shield

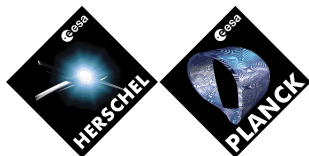


SOURCE		SignalName	DESTINATION		Interface_of_Source	Interface of Destination
Connector Reference	Pin		Connected to Unit/Connector	Pin		
SK02J10	1	ACC/EGSE_Quick_SW_Load_B_Tx_L	ACCJ051	32	EGSE_SBDL_Receiver	SBDL_Driver
SK02J10	2	ACC/EGSE_Quick_SW_Load_B_Tx_H	ACCJ051	33	EGSE_SBDL_Receiver	SBDL_Driver
SK02J10	3	EGSE/ACC_Quick_SW_Load_B_Rx_L	ACCJ051	34	EGSE_SBDL_Driver	SBDL_Receiver
SK02J10	4	EGSE/ACC_Quick_SW_Load_B_Rx_H	ACCJ051	35	EGSE_SBDL_Driver	SBDL_Receiver
SK02J10	5	CDMU/EGSE_CDMU_A_Test_Connector_Active GND	CDMUJ021	43	Link	
SK02J10	6	CDMU/EGSE_CDMU_A_Test_Connector_Active	CDMUJ021	44	Link	
SK02J10	8	CDMU/EGSE_Quick_SW_Load_B_Tx_L	CDMUJ071	32	EGSE_SBDL_Receiver	SBDL_Driver
SK02J10	9	CDMU/EGSE_Quick_SW_Load_B_Tx_H	CDMUJ071	33	EGSE_SBDL_Receiver	SBDL_Driver
SK02J10	10	EGSE/CDMU_Quick_SW_Load_B_Rx_L	CDMUJ071	34	EGSE_SBDL_Driver	SBDL_Receiver
SK02J10	11	EGSE/CDMU_Quick_SW_Load_B_Rx_H	CDMUJ071	35	EGSE_SBDL_Driver	SBDL_Receiver
SK02J10	12	CDMU/EGSE_CDMU_B_Test_Connector_Active GND	CDMUJ071	43	Link	
SK02J10	13	CDMU/EGSE_CDMU_B_Test_Connector_Active	CDMUJ071	44	Link	
SK02J10	BKSH	Shield_for_ACC/EGSE_Quick_SW_Load_B_Tx	ACCJ051	BKSH	Shield	Shield
SK02J10	BKSH	Shield_for_CDMU/EGSE_Quick_SW_Load_B_Tx	CDMUJ071	BKSH	Shield	Shield
SK02J10	BKSH	Shield_for_EGSE/ACC_Quick_SW_Load_B_Rx	ACCJ051	BKSH	Shield	Shield
SK02J10	BKSH	Shield_for_EGSE/CDMU_Quick_SW_Load_B_Rx	CDMUJ071	BKSH	Shield	Shield



4.1.10 Satellite Level Skin Connector SK02 J11 ACC - Thruster Thermocouples

SOURCE		SignalName	DESTINATION		Interface_of_Source	Interface of Destination
Connector Reference	Pin		Connected to Unit/Connector	Pin		
SK02J11	1	EGSE/ACC_LV_B_Closed_Sts	ACCJ071	4		DR_Mnt
SK02J11	2	EGSE/ACC_LV_B_Closed_Sts RTN	ACCJ071	24		DR_Mnt
SK02J11	4	EGSE/ACC_LV_B_Open_Sts	ACCJ071	5		DR_Mnt
SK02J11	5	EGSE/ACC_LV_B_Open_Sts RTN	ACCJ071	25		DR_Mnt
SK02J11	9	EGSE/ACC_THR_1N03_TS_Mnt	ACCJ073	14		THR_TS
SK02J11	10	EGSE/ACC_THR_20N07_TS_Mnt	ACCJ073	11		THR_TS
SK02J11	11	EGSE/ACC_THR_20N07_TS_Mnt RTN	ACCJ073	31		THR_TS
SK02J11	12	EGSE/ACC_THR_20N08_TS_Mnt	ACCJ073	51		THR_TS
SK02J11	13	EGSE/ACC_THR_20N08_TS_Mnt RTN	ACCJ073	71		THR_TS
SK02J11	14	EGSE/ACC_THR_20N09_TS_Mnt	ACCJ073	12		THR_TS
SK02J11	15	EGSE/ACC_THR_20N09_TS_Mnt RTN	ACCJ073	32		THR_TS
SK02J11	16	EGSE/ACC_THR_1N03_TS_Mnt RTN	ACCJ073	34		THR_TS
SK02J11	17	EGSE/ACC_THR_20N10_TS_Mnt	ACCJ073	52		THR_TS
SK02J11	18	EGSE/ACC_THR_20N10_TS_Mnt RTN	ACCJ073	72		THR_TS
SK02J11	19	EGSE/ACC_THR_20N11_TS_Mnt	ACCJ073	13		THR_TS
SK02J11	20	EGSE/ACC_THR_20N11_TS_Mnt RTN	ACCJ073	33		THR_TS
SK02J11	21	EGSE/ACC_THR_20N12_TS_Mnt	ACCJ073	53		THR_TS
SK02J11	22	EGSE/ACC_THR_20N12_TS_Mnt RTN	ACCJ073	73		THR_TS
SK02J11	23	EGSE/ACC_THR_1N04_TS_Mnt	ACCJ073	54		THR_TS
SK02J11	24	EGSE/ACC_THR_1N04_TS_Mnt RTN	ACCJ073	74		THR_TS
SK02J11	32	THR_20N10/EGSE_TS_Mnt +	THR_20N10J03	Green		THR_TS_Output
SK02J11	33	THR_20N10/EGSE_TS_Mnt -	THR_20N10J03	White		THR_TS_Output
SK02J11	34	THR_20N11/EGSE_TS_Mnt +	THR_20N11J03	Green		THR_TS_Output
SK02J11	35	THR_20N11/EGSE_TS_Mnt -	THR_20N11J03	White		THR_TS_Output
SK02J11	36	THR_20N12/EGSE_TS_Mnt +	THR_20N12J03	Green		THR_TS_Output
SK02J11	37	THR_20N12/EGSE_TS_Mnt -	THR_20N12J03	White		THR_TS_Output
SK02J11	38	THR_1N04/EGSE_TS_Mnt +	THR_1N04J03	Green		THR_TS_Output
SK02J11	39	THR_1N04/EGSE_TS_Mnt -	THR_1N04J03	White		THR_TS_Output

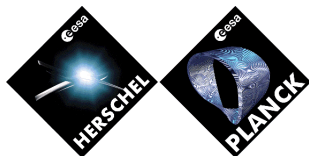


SK02J11	40	THR_20N07/EGSE_TS_Mnt +	THR_20N07J03	Green		THR_TS_Output
SK02J11	41	THR_20N07/EGSE_TS_Mnt -	THR_20N07J03	White		THR_TS_Output
SK02J11	42	THR_20N08/EGSE_TS_Mnt +	THR_20N08J03	Green		THR_TS_Output
SK02J11	43	THR_20N08/EGSE_TS_Mnt -	THR_20N08J03	White		THR_TS_Output
SK02J11	44	THR_20N09/EGSE_TS_Mnt +	THR_20N09J03	Green		THR_TS_Output
SK02J11	45	THR_20N09/EGSE_TS_Mnt -	THR_20N09J03	White		THR_TS_Output
SK02J11	46	THR_1N03/EGSE_TS_Mnt +	THR_1N03J03	Green		THR_TS_Output
SK02J11	47	LVB/EGSE_Open_Sts	LVBj01	White		LVA_DR_Output
SK02J11	48	LVB/EGSE_Open_Sts	LVBj01	Red/White		LVA_DR_Output
SK02J11	52	THR_1N03/EGSE_TS_Mnt -	THR_1N03J03	White		THR_TS_Output
SK02J11	53	LVB/EGSE_Closed_Sts	LVBj01	Green/White		LVA_DR_Output
SK02J11	54	LVB/EGSE_Closed_Sts	LVBj01	Red/White		LVA_DR_Output
SK02J11	BKSH	Shield_for_EGSE/ACC_LV_B_Closed_Sts	ACCJ071	BKSH		Shield
SK02J11	BKSH	Shield_for_EGSE/ACC_LV_B_Open_Sts	ACCJ071	BKSH		Shield
SK02J11	BKSH	Shield_for_EGSE/ACC_THR_1N03_TS_Mnt	ACCJ073	BKSH		Shield
SK02J11	BKSH	Shield_for_EGSE/ACC_THR_1N04_TS_Mnt	ACCJ073	BKSH		Shield
SK02J11	BKSH	Shield_for_EGSE/ACC_THR_20N07_TS_Mnt	ACCJ073	BKSH		Shield
SK02J11	BKSH	Shield_for_EGSE/ACC_THR_20N08_TS_Mnt	ACCJ073	BKSH		Shield
SK02J11	BKSH	Shield_for_EGSE/ACC_THR_20N09_TS_Mnt	ACCJ073	BKSH		Shield
SK02J11	BKSH	Shield_for_EGSE/ACC_THR_20N10_TS_Mnt	ACCJ073	BKSH		Shield
SK02J11	BKSH	Shield_for_EGSE/ACC_THR_20N11_TS_Mnt	ACCJ073	BKSH		Shield
SK02J11	BKSH	Shield_for_EGSE/ACC_THR_20N12_TS_Mnt	ACCJ073	BKSH		Shield
SK02J11	BKSH	Shield_for_LVB/EGSE_Closed_Sts	LVBj01	BKSH		Shield
SK02J11	BKSH	Shield_for_LVB/EGSE_Open_Sts	LVBj01	BKSH		Shield
SK02J11	BKSH	Shield_for_THR_1N03/EGSE_TS_Mnt	THR_1N03J03	BKSH		Shield
SK02J11	BKSH	Shield_for_THR_1N04/EGSE_TS_Mnt	THR_1N04J03	BKSH		Shield
SK02J11	BKSH	Shield_for_THR_20N07/EGSE_TS_Mnt	THR_20N07J03	BKSH		Shield
SK02J11	BKSH	Shield_for_THR_20N08/EGSE_TS_Mnt	THR_20N08J03	BKSH		Shield
SK02J11	BKSH	Shield_for_THR_20N09/EGSE_TS_Mnt	THR_20N09J03	BKSH		Shield
SK02J11	BKSH	Shield_for_THR_20N10/EGSE_TS_Mnt	THR_20N10J03	BKSH		Shield
SK02J11	BKSH	Shield_for_THR_20N11/EGSE_TS_Mnt	THR_20N11J03	BKSH		Shield
SK02J11	BKSH	Shield_for_THR_20N12/EGSE_TS_Mnt	THR_20N12J03	BKSH		Shield



4.1.11 Satellite Level Skin Connector SK02 J12 ACC + PCDU - Thruster Heaters

SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
SK02J12	1	ACC/EGSE_THR_20N01_Htr_Nom_Cmd	ACCJ103	1		THRDV_Htr-Cmd
SK02J12	2	ACC/EGSE_THR_20N01_Htr_Nom_Cmd RTN	ACCJ103	21		THRDV_Htr-Cmd
SK02J12	4	ACC/EGSE_THR_20N02_Htr_Nom_Cmd	ACCJ103	41		THRDV_Htr-Cmd
SK02J12	5	ACC/EGSE_THR_20N02_Htr_Nom_Cmd RTN	ACCJ103	61		THRDV_Htr-Cmd
SK02J12	6	ACC/EGSE_THR_20N03_Htr_Nom_Cmd	ACCJ103	2		THRDV_Htr-Cmd
SK02J12	7	ACC/EGSE_THR_20N03_Htr_Nom_Cmd RTN	ACCJ103	22		THRDV_Htr-Cmd
SK02J12	8	ACC/EGSE_THR_20N04_Htr_Nom_Cmd	ACCJ103	42		THRDV_Htr-Cmd
SK02J12	9	ACC/EGSE_THR_20N04_Htr_Nom_Cmd RTN	ACCJ103	62		THRDV_Htr-Cmd
SK02J12	10	ACC/EGSE_THR_20N05_Htr_Nom_Cmd	ACCJ103	3		THRDV_Htr-Cmd
SK02J12	11	ACC/EGSE_THR_20N05_Htr_Nom_Cmd RTN	ACCJ103	23		THRDV_Htr-Cmd
SK02J12	12	ACC/EGSE_THR_20N06_Htr_Nom_Cmd	ACCJ103	43		THRDV_Htr-Cmd
SK02J12	13	ACC/EGSE_THR_20N06_Htr_Nom_Cmd RTN	ACCJ103	63		THRDV_Htr-Cmd
SK02J12	14	ACC/EGSE_THR_20N07_Htr_Nom_Cmd	ACCJ103	4		THRDV_Htr-Cmd
SK02J12	15	ACC/EGSE_THR_20N07_Htr_Nom_Cmd RTN	ACCJ103	24		THRDV_Htr-Cmd
SK02J12	16	ACC/EGSE_THR_20N11_Htr_Nom_Cmd RTN	ACCJ103	26		THRDV_Htr-Cmd
SK02J12	17	ACC/EGSE_THR_20N08_Htr_Nom_Cmd	ACCJ103	44		THRDV_Htr-Cmd
SK02J12	18	ACC/EGSE_THR_20N08_Htr_Nom_Cmd RTN	ACCJ103	64		THRDV_Htr-Cmd
SK02J12	19	ACC/EGSE_THR_20N09_Htr_Nom_Cmd	ACCJ103	5		THRDV_Htr-Cmd
SK02J12	20	ACC/EGSE_THR_20N09_Htr_Nom_Cmd RTN	ACCJ103	25		THRDV_Htr-Cmd
SK02J12	21	ACC/EGSE_THR_20N10_Htr_Nom_Cmd	ACCJ103	45		THRDV_Htr-Cmd
SK02J12	22	ACC/EGSE_THR_20N10_Htr_Nom_Cmd RTN	ACCJ103	65		THRDV_Htr-Cmd
SK02J12	23	ACC/EGSE_THR_20N11_Htr_Nom_Cmd	ACCJ103	6		THRDV_Htr-Cmd
SK02J12	25	ACC/EGSE_THR_20N12_Htr_Nom_Cmd	ACCJ103	46		THRDV_Htr-Cmd
SK02J12	26	ACC/EGSE_THR_20N12_Htr_Nom_Cmd RTN	ACCJ103	66		THRDV_Htr-Cmd
SK02J12	27	ACC/EGSE_THR_1N01/02_Htr-1_Cmd	ACCJ103	7		THR1N_Htr-Cmd
SK02J12	28	ACC/EGSE_THR_1N01/02_Htr-1_Cmd RTN	ACCJ103	27		THR1N_Htr-Cmd
SK02J12	29	ACC/EGSE_THR_1N01/02_Htr-2_Cmd	ACCJ103	47		THR1N_Htr-Cmd
SK02J12	30	ACC/EGSE_THR_1N01/02_Htr-2_Cmd RTN	ACCJ103	67		THR1N_Htr-Cmd
SK02J12	32	PCDU/EGSE_THR_20N01-12_Htr_Nom_Pwr	PCDUJ36	18		PCDU_LCL
SK02J12	33	PCDU/EGSE_THR_20N01-12_Htr_Nom_Pwr	PCDUJ36	36		PCDU_LCL
SK02J12	34	EGSE/THR_20N12_Htr_Nom_Cmd	THR_20N12J02	Black		THR_20N_Heater



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SK02J12	35	EGSE/THR_20N12_Htr_Nom_Cmd	THR_20N12J02	Black	THR_20N_Heater
SK02J12	36	EGSE/THR_1N01/02_Htr-1_Cmd +	THR_1N01J02	Black	THR_1N_Heater
SK02J12	37	EGSE/THR_1N01/02_Htr-1_Cmd -	THR_1N02J02	Blue	THR_1N_Heater
SK02J12	38	EGSE/THR_1N01/02_Htr-2_Cmd +	THR_1N01J02	Red	THR_1N_Heater
SK02J12	39	EGSE/THR_1N01/02_Htr-2_Cmd -	THR_1N02J02	Green	THR_1N_Heater
SK02J12	41	EGSE/THR_20N01-12_Htr_Nom_Pwr	THR_20N01J02	Black	THR_20N_Heater
SK02J12	41	EGSE/THR_20N01-12_Htr_Nom_Pwr	THR_20N02J02	Black	THR_20N_Heater
SK02J12	41	EGSE/THR_20N01-12_Htr_Nom_Pwr	THR_20N03J02	Black	THR_20N_Heater
SK02J12	41	EGSE/THR_20N01-12_Htr_Nom_Pwr	THR_20N04J02	Black	THR_20N_Heater
SK02J12	41	EGSE/THR_20N01-12_Htr_Nom_Pwr	THR_20N05J02	Black	THR_20N_Heater
SK02J12	41	EGSE/THR_20N01-12_Htr_Nom_Pwr	THR_20N06J02	Black	THR_20N_Heater
SK02J12	41	EGSE/THR_20N01-12_Htr_Nom_Pwr	THR_20N11J02	Black	THR_20N_Heater
SK02J12	41	EGSE/THR_20N01-12_Htr_Nom_Pwr	THR_20N12J02	Black	THR_20N_Heater
SK02J12	42	EGSE/THR_20N01-12_Htr_Nom_Pwr	THR_20N05J02	Black	THR_20N_Heater
SK02J12	42	EGSE/THR_20N01-12_Htr_Nom_Pwr	THR_20N06J02	Black	THR_20N_Heater
SK02J12	42	EGSE/THR_20N01-12_Htr_Nom_Pwr	THR_20N07J02	Black	THR_20N_Heater
SK02J12	42	EGSE/THR_20N01-12_Htr_Nom_Pwr	THR_20N08J02	Black	THR_20N_Heater
SK02J12	42	EGSE/THR_20N01-12_Htr_Nom_Pwr	THR_20N09J02	Black	THR_20N_Heater
SK02J12	42	EGSE/THR_20N01-12_Htr_Nom_Pwr	THR_20N10J02	Black	THR_20N_Heater
SK02J12	42	EGSE/THR_20N01-12_Htr_Nom_Pwr	THR_20N11J02	Black	THR_20N_Heater
SK02J12	42	EGSE/THR_20N01-12_Htr_Nom_Pwr	THR_20N12J02	Black	THR_20N_Heater
SK02J12	43	EGSE/THR_20N08_Htr_Nom_Cmd	THR_20N08J02	Black	THR_20N_Heater
SK02J12	44	EGSE/THR_20N08_Htr_Nom_Cmd	THR_20N08J02	Black	THR_20N_Heater
SK02J12	45	EGSE/THR_20N09_Htr_Nom_Cmd	THR_20N09J02	Black	THR_20N_Heater
SK02J12	46	EGSE/THR_20N09_Htr_Nom_Cmd	THR_20N09J02	Black	THR_20N_Heater
SK02J12	47	EGSE/THR_20N10_Htr_Nom_Cmd	THR_20N10J02	Black	THR_20N_Heater
SK02J12	48	EGSE/THR_20N10_Htr_Nom_Cmd	THR_20N10J02	Black	THR_20N_Heater
SK02J12	49	EGSE/THR_20N11_Htr_Nom_Cmd	THR_20N11J02	Black	THR_20N_Heater
SK02J12	51	EGSE/THR_20N05_Htr_Nom_Cmd	THR_20N05J02	Black	THR_20N_Heater
SK02J12	52	EGSE/THR_20N05_Htr_Nom_Cmd	THR_20N05J02	Black	THR_20N_Heater
SK02J12	53	EGSE/THR_20N06_Htr_Nom_Cmd	THR_20N06J02	Black	THR_20N_Heater
SK02J12	54	EGSE/THR_20N06_Htr_Nom_Cmd	THR_20N06J02	Black	THR_20N_Heater
SK02J12	55	EGSE/THR_20N07_Htr_Nom_Cmd	THR_20N07J02	Black	THR_20N_Heater
SK02J12	56	EGSE/THR_20N07_Htr_Nom_Cmd	THR_20N07J02	Black	THR_20N_Heater
SK02J12	57	EGSE/THR_20N11_Htr_Nom_Cmd	THR_20N11J02	Black	THR_20N_Heater
SK02J12	58	EGSE/THR_20N02_Htr_Nom_Cmd	THR_20N02J02	Black	THR_20N_Heater
SK02J12	59	EGSE/THR_20N02_Htr_Nom_Cmd	THR_20N02J02	Black	THR_20N_Heater



SK02J12	60	EGSE/THR_20N03_Htr_Nom_Cmd	THR_20N03J02	Black	THR_20N_Heater
SK02J12	61	EGSE/THR_20N03_Htr_Nom_Cmd	THR_20N03J02	Black	THR_20N_Heater
SK02J12	62	EGSE/THR_20N04_Htr_Nom_Cmd	THR_20N04J02	Black	THR_20N_Heater
SK02J12	63	EGSE/THR_20N04_Htr_Nom_Cmd	THR_20N04J02	Black	THR_20N_Heater
SK02J12	64	EGSE/THR_20N01_Htr_Nom_Cmd	THR_20N01J02	Black	THR_20N_Heater
SK02J12	65	EGSE/THR_20N01_Htr_Nom_Cmd	THR_20N01J02	Black	THR_20N_Heater
SK02J12	BKSH	Shield_for_ACC/EGSE_THR_1N01/02_Htr-1_Cmd	ACCJ103	BKSH	Shield
SK02J12	BKSH	Shield_for_ACC/EGSE_THR_1N01/02_Htr-2_Cmd	ACCJ103	BKSH	Shield
SK02J12	BKSH	Shield_for_ACC/EGSE_THR_20N01_Htr_Nom_Cmd	ACCJ103	BKSH	Shield
SK02J12	BKSH	Shield_for_ACC/EGSE_THR_20N02_Htr_Nom_Cmd	ACCJ103	BKSH	Shield
SK02J12	BKSH	Shield_for_ACC/EGSE_THR_20N03_Htr_Nom_Cmd	ACCJ103	BKSH	Shield
SK02J12	BKSH	Shield_for_ACC/EGSE_THR_20N04_Htr_Nom_Cmd	ACCJ103	BKSH	Shield
SK02J12	BKSH	Shield_for_ACC/EGSE_THR_20N05_Htr_Nom_Cmd	ACCJ103	BKSH	Shield
SK02J12	BKSH	Shield_for_ACC/EGSE_THR_20N06_Htr_Nom_Cmd	ACCJ103	BKSH	Shield
SK02J12	BKSH	Shield_for_ACC/EGSE_THR_20N07_Htr_Nom_Cmd	ACCJ103	BKSH	Shield
SK02J12	BKSH	Shield_for_ACC/EGSE_THR_20N08_Htr_Nom_Cmd	ACCJ103	BKSH	Shield
SK02J12	BKSH	Shield_for_ACC/EGSE_THR_20N09_Htr_Nom_Cmd	ACCJ103	BKSH	Shield
SK02J12	BKSH	Shield_for_ACC/EGSE_THR_20N10_Htr_Nom_Cmd	ACCJ103	BKSH	Shield
SK02J12	BKSH	Shield_for_ACC/EGSE_THR_20N11_Htr_Nom_Cmd	ACCJ103	BKSH	Shield
SK02J12	BKSH	Shield_for_ACC/EGSE_THR_20N12_Htr_Nom_Cmd	ACCJ103	BKSH	Shield
SK02J12	BKSH	Shield_for_EGSE/THR_1N01/02_Htr-1_Cmd	THR_1N01J02	BKSH	Shield
SK02J12	BKSH	Shield_for_EGSE/THR_1N01/02_Htr-1_Cmd	THR_1N02J02	BKSH	Shield
SK02J12	BKSH	Shield_for_EGSE/THR_1N01/02_Htr-2_Cmd	THR_1N01J02	BKSH	Shield
SK02J12	BKSH	Shield_for_EGSE/THR_1N01/02_Htr-2_Cmd	THR_1N02J02	BKSH	Shield
SK02J12	BKSH	Shield_for_EGSE/THR_20N01_Htr_Nom_Cmd	THR_20N01J02	BKSH	Shield
SK02J12	BKSH	Shield_for_EGSE/THR_20N02_Htr_Nom_Cmd	THR_20N02J02	BKSH	Shield
SK02J12	BKSH	Shield_for_EGSE/THR_20N03_Htr_Nom_Cmd	THR_20N03J02	BKSH	Shield
SK02J12	BKSH	Shield_for_EGSE/THR_20N04_Htr_Nom_Cmd	THR_20N04J02	BKSH	Shield
SK02J12	BKSH	Shield_for_EGSE/THR_20N05_Htr_Nom_Cmd	THR_20N05J02	BKSH	Shield
SK02J12	BKSH	Shield_for_EGSE/THR_20N06_Htr_Nom_Cmd	THR_20N06J02	BKSH	Shield
SK02J12	BKSH	Shield_for_EGSE/THR_20N07_Htr_Nom_Cmd	THR_20N07J02	BKSH	Shield
SK02J12	BKSH	Shield_for_EGSE/THR_20N08_Htr_Nom_Cmd	THR_20N08J02	BKSH	Shield
SK02J12	BKSH	Shield_for_EGSE/THR_20N09_Htr_Nom_Cmd	THR_20N09J02	BKSH	Shield
SK02J12	BKSH	Shield_for_EGSE/THR_20N10_Htr_Nom_Cmd	THR_20N10J02	BKSH	Shield
SK02J12	BKSH	Shield_for_EGSE/THR_20N11_Htr_Nom_Cmd	THR_20N11J02	BKSH	Shield
SK02J12	BKSH	Shield_for_EGSE/THR_20N12_Htr_Nom_Cmd	THR_20N12J02	BKSH	Shield

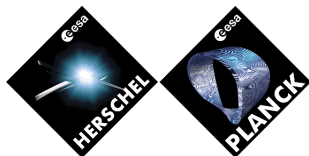


4.1.12 Satellite Level Skin Connector SK02 J13 ACC + PCDU - Thruster Heaters

SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
SK02J13	1	ACC/EGSE_THR_20N01_Htr_Red_Cmd	ACCJ113	1		THRDV_Htr-Cmd
SK02J13	2	ACC/EGSE_THR_20N01_Htr_Red_Cmd RTN	ACCJ113	21		THRDV_Htr-Cmd
SK02J13	4	ACC/EGSE_THR_20N02_Htr_Red_Cmd	ACCJ113	41		THRDV_Htr-Cmd
SK02J13	5	ACC/EGSE_THR_20N02_Htr_Red_Cmd RTN	ACCJ113	61		THRDV_Htr-Cmd
SK02J13	6	ACC/EGSE_THR_20N03_Htr_Red_Cmd	ACCJ113	2		THRDV_Htr-Cmd
SK02J13	7	ACC/EGSE_THR_20N03_Htr_Red_Cmd RTN	ACCJ113	22		THRDV_Htr-Cmd
SK02J13	8	ACC/EGSE_THR_20N04_Htr_Red_Cmd	ACCJ113	42		THRDV_Htr-Cmd
SK02J13	9	ACC/EGSE_THR_20N04_Htr_Red_Cmd RTN	ACCJ113	62		THRDV_Htr-Cmd
SK02J13	10	ACC/EGSE_THR_20N05_Htr_Red_Cmd	ACCJ113	3		THRDV_Htr-Cmd
SK02J13	11	ACC/EGSE_THR_20N05_Htr_Red_Cmd RTN	ACCJ113	23		THRDV_Htr-Cmd
SK02J13	12	ACC/EGSE_THR_20N06_Htr_Red_Cmd	ACCJ113	43		THRDV_Htr-Cmd
SK02J13	13	ACC/EGSE_THR_20N06_Htr_Red_Cmd RTN	ACCJ113	63		THRDV_Htr-Cmd
SK02J13	14	ACC/EGSE_THR_20N07_Htr_Red_Cmd	ACCJ113	4		THRDV_Htr-Cmd
SK02J13	15	ACC/EGSE_THR_20N07_Htr_Red_Cmd RTN	ACCJ113	24		THRDV_Htr-Cmd
SK02J13	16	ACC/EGSE_THR_20N11_Htr_Red_Cmd RTN	ACCJ113	26		THRDV_Htr-Cmd
SK02J13	17	ACC/EGSE_THR_20N08_Htr_Red_Cmd	ACCJ113	44		THRDV_Htr-Cmd
SK02J13	18	ACC/EGSE_THR_20N08_Htr_Red_Cmd RTN	ACCJ113	64		THRDV_Htr-Cmd
SK02J13	19	ACC/EGSE_THR_20N09_Htr_Red_Cmd	ACCJ113	5		THRDV_Htr-Cmd
SK02J13	20	ACC/EGSE_THR_20N09_Htr_Red_Cmd RTN	ACCJ113	25		THRDV_Htr-Cmd
SK02J13	21	ACC/EGSE_THR_20N10_Htr_Red_Cmd	ACCJ113	45		THRDV_Htr-Cmd
SK02J13	22	ACC/EGSE_THR_20N10_Htr_Red_Cmd RTN	ACCJ113	65		THRDV_Htr-Cmd
SK02J13	23	ACC/EGSE_THR_20N11_Htr_Red_Cmd	ACCJ113	6		THRDV_Htr-Cmd
SK02J13	25	ACC/EGSE_THR_20N12_Htr_Red_Cmd	ACCJ113	46		THRDV_Htr-Cmd
SK02J13	26	ACC/EGSE_THR_20N12_Htr_Red_Cmd RTN	ACCJ113	66		THRDV_Htr-Cmd
SK02J13	27	ACC/EGSE_THR_1N03/04_Htr-1_Cmd	ACCJ113	7		THR1N_Htr-Cmd
SK02J13	28	ACC/EGSE_THR_1N03/04_Htr-1_Cmd RTN	ACCJ113	27		THR1N_Htr-Cmd
SK02J13	29	ACC/EGSE_THR_1N03/04_Htr-2_Cmd	ACCJ113	47		THR1N_Htr-Cmd
SK02J13	30	ACC/EGSE_THR_1N03/04_Htr-2_Cmd RTN	ACCJ113	67		THR1N_Htr-Cmd
SK02J13	32	PCDU/EGSE_THR_20N01-12_Htr_Red_Pwr	PCDUJ02	18		PCDU_LCL
SK02J13	33	PCDU/EGSE_THR_20N01-12_Htr_Red_Pwr	PCDUJ02	36		PCDU_LCL
SK02J13	34	EGSE/THR_20N12_Htr_Red_Cmd	THR_20N12J02	Black		THR_20N_Heater



SK02J13	35	EGSE/THR_20N12_Htr_Red_Cmd	THR_20N12J02	Black	THR_20N_Heater
SK02J13	36	EGSE/THR_1N03/04_Htr-1_Cmd +	THR_1N03J02	Black	THR_1N_Heater
SK02J13	37	EGSE/THR_1N03/04_Htr-1_Cmd -	THR_1N04J02	Blue	THR_1N_Heater
SK02J13	38	EGSE/THR_1N03/04_Htr-2_Cmd +	THR_1N03J02	Red	THR_1N_Heater
SK02J13	39	EGSE/THR_1N03/04_Htr-2_Cmd -	THR_1N04J02	Green	THR_1N_Heater
SK02J13	41	EGSE/THR_20N01-12_Htr_Red_Pwr	THR_20N01J02	Black	THR_20N_Heater
SK02J13	41	EGSE/THR_20N01-12_Htr_Red_Pwr	THR_20N02J02	Black	THR_20N_Heater
SK02J13	41	EGSE/THR_20N01-12_Htr_Red_Pwr	THR_20N03J02	Black	THR_20N_Heater
SK02J13	41	EGSE/THR_20N01-12_Htr_Red_Pwr	THR_20N04J02	Black	THR_20N_Heater
SK02J13	41	EGSE/THR_20N01-12_Htr_Red_Pwr	THR_20N05J02	Black	THR_20N_Heater
SK02J13	41	EGSE/THR_20N01-12_Htr_Red_Pwr	THR_20N06J02	Black	THR_20N_Heater
SK02J13	41	EGSE/THR_20N01-12_Htr_Red_Pwr	THR_20N11J02	Black	THR_20N_Heater
SK02J13	41	EGSE/THR_20N01-12_Htr_Red_Pwr	THR_20N12J02	Black	THR_20N_Heater
SK02J13	42	EGSE/THR_20N01-12_Htr_Red_Pwr	THR_20N05J02	Black	THR_20N_Heater
SK02J13	42	EGSE/THR_20N01-12_Htr_Red_Pwr	THR_20N06J02	Black	THR_20N_Heater
SK02J13	42	EGSE/THR_20N01-12_Htr_Red_Pwr	THR_20N07J02	Black	THR_20N_Heater
SK02J13	42	EGSE/THR_20N01-12_Htr_Red_Pwr	THR_20N08J02	Black	THR_20N_Heater
SK02J13	42	EGSE/THR_20N01-12_Htr_Red_Pwr	THR_20N09J02	Black	THR_20N_Heater
SK02J13	42	EGSE/THR_20N01-12_Htr_Red_Pwr	THR_20N10J02	Black	THR_20N_Heater
SK02J13	42	EGSE/THR_20N01-12_Htr_Red_Pwr	THR_20N11J02	Black	THR_20N_Heater
SK02J13	42	EGSE/THR_20N01-12_Htr_Red_Pwr	THR_20N12J02	Black	THR_20N_Heater
SK02J13	43	EGSE/THR_20N08_Htr_Red_Cmd	THR_20N08J02	Black	THR_20N_Heater
SK02J13	44	EGSE/THR_20N08_Htr_Red_Cmd	THR_20N08J02	Black	THR_20N_Heater
SK02J13	45	EGSE/THR_20N09_Htr_Red_Cmd	THR_20N09J02	Black	THR_20N_Heater
SK02J13	46	EGSE/THR_20N09_Htr_Red_Cmd	THR_20N09J02	Black	THR_20N_Heater
SK02J13	47	EGSE/THR_20N10_Htr_Red_Cmd	THR_20N10J02	Black	THR_20N_Heater
SK02J13	48	EGSE/THR_20N10_Htr_Red_Cmd	THR_20N10J02	Black	THR_20N_Heater
SK02J13	49	EGSE/THR_20N11_Htr_Red_Cmd	THR_20N11J02	Black	THR_20N_Heater
SK02J13	51	EGSE/THR_20N05_Htr_Red_Cmd	THR_20N05J02	Black	THR_20N_Heater
SK02J13	52	EGSE/THR_20N05_Htr_Red_Cmd	THR_20N05J02	Black	THR_20N_Heater
SK02J13	53	EGSE/THR_20N06_Htr_Red_Cmd	THR_20N06J02	Black	THR_20N_Heater
SK02J13	54	EGSE/THR_20N06_Htr_Red_Cmd	THR_20N06J02	Black	THR_20N_Heater
SK02J13	55	EGSE/THR_20N07_Htr_Red_Cmd	THR_20N07J02	Black	THR_20N_Heater
SK02J13	56	EGSE/THR_20N07_Htr_Red_Cmd	THR_20N07J02	Black	THR_20N_Heater
SK02J13	57	EGSE/THR_20N11_Htr_Red_Cmd	THR_20N11J02	Black	THR_20N_Heater
SK02J13	58	EGSE/THR_20N02_Htr_Red_Cmd	THR_20N02J02	Black	THR_20N_Heater
SK02J13	59	EGSE/THR_20N02_Htr_Red_Cmd	THR_20N02J02	Black	THR_20N_Heater



SK02J13	60	EGSE/THR_20N03_Htr_Red_Cmd	THR_20N03J02	Black	THR_20N_Heater
SK02J13	61	EGSE/THR_20N03_Htr_Red_Cmd	THR_20N03J02	Black	THR_20N_Heater
SK02J13	62	EGSE/THR_20N04_Htr_Red_Cmd	THR_20N04J02	Black	THR_20N_Heater
SK02J13	63	EGSE/THR_20N04_Htr_Red_Cmd	THR_20N04J02	Black	THR_20N_Heater
SK02J13	64	EGSE/THR_20N01_Htr_Red_Cmd	THR_20N01J02	Black	THR_20N_Heater
SK02J13	65	EGSE/THR_20N01_Htr_Red_Cmd	THR_20N01J02	Black	THR_20N_Heater
SK02J13	BKSH	Shield_for_ACC/EGSE_THR_1N03/04_Htr-1_Cmd	ACCJ113	BKSH	Shield
SK02J13	BKSH	Shield_for_ACC/EGSE_THR_1N03/04_Htr-2_Cmd	ACCJ113	BKSH	Shield
SK02J13	BKSH	Shield_for_ACC/EGSE_THR_20N01_Htr_Red_Cmd	ACCJ113	BKSH	Shield
SK02J13	BKSH	Shield_for_ACC/EGSE_THR_20N02_Htr_Red_Cmd	ACCJ113	BKSH	Shield
SK02J13	BKSH	Shield_for_ACC/EGSE_THR_20N03_Htr_Red_Cmd	ACCJ113	BKSH	Shield
SK02J13	BKSH	Shield_for_ACC/EGSE_THR_20N04_Htr_Red_Cmd	ACCJ113	BKSH	Shield
SK02J13	BKSH	Shield_for_ACC/EGSE_THR_20N05_Htr_Red_Cmd	ACCJ113	BKSH	Shield
SK02J13	BKSH	Shield_for_ACC/EGSE_THR_20N06_Htr_Red_Cmd	ACCJ113	BKSH	Shield
SK02J13	BKSH	Shield_for_ACC/EGSE_THR_20N07_Htr_Red_Cmd	ACCJ113	BKSH	Shield
SK02J13	BKSH	Shield_for_ACC/EGSE_THR_20N08_Htr_Red_Cmd	ACCJ113	BKSH	Shield
SK02J13	BKSH	Shield_for_ACC/EGSE_THR_20N09_Htr_Red_Cmd	ACCJ113	BKSH	Shield
SK02J13	BKSH	Shield_for_ACC/EGSE_THR_20N10_Htr_Red_Cmd	ACCJ113	BKSH	Shield
SK02J13	BKSH	Shield_for_ACC/EGSE_THR_20N11_Htr_Red_Cmd	ACCJ113	BKSH	Shield
SK02J13	BKSH	Shield_for_ACC/EGSE_THR_20N12_Htr_Red_Cmd	ACCJ113	BKSH	Shield
SK02J13	BKSH	Shield_for_EGSE/THR_1N03/04_Htr-1_Cmd	THR_1N03J02	BKSH	Shield
SK02J13	BKSH	Shield_for_EGSE/THR_1N03/04_Htr-1_Cmd	THR_1N04J02	BKSH	Shield
SK02J13	BKSH	Shield_for_EGSE/THR_1N03/04_Htr-2_Cmd	THR_1N03J02	BKSH	Shield
SK02J13	BKSH	Shield_for_EGSE/THR_1N03/04_Htr-2_Cmd	THR_1N04J02	BKSH	Shield
SK02J13	BKSH	Shield_for_EGSE/THR_20N01_Htr_Red_Cmd	THR_20N01J02	BKSH	Shield
SK02J13	BKSH	Shield_for_EGSE/THR_20N02_Htr_Red_Cmd	THR_20N02J02	BKSH	Shield
SK02J13	BKSH	Shield_for_EGSE/THR_20N03_Htr_Red_Cmd	THR_20N03J02	BKSH	Shield
SK02J13	BKSH	Shield_for_EGSE/THR_20N04_Htr_Red_Cmd	THR_20N04J02	BKSH	Shield
SK02J13	BKSH	Shield_for_EGSE/THR_20N05_Htr_Red_Cmd	THR_20N05J02	BKSH	Shield
SK02J13	BKSH	Shield_for_EGSE/THR_20N06_Htr_Red_Cmd	THR_20N06J02	BKSH	Shield
SK02J13	BKSH	Shield_for_EGSE/THR_20N07_Htr_Red_Cmd	THR_20N07J02	BKSH	Shield
SK02J13	BKSH	Shield_for_EGSE/THR_20N08_Htr_Red_Cmd	THR_20N08J02	BKSH	Shield
SK02J13	BKSH	Shield_for_EGSE/THR_20N09_Htr_Red_Cmd	THR_20N09J02	BKSH	Shield
SK02J13	BKSH	Shield_for_EGSE/THR_20N10_Htr_Red_Cmd	THR_20N10J02	BKSH	Shield
SK02J13	BKSH	Shield_for_EGSE/THR_20N11_Htr_Red_Cmd	THR_20N11J02	BKSH	Shield
SK02J13	BKSH	Shield_for_EGSE/THR_20N12_Htr_Red_Cmd	THR_20N12J02	BKSH	Shield



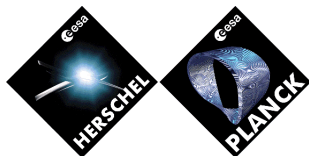
4.1.13 Satellite Level Skin Connector SK02 J14 ACC – STR1

SOURCE			DESTINATION		Interface_of_Source	Interface of Destination
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin		
SK02J14	1	EGSE/ACC_STR1_ON/OFF_Sts	ACCJ061	1		DR_Mnt
SK02J14	2	EGSE/ACC_STR1_ON/OFF_Sts RTN	ACCJ061	21		DR_Mnt
SK02J14	3	STR1/EGSE_ON/OFF_Sts	STR1J01	7		DR_Mnt
SK02J14	4	STR1/EGSE_ON/OFF_Sts RTN	STR1J01	8		DR_Mnt
SK02J14	5	ACC/EGSE_STR1_ON_Nom_Cmd	ACCJ035	6		HP_Cmd
SK02J14	6	ACC/EGSE_STR1_OFF_Nom_Cmd	ACCJ035	7		HP_Cmd
SK02J14	7	EGSE/STR1_ON_Nom_Cmd	STR1J01	5		STR_HP_Input
SK02J14	8	EGSE/STR1_OFF_Nom_Cmd	STR1J01	14		STR_HP_Input
SK02J14	11	ACC/EGSE_STR1_ON_Red_Cmd	ACCJ045	6		HP_Cmd
SK02J14	12	ACC/EGSE_STR1_OFF_Red_Cmd	ACCJ045	7		HP_Cmd
SK02J14	13	EGSE/STR1_ON_Red_Cmd	STR1J01	5		STR_HP_Input
SK02J14	14	EGSE/STR1_OFF_Red_Cmd	STR1J01	14		STR_HP_Input
SK02J14	17	ACC/EGSE_STR1_ON/OFF_RTN_Nom_Cmd RTN	ACCJ035	5		HP_Cmd
SK02J14	18	EGSE/STR1_ON/OFF_RTN_Nom_Cmd RTN	STR1J01	6		STR_HP_Input
SK02J14	20	ACC/EGSE_STR1_ON/OFF_RTN_Red_Cmd RTN	ACCJ045	5		HP_Cmd
SK02J14	21	EGSE/STR1_ON/OFF_RTN_Red_Cmd RTN	STR1J01	15		STR_HP_Input
SK02J14	BKSH	Shield_for_EGSE/ACC_STR1_ON/OFF_Sts	ACCJ061	BKSH		Shield
SK02J14	BKSH	Shield_for_STR1/EGSE_ON/OFF_Sts	STR1J01	BKSH		Shield



4.1.14 Satellite Level Skin Connector SK02 J15 ACC – STR2

SOURCE		SignalName	DESTINATION		Interface_of_Source	Interface of Destination
Connector Reference	Pin		Connected to Unit/Connector	Pin		
SK02J15	1	EGSE/ACC_STR2_ON/OFF_Sts	ACCJ071	41		DR_Mnt
SK02J15	2	EGSE/ACC_STR2_ON/OFF_Sts RTN	ACCJ071	61		DR_Mnt
SK02J15	3	STR2/EGSE_ON/OFF_Sts	STR2J01	7		DR_Mnt
SK02J15	4	STR2/EGSE_ON/OFF_Sts RTN	STR2J01	8		DR_Mnt
SK02J15	5	ACC/EGSE_STR2_ON_Nom_Cmd	ACCJ035	12		HP_Cmd
SK02J15	6	ACC/EGSE_STR2_OFF_Nom_Cmd	ACCJ035	13		HP_Cmd
SK02J15	7	EGSE/STR2_ON_Nom_Cmd	STR2J01	5		STR_HP_Input
SK02J15	8	EGSE/STR2_OFF_Nom_Cmd	STR2J01	14		STR_HP_Input
SK02J15	11	ACC/EGSE_STR2_ON_Red_Cmd	ACCJ045	12		HP_Cmd
SK02J15	12	ACC/EGSE_STR2_OFF_Red_Cmd	ACCJ045	13		HP_Cmd
SK02J15	13	EGSE/STR2_ON_Red_Cmd	STR2J01	5		STR_HP_Input
SK02J15	14	EGSE/STR2_OFF_Red_Cmd	STR2J01	14		STR_HP_Input
SK02J15	17	ACC/EGSE_STR2_ON/OFF_RTN_Nom_Cmd RTN	ACCJ035	11		HP_Cmd
SK02J15	18	EGSE/STR2_ON/OFF_RTN_Nom_Cmd RTN	STR2J01	6		STR_HP_Input
SK02J15	20	ACC/EGSE_STR2_ON/OFF_RTN_Red_Cmd RTN	ACCJ045	11		HP_Cmd
SK02J15	21	EGSE/STR2_ON/OFF_RTN_Red_Cmd RTN	STR2J01	15		STR_HP_Input
SK02J15	BKSH	Shield_for_EGSE/ACC_STR2_ON/OFF_Sts	ACCJ071	BKSH		Shield
SK02J15	BKSH	Shield_for_STR2/EGSE_ON/OFF_Sts	STR2J01	BKSH		Shield



4.1.15 Satellite Level Skin Connector SK03 J01 Xponder 1 Aux Inputs

SOURCE		SignalName	DESTINATION		Interface_of_Source	Interface of Destination
Connector Reference	Pin		Connected to Unit/Connector	Pin		
SK03J01	1	XPND1/SK03_TC_Clock -	XPND1J07	11		SBDL_Receiver
SK03J01	2	XPND1/SK03_TC_Clock +	XPND1J07	3		SBDL_Receiver
SK03J01	3	XPND1/SK03_TC_Data -	XPND1J07	15		SBDL_Receiver
SK03J01	4	XPND1/SK03_TC_Data +	XPND1J07	8		SBDL_Receiver
SK03J01	5	XPND1/SK03_TC_RF_Lock -	XPND1J07	13		SBDL_Receiver
SK03J01	6	XPND1/SK03_TC_RF_Lock +	XPND1J07	5		SBDL_Receiver
SK03J01	7	XPND1/SK03_TC_Squelch -	XPND1J07	9		SBDL_Receiver
SK03J01	8	XPND1/SK03_TC_Squelch +	XPND1J07	1		SBDL_Receiver
SK03J01	9	EPC1/SK03_ProtDIS_Jmp	EPC1J01	15		JUMPER-DIS
SK03J01	10	EPC1/SK03_ProtDIS_Jmp RTN	EPC1J01	19		JUMPER-DIS
SK03J01	BKSH	Shield_for_XPND1/SK03_TC_Clock	XPND1J07	4		Shield
SK03J01	BKSH	Shield_for_XPND1/SK03_TC_Data	XPND1J07	14		Shield
SK03J01	BKSH	Shield_for_XPND1/SK03_TC_RF_Lock	XPND1J07	12		Shield
SK03J01	BKSH	Shield_for_XPND1/SK03_TC_Squelch	XPND1J07	2		Shield



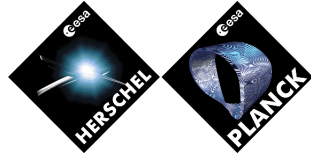
4.1.16 Satellite Level Skin Connector SK03 J02 Xponder 2 Aux Inputs

SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
SK03J02	1	XPND2/SK03_TC_Clock -	XPND2J07	11		SBDL_Receiver
SK03J02	2	XPND2/SK03_TC_Clock +	XPND2J07	3		SBDL_Receiver
SK03J02	3	XPND2/SK03_TC_Data -	XPND2J07	15		SBDL_Receiver
SK03J02	4	XPND2/SK03_TC_Data +	XPND2J07	8		SBDL_Receiver
SK03J02	5	XPND2/SK03_TC_RF_Lock -	XPND2J07	13		SBDL_Receiver
SK03J02	6	XPND2/SK03_TC_RF_Lock +	XPND2J07	5		SBDL_Receiver
SK03J02	7	XPND2/SK03_TC_Squelch -	XPND2J07	9		SBDL_Receiver
SK03J02	8	XPND2/SK03_TC_Squelch +	XPND2J07	1		SBDL_Receiver
SK03J02	9	EPC2/SK03_ProtDIS_Jmp	EPC2J01	15		JUMPER-DIS
SK03J02	10	EPC2/SK03_ProtDIS_Jmp RTN	EPC2J01	19		JUMPER-DIS
SK03J02	BKSH	Shield_for_XPND2/SK03_TC_Clock	XPND2J07	4		Shield
SK03J02	BKSH	Shield_for_XPND2/SK03_TC_Data	XPND2J07	14		Shield
SK03J02	BKSH	Shield_for_XPND2/SK03_TC_RF_Lock	XPND2J07	12		Shield
SK03J02	BKSH	Shield_for_XPND2/SK03_TC_Squelch	XPND2J07	2		Shield



4.1.17 Satellite Level Skin Connector SK05 J01 CRS1 - ACC

SOURCE		SignalName	DESTINATION		Interface_of_Source	Interface of Destination
Connector Reference	Pin		Connected to Unit/Connector	Pin		
SK05J01	1	CRS1/EGSE_Ang_X_Meas	CRS1J03	1		CRS_Output
SK05J01	2	CRS1/EGSE_Ang_X_Meas RTN	CRS1J03	6		CRS_Output
SK05J01	3	CRS1/EGSE_Ang_Y_Meas	CRS1J03	3		CRS_Output
SK05J01	4	CRS1/EGSE_Ang_Y_Meas RTN	CRS1J03	8		CRS_Output
SK05J01	5	CRS1/EGSE_Ang_Z_Meas	CRS1J03	5		CRS_Output
SK05J01	6	CRS1/EGSE_Ang_Z_Meas RTN	CRS1J03	10		CRS_Output
SK05J01	9	EGSE/ACC_CRS1_Ang_Z_Meas RTN	ACCJ133	8		CRS_Meas
SK05J01	10	EGSE/ACC_CRS1_Ang_Z_Meas	ACCJ133	3		CRS_Meas
SK05J01	11	EGSE/ACC_CRS1_Ang_Y_Meas	ACCJ133	2		CRS_Meas
SK05J01	12	EGSE/ACC_CRS1_Ang_Y_Meas RTN	ACCJ133	7		CRS_Meas
SK05J01	13	EGSE/ACC_CRS1_Ang_X_Meas RTN	ACCJ133	6		CRS_Meas
SK05J01	14	EGSE/ACC_CRS1_Ang_X_Meas	ACCJ133	1		CRS_Meas
SK05J01	16	CRS1/EGSE_Therm_Mnt	CRS1J02	15		Therm_Mnt
SK05J01	17	CRS1/EGSE_Therm_Mnt	CRS1J02	10		Therm_Mnt
SK05J01	19	EGSE/ACC_CRS1_Therm_Mnt	ACCJ061	15		Therm_Mnt
SK05J01	20	EGSE/ACC_CRS1_Therm_Mnt RTN	ACCJ061	35		Therm_Mnt
SK05J01	BKSH	Shield_for_CRS1/EGSE_Ang_X_Meas	CRS1J03	BKSH		Shield
SK05J01	BKSH	Shield_for_CRS1/EGSE_Ang_Y_Meas	CRS1J03	BKSH		Shield
SK05J01	BKSH	Shield_for_CRS1/EGSE_Ang_Z_Meas	CRS1J03	BKSH		Shield
SK05J01	BKSH	Shield_for_CRS1/EGSE_Therm_Mnt	CRS1J02	BKSH		Shield
SK05J01	BKSH	Shield_for_EGSE/ACC_CRS1_Ang_X_Meas	ACCJ133	BKSH		Shield
SK05J01	BKSH	Shield_for_EGSE/ACC_CRS1_Ang_Y_Meas	ACCJ133	BKSH		Shield
SK05J01	BKSH	Shield_for_EGSE/ACC_CRS1_Ang_Z_Meas	ACCJ133	BKSH		Shield
SK05J01	BKSH	Shield_for_EGSE/ACC_CRS1_Therm_Mnt	ACCJ061	BKSH		Shield



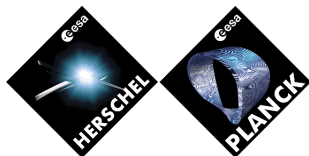
4.1.18 Satellite Level Skin Connector SK05 J02 CRS2 - ACC

SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
SK05J02	1	CRS2/EGSE_Ang_X_Meas	CRS2J03	1		CRS_Output
SK05J02	2	CRS2/EGSE_Ang_X_Meas RTN	CRS2J03	6		CRS_Output
SK05J02	3	CRS2/EGSE_Ang_Y_Meas	CRS2J03	3		CRS_Output
SK05J02	4	CRS2/EGSE_Ang_Y_Meas RTN	CRS2J03	8		CRS_Output
SK05J02	5	CRS2/EGSE_Ang_Z_Meas	CRS2J03	5		CRS_Output
SK05J02	6	CRS2/EGSE_Ang_Z_Meas RTN	CRS2J03	10		CRS_Output
SK05J02	9	EGSE/ACC_CRS2_Ang_Z_Meas RTN	ACCJ134	8		CRS_Meas
SK05J02	10	EGSE/ACC_CRS2_Ang_Z_Meas	ACCJ134	3		CRS_Meas
SK05J02	11	EGSE/ACC_CRS2_Ang_Y_Meas	ACCJ134	2		CRS_Meas
SK05J02	12	EGSE/ACC_CRS2_Ang_Y_Meas RTN	ACCJ134	7		CRS_Meas
SK05J02	13	EGSE/ACC_CRS2_Ang_X_Meas RTN	ACCJ134	6		CRS_Meas
SK05J02	14	EGSE/ACC_CRS2_Ang_X_Meas	ACCJ134	1		CRS_Meas
SK05J02	16	CRS2/EGSE_Therm_Mnt	CRS2J02	15		Therm_Mnt
SK05J02	17	CRS2/EGSE_Therm_Mnt	CRS2J02	10		Therm_Mnt
SK05J02	19	EGSE/ACC_CRS2_Therm_Mnt	ACCJ063	8		Therm_Mnt
SK05J02	20	EGSE/ACC_CRS2_Therm_Mnt RTN	ACCJ063	28		Therm_Mnt
SK05J02	BKSH	Shield_for_CRS2/EGSE_Ang_X_Meas	CRS2J03	BKSH		Shield
SK05J02	BKSH	Shield_for_CRS2/EGSE_Ang_Y_Meas	CRS2J03	BKSH		Shield
SK05J02	BKSH	Shield_for_CRS2/EGSE_Ang_Z_Meas	CRS2J03	BKSH		Shield
SK05J02	BKSH	Shield_for_CRS2/EGSE_Therm_Mnt	CRS2J02	BKSH		Shield
SK05J02	BKSH	Shield_for_EGSE/ACC_CRS2_Ang_X_Meas	ACCJ134	BKSH		Shield
SK05J02	BKSH	Shield_for_EGSE/ACC_CRS2_Ang_Y_Meas	ACCJ134	BKSH		Shield
SK05J02	BKSH	Shield_for_EGSE/ACC_CRS2_Ang_Z_Meas	ACCJ134	BKSH		Shield
SK05J02	BKSH	Shield_for_EGSE/ACC_CRS2_Therm_Mnt	ACCJ063	BKSH		Shield



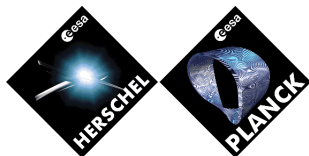
4.1.19 Satellite Level Skin Connector SK05 J03 CRS3 – ACC

SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
SK05J03	1	CRS3/EGSE_Ang_X_Meas	CRS3J03	1		CRS_Output
SK05J03	2	CRS3/EGSE_Ang_X_Meas RTN	CRS3J03	6		CRS_Output
SK05J03	3	CRS3/EGSE_Ang_Y_Meas	CRS3J03	3		CRS_Output
SK05J03	4	CRS3/EGSE_Ang_Y_Meas RTN	CRS3J03	8		CRS_Output
SK05J03	5	CRS3/EGSE_Ang_Z_Meas	CRS3J03	5		CRS_Output
SK05J03	6	CRS3/EGSE_Ang_Z_Meas RTN	CRS3J03	10		CRS_Output
SK05J03	9	EGSE/ACC_CRS3_Ang_Z_Meas RTN	ACCJ133	12		CRS_Meas
SK05J03	10	EGSE/ACC_CRS3_Ang_Z_Meas	ACCJ133	11		CRS_Meas
SK05J03	11	EGSE/ACC_CRS3_Ang_Y_Meas	ACCJ133	5		CRS_Meas
SK05J03	12	EGSE/ACC_CRS3_Ang_Y_Meas RTN	ACCJ133	10		CRS_Meas
SK05J03	13	EGSE/ACC_CRS3_Ang_X_Meas RTN	ACCJ133	9		CRS_Meas
SK05J03	14	EGSE/ACC_CRS3_Ang_X_Meas	ACCJ133	4		CRS_Meas
SK05J03	16	CRS3/EGSE_Therm_Mnt	CRS3J02	15		Therm_Mnt
SK05J03	17	CRS3/EGSE_Therm_Mnt	CRS3J02	10		Therm_Mnt
SK05J03	19	EGSE/ACC_CRS3_Therm_Mnt	ACCJ071	15		Therm_Mnt
SK05J03	20	EGSE/ACC_CRS3_Therm_Mnt RTN	ACCJ071	35		Therm_Mnt
SK05J03	BKSH	Shield_for_CRS3/EGSE_Ang_X_Meas	CRS3J03	BKSH		Shield
SK05J03	BKSH	Shield_for_CRS3/EGSE_Ang_Y_Meas	CRS3J03	BKSH		Shield
SK05J03	BKSH	Shield_for_CRS3/EGSE_Ang_Z_Meas	CRS3J03	BKSH		Shield
SK05J03	BKSH	Shield_for_CRS3/EGSE_Therm_Mnt	CRS3J02	BKSH		Shield
SK05J03	BKSH	Shield_for_EGSE/ACC_CRS3_Ang_X_Meas	ACCJ133	BKSH		Shield
SK05J03	BKSH	Shield_for_EGSE/ACC_CRS3_Ang_Y_Meas	ACCJ133	BKSH		Shield
SK05J03	BKSH	Shield_for_EGSE/ACC_CRS3_Ang_Z_Meas	ACCJ133	BKSH		Shield
SK05J03	BKSH	Shield_for_EGSE/ACC_CRS3_Therm_Mnt	ACCJ071	BKSH		Shield



4.1.20 Satellite Level Skin Connector SK05 J04 CRS1 Stimuli

SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
SK05J04	4	EGSE/CRS1_X-Chan_TE_Stimuli	CRS1J04	1		CRS_Stim_input
SK05J04	5	EGSE/CRS1_X-Chan_TE_Stimuli RTN	CRS1J04	6		CRS_Stim_input
SK05J04	6	EGSE/CRS1_Y-Chan_TE_Stimuli	CRS1J04	2		CRS_Stim_input
SK05J04	7	EGSE/CRS1_Y-Chan_TE_Stimuli RTN	CRS1J04	7		CRS_Stim_input
SK05J04	8	EGSE/CRS1_Z-Chan_TE_Stimuli	CRS1J04	3		CRS_Stim_input
SK05J04	9	EGSE/CRS1_Z-Chan_TE_Stimuli RTN	CRS1J04	8		CRS_Stim_input
SK05J04	17	EGSE/CRS2_X-Chan_TE_Stimuli	CRS2J04	1		CRS_Stim_input
SK05J04	18	EGSE/CRS2_X-Chan_TE_Stimuli RTN	CRS2J04	6		CRS_Stim_input
SK05J04	19	EGSE/CRS2_Y-Chan_TE_Stimuli	CRS2J04	2		CRS_Stim_input
SK05J04	20	EGSE/CRS2_Y-Chan_TE_Stimuli RTN	CRS2J04	7		CRS_Stim_input
SK05J04	21	EGSE/CRS2_Z-Chan_TE_Stimuli	CRS2J04	3		CRS_Stim_input
SK05J04	22	EGSE/CRS2_Z-Chan_TE_Stimuli RTN	CRS2J04	8		CRS_Stim_input
SK05J04	32	EGSE/CRS3_X-Chan_TE_Stimuli	CRS3J04	1		CRS_Stim_input
SK05J04	33	EGSE/CRS3_X-Chan_TE_Stimuli RTN	CRS3J04	6		CRS_Stim_input
SK05J04	34	EGSE/CRS3_Y-Chan_TE_Stimuli	CRS3J04	2		CRS_Stim_input
SK05J04	35	EGSE/CRS3_Y-Chan_TE_Stimuli RTN	CRS3J04	7		CRS_Stim_input
SK05J04	36	EGSE/CRS3_Z-Chan_TE_Stimuli	CRS3J04	3		CRS_Stim_input
SK05J04	37	EGSE/CRS3_Z-Chan_TE_Stimuli RTN	CRS3J04	8		CRS_Stim_input



4.1.21 Satellite Level Skin Connector SK05 J05 AAD – ACC

SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
SK05J05	1	AAD/EGSE_PH_Conical_Mnt	AADJ01	5		AAD_Output
SK05J05	2	AAD/EGSE_PH_Conical_Mnt RTN	AADJ01	9		AAD_Output
SK05J05	3	EGSE/ACC_AAD_PH_Conical_Mnt Cathode	ACCJ133	14		AAD_Mnt
SK05J05	4	EGSE/ACC_AAD_PH_Conical_Mnt Anode	ACCJ133	13		AAD_Mnt
SK05J05	BKSH	Shield_for_AAD/EGSE_PH_Conical_Mnt	AADJ01	BKSH		Shield
SK05J05	BKSH	Shield_for_EGSE/ACC_AAD_PH_Conical_Mnt	ACCJ133	BKSH		Shield



4.1.22 Satellite Level Skin Connector SK05 J06 SAS1 & SAS2 – ACC

SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
SK05J06	1	EGSE/ACC_SAS1_PH1_Nom_Mnt Anode	ACCJ063	2		SAS_Mnt
SK05J06	2	EGSE/ACC_SAS1_PH1_Nom_Mnt Cathode	ACCJ063	22		SAS_Mnt
SK05J06	3	EGSE/ACC_SAS1_PH2_Nom_Mnt Anode	ACCJ063	41		SAS_Mnt
SK05J06	4	EGSE/ACC_SAS1_PH2_Nom_Mnt Cathode	ACCJ063	61		SAS_Mnt
SK05J06	5	EGSE/ACC_SAS1_PH3_Nom_Mnt Anode	ACCJ063	3		SAS_Mnt
SK05J06	6	EGSE/ACC_SAS1_PH3_Nom_Mnt Cathode	ACCJ063	23		SAS_Mnt
SK05J06	7	EGSE/ACC_SAS1_PH4_Nom_Mnt Anode	ACCJ063	42		SAS_Mnt
SK05J06	8	EGSE/ACC_SAS1_PH4_Nom_Mnt Cathode	ACCJ063	62		SAS_Mnt
SK05J06	11	EGSE/ACC_SAS2_PH4_Nom_Mnt Anode	ACCJ063	45		SAS_Mnt
SK05J06	12	EGSE/ACC_SAS2_PH4_Nom_Mnt Cathode	ACCJ063	65		SAS_Mnt
SK05J06	13	EGSE/ACC_SAS2_PH3_Nom_Mnt Anode	ACCJ063	6		SAS_Mnt
SK05J06	14	EGSE/ACC_SAS2_PH3_Nom_Mnt Cathode	ACCJ063	26		SAS_Mnt
SK05J06	15	EGSE/ACC_SAS2_PH2_Nom_Mnt Anode	ACCJ063	44		SAS_Mnt
SK05J06	16	EGSE/ACC_SAS2_PH2_Nom_Mnt Cathode	ACCJ063	64		SAS_Mnt
SK05J06	17	EGSE/ACC_SAS2_PH1_Nom_Mnt Anode	ACCJ063	5		SAS_Mnt
SK05J06	18	EGSE/ACC_SAS2_PH1_Nom_Mnt Cathode	ACCJ063	25		SAS_Mnt
SK05J06	19	SAS1/EGSE_PH1_Nom_Mnt	SAS1J01	8		SAS_Output
SK05J06	20	SAS1/EGSE_PH1_Nom_Mnt RTN	SAS1J01	15		SAS_Output
SK05J06	21	SAS1/EGSE_PH2_Nom_Mnt	SAS1J01	7		SAS_Output
SK05J06	22	SAS1/EGSE_PH2_Nom_Mnt RTN	SAS1J01	15		SAS_Output
SK05J06	23	SAS1/EGSE_PH3_Nom_Mnt	SAS1J01	13		SAS_Output
SK05J06	24	SAS1/EGSE_PH3_Nom_Mnt RTN	SAS1J01	14		SAS_Output
SK05J06	25	SAS2/EGSE_PH3_Nom_Mnt	SAS2J01	13		SAS_Output
SK05J06	26	SAS2/EGSE_PH3_Nom_Mnt RTN	SAS2J01	14		SAS_Output
SK05J06	27	SAS2/EGSE_PH2_Nom_Mnt	SAS2J01	7		SAS_Output
SK05J06	28	SAS2/EGSE_PH2_Nom_Mnt RTN	SAS2J01	15		SAS_Output
SK05J06	29	SAS2/EGSE_PH1_Nom_Mnt	SAS2J01	8		SAS_Output

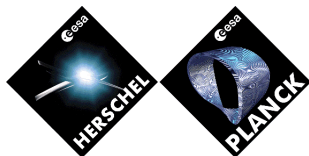


SK05J06	30	SAS2/EGSE_PH1_Nom_Mnt RTN	SAS2J01	15	SAS_Output
SK05J06	32	SAS1/EGSE_PH4_Nom_Mnt	SAS1J01	6	SAS_Output
SK05J06	33	SAS1/EGSE_PH4_Nom_Mnt RTN	SAS1J01	14	SAS_Output
SK05J06	34	SAS2/EGSE_PH4_Nom_Mnt	SAS2J01	6	SAS_Output
SK05J06	35	SAS2/EGSE_PH4_Nom_Mnt RTN	SAS2J01	14	SAS_Output
SK05J06	BKSH	Shield_for_EGSE/ACC_SAS1_PH1_Nom_Mnt	ACCJ063	BKSH	Shield
SK05J06	BKSH	Shield_for_EGSE/ACC_SAS1_PH2_Nom_Mnt	ACCJ063	BKSH	Shield
SK05J06	BKSH	Shield_for_EGSE/ACC_SAS1_PH3_Nom_Mnt	ACCJ063	BKSH	Shield
SK05J06	BKSH	Shield_for_EGSE/ACC_SAS1_PH4_Nom_Mnt	ACCJ063	BKSH	Shield
SK05J06	BKSH	Shield_for_EGSE/ACC_SAS2_PH1_Nom_Mnt	ACCJ063	BKSH	Shield
SK05J06	BKSH	Shield_for_EGSE/ACC_SAS2_PH2_Nom_Mnt	ACCJ063	BKSH	Shield
SK05J06	BKSH	Shield_for_EGSE/ACC_SAS2_PH3_Nom_Mnt	ACCJ063	BKSH	Shield
SK05J06	BKSH	Shield_for_EGSE/ACC_SAS2_PH4_Nom_Mnt	ACCJ063	BKSH	Shield
SK05J06	BKSH	Shield_for_SAS1/EGSE_PH1_Nom_Mnt	SAS1J01	BKSH	Shield
SK05J06	BKSH	Shield_for_SAS1/EGSE_PH2_Nom_Mnt	SAS1J01	BKSH	Shield
SK05J06	BKSH	Shield_for_SAS1/EGSE_PH3_Nom_Mnt	SAS1J01	BKSH	Shield
SK05J06	BKSH	Shield_for_SAS1/EGSE_PH4_Nom_Mnt	SAS1J01	BKSH	Shield
SK05J06	BKSH	Shield_for_SAS2/EGSE_PH1_Nom_Mnt	SAS2J01	BKSH	Shield
SK05J06	BKSH	Shield_for_SAS2/EGSE_PH2_Nom_Mnt	SAS2J01	BKSH	Shield
SK05J06	BKSH	Shield_for_SAS2/EGSE_PH3_Nom_Mnt	SAS2J01	BKSH	Shield
SK05J06	BKSH	Shield_for_SAS2/EGSE_PH4_Nom_Mnt	SAS2J01	BKSH	Shield



4.1.23 Satellite Level Skin Connector SK05 J07 SAS1 & SAS2 – ACC

SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
SK05J07	1	EGSE/ACC_SAS1_PH1_Red_Mnt Anode	ACCJ073	2		SAS_Mnt
SK05J07	2	EGSE/ACC_SAS1_PH1_Red_Mnt Cathode	ACCJ073	22		SAS_Mnt
SK05J07	3	EGSE/ACC_SAS1_PH2_Red_Mnt Anode	ACCJ073	41		SAS_Mnt
SK05J07	4	EGSE/ACC_SAS1_PH2_Red_Mnt Cathode	ACCJ073	61		SAS_Mnt
SK05J07	5	EGSE/ACC_SAS1_PH3_Red_Mnt Anode	ACCJ073	3		SAS_Mnt
SK05J07	6	EGSE/ACC_SAS1_PH3_Red_Mnt Cathode	ACCJ073	23		SAS_Mnt
SK05J07	7	EGSE/ACC_SAS1_PH4_Red_Mnt Anode	ACCJ073	42		SAS_Mnt
SK05J07	8	EGSE/ACC_SAS1_PH4_Red_Mnt Cathode	ACCJ073	62		SAS_Mnt
SK05J07	11	EGSE/ACC_SAS2_PH4_Red_Mnt Anode	ACCJ073	45		SAS_Mnt
SK05J07	12	EGSE/ACC_SAS2_PH4_Red_Mnt Cathode	ACCJ073	65		SAS_Mnt
SK05J07	13	EGSE/ACC_SAS2_PH3_Red_Mnt Anode	ACCJ073	6		SAS_Mnt
SK05J07	14	EGSE/ACC_SAS2_PH3_Red_Mnt Cathode	ACCJ073	26		SAS_Mnt
SK05J07	15	EGSE/ACC_SAS2_PH2_Red_Mnt Anode	ACCJ073	44		SAS_Mnt
SK05J07	16	EGSE/ACC_SAS2_PH2_Red_Mnt Cathode	ACCJ073	64		SAS_Mnt
SK05J07	17	EGSE/ACC_SAS2_PH1_Red_Mnt Anode	ACCJ073	5		SAS_Mnt
SK05J07	18	EGSE/ACC_SAS2_PH1_Red_Mnt Cathode	ACCJ073	25		SAS_Mnt
SK05J07	19	SAS1/EGSE_PH1_Red_Mnt	SAS1J01	11		SAS_Output
SK05J07	20	SAS1/EGSE_PH1_Red_Mnt RTN	SAS1J01	10		SAS_Output
SK05J07	21	SAS1/EGSE_PH2_Red_Mnt	SAS1J01	3		SAS_Output
SK05J07	22	SAS1/EGSE_PH2_Red_Mnt RTN	SAS1J01	10		SAS_Output
SK05J07	23	SAS1/EGSE_PH3_Red_Mnt	SAS1J01	1		SAS_Output
SK05J07	24	SAS1/EGSE_PH3_Red_Mnt RTN	SAS1J01	9		SAS_Output
SK05J07	25	SAS2/EGSE_PH3_Red_Mnt	SAS2J01	1		SAS_Output
SK05J07	26	SAS2/EGSE_PH3_Red_Mnt RTN	SAS2J01	9		SAS_Output
SK05J07	27	SAS2/EGSE_PH2_Red_Mnt	SAS2J01	3		SAS_Output
SK05J07	28	SAS2/EGSE_PH2_Red_Mnt RTN	SAS2J01	10		SAS_Output
SK05J07	29	SAS2/EGSE_PH1_Red_Mnt	SAS2J01	11		SAS_Output
SK05J07	30	SAS2/EGSE_PH1_Red_Mnt RTN	SAS2J01	10		SAS_Output



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SK05J07	32	SAS1/EGSE_PH4_Red_Mnt	SAS1J01	2	SAS_Output
SK05J07	33	SAS1/EGSE_PH4_Red_Mnt RTN	SAS1J01	9	SAS_Output
SK05J07	34	SAS2/EGSE_PH4_Red_Mnt	SAS2J01	2	SAS_Output
SK05J07	35	SAS2/EGSE_PH4_Red_Mnt RTN	SAS2J01	9	SAS_Output
SK05J07	BKSH	Shield_for_EGSE/ACC_SAS1_PH1_Red_Mnt	ACCJ073	BKSH	Shield
SK05J07	BKSH	Shield_for_EGSE/ACC_SAS1_PH2_Red_Mnt	ACCJ073	BKSH	Shield
SK05J07	BKSH	Shield_for_EGSE/ACC_SAS1_PH3_Red_Mnt	ACCJ073	BKSH	Shield
SK05J07	BKSH	Shield_for_EGSE/ACC_SAS1_PH4_Red_Mnt	ACCJ073	BKSH	Shield
SK05J07	BKSH	Shield_for_EGSE/ACC_SAS2_PH1_Red_Mnt	ACCJ073	BKSH	Shield
SK05J07	BKSH	Shield_for_EGSE/ACC_SAS2_PH2_Red_Mnt	ACCJ073	BKSH	Shield
SK05J07	BKSH	Shield_for_EGSE/ACC_SAS2_PH3_Red_Mnt	ACCJ073	BKSH	Shield
SK05J07	BKSH	Shield_for_EGSE/ACC_SAS2_PH4_Red_Mnt	ACCJ073	BKSH	Shield
SK05J07	BKSH	Shield_for_SAS1/EGSE_PH1_Red_Mnt	SAS1J01	BKSH	Shield
SK05J07	BKSH	Shield_for_SAS1/EGSE_PH2_Red_Mnt	SAS1J01	BKSH	Shield
SK05J07	BKSH	Shield_for_SAS1/EGSE_PH3_Red_Mnt	SAS1J01	BKSH	Shield
SK05J07	BKSH	Shield_for_SAS1/EGSE_PH4_Red_Mnt	SAS1J01	BKSH	Shield
SK05J07	BKSH	Shield_for_SAS2/EGSE_PH1_Red_Mnt	SAS2J01	BKSH	Shield
SK05J07	BKSH	Shield_for_SAS2/EGSE_PH2_Red_Mnt	SAS2J01	BKSH	Shield
SK05J07	BKSH	Shield_for_SAS2/EGSE_PH3_Red_Mnt	SAS2J01	BKSH	Shield
SK05J07	BKSH	Shield_for_SAS2/EGSE_PH4_Red_Mnt	SAS2J01	BKSH	Shield



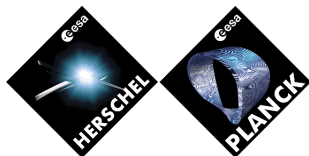
4.1.24 Satellite Level Skin Connector SK05 J08 AAD – ACC

SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
SK05J08	1	AAD/EGSE_PH_Square_Mnt	AADJ01	1		AAD_Output
SK05J08	2	AAD/EGSE_PH_Square_Mnt RTN	AADJ01	6		AAD_Output
SK05J08	3	EGSE/ACC_AAD_PH_Square_Mnt Cathode	ACCJ134	14		AAD_Mnt
SK05J08	4	EGSE/ACC_AAD_PH_Square_Mnt Anode	ACCJ134	13		AAD_Mnt
SK05J08	BKSH	Shield_for_AAD/EGSE_PH_Square_Mnt	AADJ01	BKSH		Shield
SK05J08	BKSH	Shield_for_EGSE/ACC_AAD_PH_Square_Mnt	ACCJ134	BKSH		Shield



4.1.25 Satellite Level Skin Connector SK06 J01 STR1 Stimuli

SOURCE		SignalName	DESTINATION		Interface_of_Source	Interface of Destination
Connector Reference	Pin		Connected to Unit/Connector	Pin		
SK06J01	6	STR1/EGSE_JMP_Data_Len0	STR1J02	6		JUMPER
SK06J01	7	STR1/EGSE_AUX_CMD_CLK_Stimuli +	STR1J02	7		STR_Stim
SK06J01	8	STR1/EGSE_AUX_CMD_CLK_Stimuli -	STR1J02	8		STR_Stim
SK06J01	9	STR1/EGSE_AUX_CMD_WIN_Stimuli +	STR1J02	9		STR_Stim
SK06J01	10	STR1/EGSE_AUX_CMD_WIN_Stimuli -	STR1J02	10		STR_Stim
SK06J01	11	STR1/EGSE_AUX_CMD_OUT_Stimuli +	STR1J02	11		STR_Stim
SK06J01	12	STR1/EGSE_AUX_CMD_OUT_Stimuli -	STR1J02	12		STR_Stim
SK06J01	13	STR1/EGSE_AUX_SYNCHRO_Stimuli +	STR1J02	13		STR_Stim
SK06J01	14	STR1/EGSE_AUX_SYNCHRO_Stimuli -	STR1J02	14		STR_Stim
SK06J01	15	STR1/EGSE_JMP_TC_Pres	STR1J02	15		JUMPER
SK06J01	20	STR1/EGSE_JMP_EGS_Enable	STR1J02	20		JUMPER
SK06J01	21	STR1/EGSE_JMP_J02-21_GROUND RTN	STR1J02	21		GND
SK06J01	26	EGSE/STR1_ADC_DATA_Stimuli +	STR1J02	26		STR_Stim
SK06J01	27	EGSE/STR1_ADC_WIN_Stimuli +	STR1J02	27		STR_Stim
SK06J01	28	STR1/EGSE_PIX_VALID_Stimuli +	STR1J02	28		STR_Stim
SK06J01	29	STR1/EGSE_PIX_GAIN_Stimuli +	STR1J02	29		STR_Stim
SK06J01	30	EGSE/STR1_ADC_CLK_Stimuli +	STR1J02	30		STR_Stim
SK06J01	35	STR1/EGSE_JMP_J02-35_GROUND RTN	STR1J02	35		GND
SK06J01	40	EGSE/STR1_ADC_DATA_Stimuli -	STR1J02	40		STR_Stim
SK06J01	41	EGSE/STR1_ADC_WIN_Stimuli -	STR1J02	41		STR_Stim
SK06J01	42	STR1/EGSE_PIX_VALID_Stimuli -	STR1J02	42		STR_Stim
SK06J01	43	STR1/EGSE_PIX_GAIN_Stimuli -	STR1J02	43		STR_Stim
SK06J01	44	EGSE/STR1_ADC_CLK_Stimuli -	STR1J02	44		STR_Stim
SK06J01	BKSH	Shield_for_STR1/EGSE_Overall_Shield	STR1J02	BKSH		Shield



4.1.26 Satellite Level Skin Connector SK06 J02 STR2 Stimuli

SOURCE		SignalName	DESTINATION		Interface_of_Source	Interface of Destination
Connector Reference	Pin		Connected to Unit/Connector	Pin		
SK06J02	6	STR2/EGSE_JMP_Data_Len0	STR2J02	6		JUMPER
SK06J02	7	STR2/EGSE_AUX_CMD_CLK_Stimuli +	STR2J02	7		STR_Stim
SK06J02	8	STR2/EGSE_AUX_CMD_CLK_Stimuli -	STR2J02	8		STR_Stim
SK06J02	9	STR2/EGSE_AUX_CMD_WIN_Stimuli +	STR2J02	9		STR_Stim
SK06J02	10	STR2/EGSE_AUX_CMD_WIN_Stimuli -	STR2J02	10		STR_Stim
SK06J02	11	STR2/EGSE_AUX_CMD_OUT_Stimuli +	STR2J02	11		STR_Stim
SK06J02	12	STR2/EGSE_AUX_CMD_OUT_Stimuli -	STR2J02	12		STR_Stim
SK06J02	13	STR2/EGSE_AUX_SYNCHRO_Stimuli +	STR2J02	13		STR_Stim
SK06J02	14	STR2/EGSE_AUX_SYNCHRO_Stimuli -	STR2J02	14		STR_Stim
SK06J02	15	STR2/EGSE_JMP_TC_Pres	STR2J02	15		JUMPER
SK06J02	20	STR2/EGSE_JMP_EGS_Enable	STR2J02	20		JUMPER
SK06J02	21	STR2/EGSE_JMP_J02-21_GROUND RTN	STR2J02	21		GND
SK06J02	26	EGSE/STR2_ADC_DATA_Stimuli +	STR2J02	26		STR_Stim
SK06J02	27	EGSE/STR2_ADC_WIN_Stimuli +	STR2J02	27		STR_Stim
SK06J02	28	STR2/EGSE_PIX_VALID_Stimuli +	STR2J02	28		STR_Stim
SK06J02	29	STR2/EGSE_PIX_GAIN_Stimuli +	STR2J02	29		STR_Stim
SK06J02	30	EGSE/STR2_ADC_CLK_Stimuli +	STR2J02	30		STR_Stim
SK06J02	35	STR2/EGSE_JMP_J02-35_GROUND RTN	STR2J02	35		GND
SK06J02	40	EGSE/STR2_ADC_DATA_Stimuli -	STR2J02	40		STR_Stim
SK06J02	41	EGSE/STR2_ADC_WIN_Stimuli -	STR2J02	41		STR_Stim
SK06J02	42	STR2/EGSE_PIX_VALID_Stimuli -	STR2J02	42		STR_Stim
SK06J02	43	STR2/EGSE_PIX_GAIN_Stimuli -	STR2J02	43		STR_Stim
SK06J02	44	EGSE/STR2_ADC_CLK_Stimuli -	STR2J02	44		STR_Stim
SK06J02	BKSH	Shield_for_STR2/EGSE_Overall_Shield	STR2J02	BKSH		Shield



4.2 Satellite Level PLM – SVM Connections

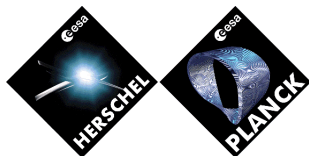
4.2.1 PLM – SVM Connections CBPLMP01

SOURCE		SignalName	DESTINATION		Interface_of_Source	Interface of Destination
Connector Reference	Pin		Connected to Unit/Connector	Pin		
CBPLMP01	1	PCDU/CBPLM_Primary_Reflector-1_Htr_Nom_Pwr	PCDUJ01	1	Primary_Reflector_Heater	PCDU_Heater_pwr
CBPLMP01	2	PCDU/CBPLM_Primary_Reflector-2_Htr_Nom_Pwr	PCDUJ03	1	Primary_Reflector_Heater	PCDU_Heater_pwr
CBPLMP01	4	PCDU/CBPLM_Secondary_Reflector_Htr_Nom_Pwr	PCDUJ05	1	Secondary_Reflector_Heater	PCDU_Heater_pwr
CBPLMP01	6	PCDU/CBPLM_FPU-1_Htr_Nom_Pwr	PCDUJ07	1	FPU_Heater	PCDU_Heater_pwr
CBPLMP01	7	PCDU/CBPLM_FPU-2_Htr_Nom_Pwr	PCDUJ09	1	FPU_Heater	PCDU_Heater_pwr
CBPLMP01	9	PCDU/CBPLM_Primary_Reflector-1_Htr_Nom_Pwr RTN	PCDUJ01	14	Primary_Reflector_Heater	PCDU_Heater_pwr
CBPLMP01	10	PCDU/CBPLM_Primary_Reflector-2_Htr_Nom_Pwr RTN	PCDUJ03	14	Primary_Reflector_Heater	PCDU_Heater_pwr
CBPLMP01	12	PCDU/CBPLM_Secondary_Reflector_Htr_Nom_Pwr RTN	PCDUJ05	14	Secondary_Reflector_Heater	PCDU_Heater_pwr
CBPLMP01	14	PCDU/CBPLM_FPU-1_Htr_Nom_Pwr RTN	PCDUJ07	14	FPU_Heater	PCDU_Heater_pwr
CBPLMP01	15	PCDU/CBPLM_FPU-2_Htr_Nom_Pwr RTN	PCDUJ09	14	FPU_Heater	PCDU_Heater_pwr



4.2.2 PLM – SVM Connections CBPLMP02

SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
CBPLMP02	1	PCDU/CBPLM_Primary_Reflector-1_Htr_Red_Pwr	PCDUJ35	1	Primary_Reflector_Heater	PCDU_Heater_pwr
CBPLMP02	4	PCDU/CBPLM_Secondary_Reflector_Htr_Red_Pwr	PCDUJ31	8	Secondary_Reflector_Heater	PCDU_Heater_pwr
CBPLMP02	6	PCDU/CBPLM_FPU-1_Htr_Red_Pwr	PCDUJ29	8	FPU_Heater	PCDU_Heater_pwr
CBPLMP02	7	PCDU/CBPLM_FPU-2_Htr_Red_Pwr	PCDUJ27	8	FPU_Heater	PCDU_Heater_pwr
CBPLMP02	9	PCDU/CBPLM_Primary_Reflector-1_Htr_Red_Pwr RTN	PCDUJ35	14	Primary_Reflector_Heater	PCDU_Heater_pwr
CBPLMP02	12	PCDU/CBPLM_Secondary_Reflector_Htr_Red_Pwr RTN	PCDUJ31	20	Secondary_Reflector_Heater	PCDU_Heater_pwr
CBPLMP02	14	PCDU/CBPLM_FPU-1_Htr_Red_Pwr RTN	PCDUJ29	20	FPU_Heater	PCDU_Heater_pwr
CBPLMP02	15	PCDU/CBPLM_FPU-2_Htr_Red_Pwr RTN	PCDUJ27	20	FPU_Heater	PCDU_Heater_pwr



4.2.3 PLM – SVM Connections CBPLMP03

SOURCE		SignalName	DESTINATION		Interface_of_Source	Interface of Destination
Connector Reference	Pin		Connected to Unit/Connector	Pin		
CBPLMP03	1	CBPLM/CDMU_CRW_Pri-Refl_Decon_TC1_Mnt_H	CDMUJ101	78	Thermistor	CR-W
CBPLMP03	2	CBPLM/CDMU_CRW_Pri-Refl_Decon_TC2_Mnt_H	CDMUJ111	78	Thermistor	CR-W
CBPLMP03	3	CBPLM/CDMU_CRN_Reflector_PR1_Nom_Mnt_H	CDMUJ101	72	Thermistor	CR-N
CBPLMP03	4	CBPLM/CDMU_CRN_Reflector_PR2_Nom_Mnt_H	CDMUJ101	53	Thermistor	CR-N
CBPLMP03	6	CBPLM/CDMU_CRW_Pri-Refl_Decon_TC1_Mnt_L	CDMUJ101	77	Thermistor	CR-W
CBPLMP03	7	CBPLM/CDMU_CRW_Pri-Refl_Decon_TC2_Mnt_L	CDMUJ111	77	Thermistor	CR-W
CBPLMP03	8	CBPLM/CDMU_CRN_Reflector_PR1_Nom_Mnt_L	CDMUJ101	71	Thermistor	CR-N
CBPLMP03	9	CBPLM/CDMU_CRN_Reflector_PR2_Nom_Mnt_L	CDMUJ101	52	Thermistor	CR-N
CBPLMP03	BKSH	Shield_for_CBPLM/CDMU_CRN_Reflector_PR1_Nom_Mnt	CDMUJ101	BKSH	Shield	Shield
CBPLMP03	BKSH	Shield_for_CBPLM/CDMU_CRN_Reflector_PR2_Nom_Mnt	CDMUJ101	BKSH	Shield	Shield
CBPLMP03	BKSH	Shield_for_CBPLM/CDMU_CRW_Pri-Refl_Decon_TC1_Mnt	CDMUJ101	BKSH	Shield	Shield
CBPLMP03	BKSH	Shield_for_CBPLM/CDMU_CRW_Pri-Refl_Decon_TC2_Mnt	CDMUJ111	BKSH	Shield	Shield



4.2.4 PLM – SVM Connections CBPLMP04

SOURCE		SignalName	DESTINATION		Interface_of_Source	Interface of Destination
Connector Reference	Pin		Connected to Unit/Connector	Pin		
CBPLMP04	1	CBPLM/CDMU_CRW_Pri-Refl_Decon_TC3_Mnt_H	CDMUJ121	78	Thermistor	CR-W
CBPLMP04	3	CBPLM/CDMU_CRN_Reflector_PR1_Red_Mnt_H	CDMUJ121	76	Thermistor	CR-N
CBPLMP04	4	CBPLM/CDMU_CRN_Reflector_PR2_Red_Mnt_H	CDMUJ121	57	Thermistor	CR-N
CBPLMP04	6	CBPLM/CDMU_CRW_Pri-Refl_Decon_TC3_Mnt_L	CDMUJ121	77	Thermistor	CR-W
CBPLMP04	8	CBPLM/CDMU_CRN_Reflector_PR1_Red_Mnt_L	CDMUJ121	75	Thermistor	CR-N
CBPLMP04	9	CBPLM/CDMU_CRN_Reflector_PR2_Red_Mnt_L	CDMUJ121	56	Thermistor	CR-N
CBPLMP04	BKSH	Shield_for_CBPLM/CDMU_CRN_Reflector_PR1_Red_Mnt	CDMUJ121	BKSH	Shield	Shield
CBPLMP04	BKSH	Shield_for_CBPLM/CDMU_CRN_Reflector_PR2_Red_Mnt	CDMUJ121	BKSH	Shield	Shield
CBPLMP04	BKSH	Shield_for_CBPLM/CDMU_CRW_Pri-Refl_Decon_TC3_Mnt	CDMUJ121	BKSH	Shield	Shield



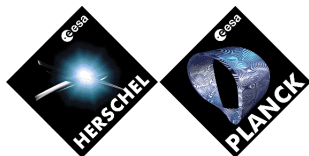
4.2.5 PLM – SVM Connections CBPLMP05

SOURCE		SignalName	DESTINATION		Interface_of_Source	Interface of Destination
Connector Reference	Pin		Connected to Unit/Connector	Pin		
CBPLMP05	1	CBPLM/CDMU_CRW_Sec-Refl_Decon_TC1_Mnt_H	CDMUJ101	59	Thermistor	CR-W
CBPLMP05	2	CBPLM/CDMU_CRW_Sec-Refl_Decon_TC2_Mnt_H	CDMUJ111	59	Thermistor	CR-W
CBPLMP05	3	CBPLM/CDMU_CRN_Reflecter_SR1_Nom_Mnt_H	CDMUJ111	72	Thermistor	CR-N
CBPLMP05	4	CBPLM/CDMU_CRN_Reflecter_SR2_Nom_Mnt_H	CDMUJ111	53	Thermistor	CR-N
CBPLMP05	6	CBPLM/CDMU_CRW_Sec-Refl_Decon_TC1_Mnt_L	CDMUJ101	58	Thermistor	CR-W
CBPLMP05	7	CBPLM/CDMU_CRW_Sec-Refl_Decon_TC2_Mnt_L	CDMUJ111	58	Thermistor	CR-W
CBPLMP05	8	CBPLM/CDMU_CRN_Reflecter_SR1_Nom_Mnt_L	CDMUJ111	71	Thermistor	CR-N
CBPLMP05	9	CBPLM/CDMU_CRN_Reflecter_SR2_Nom_Mnt_L	CDMUJ111	52	Thermistor	CR-N
CBPLMP05	BKSH	Shield_for_CBPLM/CDMU_CRN_Reflecter_SR1_Nom_Mnt	CDMUJ111	BKSH	Shield	Shield
CBPLMP05	BKSH	Shield_for_CBPLM/CDMU_CRN_Reflecter_SR2_Nom_Mnt	CDMUJ111	BKSH	Shield	Shield
CBPLMP05	BKSH	Shield_for_CBPLM/CDMU_CRW_Sec-Refl_Decon_TC1_Mnt	CDMUJ101	BKSH	Shield	Shield
CBPLMP05	BKSH	Shield_for_CBPLM/CDMU_CRW_Sec-Refl_Decon_TC2_Mnt	CDMUJ111	BKSH	Shield	Shield



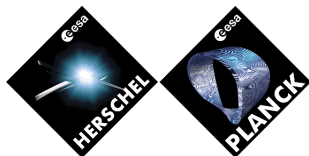
4.2.6 PLM – SVM Connections CBPLMP06

SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
CBPLMP06	1	CBPLM/CDMU_CRW_Sec-Refl_Decon_TC3_Mnt_H	CDMUJ121	59	Thermistor	CR-W
CBPLMP06	3	CBPLM/CDMU_CRN_Reflector_SR1_Red_Mnt_H	CDMUJ121	36	Thermistor	CR-N
CBPLMP06	4	CBPLM/CDMU_CRN_Reflector_SR2_Red_Mnt_H	CDMUJ121	17	Thermistor	CR-N
CBPLMP06	6	CBPLM/CDMU_CRW_Sec-Refl_Decon_TC3_Mnt_L	CDMUJ121	58	Thermistor	CR-W
CBPLMP06	8	CBPLM/CDMU_CRN_Reflector_SR1_Red_Mnt_L	CDMUJ121	35	Thermistor	CR-N
CBPLMP06	9	CBPLM/CDMU_CRN_Reflector_SR2_Red_Mnt_L	CDMUJ121	16	Thermistor	CR-N
CBPLMP06	BKSH	Shield_for_CBPLM/CDMU_CRN_Reflector_SR1_Red_Mnt	CDMUJ121	BKSH	Shield	Shield
CBPLMP06	BKSH	Shield_for_CBPLM/CDMU_CRN_Reflector_SR2_Red_Mnt	CDMUJ121	BKSH	Shield	Shield
CBPLMP06	BKSH	Shield_for_CBPLM/CDMU_CRW_Sec-Refl_Decon_TC3_Mnt	CDMUJ121	BKSH	Shield	Shield



4.2.7 PLM – SVM Connections CBPLMP07

SOURCE		SignalName	DESTINATION		Interface_of_Source	Interface of Destination
Connector Reference	Pin		Connected to Unit/Connector	Pin		
CBPLMP07	1	CBPLM/CDMU_CRW_FPU-HFI_Decon_TC1_Mnt_H	CDMUJ101	38	Thermistor	CR-W
CBPLMP07	2	CBPLM/CDMU_CRW_FPU-HFI_Decon_TC2_Mnt_H	CDMUJ111	38	Thermistor	CR-W
CBPLMP07	3	CBPLM/CDMU_CRW_FPU-LFI_Decon_TC1_Mnt_H	CDMUJ101	19	Thermistor	CR-W
CBPLMP07	9	CBPLM/CDMU_CRW_FPU-HFI_Decon_TC1_Mnt_L	CDMUJ101	37	Thermistor	CR-W
CBPLMP07	10	CBPLM/CDMU_CRW_FPU-HFI_Decon_TC2_Mnt_L	CDMUJ111	37	Thermistor	CR-W
CBPLMP07	11	CBPLM/CDMU_CRW_FPU-LFI_Decon_TC1_Mnt_L	CDMUJ101	18	Thermistor	CR-W
CBPLMP07	BKSH	Shield_for_CBPLM/CDMU_CRW_FPU-HFI_Decon_TC1_Mnt	CDMUJ101	BKSH	Shield	Shield
CBPLMP07	BKSH	Shield_for_CBPLM/CDMU_CRW_FPU-HFI_Decon_TC2_Mnt	CDMUJ111	BKSH	Shield	Shield
CBPLMP07	BKSH	Shield_for_CBPLM/CDMU_CRW_FPU-LFI_Decon_TC1_Mnt	CDMUJ101	BKSH	Shield	Shield



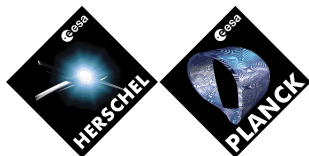
4.2.8 PLM – SVM Connections CBPLMP08

SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
CBPLMP08	1	CBPLM/CDMU_CRW_FPU-LFI_Decon_TC2_Mnt_H	CDMUJ111	19	Thermistor	CR-W
CBPLMP08	2	CBPLM/CDMU_CRW_FPU-LFI_Decon_TC3_Mnt_H	CDMUJ121	19	Thermistor	CR-W
CBPLMP08	3	CBPLM/CDMU_CRW_FPU-HFI_Decon_TC3_Mnt_H	CDMUJ121	38	Thermistor	CR-W
CBPLMP08	9	CBPLM/CDMU_CRW_FPU-LFI_Decon_TC2_Mnt_L	CDMUJ111	18	Thermistor	CR-W
CBPLMP08	10	CBPLM/CDMU_CRW_FPU-LFI_Decon_TC3_Mnt_L	CDMUJ121	18	Thermistor	CR-W
CBPLMP08	11	CBPLM/CDMU_CRW_FPU-HFI_Decon_TC3_Mnt_L	CDMUJ121	37	Thermistor	CR-W
CBPLMP08	BKSH	Shield_for_CBPLM/CDMU_CRW_FPU-HFI_Decon_TC3_Mnt	CDMUJ121	BKSH	Shield	Shield
CBPLMP08	BKSH	Shield_for_CBPLM/CDMU_CRW_FPU-LFI_Decon_TC2_Mnt	CDMUJ111	BKSH	Shield	Shield
CBPLMP08	BKSH	Shield_for_CBPLM/CDMU_CRW_FPU-LFI_Decon_TC3_Mnt	CDMUJ121	BKSH	Shield	Shield

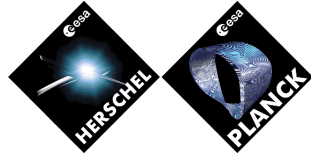


4.2.9 PLM – SVM Connections CBPLMP09

SOURCE		SignalName	DESTINATION		Interface_of_Source	Interface of Destination
Connector Reference	Pin		Connected to Unit/Connector	Pin		
CBPLMP09	1	CBPLM/CDMU_CRW_Groove-1_SC_HeatExc-1_Nom_Mnt_H	CDMUJ081	72	Thermistor	CR-W
CBPLMP09	2	CBPLM/CDMU_CRW_Groove-1_SC_HeatExc-1_Nom_Mnt_L	CDMUJ081	71	Thermistor	CR-W
CBPLMP09	3	CBPLM/CDMU_CRW_Groove-2_SC_HeatExc-1_Nom_Mnt_H	CDMUJ081	13	Thermistor	CR-W
CBPLMP09	4	CBPLM/CDMU_CRW_Groove-2_SC_HeatExc-1_Nom_Mnt_L	CDMUJ081	12	Thermistor	CR-W
CBPLMP09	5	CBPLM/CDMU_CRN_Groove-3_SC_HeatExc-2_Nom_Mnt_H	CDMUJ081	30	Thermistor	CR-N
CBPLMP09	6	CBPLM/CDMU_CRN_Groove-3_SC_HeatExc-2_Nom_Mnt_L	CDMUJ081	29	Thermistor	CR-N
CBPLMP09	7	CBPLM/CDMU_CRN_Groove-3_OpticalCavity_Nom_Mnt_H	CDMUJ081	49	Thermistor	CR-N
CBPLMP09	8	CBPLM/CDMU_CRN_Groove-3_OpticalCavity_Nom_Mnt_L	CDMUJ081	48	Thermistor	CR-N
CBPLMP09	9	CBPLM/CDMU_CRN_FPU-2_Nom_Mnt_H	CDMUJ101	36	Thermistor	CR-N
CBPLMP09	10	CBPLM/CDMU_CRN_FPU-2_Nom_Mnt_L	CDMUJ101	35	Thermistor	CR-N
CBPLMP09	11	CBPLM/CDMU_CRN_Baffle-2_Lateral_Nom_Mnt_H	CDMUJ101	55	Thermistor	CR-N
CBPLMP09	12	CBPLM/CDMU_CRN_Baffle-2_Lateral_Nom_Mnt_L	CDMUJ101	54	Thermistor	CR-N
CBPLMP09	18	CBPLM/CDMU_CRW_Groove-1_SC_HeatExc-2_Nom_Mnt_H	CDMUJ081	53	Thermistor	CR-W
CBPLMP09	19	CBPLM/CDMU_CRW_Groove-1_SC_HeatExc-2_Nom_Mnt_L	CDMUJ081	52	Thermistor	CR-W
CBPLMP09	20	CBPLM/CDMU_CRW_Groove-2_SC_HeatExc-2_Nom_Mnt_H	CDMUJ081	70	Thermistor	CR-W
CBPLMP09	21	CBPLM/CDMU_CRW_Groove-2_SC_HeatExc-2_Nom_Mnt_L	CDMUJ081	69	Thermistor	CR-W
CBPLMP09	22	CBPLM/CDMU_CRN_Groove-3_WaveGuide-1_Nom_Mnt_H	CDMUJ081	11	Thermistor	CR-N
CBPLMP09	23	CBPLM/CDMU_CRN_Groove-3_WaveGuide-1_Nom_Mnt_L	CDMUJ081	10	Thermistor	CR-N
CBPLMP09	24	CBPLM/CDMU_CRN_JFET_Nom_Mnt_H	CDMUJ101	76	Thermistor	CR-N
CBPLMP09	25	CBPLM/CDMU_CRN_JFET_Nom_Mnt_L	CDMUJ101	75	Thermistor	CR-N
CBPLMP09	26	CBPLM/CDMU_CRN_FPU-3_Nom_Mnt_H	CDMUJ101	17	Thermistor	CR-N
CBPLMP09	27	CBPLM/CDMU_CRN_FPU-3_Nom_Mnt_L	CDMUJ101	16	Thermistor	CR-N
CBPLMP09	28	CBPLM/CDMU_CRN_Baffle-3_Lateral_Nom_Mnt_H	CDMUJ101	34	Thermistor	CR-N
CBPLMP09	29	CBPLM/CDMU_CRN_Baffle-3_Lateral_Nom_Mnt_L	CDMUJ101	33	Thermistor	CR-N
CBPLMP09	34	CBPLM/CDMU_CRW_Groove-1_External_Nom_Mnt_H	CDMUJ081	32	Thermistor	CR-W
CBPLMP09	35	CBPLM/CDMU_CRW_Groove-1_External_Nom_Mnt_L	CDMUJ081	31	Thermistor	CR-W
CBPLMP09	36	CBPLM/CDMU_CRN_Groove-3_SC_HeatExc-1_Nom_Mnt_H	CDMUJ081	51	Thermistor	CR-N

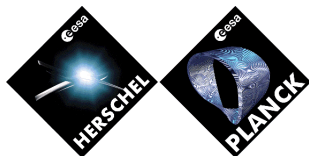


CBPLMP09	37	CBPLM/CDMU_CRN_Groove-3_SC_HeatExc-1_Nom_Mnt_L	CDMUJ081	50	Thermistor	CR-N
CBPLMP09	38	CBPLM/CDMU_CRN_Groove-3_WaveGuide-2_Nom_Mnt_H	CDMUJ081	68	Thermistor	CR-N
CBPLMP09	39	CBPLM/CDMU_CRN_Groove-3_WaveGuide-2_Nom_Mnt_L	CDMUJ081	67	Thermistor	CR-N
CBPLMP09	40	CBPLM/CDMU_CRN_FPU-1_Nom_Mnt_H	CDMUJ101	57	Thermistor	CR-N
CBPLMP09	41	CBPLM/CDMU_CRN_FPU-1_Nom_Mnt_L	CDMUJ101	56	Thermistor	CR-N
CBPLMP09	42	CBPLM/CDMU_CRN_Baffle-1_Front_Nom_Mnt_H	CDMUJ101	74	Thermistor	CR-N
CBPLMP09	43	CBPLM/CDMU_CRN_Baffle-1_Front_Nom_Mnt_L	CDMUJ101	73	Thermistor	CR-N
CBPLMP09	44	CBPLM/CDMU_CRN_Baffle-3_Rear_Nom_Mnt_H	CDMUJ101	15	Thermistor	CR-N
CBPLMP09	45	CBPLM/CDMU_CRN_Baffle-3_Rear_Nom_Mnt_L	CDMUJ101	14	Thermistor	CR-N
CBPLMP09	BKSH	Shield_for_CBPLM/CDMU_CRN_Baffle-1_Front_Nom_Mnt	CDMUJ101	BKSH	Shield	Shield
CBPLMP09	BKSH	Shield_for_CBPLM/CDMU_CRN_Baffle-2_Lateral_Nom_Mnt	CDMUJ101	BKSH	Shield	Shield
CBPLMP09	BKSH	Shield_for_CBPLM/CDMU_CRN_Baffle-3_Lateral_Nom_Mnt	CDMUJ101	BKSH	Shield	Shield
CBPLMP09	BKSH	Shield_for_CBPLM/CDMU_CRN_Baffle-3_Rear_Nom_Mnt	CDMUJ101	BKSH	Shield	Shield
CBPLMP09	BKSH	Shield_for_CBPLM/CDMU_CRN_FPU-1_Nom_Mnt	CDMUJ101	BKSH	Shield	Shield
CBPLMP09	BKSH	Shield_for_CBPLM/CDMU_CRN_FPU-2_Nom_Mnt	CDMUJ101	BKSH	Shield	Shield
CBPLMP09	BKSH	Shield_for_CBPLM/CDMU_CRN_FPU-3_Nom_Mnt	CDMUJ101	BKSH	Shield	Shield
CBPLMP09	BKSH	Shield_for_CBPLM/CDMU_CRN_Groove-3_OpticalCavity_Nom_Mnt	CDMUJ081	BKSH	Shield	Shield
CBPLMP09	BKSH	Shield_for_CBPLM/CDMU_CRN_Groove-3_SC_HeatExc-1_Nom_Mnt	CDMUJ081	BKSH	Shield	Shield
CBPLMP09	BKSH	Shield_for_CBPLM/CDMU_CRN_Groove-3_SC_HeatExc-2_Nom_Mnt	CDMUJ081	BKSH	Shield	Shield
CBPLMP09	BKSH	Shield_for_CBPLM/CDMU_CRN_Groove-3_WaveGuide-1_Nom_Mnt	CDMUJ081	BKSH	Shield	Shield
CBPLMP09	BKSH	Shield_for_CBPLM/CDMU_CRN_Groove-3_WaveGuide-2_Nom_Mnt	CDMUJ081	BKSH	Shield	Shield
CBPLMP09	BKSH	Shield_for_CBPLM/CDMU_CRN_JFET_Nom_Mnt	CDMUJ101	BKSH	Shield	Shield
CBPLMP09	BKSH	Shield_for_CBPLM/CDMU_CRW_Groove-1_External_Nom_Mnt	CDMUJ081	BKSH	Shield	Shield
CBPLMP09	BKSH	Shield_for_CBPLM/CDMU_CRW_Groove-1_SC_HeatExc-1_Nom_Mnt	CDMUJ081	BKSH	Shield	Shield
CBPLMP09	BKSH	Shield_for_CBPLM/CDMU_CRW_Groove-1_SC_HeatExc-2_Nom_Mnt	CDMUJ081	BKSH	Shield	Shield
CBPLMP09	BKSH	Shield_for_CBPLM/CDMU_CRW_Groove-2_SC_HeatExc-1_Nom_Mnt	CDMUJ081	BKSH	Shield	Shield
CBPLMP09	BKSH	Shield_for_CBPLM/CDMU_CRW_Groove-2_SC_HeatExc-2_Nom_Mnt	CDMUJ081	BKSH	Shield	Shield



4.2.10 PLM – SVM Connections CBPLMP10

SOURCE		SignalName	DESTINATION		Interface_of_Source	Interface of Destination
Connector Reference	Pin		Connected to Unit/Connector	Pin		
CBPLMP10	1	CBPLM/CDMU_CRW_Groove-1_SC_HeatExc-1_Red_Mnt_H	CDMUJ091	72	Thermistor	CR-W
CBPLMP10	2	CBPLM/CDMU_CRW_Groove-1_SC_HeatExc-1_Red_Mnt_L	CDMUJ091	71	Thermistor	CR-W
CBPLMP10	3	CBPLM/CDMU_CRW_Groove-2_SC_HeatExc-2_Red_Mnt_H	CDMUJ091	70	Thermistor	CR-W
CBPLMP10	4	CBPLM/CDMU_CRW_Groove-2_SC_HeatExc-2_Red_Mnt_L	CDMUJ091	69	Thermistor	CR-W
CBPLMP10	5	CBPLM/CDMU_CRW_Groove-3_SC_HeatExc-2_Red_Mnt_H	CDMUJ091	30	Thermistor	CR-W
CBPLMP10	6	CBPLM/CDMU_CRW_Groove-3_SC_HeatExc-2_Red_Mnt_L	CDMUJ091	29	Thermistor	CR-W
CBPLMP10	7	CBPLM/CDMU_CRW_Groove-3_OpticalCavity_Red_Mnt_H	CDMUJ091	49	Thermistor	CR-W
CBPLMP10	8	CBPLM/CDMU_CRW_Groove-3_OpticalCavity_Red_Mnt_L	CDMUJ091	48	Thermistor	CR-W
CBPLMP10	9	CBPLM/CDMU_CRN_FPU-2_Red_Mnt_H	CDMUJ111	36	Thermistor	CR-N
CBPLMP10	10	CBPLM/CDMU_CRN_FPU-2_Red_Mnt_L	CDMUJ111	35	Thermistor	CR-N
CBPLMP10	11	CBPLM/CDMU_CRW_Baffle-2_Lateral_Red_Mnt_H	CDMUJ111	55	Thermistor	CR-W
CBPLMP10	12	CBPLM/CDMU_CRW_Baffle-2_Lateral_Red_Mnt_L	CDMUJ111	54	Thermistor	CR-W
CBPLMP10	18	CBPLM/CDMU_CRW_Groove-1_SC_HeatExc-2_Red_Mnt_H	CDMUJ091	53	Thermistor	CR-W
CBPLMP10	19	CBPLM/CDMU_CRW_Groove-1_SC_HeatExc-2_Red_Mnt_L	CDMUJ091	52	Thermistor	CR-W
CBPLMP10	20	CBPLM/CDMU_CRW_Groove-2_SC_HeatExc-1_Red_Mnt_H	CDMUJ091	13	Thermistor	CR-W
CBPLMP10	21	CBPLM/CDMU_CRW_Groove-2_SC_HeatExc-1_Red_Mnt_L	CDMUJ091	12	Thermistor	CR-W
CBPLMP10	22	CBPLM/CDMU_CRW_Groove-3_WaveGuide-1_Red_Mnt_H	CDMUJ091	11	Thermistor	CR-W
CBPLMP10	23	CBPLM/CDMU_CRW_Groove-3_WaveGuide-1_Red_Mnt_L	CDMUJ091	10	Thermistor	CR-W
CBPLMP10	24	CBPLM/CDMU_CRN_JFET_Red_Mnt_H	CDMUJ111	76	Thermistor	CR-N
CBPLMP10	25	CBPLM/CDMU_CRN_JFET_Red_Mnt_L	CDMUJ111	75	Thermistor	CR-N
CBPLMP10	26	CBPLM/CDMU_CRW_FPU-3_Red_Mnt_H	CDMUJ111	17	Thermistor	CR-W
CBPLMP10	27	CBPLM/CDMU_CRW_FPU-3_Red_Mnt_L	CDMUJ111	16	Thermistor	CR-W
CBPLMP10	28	CBPLM/CDMU_CRW_Baffle-3_Lateral_Red_Mnt_H	CDMUJ111	34	Thermistor	CR-W
CBPLMP10	29	CBPLM/CDMU_CRW_Baffle-3_Lateral_Red_Mnt_L	CDMUJ111	33	Thermistor	CR-W
CBPLMP10	34	CBPLM/CDMU_CRW_Groove-1_External_Red_Mnt_H	CDMUJ091	32	Thermistor	CR-W
CBPLMP10	35	CBPLM/CDMU_CRW_Groove-1_External_Red_Mnt_L	CDMUJ091	31	Thermistor	CR-W
CBPLMP10	36	CBPLM/CDMU_CRN_Groove-3_SC_HeatExc-1_Red_Mnt_H	CDMUJ091	51	Thermistor	CR-N
CBPLMP10	37	CBPLM/CDMU_CRN_Groove-3_SC_HeatExc-1_Red_Mnt_L	CDMUJ091	50	Thermistor	CR-N



CBPLMP10	38	CBPLM/CDMU_CRW_Groove-3_WaveGuide-2_Red_Mnt_H	CDMUJ091	68	Thermistor	CR-W
CBPLMP10	39	CBPLM/CDMU_CRW_Groove-3_WaveGuide-2_Red_Mnt_L	CDMUJ091	67	Thermistor	CR-W
CBPLMP10	40	CBPLM/CDMU_CRW_FPU-1_Red_Mnt_H	CDMUJ111	57	Thermistor	CR-W
CBPLMP10	41	CBPLM/CDMU_CRW_FPU-1_Red_Mnt_L	CDMUJ111	56	Thermistor	CR-W
CBPLMP10	42	CBPLM/CDMU_CRW_Baffle-1_Front_Red_Mnt_H	CDMUJ111	73	Thermistor	CR-W
CBPLMP10	43	CBPLM/CDMU_CRW_Baffle-1_Front_Red_Mnt_L	CDMUJ111	72	Thermistor	CR-W
CBPLMP10	44	CBPLM/CDMU_CRW_Baffle-3_Rear_Red_Mnt_H	CDMUJ111	15	Thermistor	CR-W
CBPLMP10	45	CBPLM/CDMU_CRW_Baffle-3_Rear_Red_Mnt_L	CDMUJ111	14	Thermistor	CR-W
CBPLMP10	BKSH	Shield_for_CBPLM/CDMU_CRN_FPU-2_Red_Mnt	CDMUJ111	BKSH	Shield	Shield
CBPLMP10	BKSH	Shield_for_CBPLM/CDMU_CRN_Groove-3_SC_HeatExc-1_Red_Mnt	CDMUJ091	BKSH	Shield	Shield
CBPLMP10	BKSH	Shield_for_CBPLM/CDMU_CRN_JFET_Red_Mnt	CDMUJ111	BKSH	Shield	Shield
CBPLMP10	BKSH	Shield_for_CBPLM/CDMU_CRW_Baffle-1_Front_Red_Mnt	CDMUJ111	BKSH	Shield	Shield
CBPLMP10	BKSH	Shield_for_CBPLM/CDMU_CRW_Baffle-2_Lateral_Red_Mnt	CDMUJ111	BKSH	Shield	Shield
CBPLMP10	BKSH	Shield_for_CBPLM/CDMU_CRW_Baffle-3_Lateral_Red_Mnt	CDMUJ111	BKSH	Shield	Shield
CBPLMP10	BKSH	Shield_for_CBPLM/CDMU_CRW_Baffle-3_Rear_Red_Mnt	CDMUJ111	BKSH	Shield	Shield
CBPLMP10	BKSH	Shield_for_CBPLM/CDMU_CRW_FPU-1_Red_Mnt	CDMUJ111	BKSH	Shield	Shield
CBPLMP10	BKSH	Shield_for_CBPLM/CDMU_CRW_FPU-3_Red_Mnt	CDMUJ111	BKSH	Shield	Shield
CBPLMP10	BKSH	Shield_for_CBPLM/CDMU_CRW_Groove-1_External_Red_Mnt	CDMUJ091	BKSH	Shield	Shield
CBPLMP10	BKSH	Shield_for_CBPLM/CDMU_CRW_Groove-1_SC_HeatExc-1_Red_Mnt	CDMUJ091	BKSH	Shield	Shield
CBPLMP10	BKSH	Shield_for_CBPLM/CDMU_CRW_Groove-1_SC_HeatExc-2_Red_Mnt	CDMUJ091	BKSH	Shield	Shield
CBPLMP10	BKSH	Shield_for_CBPLM/CDMU_CRW_Groove-2_SC_HeatExc-1_Red_Mnt	CDMUJ091	BKSH	Shield	Shield
CBPLMP10	BKSH	Shield_for_CBPLM/CDMU_CRW_Groove-2_SC_HeatExc-2_Red_Mnt	CDMUJ091	BKSH	Shield	Shield
CBPLMP10	BKSH	Shield_for_CBPLM/CDMU_CRW_Groove-3_OpticalCavity_Red_Mnt	CDMUJ091	BKSH	Shield	Shield
CBPLMP10	BKSH	Shield_for_CBPLM/CDMU_CRW_Groove-3_SC_HeatExc-2_Red_Mnt	CDMUJ091	BKSH	Shield	Shield
CBPLMP10	BKSH	Shield_for_CBPLM/CDMU_CRW_Groove-3_WaveGuide-1_Red_Mnt	CDMUJ091	BKSH	Shield	Shield
CBPLMP10	BKSH	Shield_for_CBPLM/CDMU_CRW_Groove-3_WaveGuide-2_Red_Mnt	CDMUJ091	BKSH	Shield	Shield

4.3 Satellite Level HFI – SVM Connections

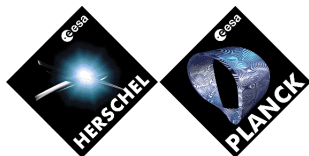
4.3.1 DPU Nominal

Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
PHBANJ01	2	DMS_1553A	CDMUJ023	1	1553_RT	1553_BC
PHBANJ01	2	DMS_1553A	CDMUJ073	1	1553_RT	1553_BC
PHBANJ01	6	DMS_1553A RTN	CDMUJ023	11	1553_RT	1553_BC
PHBANJ01	6	DMS_1553A RTN	CDMUJ073	11	1553_RT	1553_BC
PHBANJ01	BKSH	Shield_for_DMS_1553A	CDMUJ023	BKSH	Shield	Shield
PHBANJ01	BKSH	Shield_for_DMS_1553A	CDMUJ073	BKSH	Shield	Shield
PHBANJ02	2	DMS_1553B	CDMUJ024	1	1553_RT	1553_BC
PHBANJ02	2	DMS_1553B	CDMUJ074	1	1553_RT	1553_BC
PHBANJ02	6	DMS_1553B RTN	CDMUJ024	11	1553_RT	1553_BC
PHBANJ02	6	DMS_1553B RTN	CDMUJ074	11	1553_RT	1553_BC
PHBANJ02	BKSH	Shield_for_DMS_1553B	CDMUJ024	BKSH	Shield	Shield
PHBANJ02	BKSH	Shield_for_DMS_1553B	CDMUJ074	BKSH	Shield	Shield
PHBANJ05	2	PCDU/PHBA_N_DPU_Nom_Pwr	PCDUJ06	11	PWR_Input	PCDU_LCL
PHBANJ05	4	PCDU/PHBA_N_DPU_Nom_Pwr RTN	PCDUJ06	30	PWR_Input	PCDU_LCL
PHBANJ141	1	PCDU/PHBAN_REU_Belts10&11_Pwr	PCDUJ06	3	PWR_Input	PCDU_LCL
PHBANJ141	3	PCDU/PHBAN_REU_Belts8&9_Pwr	PCDUJ32	3	PWR_Input	PCDU_LCL
PHBANJ141	5	PCDU/PHBAN_REU_Belts6&7_Pwr	PCDUJ08	5	PWR_Input	PCDU_LCL
PHBANJ141	9	PCDU/PHBAN_REU_Belts10&11_Pwr RTN	PCDUJ06	22	PWR_Input	PCDU_LCL
PHBANJ141	11	PCDU/PHBAN_REU_Belts8&9_Pwr RTN	PCDUJ32	22	PWR_Input	PCDU_LCL
PHBANJ141	13	PCDU/PHBAN_REU_Belts6&7_Pwr RTN	PCDUJ08	24	PWR_Input	PCDU_LCL
PHBANJ15	2	CDMU/PHBAN_Sync	CDMUJ083	78	Sync_in	LOBT_Sync
PHBANJ15	6	CDMU/PHBAN_Sync	CDMUJ083	77	Sync_in	LOBT_Sync
PHBANJ15	BKSH	Shield_for_CDMU/PHBAN_Sync	CDMUJ083	BKSH	Shield	Shield



4.3.2 DPU Redundant

Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
PHBARJ01	2	DMS_1553A	CDMUJ023	1	1553_RT	1553_BC
PHBARJ01	2	DMS_1553A	CDMUJ073	1	1553_RT	1553_BC
PHBARJ01	6	DMS_1553A RTN	CDMUJ023	11	1553_RT	1553_BC
PHBARJ01	6	DMS_1553A RTN	CDMUJ073	11	1553_RT	1553_BC
PHBARJ01	BKSH	Shield_for_DMS_1553A	CDMUJ023	BKSH	Shield	Shield
PHBARJ01	BKSH	Shield_for_DMS_1553A	CDMUJ073	BKSH	Shield	Shield
PHBARJ02	2	DMS_1553B	CDMUJ024	1	1553_RT	1553_BC
PHBARJ02	2	DMS_1553B	CDMUJ074	1	1553_RT	1553_BC
PHBARJ02	6	DMS_1553B RTN	CDMUJ024	11	1553_RT	1553_BC
PHBARJ02	6	DMS_1553B RTN	CDMUJ074	11	1553_RT	1553_BC
PHBARJ02	BKSH	Shield_for_DMS_1553B	CDMUJ024	BKSH	Shield	Shield
PHBARJ02	BKSH	Shield_for_DMS_1553B	CDMUJ074	BKSH	Shield	Shield
PHBARJ05	2	PCDU/PHBA_R_DPU_Red_Pwr	PCDUJ32	11	PWR_Input	PCDU_LCL
PHBARJ05	4	PCDU/PHBA_R_DPU_Red_Pwr RTN	PCDUJ32	30	PWR_Input	PCDU_LCL
PHBARJ142	1	PCDU/PHBAR_REU_Belts4&5_Pwr	PCDUJ30	5	PWR_Input	PCDU_LCL
PHBARJ142	3	PCDU/PHBAR_REU_Belts2&3_Pwr	PCDUJ30	3	PWR_Input	PCDU_LCL
PHBARJ142	5	PCDU/PHBAR_REU_Belts0&1_Pwr	PCDUJ08	3	PWR_Input	PCDU_LCL
PHBARJ142	9	PCDU/PHBAR_REU_Belts4&5_Pwr RTN	PCDUJ30	24	PWR_Input	PCDU_LCL
PHBARJ142	11	PCDU/PHBAR_REU_Belts2&3_Pwr RTN	PCDUJ30	22	PWR_Input	PCDU_LCL
PHBARJ142	13	PCDU/PHBAR_REU_Belts0&1_Pwr RTN	PCDUJ08	22	PWR_Input	PCDU_LCL
PHBARJ15	2	CDMU/PHBAR_Sync	CDMUJ093	78	Sync_in	LOBT_Sync
PHBARJ15	6	CDMU/PHBAR_Sync	CDMUJ093	77	Sync_in	LOBT_Sync
PHBARJ15	BKSH	Shield_for_CDMU/PHBAR_Sync	CDMUJ093	BKSH	Shield	Shield



4.3.3 REU

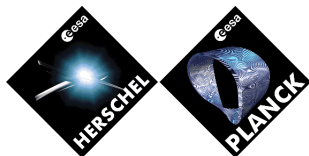
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PHCBC12J06	2	PCDU/PHCBC_Reu_Nom_Pwr	PCDUJ06	7	PWR_Input	PCDU_LCL
PHCBC12J06	4	PCDU/PHCBC_Reu_Nom_Pwr RTN	PCDUJ06	26	PWR_Input	PCDU_LCL
PHCBC13J06	2	PCDU/PHCBC_Reu_Red_Pwr	PCDUJ32	7	PWR_Input	PCDU_LCL
PHCBC13J06	4	PCDU/PHCBC_Reu_Red_Pwr RTN	PCDUJ32	26	PWR_Input	PCDU_LCL



4.3.4 4K CEU

Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
PHDCJ01A	9	PCDU/PHDC_4KCDE_Nom_Pwr Rtn	PCDUJ02	22	PWR_Input	PCDU_LCL
PHDCJ01A	2	PCDU/PHDC_4KCDE_Nom_Pwr	PCDUJ02	3	PWR_Input	PCDU_LCL
PHDCJ01A	15	4KC_DriveB_Nom4_Pwr Rtn	4KREGJ02	17	PWR_Input	4KREG
PHDCJ01A	7	4KC_DriveB_Nom4_Pwr	4KREGJ02	4	PWR_Input	4KREG
PHDCJ01A	14	4KC_DriveB_Nom3_Pwr Rtn	4KREGJ02	16	PWR_Input	4KREG
PHDCJ01A	6	4KC_DriveB_Nom3_Pwr	4KREGJ02	3	PWR_Input	4KREG
PHDCJ01A	13	4KC_DriveB_Nom2_Pwr Rtn	4KREGJ02	15	PWR_Input	4KREG
PHDCJ01A	5	4KC_DriveB_Nom2_Pwr	4KREGJ02	2	PWR_Input	4KREG
PHDCJ01A	12	4KC_DriveB_Nom1_Pwr Rtn	4KREGJ02	14	PWR_Input	4KREG
PHDCJ01A	4	4KC_DriveB_Nom1_Pwr	4KREGJ02	1	PWR_Input	4KREG

Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
PHDCJ01B	9	PCDU/PHDC_4KCDE_Red_Pwr Rtn	PCDUJ36	22	PWR_Input	PCDU_LCL
PHDCJ01B	2	PCDU/PHDC_4KCDE_Red_Pwr	PCDUJ36	3	PWR_Input	PCDU_LCL
PHDCJ01B	15	4KC_DriveB_Red4_Pwr Rtn	4KREGJ02	21	PWR_Input	4KREG
PHDCJ01B	7	4KC_DriveB_Red4_Pwr	4KREGJ02	8	PWR_Input	4KREG
PHDCJ01B	14	4KC_DriveB_Red3_Pwr Rtn	4KREGJ02	20	PWR_Input	4KREG
PHDCJ01B	6	4KC_DriveB_Red3_Pwr	4KREGJ02	7	PWR_Input	4KREG
PHDCJ01B	13	4KC_DriveB_Red2_Pwr Rtn	4KREGJ02	19	PWR_Input	4KREG
PHDCJ01B	5	4KC_DriveB_Red2_Pwr	4KREGJ02	6	PWR_Input	4KREG
PHDCJ01B	12	4KC_DriveB_Red1_Pwr Rtn	4KREGJ02	18	PWR_Input	4KREG
PHDCJ01B	4	4KC_DriveB_Red1_Pwr	4KREGJ02	5	PWR_Input	4KREG

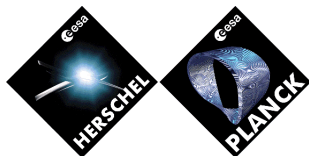


4.3.5 4K REG

Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
4KREGJ01	1	4KReg_Drive_Pwr1	PCDUJ08	13	PWR_Input	PCDU_LCL
4KREGJ01	14	4KReg_Drive_Pwr1_Rtn	PCDUJ08	32	PWR_Input	PCDU_LCL
4KREGJ01	2	4KReg_Drive_Pwr2	PCDUJ08	11	PWR_Input	PCDU_LCL
4KREGJ01	15	4KReg_Drive_Pwr2_Rtn	PCDUJ08	30	PWR_Input	PCDU_LCL
4KREGJ01	3	4KReg_Drive_Pwr3	PCDUJ08	13	PWR_Input	PCDU_LCL
4KREGJ01	16	4KReg_Drive_Pwr3_Rtn	PCDUJ08	32	PWR_Input	PCDU_LCL
4KREGJ01	4	4KReg_Drive_Pwr4	PCDUJ08	11	PWR_Input	PCDU_LCL
4KREGJ01	17	4KReg_Drive_Pwr4_Rtn	PCDUJ08	30	PWR_Input	PCDU_LCL
4KREGJ01	5	4KReg_Drive_Pwr5	PCDUJ30	13	PWR_Input	PCDU_LCL
4KREGJ01	18	4KReg_Drive_Pwr5_Rtn	PCDUJ30	32	PWR_Input	PCDU_LCL
4KREGJ01	6	4KReg_Drive_Pwr6	PCDUJ30	11	PWR_Input	PCDU_LCL
4KREGJ01	19	4KReg_Drive_Pwr6_Rtn	PCDUJ30	30	PWR_Input	PCDU_LCL
4KREGJ01	7	4KReg_Drive_Pwr7	PCDUJ30	13	PWR_Input	PCDU_LCL
4KREGJ01	20	4KReg_Drive_Pwr7_Rtn	PCDUJ30	32	PWR_Input	PCDU_LCL
4KREGJ01	8	4KReg_Drive_Pwr8	PCDUJ30	11	PWR_Input	PCDU_LCL
4KREGJ01	21	4KReg_Drive_Pwr8_Rtn	PCDUJ30	30	PWR_Input	PCDU_LCL



Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
4KREGJ02	1	4KC_DriveB_Nom1_Pwr	PHDCJ01A	4	PWR_Output	PWR_Input
4KREGJ02	14	4KC_DriveB_Nom1_Pwr Rtn	PHDCJ01A	12	PWR_Output	PWR_Input
4KREGJ02	2	4KC_DriveB_Nom2_Pwr	PHDCJ01A	5	PWR_Output	PWR_Input
4KREGJ02	15	4KC_DriveB_Nom2_Pwr Rtn	PHDCJ01A	13	PWR_Output	PWR_Input
4KREGJ02	3	4KC_DriveB_Nom3_Pwr	PHDCJ01A	6	PWR_Output	PWR_Input
4KREGJ02	16	4KC_DriveB_Nom3_Pwr Rtn	PHDCJ01A	14	PWR_Output	PWR_Input
4KREGJ02	4	4KC_DriveB_Nom4_Pwr	PHDCJ01A	7	PWR_Output	PWR_Input
4KREGJ02	17	4KC_DriveB_Nom4_Pwr Rtn	PHDCJ01A	15	PWR_Output	PWR_Input
4KREGJ02	5	4KC_DriveB_Red1_Pwr	PHDCJ01B	4	PWR_Output	PWR_Input
4KREGJ02	18	4KC_DriveB_Red1_Pwr Rtn	PHDCJ01B	12	PWR_Output	PWR_Input
4KREGJ02	6	4KC_DriveB_Red2_Pwr	PHDCJ01B	5	PWR_Output	PWR_Input
4KREGJ02	19	4KC_DriveB_Red2_Pwr Rtn	PHDCJ01B	13	PWR_Output	PWR_Input
4KREGJ02	7	4KC_DriveB_Red3_Pwr	PHDCJ01B	6	PWR_Output	PWR_Input
4KREGJ02	20	4KC_DriveB_Red3_Pwr Rtn	PHDCJ01B	14	PWR_Output	PWR_Input
4KREGJ02	8	4KC_DriveB_Red4_Pwr	PHDCJ01B	7	PWR_Output	PWR_Input
4KREGJ02	21	4KC_DriveB_Red4_Pwr Rtn	PHDCJ01B	15	PWR_Output	PWR_Input



4.3.6 DCCU

Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
PHECJ01	2	PCDU/PHEC_Pwr	PCDUJ28	3	PWR_Input	PCDU_LCL
PHECJ01	6	PCDU/PHEC_Pwr RTN	PCDUJ28	22	PWR_Input	PCDU_LCL



4.4 Satellite Level LFI – SVM Connections

4.4.1 DAE Power Box

Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
PLAEFJ01	2	PCDU/PLAEF_DAE_Nom_Pwr	PCDUJ08	7	PWR_Input	PCDU_LCL
PLAEFJ01	6	PCDU/PLAEF_DAE_Nom_Pwr RTN	PCDUJ08	26	PWR_Input	PCDU_LCL
PLAEFJ84	2	PCDU/PLAEF_DAE_Red_Pwr	PCDUJ30	7	PWR_Input	PCDU_LCL
PLAEFJ84	6	PCDU/PLAEF_DAE_Red_Pwr RTN	PCDUJ30	26	PWR_Input	PCDU_LCL

4.4.2 BEU

Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
PLBEUJ09	1	CDMU/PLBEU_LFI-DAE_Nom_Sync	CDMUJ083	59	Sync_in	LOBT_Sync
PLBEUJ09	6	CDMU/PLBEU_LFI-DAE_Nom_Sync	CDMUJ083	58	Sync_in	LOBT_Sync
PLBEUJ09	BKSH	Shield_for_CDMU/PLBEU_LFI-DAE_Nom_Sync	CDMUJ083	BKSH	Shield	Shield
PLBEUJ83	1	CDMU/PLBEU_LFI-DAE_Red_Sync	CDMUJ093	59	Sync_in	LOBT_Sync
PLBEUJ83	6	CDMU/PLBEU_LFI-DAE_Red_Sync	CDMUJ093	58	Sync_in	LOBT_Sync
PLBEUJ83	BKSH	Shield_for_CDMU/PLBEU_LFI-DAE_Red_Sync	CDMUJ093	BKSH	Shield	Shield



4.4.3 REBA Nominal

Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
PLRENJ11	2	PCDU/PLREN_Reba_Nom_Pwr	PCDUJ32	9	PWR_Input	PCDU_LCL
PLRENJ11	4	PCDU/PLREN_Reba_Nom_Pwr RTN	PCDUJ32	28	PWR_Input	PCDU_LCL
PLRENJ12	4	CDMU/PLREN_LFI-REBA_Nom_Sync	CDMUJ083	38	Sync_in	LOBT_Sync
PLRENJ12	5	CDMU/PLREN_LFI-REBA_Nom_Sync	CDMUJ083	37	Sync_in	LOBT_Sync
PLRENJ12	BKSH	Shield_for_CDMU/PLREN_LFI-REBA_Nom_Sync	CDMUJ083	BKSH	Shield	Shield
PLRENJ41	2	DMS_1553A	CDMUJ023	1	1553_RT	1553_BC
PLRENJ41	2	DMS_1553A	CDMUJ073	1	1553_RT	1553_BC
PLRENJ41	6	DMS_1553A RTN	CDMUJ023	11	1553_RT	1553_BC
PLRENJ41	6	DMS_1553A RTN	CDMUJ073	11	1553_RT	1553_BC
PLRENJ41	BKSH	Shield_for_DMS_1553A	CDMUJ023	BKSH	Shield	Shield
PLRENJ41	BKSH	Shield_for_DMS_1553A	CDMUJ073	BKSH	Shield	Shield
PLRENJ42	2	DMS_1553B	CDMUJ024	1	1553_RT	1553_BC
PLRENJ42	2	DMS_1553B	CDMUJ074	1	1553_RT	1553_BC
PLRENJ42	6	DMS_1553B RTN	CDMUJ024	11	1553_RT	1553_BC
PLRENJ42	6	DMS_1553B RTN	CDMUJ074	11	1553_RT	1553_BC
PLRENJ42	BKSH	Shield_for_DMS_1553B	CDMUJ024	BKSH	Shield	Shield
PLRENJ42	BKSH	Shield_for_DMS_1553B	CDMUJ074	BKSH	Shield	Shield

4.4.4 REBA Redundant

Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
PLRERJ11	2	PCDU/PLRER_Reba_Red_Pwr	PCDUJ06	9	PWR_Input	PCDU_LCL
PLRERJ11	4	PCDU/PLRER_Reba_Red_Pwr RTN	PCDUJ06	28	PWR_Input	PCDU_LCL
PLRERJ12	4	CDMU/PLRER_LFI-REBA_Red_Sync	CDMUJ093	38	Sync_in	LOBT_Sync
PLRERJ12	5	CDMU/PLRER_LFI-REBA_Red_Sync	CDMUJ093	37	Sync_in	LOBT_Sync
PLRERJ12	BKSH	Shield_for_CDMU/PLRER_LFI-REBA_Red_Sync	CDMUJ093	BKSH	Shield	Shield
PLRERJ41	2	DMS_1553A	CDMUJ023	1	1553_RT	1553_BC
PLRERJ41	2	DMS_1553A	CDMUJ073	1	1553_RT	1553_BC
PLRERJ41	6	DMS_1553A RTN	CDMUJ023	11	1553_RT	1553_BC
PLRERJ41	6	DMS_1553A RTN	CDMUJ073	11	1553_RT	1553_BC
PLRERJ41	BKSH	Shield_for_DMS_1553A	CDMUJ023	BKSH	Shield	Shield
PLRERJ41	BKSH	Shield_for_DMS_1553A	CDMUJ073	BKSH	Shield	Shield
PLRERJ42	2	DMS_1553B	CDMUJ024	1	1553_RT	1553_BC
PLRERJ42	2	DMS_1553B	CDMUJ074	1	1553_RT	1553_BC
PLRERJ42	6	DMS_1553B RTN	CDMUJ024	11	1553_RT	1553_BC
PLRERJ42	6	DMS_1553B RTN	CDMUJ074	11	1553_RT	1553_BC
PLRERJ42	BKSH	Shield_for_DMS_1553B	CDMUJ024	BKSH	Shield	Shield
PLRERJ42	BKSH	Shield_for_DMS_1553B	CDMUJ074	BKSH	Shield	Shield



4.4.5 SCE Nomina

SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
PSM4J21	2	DMS_1553B	CDMUJ023	1	1553_RT	1553_BC
PSM4J21	2	DMS_1553B	CDMUJ073	1	1553_RT	1553_BC
PSM4J21	5	CDMU/PSM4_Sync	CDMUJ083	19	Sync_in	LOBT_Sync
PSM4J21	6	DMS_1553B RTN	CDMUJ023	11	1553_RT	1553_BC
PSM4J21	6	DMS_1553B RTN	CDMUJ073	11	1553_RT	1553_BC
PSM4J21	9	CDMU/PSM4_Sync_Rtn	CDMUJ083	18	Sync_in	LOBT_Sync
PSM4J21	BKSH	Shield_for_DMS_1553B	CDMUJ023	BKSH	Shield	Shield
PSM4J21	BKSH	Shield_for_DMS_1553B	CDMUJ073	BKSH	Shield	Shield
PSM4J22	2	DMS_1553B	CDMUJ024	1	1553_RT	1553_BC
PSM4J22	2	DMS_1553B	CDMUJ074	1	1553_RT	1553_BC
PSM4J22	6	DMS_1553B RTN	CDMUJ024	11	1553_RT	1553_BC
PSM4J22	6	DMS_1553B RTN	CDMUJ074	11	1553_RT	1553_BC
PSM4J22	BKSH	Shield_for_DMS_1553B	CDMUJ024	BKSH	Shield	Shield
PSM4J22	BKSH	Shield_for_DMS_1553B	CDMUJ074	BKSH	Shield	Shield
PSM4J23	2	PCDU/PSM4_SC_Electronics_Nom_Pwr	PCDUJ30	9	PWR_Input	PCDU_LCL
PSM4J23	6	PCDU/PSM4_SC_Electronics_Nom_Pwr_Rtn	PCDUJ30	28	PWR_Input	PCDU_LCL
PSM4J24	A	PCDU/PSM4_SC_Compressor_Nom1_Pwr	PCDUJ10	7	PWR_Input	PCDU_LCL
PSM4J24	B	PCDU/PSM4_SC_Compressor_Nom2_Pwr	PCDUJ10	9	PWR_Input	PCDU_LCL
PSM4J24	C	PCDU/PSM4_SC_Compressor_Nom3_Pwr	PCDUJ10	11	PWR_Input	PCDU_LCL
PSM4J24	D	PCDU/PSM4_SC_Compressor_Nom1_Pwr_Rtn	PCDUJ10	26	PWR_Input	PCDU_LCL
PSM4J24	E	PCDU/PSM4_SC_Compressor_Nom4_Pwr	PCDUJ10	13	PWR_Input	PCDU_LCL
PSM4J24	F	PCDU/PSM4_SC_Compressor_Nom2_Pwr_Rtn	PCDUJ10	28	PWR_Input	PCDU_LCL
PSM4J24	G	PCDU/PSM4_SC_Compressor_Nom3_Pwr_Rtn	PCDUJ10	30	PWR_Input	PCDU_LCL
PSM4J24	H	PCDU/PSM4_SC_Compressor_Nom4_Pwr_Rtn	PCDUJ10	32	PWR_Input	PCDU_LCL



4.4.6 SCE Redundant

SOURCE		DESTINATION				
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
PSR4J21	2	DMS_1553B	CDMUJ023	1	1553_RT	1553_BC
PSR4J21	2	DMS_1553B	CDMUJ073	1	1553_RT	1553_BC
PSR4J21	5	CDMU/PSR4_Sync	CDMUJ083	19	Sync_in	LOBT_Sync
PSR4J21	6	DMS_1553B RTN	CDMUJ023	11	1553_RT	1553_BC
PSR4J21	6	DMS_1553B RTN	CDMUJ073	11	1553_RT	1553_BC
PSR4J21	9	CDMU/PSR4_Sync_Rtn	CDMUJ083	18	Sync_in	LOBT_Sync
PSR4J21	BKSH	Shield_for_DMS_1553B	CDMUJ023	BKSH	Shield	Shield
PSR4J21	BKSH	Shield_for_DMS_1553B	CDMUJ073	BKSH	Shield	Shield
PSR4J22	2	DMS_1553B	CDMUJ024	1	1553_RT	1553_BC
PSR4J22	2	DMS_1553B	CDMUJ074	1	1553_RT	1553_BC
PSR4J22	6	DMS_1553B RTN	CDMUJ024	11	1553_RT	1553_BC
PSR4J22	6	DMS_1553B RTN	CDMUJ074	11	1553_RT	1553_BC
PSR4J22	BKSH	Shield_for_DMS_1553B	CDMUJ024	BKSH	Shield	Shield
PSR4J22	BKSH	Shield_for_DMS_1553B	CDMUJ074	BKSH	Shield	Shield
PSR4J23	2	PCDU/PSR4_SC_Electronics_Nom_Pwr	PCDUJ08	9	PWR_Input	PCDU_LCL
PSR4J23	6	PCDU/PSR4_SC_Electronics_Nom_Pwr_Rtn	PCDUJ08	28	PWR_Input	PCDU_LCL
PSR4J24	A	PCDU/PSR4_SC_Compressor_Nom1_Pwr	PCDUJ28	7	PWR_Input	PCDU_LCL
PSR4J24	B	PCDU/PSR4_SC_Compressor_Nom2_Pwr	PCDUJ28	9	PWR_Input	PCDU_LCL
PSR4J24	C	PCDU/PSR4_SC_Compressor_Nom3_Pwr	PCDUJ28	11	PWR_Input	PCDU_LCL
PSR4J24	D	PCDU/PSR4_SC_Compressor_Nom1_Pwr_Rtn	PCDUJ28	26	PWR_Input	PCDU_LCL
PSR4J24	E	PCDU/PSR4_SC_Compressor_Nom4_Pwr	PCDUJ28	13	PWR_Input	PCDU_LCL
PSR4J24	F	PCDU/PSR4_SC_Compressor_Nom2_Pwr_Rtn	PCDUJ28	28	PWR_Input	PCDU_LCL
PSR4J24	G	PCDU/PSR4_SC_Compressor_Nom3_Pwr_Rtn	PCDUJ28	30	PWR_Input	PCDU_LCL
PSR4J24	H	PCDU/PSR4_SC_Compressor_Nom4_Pwr_Rtn	PCDUJ28	32	PWR_Input	PCDU_LCL

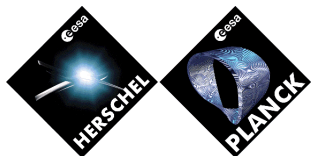
4.5 Miscellaneous Connections

4.5.1 Miscellaneous Connections, SREM

Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
SREMJ01	7	PCDU/SREM_Pwr RTN	PCDUJ06	24	PWR_Input	PCDU_LCL
SREMJ01	8	PCDU/SREM_Pwr	PCDUJ06	5	PWR_Input	PCDU_LCL
SREMJ02	3	SREM/CDMU_DS1_Data_Mnt	CDMUJ081	24	SREM_SBDL_Driver	SBDL_Receiver
SREMJ02	4	CDMU/SREM_ML1_Data_Cmd	CDMUJ081	3	SREM_SBDL_Receiver	SBDL_Driver
SREMJ02	5	SREM/CDMU_DS1_Address_Mnt	CDMUJ081	25	SREM_SBDL_Receiver	SBDL_Driver
SREMJ02	6	CDMU/SREM_ML1_Address_Cmd	CDMUJ081	6	SREM_SBDL_Receiver	SBDL_Driver
SREMJ02	8	CDMU/SREM_ML1_Clock_Cmd	CDMUJ081	5	SREM_SBDL_Receiver	SBDL_Driver
SREMJ02	10	SREM/CDMU_DS1_Data_Mnt	CDMUJ081	23	SREM_SBDL_Driver	SBDL_Receiver
SREMJ02	11	CDMU/SREM_ML1_Data_Cmd	CDMUJ081	2	SREM_SBDL_Receiver	SBDL_Driver
SREMJ02	13	SREM/CDMU_DS1_Address_Mnt	CDMUJ081	26	SREM_SBDL_Receiver	SBDL_Driver
SREMJ02	14	CDMU/SREM_ML1_Address_Cmd	CDMUJ081	7	SREM_SBDL_Receiver	SBDL_Driver
SREMJ02	15	CDMU/SREM_ML1_Clock_Cmd	CDMUJ081	4	SREM_SBDL_Receiver	SBDL_Driver
SREMJ02	BKSH	Shield_for_CDMU/SREM_ML1_Address_Cmd	CDMUJ081	BKSH	Shield	Shield
SREMJ02	BKSH	Shield_for_CDMU/SREM_ML1_Clock_Cmd	CDMUJ081	BKSH	Shield	Shield
SREMJ02	BKSH	Shield_for_CDMU/SREM_ML1_Data_Cmd	CDMUJ081	BKSH	Shield	Shield
SREMJ02	BKSH	Shield_for_SREM/CDMU_DS1_Address_Mnt	CDMUJ081	BKSH	Shield	Shield
SREMJ02	BKSH	Shield_for_SREM/CDMU_DS1_Data_Mnt	CDMUJ081	BKSH	Shield	Shield

4.5.2 Miscellaneous Connections, FOG1

SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
FOGJ02	1	ACMS_1553A	ACCJ024	5	1553_RT	1553_BC
FOGJ02	1	ACMS_1553A	ACCJ054	5	1553_RT	1553_BC
FOGJ02	2	FOG1/FOG1_JMP_J1_2-5_1553Address	FOG1J01	5	JUMPER	
FOGJ02	4	FOG1/FOG1_JMP_J1_4-5_1553Address	FOG1J01	5	JUMPER	
FOGJ02	5	FOG1/FOG1_JMP_J1_2-5_1553Address	FOG1J01	2	JUMPER	
FOGJ02	5	FOG1/FOG1_JMP_J1_4-5_1553Address	FOG1J01	4	JUMPER	
FOGJ02	7	ACMS_1553B	ACCJ023	5	1553_RT	1553_BC
FOGJ02	7	ACMS_1553B	ACCJ053	5	1553_RT	1553_BC
FOGJ02	9	ACMS_1553A RTN	ACCJ024	15	1553_RT	1553_BC
FOGJ02	9	ACMS_1553A RTN	ACCJ054	15	1553_RT	1553_BC
FOGJ02	10	FOG1/FOG1_JMP_J1_10-13_1553Address	FOG1J01	13	JUMPER	
FOGJ02	11	FOG1/FOG1_JMP_J1_11-13_1553Address	FOG1J01	13	JUMPER	
FOGJ02	12	FOG1/FOG1_JMP_J1_12-13_1553Address	FOG1J01	13	JUMPER	
FOGJ02	13	FOG1/FOG1_JMP_J1_10-13_1553Address	FOG1J01	10	JUMPER	
FOGJ02	13	FOG1/FOG1_JMP_J1_11-13_1553Address	FOG1J01	11	JUMPER	
FOGJ02	13	FOG1/FOG1_JMP_J1_12-13_1553Address	FOG1J01	12	JUMPER	
FOGJ02	14	ACMS_1553B RTN	ACCJ023	15	1553_RT	1553_BC
FOGJ02	14	ACMS_1553B RTN	ACCJ053	15	1553_RT	1553_BC
FOGJ02	BKSH	Shield_for_ACMS_1553A	ACCJ024	BKSH	Shield	Shield
FOGJ02	BKSH	Shield_for_ACMS_1553A	ACCJ054	BKSH	Shield	Shield
FOGJ02	BKSH	Shield_for_ACMS_1553B	ACCJ023	BKSH	Shield	Shield
FOGJ02	BKSH	Shield_for_ACMS_1553B	ACCJ053	BKSH	Shield	Shield



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DATE : 17-May-2004

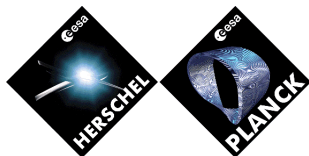
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FOGJ01	1	PCDU/FOG_Chan1_Pwr	PCDUJ34	3	PWR_Input	PCDU_LCL
FOGJ01	5	ACC/FOG_Chan1_ON_Cmd	ACCJ035	9	FOG_HP_Cmd_input	HP_Cmd
FOGJ01	6	ACC/FOG_Chan1_OFF_Cmd	ACCJ035	10	FOG_HP_Cmd_input	HP_Cmd
FOGJ01	9	PCDU/FOG_Chan1_Pwr RTN	PCDUJ34	22	PWR_Input	PCDU_LCL
FOGJ01	12	ACC/FOG_Chan1_ON_Cmd RTN	ACCJ035	8	FOG_HP_Cmd_input	HP_Cmd
FOGJ01	13	ACC/FOG_Chan1_OFF_Cmd RTN	ACCJ035	11	FOG_HP_Cmd_input	HP_Cmd
FOGJ01	BKSH	Shield_for_ACC/FOG_Chan1_OFF_Cmd	ACCJ035	BKSH	Shield	Shield
FOGJ01	BKSH	Shield_for_ACC/FOG_Chan1_ON_Cmd	ACCJ035	BKSH	Shield	Shield



4.5.3 Miscellaneous Connections, FOG2

SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
FOGJ06	1	ACMS_1553A	ACCJ024	5	1553_RT	1553_BC
FOGJ06	1	ACMS_1553A	ACCJ054	5	1553_RT	1553_BC
FOGJ06	4	FOG2/FOG2_JMP_J1_4-5_1553Address	FOG2J01	5	JUMPER	
FOGJ06	5	FOG2/FOG2_JMP_J1_4-5_1553Address	FOG2J01	4	JUMPER	
FOGJ06	7	ACMS_1553B	ACCJ023	5	1553_RT	1553_BC
FOGJ06	7	ACMS_1553B	ACCJ053	5	1553_RT	1553_BC
FOGJ06	9	ACMS_1553A RTN	ACCJ024	15	1553_RT	1553_BC
FOGJ06	9	ACMS_1553A RTN	ACCJ054	15	1553_RT	1553_BC
FOGJ06	10	FOG2/FOG2_JMP_J1_10-13_1553Address	FOG2J01	13	JUMPER	
FOGJ06	11	FOG2/FOG2_JMP_J1_11-13_1553Address	FOG2J01	13	JUMPER	
FOGJ06	13	FOG2/FOG2_JMP_J1_10-13_1553Address	FOG2J01	10	JUMPER	
FOGJ06	13	FOG2/FOG2_JMP_J1_11-13_1553Address	FOG2J01	11	JUMPER	
FOGJ06	14	ACMS_1553B RTN	ACCJ023	15	1553_RT	1553_BC
FOGJ06	14	ACMS_1553B RTN	ACCJ053	15	1553_RT	1553_BC
FOGJ06	BKSH	Shield_for_ACMS_1553A	ACCJ024	BKSH	Shield	Shield
FOGJ06	BKSH	Shield_for_ACMS_1553A	ACCJ054	BKSH	Shield	Shield
FOGJ06	BKSH	Shield_for_ACMS_1553B	ACCJ023	BKSH	Shield	Shield
FOGJ06	BKSH	Shield_for_ACMS_1553B	ACCJ053	BKSH	Shield	Shield



REFERENCE : H-P-3-ASPI-ID-0259

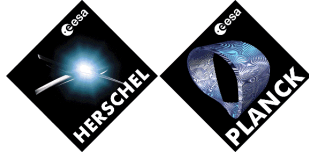
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FOGJ05	1	PCDU/FOG_Chan2_Pwr	PCDUJ04	3	PWR_Input	PCDU_LCL
FOGJ05	5	ACC/FOG_Chan2_ON_Cmd	ACCJ035	27	FOG_HP_Cmd_input	HP_Cmd
FOGJ05	6	ACC/FOG_Chan2_OFF_Cmd	ACCJ035	28	FOG_HP_Cmd_input	HP_Cmd
FOGJ05	9	PCDU/FOG_Chan2_Pwr RTN	PCDUJ04	22	PWR_Input	PCDU_LCL
FOGJ05	12	ACC/FOG_Chan2_ON_Cmd RTN	ACCJ035	26	FOG_HP_Cmd_input	HP_Cmd
FOGJ05	13	ACC/FOG_Chan2_OFF_Cmd RTN	ACCJ035	29	FOG_HP_Cmd_input	HP_Cmd
FOGJ05	BKSH	Shield_for_ACC/FOG_Chan2_OFF_Cmd	ACCJ035	BKSH	Shield	Shield
FOGJ05	BKSH	Shield_for_ACC/FOG_Chan2_ON_Cmd	ACCJ035	BKSH	Shield	Shield

4.5.4 Miscellaneous Connections, FOG3

SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
FOGJ10	1	ACMS_1553A	ACCJ024	5	1553_RT	1553_BC
FOGJ10	1	ACMS_1553A	ACCJ054	5	1553_RT	1553_BC
FOGJ10	2	FOG3/FOG3_JMP_J1_2-5_1553Address	FOG3J01	5	JUMPER	
FOGJ10	4	FOG3/FOG3_JMP_J1_4-5_1553Address	FOG3J01	5	JUMPER	
FOGJ10	5	FOG3/FOG3_JMP_J1_2-5_1553Address	FOG3J01	2	JUMPER	
FOGJ10	5	FOG3/FOG3_JMP_J1_4-5_1553Address	FOG3J01	4	JUMPER	
FOGJ10	7	ACMS_1553B	ACCJ023	5	1553_RT	1553_BC
FOGJ10	7	ACMS_1553B	ACCJ053	5	1553_RT	1553_BC
FOGJ10	9	ACMS_1553A RTN	ACCJ024	15	1553_RT	1553_BC
FOGJ10	9	ACMS_1553A RTN	ACCJ054	15	1553_RT	1553_BC
FOGJ10	11	FOG3/FOG3_JMP_J1_11-13_1553Address	FOG3J01	13	JUMPER	
FOGJ10	13	FOG3/FOG3_JMP_J1_11-13_1553Address	FOG3J01	11	JUMPER	
FOGJ10	14	ACMS_1553B RTN	ACCJ023	15	1553_RT	1553_BC
FOGJ10	14	ACMS_1553B RTN	ACCJ053	15	1553_RT	1553_BC
FOGJ10	BKSH	Shield_for_ACMS_1553A	ACCJ024	BKSH	Shield	Shield
FOGJ10	BKSH	Shield_for_ACMS_1553A	ACCJ054	BKSH	Shield	Shield
FOGJ10	BKSH	Shield_for_ACMS_1553B	ACCJ023	BKSH	Shield	Shield
FOGJ10	BKSH	Shield_for_ACMS_1553B	ACCJ053	BKSH	Shield	Shield



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FOGJ09	1	PCDU/FOG_Chan3_Pwr	PCDUJ36	5	PWR_Input	PCDU_LCL
FOGJ09	5	ACC/FOG_Chan3_ON_Cmd	ACCJ035	47	FOG_HP_Cmd_input	HP_Cmd
FOGJ09	6	ACC/FOG_Chan3_OFF_Cmd	ACCJ035	48	FOG_HP_Cmd_input	HP_Cmd
FOGJ09	9	PCDU/FOG_Chan3_Pwr RTN	PCDUJ36	24	PWR_Input	PCDU_LCL
FOGJ09	12	ACC/FOG_Chan3_ON_Cmd RTN	ACCJ035	46	FOG_HP_Cmd_input	HP_Cmd
FOGJ09	13	ACC/FOG_Chan3_OFF_Cmd RTN	ACCJ035	49	FOG_HP_Cmd_input	HP_Cmd
FOGJ09	BKSH	Shield_for_ACC/FOG_Chan3_OFF_Cmd	ACCJ035	BKSH	Shield	Shield
FOGJ09	BKSH	Shield_for_ACC/FOG_Chan3_ON_Cmd	ACCJ035	BKSH	Shield	Shield



4.5.5 Miscellaneous Connections, FOG4

SOURCE			DESTINATION			
Connector Reference	Pin	SignalName	Connected to Unit/Connector	Pin	Interface_of_Source	Interface of Destination
FOGJ14	1	ACMS_1553A	ACCJ024	5	1553_RT	1553_BC
FOGJ14	1	ACMS_1553A	ACCJ054	5	1553_RT	1553_BC
FOGJ14	4	FOG4/FOG4_JMP_J1_4-5_1553Address	FOG4J01	5	JUMPER	
FOGJ14	5	FOG4/FOG4_JMP_J1_4-5_1553Address	FOG4J01	4	JUMPER	
FOGJ14	7	ACMS_1553B	ACCJ023	5	1553_RT	1553_BC
FOGJ14	7	ACMS_1553B	ACCJ053	5	1553_RT	1553_BC
FOGJ14	9	ACMS_1553A RTN	ACCJ024	15	1553_RT	1553_BC
FOGJ14	9	ACMS_1553A RTN	ACCJ054	15	1553_RT	1553_BC
FOGJ14	11	FOG4/FOG4_JMP_J1_11-13_1553Address	FOG4J01	13	JUMPER	
FOGJ14	12	FOG4/FOG4_JMP_J1_12-13_1553Address	FOG4J01	13	JUMPER	
FOGJ14	13	FOG4/FOG4_JMP_J1_11-13_1553Address	FOG4J01	11	JUMPER	
FOGJ14	13	FOG4/FOG4_JMP_J1_12-13_1553Address	FOG4J01	12	JUMPER	
FOGJ14	14	ACMS_1553B RTN	ACCJ023	15	1553_RT	1553_BC
FOGJ14	14	ACMS_1553B RTN	ACCJ053	15	1553_RT	1553_BC
FOGJ14	BKSH	Shield_for_ACMS_1553A	ACCJ024	BKSH	Shield	Shield
FOGJ14	BKSH	Shield_for_ACMS_1553A	ACCJ054	BKSH	Shield	Shield
FOGJ14	BKSH	Shield_for_ACMS_1553B	ACCJ023	BKSH	Shield	Shield
FOGJ14	BKSH	Shield_for_ACMS_1553B	ACCJ053	BKSH	Shield	Shield
FOGJ13	1	PCDU/FOG_Chan4_Pwr	PCDUJ10	3	PWR_Input	PCDU_LCL
FOGJ13	5	ACC/FOG_Chan4_ON_Cmd	ACCJ035	67	FOG_HP_Cmd_input	HP_Cmd
FOGJ13	6	ACC/FOG_Chan4_OFF_Cmd	ACCJ035	68	FOG_HP_Cmd_input	HP_Cmd
FOGJ13	9	PCDU/FOG_Chan4_Pwr RTN	PCDUJ10	22	PWR_Input	PCDU_LCL
FOGJ13	12	ACC/FOG_Chan4_ON_Cmd RTN	ACCJ035	66	FOG_HP_Cmd_input	HP_Cmd
FOGJ13	13	ACC/FOG_Chan4_OFF_Cmd RTN	ACCJ035	69	FOG_HP_Cmd_input	HP_Cmd
FOGJ13	BKSH	Shield_for_ACC/FOG_Chan4_OFF_Cmd	ACCJ035	BKSH	Shield	Shield
FOGJ13	BKSH	Shield_for_ACC/FOG_Chan4_ON_Cmd	ACCJ035	BKSH	Shield	Shield

5. INTERFACE SIGNAL CHARACTERISTICS

This chapter gives the electrical interface characteristics per individual interface. for each interface type referenced in the tables in paragraph 4 . Where available actual performance figures are given , derived from unit test data, otherwise specified parameters are given.

5.1 Power Interfaces

5.1.1 BatteryPower & PCDU_Battery_Input

Battery/PCDU

The battery consists of 24 strings of 6 Li-ion 1.5Ah@cells.

Battery power output I/F.

The electrical characteristics of the power source and destination during the battery discharging shall be as listed in the following table:

<i>Parameter</i>	<i>SOURCE (BATTERY)</i>	<i>RECEIVER (PCDU BDR)</i>	<i>Remarks</i>
Circuit type	Isolated Li-ion battery	Grounded	
Transfer	DC coupled	DC coupled	
Battery capacity	36Ah±2.5%	N/A	
Voltage discharge threshold Maximum	16V±20mV	16±0.25V	
Voltage discharge threshold Minimum	14.75V	15±0.25V	
Voltage nominal range	17.1V to 25.5V	16 to 26V(2)	
Resistance	<30mOhm (1)	N/A	
Fault discharge tolerance	up to 120A peak for 1ms	N/A	
Discharge rate limiter	N/A	120A peak for 1ms	
Grounding isolation	≥100MΩ@500V	N/A	

- (1) The battery resistance is referred to the following conditions:
ambient temperature, nominal max voltage and up to 1000Hz frequency



Battery power Input I/F.

The electrical characteristics of the power source and destination during the battery charging shall be as listed in the following table:

<i>Parameter</i>	<i>SOURCE (PCDU BCR)</i>	<i>RECEIVER (BATTERY)</i>	<i>Remarks</i>
Circuit type	Grounded	Isolated Li-ion battery	
Transfer	DC coupled	DC coupled	
Battery capacity (C)	N/A	36Ah±2.5%	
Voltage E.o.C. nominal threshold	25.2+0.3/-0V	25.2V±2V	
Voltage E.o.C. maximum threshold	<26.4V	N/A	Only allowed once in the mission period due to degradation of BCR
Ripple	3.5Apeak (DC+150KHz)	N/A	
Resistance	N/A	<30mOhm (1)	
Max Charge rate	5A<I<7A	7.5A	
Fault charge voltage	(2)	N/A	
Fault condition tolerance	N/A	28V for 2s	
Grounding isolation	N/A	≥100MΩ@500V	

(1) The battery resistance is referred to the following conditions: ambient temperature, nominal max voltage and up to 1000Hz frequency

5.1.2 SA_Aux_PWR_in

SOLAR ARRAY/ PCDU

The association of solar arrays is the primary source of power for the satellite.
 The solar array consists of 30 sections which are connected to the SAR and 3 of them also to the BCR.
 Each section collects up to 5 strings of 21 cells. The SAR and the BCR sections are part of the PCDU.

Power source I/F

The electrical characteristics of each power source section and destination shall be as listed in the following table:

Parameter	SOURCE (SA)	RECEIVER (PCDU SAR/BCR)	Remarks
Circuit type	Isolated	Grounded	
Transfer	DC coupled	DC coupled	
Open circuit voltage	0 to 60V	N/A	
Current nominal range	0 to 3A	N/A	
Peak current	N/A	(2)	
Capacitance	≤2μF	N/A	
Resistance:			
- series	0.09Ω	(3)	
- parallel	203Ω		
Inductance	1 to 6μH	N/A	
Operating point	N/A	29.72V/3A (1)	
Blocking diodes (Y/N)	Y	N/A	

(1) The regulation point (A) is internal to the SAR downstream the diodes and the value is 28.14V±0.5%.

(2) THE RATE OF CHANGE OF VOLTAGE OF THE PCDU/SAR INTERFACE DESIGN SHALL ENSURE THAT THE PEAK SOLAR ARRAY SECTION CURRENT DOES NOT EXCEED 7.5 A (INCLUDING THE SAR ARRAY SECTION CURRENT) AND SHALL IN ANY CASE NOT EXCEED 17 A AFTER A FAILURE.

(3) Regulated to maintain the operating point.

5.1.3 PCDU_LCL

Electrical Characteristics To be provided

5.1.4 PCDU_Heater_pwr

Electrical Characteristics To be provided

5.1.5 FPU_Heater , Primary_Reflector_Heater & Secondary_Reflector_Heater

The following tables define the requirements for the heater lines (used for decontamination purposes) for the PLM.

Note that to minimise the thermal load on the PLM, these heaters are connected to the PLM connectors via AWG 24 brass harness.

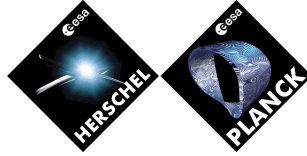
Items	Line definition	Type	Power (W)	Comments
Primary Reflector	PR_N1	N	60	switch on/off simultaneously to inject the maximum power needed by the PR decontamination phase (120W TBC).
	PR_N2	N	60	
	PR_R	R	60	
Secondary Reflector	SR_N	N	60	
	SR_R	R	60	
Focal Plane Unit -HFI	FPUHFI_N	N	60	
	FPUHFI_R	R	60	
Focal Plane Unit -HFI	FPULFI_N	N	60	
	FPULFI_R	R	60	
Total nominal line		5		
Total redundant line		4		

5.1.6 PT_Input_Pwr & PT_Pwr

PT 28V Power I/F.

The electrical characteristics of the power source and destination shall be as listed in the following table:

Parameter	SOURCE (ACC)	RECEIVER (PT)	Remarks
Voltage range	25 to 32Vdc	24 to 32Vdc	
Impedance (DC to 100KHz)	N/A	2KΩ	
Fault voltage	32.85Vdc up to 10ms	N/A	
Overvoltage tolerance	N/A	32.85Vdc up to 10ms	
U/V switch-off	N/A	<24Vdc	
Maximum Current	N/A	17mA	
Nominal current	N/A	10mA	
Protection type	Current Limiter	Current Limiter	
- limitation current	100mA	22mA	
- limitation duration	Infinite	Infinite	
Drop-out duration	N/A	10ms	
Input power isolation	N/A	>1 GΩ	



5.1.7 PWR_Input

Electrical Characteristics to be provided for :

4KC_DriveB_Nom1_Pwr
4KC_DriveB_Nom2_Pwr
4KC_DriveB_Red1_Pwr
4KC_DriveB_Red2_Pwr
4KCDE_Nom_Pwr
4KCDE_Red_Pwr
DAE_Nom_Pwr
DAE_Red_Pwr
DPU_Nom_Pwr
DPU_Red_Pwr
FOG_Chan1_Pwr
FOG_Chan2_Pwr
FOG_Chan3_Pwr
FOG_Chan4_Pwr
PHEC_Pwr (DDCU)
Reba_Nom_Pwr
Reba_Red_Pwr
REU_Belts0&1_Pwr
REU_Belts10&11_Pwr
REU_Belts2&3_Pwr
REU_Belts4&5_Pwr
REU_Belts6&7_Pwr
REU_Belts8&9_Pwr
Reu_Nom_Pwr
Reu_Red_Pwr
SCE Nom
SCE Red
SREM_Pwr

5.1.8 THR_1N_Heater & THR_20N_Heater

ACC to 20.0N Thruster Cat Bed Heater power I/F.

Parameter	SOURCE (ACC)	RECEIVER (Thruster)	Remarks
Output Voltage range	25Vdc to 32Vdc	N/A	
Heater Voltage range	N/A	24Vdc to 32Vdc	
Quiescent Voltage	-0.5 to +2Vdc	N/A	
Fault Voltage Emission	≤+37Vdc	N/A	
Heater resistance	N/A	257Ω±13Ω (2)	
Current capability	>250mA	N/A	
Short circuit tolerance	(1)	N/A	

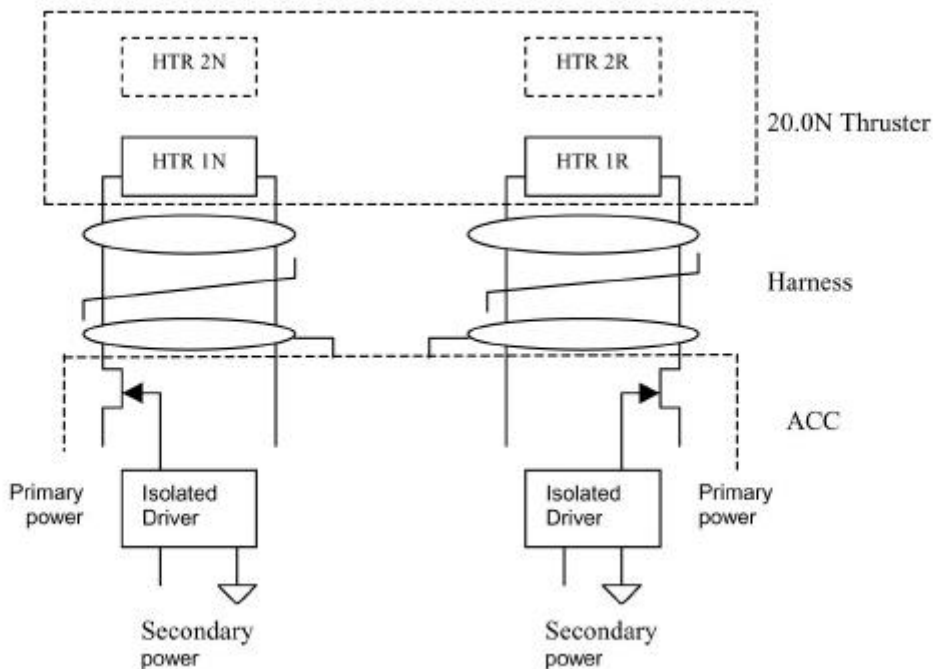
(1) the value corresponds to the upstream LCL III current limitation specified in GDEL-600

(2) The values refer to the ambient temperature 20±3°C.

The electrical characteristics of the interconnecting harness shall be as listed in the following table:

Line characteristics	Bonded Ends	CC capacitance (pF)	CS capacitance (pF/m)	Resistance (Ω)
TSP type	source side	<900	<1800	<1 (1)

(1) referred to the line included return.



5.2 MIL-1553 Interfaces

5.3 TM Interfaces

5.3.1 +28V_TM

The +28V TM interface is the Mainbus voltage protected by a 100kΩ resistor, therefore the characteristics of the TM will be the same as for the Mainbus, i.e Mainbus Voltage = 28.14V

5.3.2 AAD_Mnt & AAD_Output

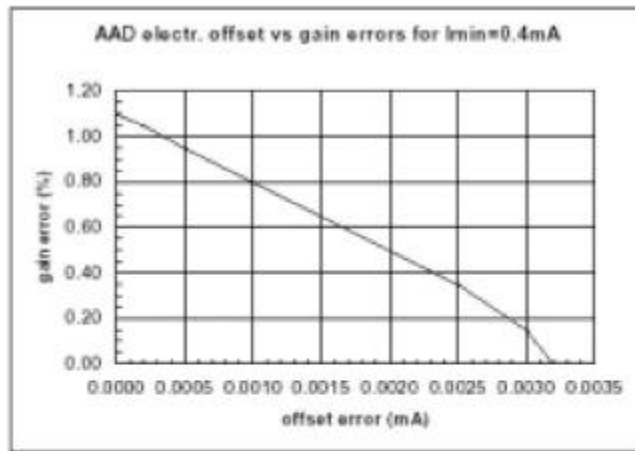
AAD Current Analog signal I/F:

Parameter	SOURCE (AAD)	RECEIVER (ACC AAU)	Remarks
Circuit type	Isolated	Single ended	
Transfer	DC coupled	DC coupled	
Current range	0 to 0.57mA (7)	0 to 0.60mA	
LPF Frequency (-3dB)	N/A	30Hz <f<150Hz 1 st order Low Pass Filter	
Capacitance	<17nF (5)	N/A	
Impedance	>10MΩ Current source parallel resistance 1Ω series resistance (5)	<1Ω @100Hz <100mΩ@10Hz (4)	
Absolute accuracy	N/A	(1)(2)(3)	
Input bias voltage	N/A	±50μV	
Fault Voltage Emission	N/A	±16.5V in series with ≥1.5KΩ	
Overvoltage tolerance	±16.5V with an overvoltage source series impedance ≥1.5KΩ	N/A	
Sampling Frequency	N/A	250Hz	
Dead time delay	N/A	<7ms (6)	Includes multiplexing, AD conversion, processing, internal data transfer

(1) receiver accuracy at A/D Converter output

(2) The combination of offset and gain errors in the receiver, with a temperature in the receiver electronics stable in a band of 20K and I_{min}=0.4mA, shall not exceed the combination of offset and scale factor figures listed below:

offset (mA)	gain(%)
0.0000	1.10
0.0002	1.05
0.0005	0.95
0.0010	0.80
0.0015	0.65
0.0020	0.50
0.0025	0.35
0.0030	0.15
0.0032	0.00

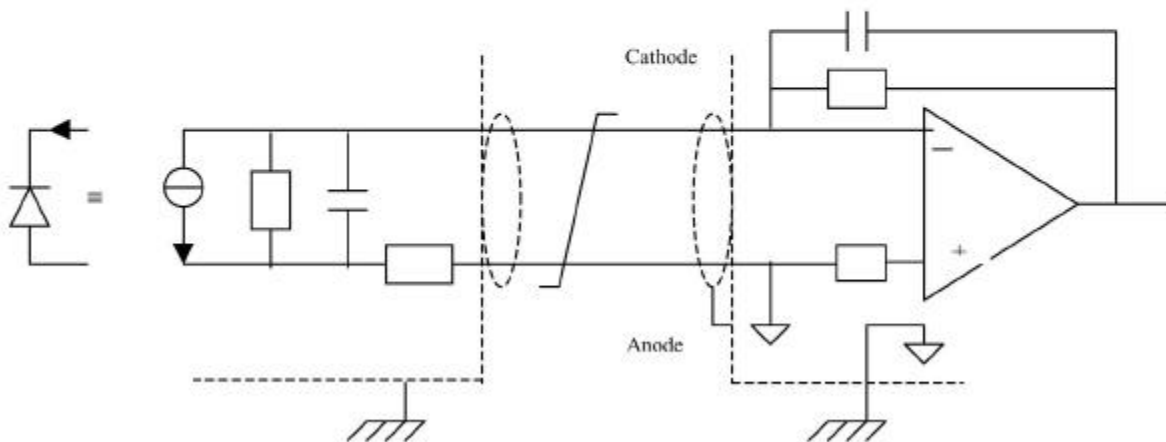


- (3) Since calibration in flight is not possible, the manufacturer of the receiver shall factory calibrate the interface so that initial offsets and scale factor are accurately known and only changes in orbit due to temperature variations, aging and radiation are to be considered. The temperature stability of the electronics may be assumed 20K.
- (4) Input impedance shall be maintained sufficiently low to ensure that the photo-cell works in a linear range.
- (5) The values refer to the 1st order model.
- (6) The overall delay, including the LP-filter delay and dead time delay, shall be <0.1s.
- (7) The dark current (leakage) is <50µA.

The electrical characteristics of the interconnecting harness shall be as listed in the following table:

Line characteristics	Bonded Ends	CC capacitance (pF)	CS capacitance (nF)	Resistance (Ω)
TPS type	receiver side	<900	<1.8	<2.5 (1)

(1) referred to the line included return.



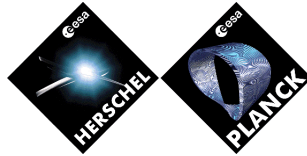
5.3.3 CR-N & CR-W

The Requirements are :

Description	Quantity	number (ID)	Type	Nominal T	Range	Accuracy (1)
Groove 1						
SC heat exchanger 1	1	1	N	135K	40-350K	±2.5 K
	1	101	R	135K	40-350K	±2.5 K
SC heat exchanger 2	1	2	N	135K	40-350K	±2.5 K
	1	102	R	135K	40-350K	±2.5 K
+ Z External edge	1	3	N	113K	40-350K	±2.5 K
	1	103	R	113K	40-350K	±2.5 K
Sub total1	6					
Groove 2						
SC heat exchanger 1	1	4	N	80K	40-350K	±2.5 K
	1	104	R	80K	40-350K	±2.5 K
SC heat exchanger 2	1	5	N	80K	40-350K	±2.5 K
	1	105	R	80K	40-350K	±2.5 K
Sub total 2	4					
Groove 3						
SC heat exchanger 1	1	6	N	50K	40-70K	± 1K
	1	106	R	50K	40-70K	± 1K
SC heat exchanger 2	1	7	N	50K	40-70K	± 1K
	1	107	R	50K	40-350K	±2.5 K
Wave Guides Interface1	1	8	N	50K	40-70K	± 1K
	1	108	R	50K	40-350K	±2.5 K
Wave Guides Interface2	1	9	N	50K	40-70K	± 1K
	1	109	R	50K	40-350K	±2.5 K
Optical cavity	1	10	N	50K	40-70K	± 1K
	1	110	R	50K	40-350K	±2.5 K
Sub total 3	10					
PR panel						
JFET interfaces	1	11	N	40K	40-70K	± 1K
	1	111	R	40K	40-70K	± 1K
FPU interface 1(lower beam)	1	12	N	40K	40-70K	± 1K
	1	112	R	40K	40-350K	±2.5 K
FPU interface2 (+Y)	1	13	N	40K	40-70K	± 1K
	1	113	R	40K	40-70K	± 1K
FPU interface3 (-Y)	1	14	N	40K	40-70K	± 1K
	1	114	R	40K	40-350K	±2.5 K
Sub total 4	8					

PPLM temperature sensors definition (1/2)

(1) : Accuracy for the electronic only, worst case with out calibration. Calibration is requested to improve the value.



Description	Quantity	number (ID)	Type	Nominal T	Range	Accuracy(1)
Baffle						
Baffle 1 (Front face)	1	15	N	40K	40-70K	± 1K
	1	115	R	40K	40-350K	± 2.5K
Baffle 2 (Lateral face medium)	1	16	N	40K	40-70K	± 1K
	1	116	R	40K	40-350K	± 2.5K
Baffle 3 (Lateral face upper position)	1	17	N	40K	40-70K	± 1K
	1	117	R	40K	40-350K	± 2.5K
Baffle 3 (Rear face)	1	18	N	40K	40-70K	± 1K
	1	118	R	40K	40-350K	± 2.5K
Sub total 5	8					
Reflectors						
PR1	1	19	N	40K	40-70K	± 1K
	1	119	R	40K	40-70K	± 1K
PR2	1	20	N	40K	40-70K	± 1K
	1	120	R	40K	40-70K	± 1K
SR1	1	21	N	40K	40-70K	± 1K
	1	121	R	40K	40-70K	± 1K
SR2	1	22	N	40K	40-70K	± 1K
	1	122	R	40K	40-70K	± 1K
Sub total 6	8					
Total of small range sensor	24					
Total of wide range sensor	20					
Total	44					

PPLM Temperature sensors definition (2/2)

(1) : Accuracy for the electronic only, worst case with out calibration. Calibration is requested to improve the value.

N. B. For most of the operational sensors the Nominal sensor is used to acquire a narrow temperature range with a high accuracy, and the Redundant sensor is used to acquire an extended temperature range with reduced accuracy. However there are exceptions to this general rule, particularly for the telescope reflectors, the Jfet, FPU interface 2, SC heat exchanger 1.

5.3.4 Thermistor and Therm_Mnt

Electrical Characteristics of implementation to be provided

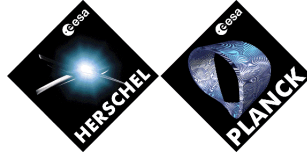
General requirements are :



	SOURCE	RECEIVER
CIRCUIT TYPE	Thermistor (Ex: Type Fenwall - GB 42)	Single ended Receiver
VOLTAGE AFTER THERMISTOR CONDITIONING (after resistor bridge)		0 V < U < 5.12 V (cod. range) -->LSB = 20 mV
IMPEDANCE Min, Max	TBD (Typical: 15 kΩ at 25°C)	depends on bridge resistances values
CAPACITANCE (differential)		< 1μF (at input) < 100 pF (after MUX)
THERMISTOR SELF POWER DISSIPATION < 0.05 W	Th. current < $\sqrt{\frac{0.05 W}{R_{min}}}$ Rmin = thermistor min. resist.	Voltage applied to thermistor < $\sqrt{0.05 W * R_{min}}$
FAULT VOLTAGE - Emission	short circuit to mechanical ground	-16 V < U < +16 V (in series with ≥ 1.5 kΩ)
Tolerance	-16 V < U < +16 V (in series with ≥ 1.5 kΩ)	short circuit to mechanical ground (1)
LINE CHARACTERISTICS Type Total Core to core Capacitance Total Core to shield Capacitance	Twisted shielded pair - AWG26 < 900 pF < 1.8 nF	

THERM/EGSE_Therm1_Tank
 THERM/EGSE_Therm2_Tank
 THERM/EGSE_Therm3_Tank
 Baffle-1_Front_Nom
 Baffle-1_Front_Red
 Baffle-2_Lateral_Nom
 Baffle-2_Lateral_Red
 Baffle-3_Lateral_Nom
 Baffle-3_Lateral_Red
 Baffle-3_Rear_Nom
 Baffle-3_Rear_Red
 FPU-1_Nom
 FPU-1_Red
 FPU-2_Nom
 FPU-2_Red
 FPU-3_Nom
 FPU-3_Red
 FPU-HFI_Decon_TC1
 FPU-HFI_Decon_TC2
 FPU-HFI_Decon_TC3
 FPU-LFI_Decon_TC1
 FPU-LFI_Decon_TC2

FPU-LFI_Decon_TC3
 Groove-1_External_Nom
 Groove-1_External_Red
 Groove-1_SC_HeatExc-1_Nom
 Groove-1_SC_HeatExc-1_Red
 Groove-1_SC_HeatExc-2_Nom
 Groove-1_SC_HeatExc-2_Red
 Groove-2_SC_HeatExc-1_Nom
 Groove-2_SC_HeatExc-1_Red
 Groove-2_SC_HeatExc-2_Nom
 Groove-2_SC_HeatExc-2_Red
 Groove-3_OpticalCavity_Nom
 Groove-3_OpticalCavity_Red
 Groove-3_SC_HeatExc-1_Nom
 Groove-3_SC_HeatExc-1_Red
 Groove-3_SC_HeatExc-2_Nom
 Groove-3_SC_HeatExc-2_Red
 Groove-3_WaveGuide-1_Nom
 Groove-3_WaveGuide-1_Red
 Groove-3_WaveGuide-2_Nom
 Groove-3_WaveGuide-2_Red
 JFET_Nom



JFET_Red
 Pri-Refl_Decon_TC1
 Pri-Refl_Decon_TC2
 Pri-Refl_Decon_TC3
 Reflector_PR1_Nom
 Reflector_PR1_Red
 Reflector_PR2_Nom
 Reflector_PR2_Red

Reflector_SR1_Nom
 Reflector_SR1_Red
 Reflector_SR2_Nom
 Reflector_SR2_Red
 Sec-Refl_Decon_TC1
 Sec-Refl_Decon_TC2
 Sec-Refl_Decon_TC3

Component number Fenwal 526-31-BS09-153
Nominal resistance (at 25°C) 15KΩ±1%

Temp [°C]	Nom R [Ohm]	Min R [Ohm]	Max R [Ohm]
10	28230	27945	28515
11	27020	26746	27294
12	25860	25597	26123
13	24770	24517	25023
14	23720	23477	23963
15	22730	22497	22963
16	21780	21556	22004
17	20870	20655	21085
18	20010	19804	20216
19	19190	18992	19388
20	18410	18220	18600
21	17660	17479	17841
22	16950	16777	17123
23	16280	16114	16446
24	15620	15461	15779
25	15000	14848	15152
26	14410	14263	14557
27	13840	13699	13981
28	13310	13174	13446
29	12790	12659	12921
30	12300	12174	12426
31	11820	11699	11941
32	11370	11253	11487
33	10940	10828	11052
34	10530	10422	10638
35	10130	10026	10234
36	9756	9656	9856
37	9393	9296	9490
38	9047	8954	9140
39	8715	8625	8805
40	8397	8310	8484

5.3.5 CRS_Meas & CRS_Output

CRS Angular rate analog signal I/F.

Parameter	SOURCE (CRS)	RECEIVER (ACC AAU)	Remarks
Circuit type	single ended	Differential	
Transfer	DC coupled	DC coupled	
Voltage range channel X	-10.2V to +10.2V (2)	-8.5V to +8.5V (3)	
Voltage range precision channels Y, Z	-10.2V to +10.2V (2)	-2V to +2V	
CMV	N/A	± 2.5V	
CMRR (up to 10KHz)	N/A	60dB up to 10KHz falling 20dBdecade up to 1MHz	
Common mode impedance	N/A	>10MΩ	
LPF Frequency (-3dB)	N/A	30Hz <f< 150Hz 1 st order Low Pass Filter	
Impedance	100Ω±1%	>20KΩ (5)	
Capacitance	<50nF	N/A	
Absolute Accuracy channel X	±0.02% (±5mV)	f.s. ±0.1V in the range ± 8.5V (1)	
Absolute Accuracy precision channels Y,Z	±0.02% (±5mV)	f.s. ±16mV (1)(4)	
Fault Voltage Emission (max)	±16.5V	±16.5V in series with ≥1.5KΩ	
Overvoltage tolerance (min.)	±16.5V with an overvoltage source series impedance ≥1.5KΩ	±16.5V	both ON and OFF condition
Short circuit protection	Yes	N/A	
Sampling frequency	N/A	250Hz	
Dead time delay	N/A	<7ms	Includes Sampling, ADC, internal data transfer to ASW. No input filter included.

(1) receiver accuracy at A/D Converter output

The manufacturer of the receiver shall factory calibrate the interface so that initial offsets and scale factor are accurately known. Only changes in orbit due to temperature variations, aging and radiation are to be considered.

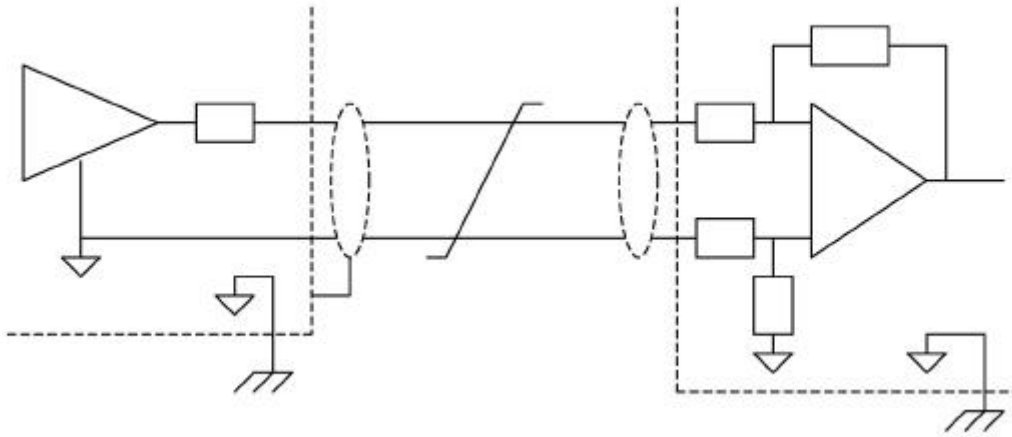
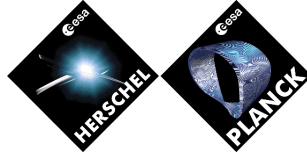
Condition between calibrations are: thermal stability 20K (pp variation), time 1 month

(2) maximum expected range is up to ±7V.

(3) The guaranteed conversion range shall be ±8.39V.

(4) ±8mV assuming a monthly in-orbit calibration..

(5) To be measured with an accuracy of 5% by manufacturer.



5.3.6 DR_Mnt

General Requirements :

	SOURCE	LOAD
CIRCUIT TYPE	Single ended driver	Relay
VOLTAGE - Active voltage - Passive voltage	22 V < U < 29 V (when load > 162 Ω) 0 V < U < 2 V	Max switching threshold: [a] < 22 V ; [b] < 16.6 V Min switch. threshold > 4V
CURRENT - Sinking current - Driving capability - Leakage - Short circuit	< 50 μA ≥ 180 mA (with U > 16.6V) ≥ 135 mA (with U > 22 V) ≤ 2.5 μA ≤ 400 mA (during pulse)	[b] Rmin > 92 Ω [a] Rmin > 162 Ω
SIGNAL SHAPE - Pulse length (Tp) - Rise time(10%-90%) - Fall time(10%-90%)	26 ms ± 2 ms 50 μs < Tr < 500 μs (*) 50 μs < Tf < 500 μs (*)	
CAPACITY	< 50 pF	< 300 pF
REPETITION RATE	Pulse rate < 4 Hz	
FAILURE CASE - Max. Emission - Tolerance	0 V < U < 33 V (current limited to 400 mA) The interface shall withstand a permanent short circuit condition between + & return outputs with no resulting damage -5 V < U < 48 V	+ & return short circuit -5 V < U < 48 V 0 V < U < 33 V (Fault source limited to 400 mA)
LINE CHARACTERISTICS Type Total Differential Capacitance Total Voltage Drop		Twisted-AWG26 < 600 pF < 240mV

5.3.7 PT_Meas & PT_Meas_Output

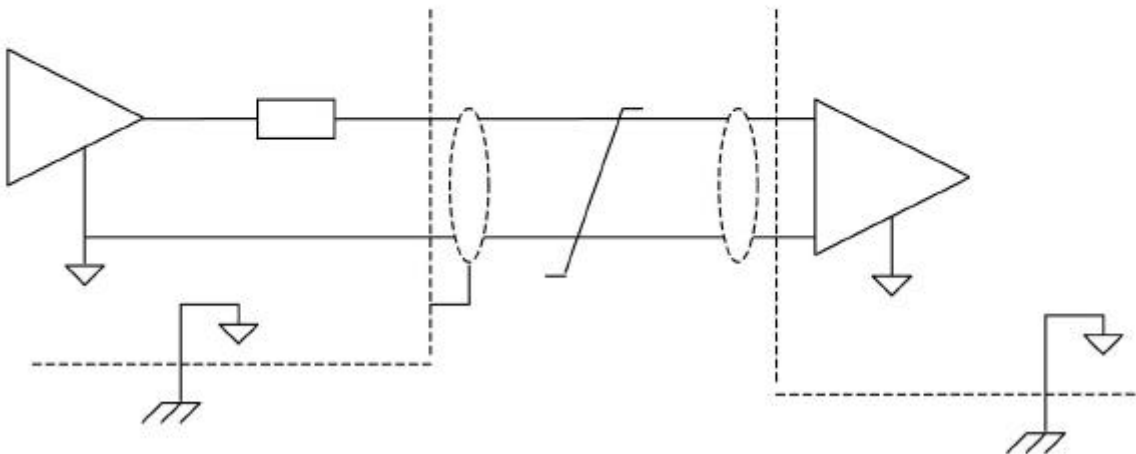
Parameter	SOURCE (PT)	RECEIVER (ACC)	Remarks
Circuit type	Single ended	Differential	
Transfer	DC coupled	DC coupled	
Output voltage	0 to +5V	0 to +5V	
CMV	N/A	± 2.5V	
CMRR (up to 10KHz)	N/A	≥ 60dB falling 20dB/dec. up 1MHz	
LPF Frequency (-3dB)	N/A	1KHz max	
Roll-off rate	N/A	6dB/Oct	
Output Ripple and Noise	20mVpp	N/A	
Capacitance	50pF	<10nF	
Impedance	<100Ω	≥500KΩ	
Accuracy	±0.7%f.s.	±2% f.s. (1)	
Fault Voltage Emission	-1V /+7.5V	±16.5V in series with ≥1.5KΩ	
Overvoltage tolerance	±16.5V with an overvoltage source series impedance ≥1.5KΩ	1V /+7.5V	
Current limitation	10mA	N/A	

(1) receiver accuracy at A/D Converter output

The electrical characteristics of the interconnecting harness shall be as listed in the following table:

Line characteristics	Bonded Ends	CC capacitance (pF)	CS capacitance (nF)	Resistance (Ω)
TPS type	source side	900	1.8	<2.5 (1)

(1) referred to the line included return.



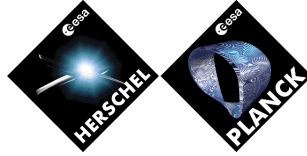
5.3.8 SAS_Mnt & SAS_Output

SAS Current Analog signal I/F :

Parameter	SOURCE (SAS)	RECEIVER (ACCAIU)	Remarks
Circuit type	Isolated	Single ended	
Transfer	DC coupled	DC coupled	
Current range	0 to 35mA (4)	0 to 40mA	
Input Bias Voltage	N/A	±4mV	
LPF Frequency (-3dB)	N/A	20Hz <f< 100Hz 1° order LPF	
Capacitance (parasitic)	<17nF (3)	N/A	
Impedance	>10MΩ Current source parallel resistance (3) 1Ω series resistance	<1Ω @100Hz <100mΩ @10Hz (2)	
Absolute Accuracy	N/A	±0.15mA @10mA (1)	
Fault Voltage Emission	N/A	±16.5V in series with ≥1.5KΩ	
Overvoltage tolerance	±16.5V with an overvoltage source series impedance ≥1.5KΩ	N/A	
Dead time delay	N/A	≤3ms	Includes AD conversion, processing, internal data transfer (5)
Sampling frequency	N/A	4Hz	

(1) This figure includes offset and gain errors. If offsets are negligible or can be trimmed to zero or calibrated this error corresponds to a scale factor error of 1.5%. Alternatively, if the scale factor would be accurately known by calibration and if stability over temperature and aging is assured, an offset of 0.15mA would still be acceptable. The manufacturer of the receiver has the freedom to select components and perform calibration/characterization in such a way that the absolute accuracy requirement of 0.15mA @ 10mA can be met. This accuracy is absolute and applies to each individual channel. It does not consider possible improvement by matching channels.

(2) Input impedance shall be maintained sufficiently low to ensure that the photo-cell works in a linear range.

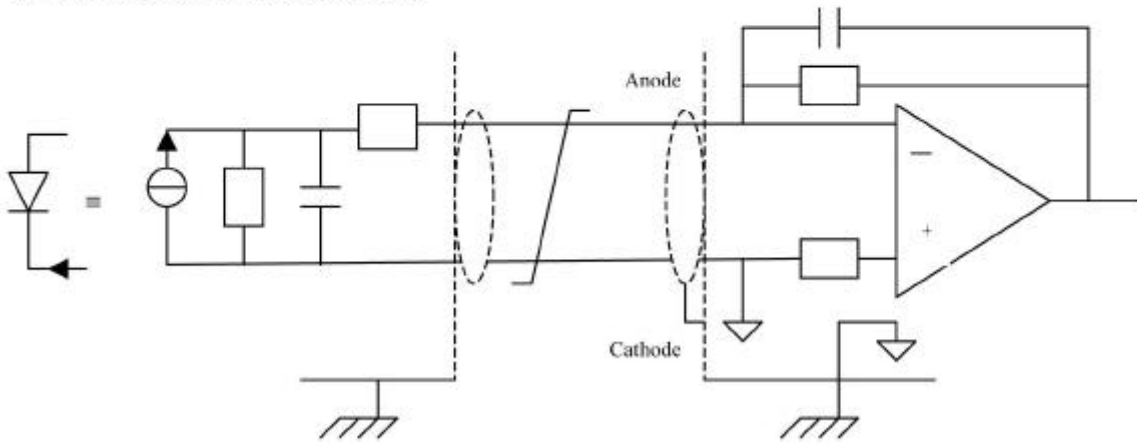


- (3) The values refer to the 1st order model.
- (4) The dark current (leakage) is <math>< 50\mu A</math>.
- (5) The total delay (including LP-filter and the dead time delay) shall not exceed 0.1s

The electrical characteristics of the interconnecting harness shall be as listed in the following table:

Line characteristics	Bonded Ends	CC capacitance (pF)	CS capacitance (nF)	Resistance (Ω)
TPS type	receiver side	<math>< 900</math>	<math>< 1.8</math>	<math>< 2.5 (1)</math>

(1) referred to the line included return.



5.3.9 SBDL_Driver & SBDL_Receiver

CDMU/XPND TC/TM Interfaces.

The CDMU exchanges TC/TM with XPND transponder.

- Each CDMU /XPND TM interface consists of the following signals:
 TM_data
 TM_clock
- Each XPND / CDMU TC interface consists of the following signals:
 TC_Act (Squelch Function: validation of data and clock signals)
 TC_Clk (TC bit clock signal)
 TC_In (TC incoming data bit Stream)
 RF Av_N (Rx carrier lock: transponder has locked on an RF carrier)

4.1.3.1 CDMU/XPND TM Interface.

The interface used for the TM_Data and the TM_Clock signals is a Special I/F which, although it is a Balanced Digital type, differs from the SBDL I/F already specified in H-P-I-ASPI-SP-0027 GDIR section 6.8.1.4.

The electrical characteristics of the Balanced Digital Link source and receiver shall be as listed in the following table:

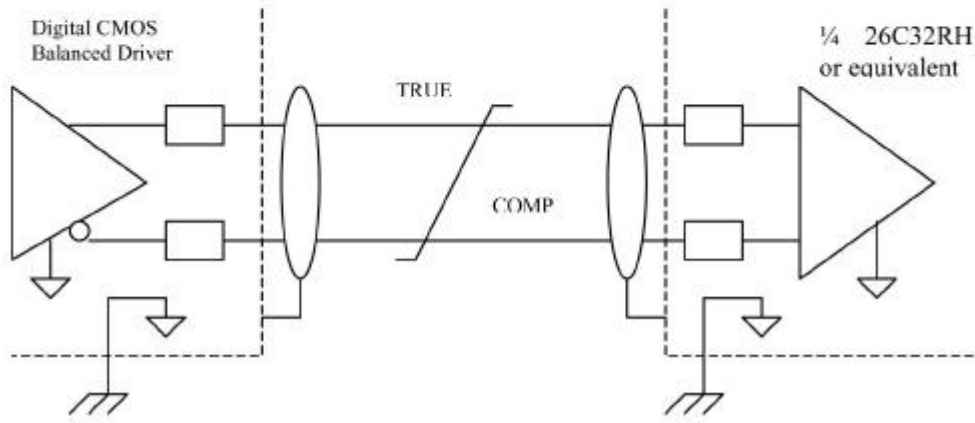
Parameter	SOURCE (CDMU)	RECEIVER (XPND)	Remarks
Low Level Output Voltage	$0\text{ V} \leq V_{OL} \leq 0.5\text{ V}$ (1)	N/A	
High Level Output Voltage	$2.5\text{ V} \leq V_{OH} \leq 5.5\text{ V}$ (1)	N/A	
Differential Voltage	$2.0\text{ V} \leq V_{OD} \leq 5.5\text{ V}$	Low : $V_{ID} \leq -1\text{V}$ High $V_{ID} \geq +1\text{V}$	
Rise and Fall Times	$10\text{ns} \leq t_r \leq 58\text{ns}$ (2) $10\text{ns} \leq t_f \leq 58\text{ns}$ (2)	N/A	
Differential Impedance	$107\Omega \leq Z_d \leq 140\Omega$ (4)	DC: $\geq 5\text{ kOhm}$ (4)	
Common Mode Voltage	N/A	$-2.5\text{ V} \leq V_{CM} \leq +2.5\text{ V}$	
Current Drive and Sink Capability	sufficient to comply with specified t_r & t_f (2)	N/A	
Short Circuit	short circuit proof; current limited to <150 mA	N/A	
Fault Voltage Emission	0V to +5V	0 V to +5V (3)	
Fault Voltage Tolerance	-12 V to +12 V (3)	-0.5 V to +7 V	(5)
Zero Reference	signal ground	N/A	

- (1) non-inverting (true) & inverting (comp) output with ref. to signal ground;
- (2) when loaded with differential 400pF (harness & user input capacitance). Max TM rate is 3441300 Symbol Rate = 290 ns Symbol period).
- (3) with an overvoltage source impedance of 1kOhm.
- (4) Both source and receiver I/Fs shall provide a serial termination on the true and complementary line.
- (5) additional protection components may be added as deemed necessary by the circuit designer.

The electrical characteristics of the interconnecting harness shall be as listed in the following table:

Line characteristics	Bonded Ends	Differential capacitance (pF)	Characteristic Impedance (Ω)
TSP (1)	both side	< 380 (2)	125

- (1) the TwinAx Gore GSC 6509,Rev.6 shall be used.
- (2) the value is referred to a cable length ~ 5m



TM_Clock/DataTiming.

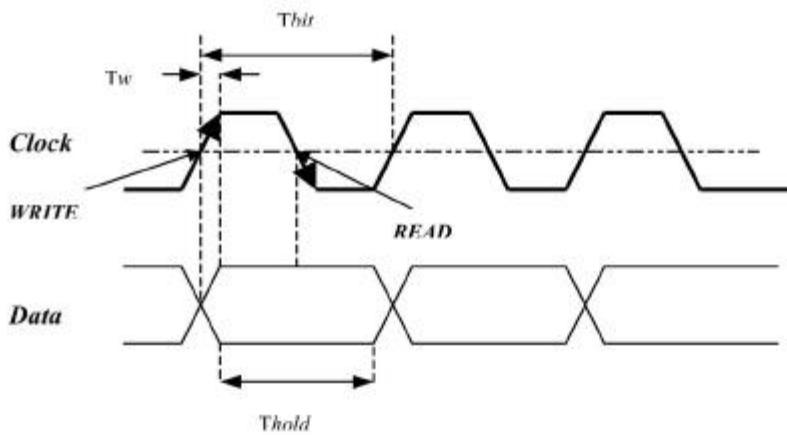
The serial digital data provided by the transmitter shall be a serial NRZ-L binary.

The transfer of the data shall be controlled by the edges of the clock signal.

The transmitter shall transfer (WRITE) the TM data on the data channel during the low level to high level voltage transition of clock signal (rising edge).

The receiver shall acquire (READ) the TM data during the high level to low level voltage transition of clock signal (falling edge).

The requested timing for the CLOCK and DATA lines is the following:



Data rate (bps)	LOW-1 500	LOW-2 5000	MEDIUM 150000	HIGH 1500000
Symbol rate (sps)	1147.1	11471	344130	3441300
Tbit (µs)	871.764	87.176	2.906	0.291
Tw (µs)	87.176	8.718	0.291	0.029
Thold (µs)	697.411	69.741	2.325	0.232

All the timing are referred at the zero level crossing of the differential signals. "Tw" is the max elapsed time from the rising edge of clock after that the new data shall have to be stable.

"Thold" shall be the guaranteed time duration of the stable data on the channel by the transmitter.

"Tbit" is the clock signal time period.

XPND/CDMU TC Interface.

The interface type used for these signals is a SBDL I/F and the electrical characteristics of the source and destination will be in accordance with H-P-1-ASPI-SP-0027 GDIR section 6.8.1.4.

TC_Act/Clock/DataTiming

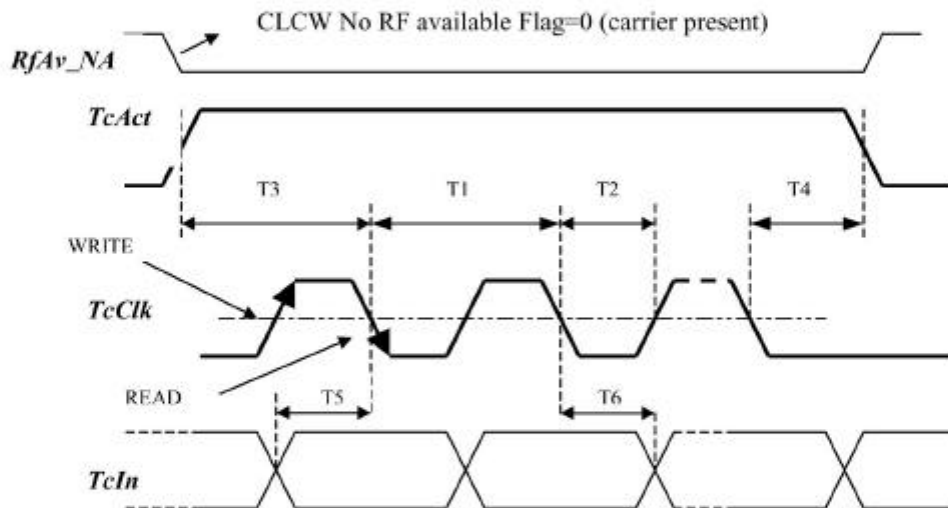
The serial digital data provided by the transmitter (XPND) shall be a serial NRZ-L binary.

The transfer of the data shall be controlled by the edges of the clock signal.

The transmitter (XPND) shall transfer (WRITE) the TC data on the data channel during the low level to high level voltage transition of clock signal (rising edge).

The receiver (CDMU) shall acquire (READ) the TM data during the high level to low level voltage transition of clock signal (falling edge).

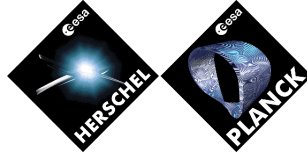
The requested timing for the ACT, CLOCK and DATA lines is the following:



<i>parameter</i>	<i>parameter description</i>	<i>Min.time</i>
T1	<i>TcClk</i> period	10µs
T2	<i>TcClk</i> width high, low	5µs
T3	<i>TcAct</i> high to first <i>TcClk</i> falling	4µs
T4	<i>TcClk</i> to <i>TcAct</i> low	4µs
T5	<i>TcIn</i> setup before <i>TcClk</i> falling	2µs
T6	<i>TcIn</i> hold after <i>TcClk</i> falling	4µs

All the timing are referred at the zero level crossing of the differential signals

- *TcAct*, (Squelch Function) a high level on this signal indicates that clock and data are valid
- *TcClk*, (TC Bit Clock) clock signal
- *TcIn*, (TC Data Bit Stream) incoming data
- *RFav_N*, (Rx Carrier Lock) indicates that the Transponder has locked on an RF carrier.



5.3.10 SREM_SBDL_Driver

Electrical characteristics to be provided

5.3.11 SREM_SBDL_Receiver

Electrical characteristics to be provided

5.4 TC Interfaces

5.4.1 CRS_Stim_input

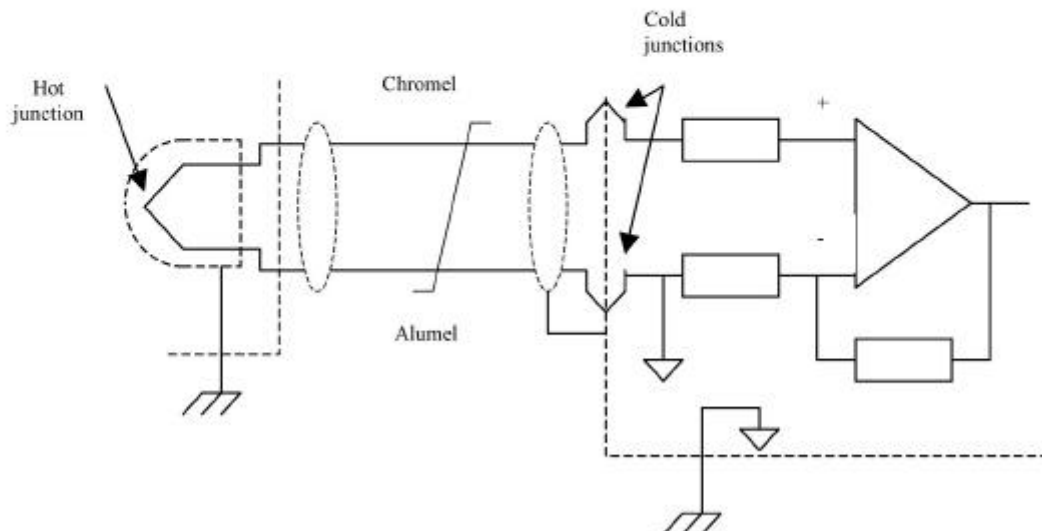
Electrical characteristics to be provided

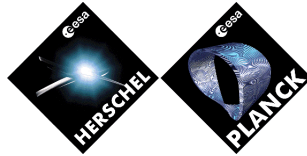
5.4.2 THR_TS & THR_TS_Output

1.0N Thermocouple signal I/F.

Parameter	SOURCE (Thruster)	RECEIVER (ACC)	Remarks
Circuit type	Isolated hot junction	Single ended	
Transfer	DC coupled	DC coupled	
Component type	type K	N/A	Thermocoax Part Number 2ABI10/TL/65/D50/2ABT35/2m
Resolution	see table below	N/A	i.e. 41µV/°C
Impedance	10-20Ω	N/A	
Full temperature range	-200 to 1370°C	N/A	
Operating temperature range	0 to 800°C	N/A	
LPF Frequency (-3dB)	N/A	>10Hz	
Relative Accuracy	N/A	±2°C (2)	
Cold junction compensation (Y/N)	N	Y (1)	
Fault Voltage emission	short circuit to ground	16.5V in series with ≥1.5KΩ	
Overvoltage tolerance	16.5V with an overvoltage source series impedance ≥1.5KΩ	N/A	

- (1) the cold junctions shall be represented by the ACC and Harness S/S mated contacts at I/F plane as default but the receiver manufacturer can place the junctions inside the ACC unit if the connections are extended by using Nickel-Chrom/Nickel contacts and internal wires.
- (2) receiver accuracy at A/D Converter output.





°C	0	10	20	30	40	50	60	70	80	90
-200	5.891	-6.035	-6.158	6.262	6.344	6.404	6.441	-6.458		
-100	3.553	-3.852	-4.138	4.410	4.669	4.912	5.141	5.354	5.550	5.730
0	0	-0.392	-0.777	-1.156	-1.527	-1.889	-2.243	-2.586	-2.920	-3.242
+0	0	0.397	0.798	1.203	1.611	2.022	2.436	2.850	3.266	3.681
100	4.095	4.508	4.919	5.327	5.733	6.137	6.539	6.939	7.388	7.737
200	8.137	8.537	8.938	9.341	9.745	10.151	10.560	10.969	11.381	11.793
300	12.207	12.623	13.039	13.456	13.874	14.292	14.712	15.132	15.552	15.974
400	16.396	16.818	17.241	17.664	18.088	18.513	18.938	19.363	19.788	20.214
500	20.640	21.066	21.493	21.919	22.346	22.772	23.198	23.624	24.050	24.476
600	24.902	25.327	25.751	26.176	26.599	27.022	27.445	27.867	28.288	28.709
700	29.128	29.547	29.965	30.383	30.799	31.214	31.629	32.042	32.455	32.866
800	33.277	33.686	34.095	34.502	34.909	35.314	35.718	36.121	36.524	36.925
900	37.325	37.724	38.122	38.519	38.915	39.310	39.703	40.096	40.488	40.879
1000	41.269	41.657	42.045	42.432	42.817	43.202	43.585	43.968	44.349	44.729
1100	45.108	45.486	45.863	46.238	46.612	46.985	47.356	47.726	48.095	48.462
1200	48.828	49.192	49.555	49.916	50.276	50.633	50.990	51.344	51.697	52.049
1300	52.398	52.747	53.093	53.439	53.782	54.125	54.466	54.807		

(voltages in [mV])

The cable wires and the plug connector contacts shall be Nickel-Chrom/Nickel.

5.4.3 HL_Cmd & HP_Cmd

	SOURCE	LOAD
CIRCUIT TYPE	Single ended driver	Opto-coupler
VOLTAGE - Active voltage - Passive voltage	22 V < U < 29 V (when load > 162 Ω) 0 V < U < 2 V	Max switching threshold: < 14 V Min switch. threshold: > 5 V
CURRENT - Sinking current - Driving capability - Leakage - Short circuit	< 50 μA ≥ 10 mA ≤ 2.5 μA ≤ 400 mA (during pulse)	Min. current > 2.5 mA
SIGNAL SHAPE - Pulse length - Rise time(10%-90%) - Fall time(10%-90%)	Tp = 26 ms ± 2 ms 50 μs < Tr < 500 μs (*) 50 μs < Tf < 500 μs (*)	<u>command validation:</u> time threshold ≥ 5 ms
CAPACITY	< 50 pF	< 300 pF
° REPETITION RATE	Pulse rate < 4Hz	
FAILURE CASE - Max. Emission - Tolerance	0 V < U < 33 V (Fault source imp. ≥ 1.5 kΩ) The interface shall withstand a permanent short circuit condition between + & return outputs with no resulting damage -5 V < U < 48 V	+ & return short circuit -5 V < U < 48 V » 0 V < U < 33 V (Fault source imp. ≥ 1.5 kΩ)
² LINE CHARACTERISTICS	Type Total Differential Capacitance Total Voltage Drop	Twisted-AWG26 < 600 pF < 240mV

5.4.4 FOG_HP_Cmd_input

Electrical characteristics to be provided

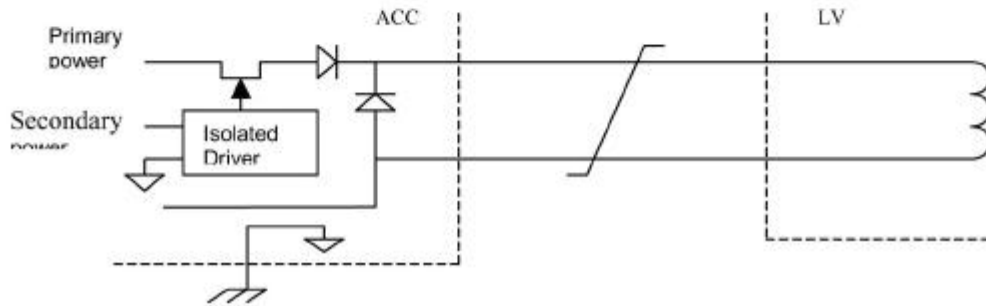
5.4.5 LV_Cmd_Input

LV 28V Pulse Command I/F.

The electrical characteristics of the command source and destination shall be as listed in the following table:

Parameter	SOURCE (ACC)	RECEIVER (LV)	Remarks
Circuit type	Single ended	Isolated coil	
Transfer	DC coupled	DC coupled	
Voltage active	25V to 32V	24V to 32V	
Voltage quiescent	-0.5V to +2V (4)	-0.5V to +2V (4)	
Leakage (quiescent state)	<1mA	N/A	
Current driver capability	≥ 1.2A (3)	N/A	
Pulse width	50ms to 100ms	N/A	
Rise/Fall time	t ≤ 100 μs	N/A	
Fault Voltage Emission	+32V	N/A	
Short circuit tolerance	(2)	N/A	
Oversvoltage tolerance	(1)	35V up 125ms	
Coil ohmic resistance	N/A	29 to 31 Ω	
Coil inductance	N/A	≤200 mH	
Switching time	N/A	<20ms	

- (1) The driver shall be equipped with opportune suppress device.
- (2) The value corresponds to the upstream LCL III current limitation.
- (3) Calculated at min. 26.21Ω @0°C.
- (4) With a max provided current 1mA by ACC.



5.4.6 LVA_DR_Output

The LV position status shall be a Standard I/F and the electrical characteristics of the monitor source and destination shall be in accordance with H-P-1-ASPI-SP-0027 GDIR section 6.8.2.4.

The harness shall realize the interconnection with a twisted three-wires (T3).

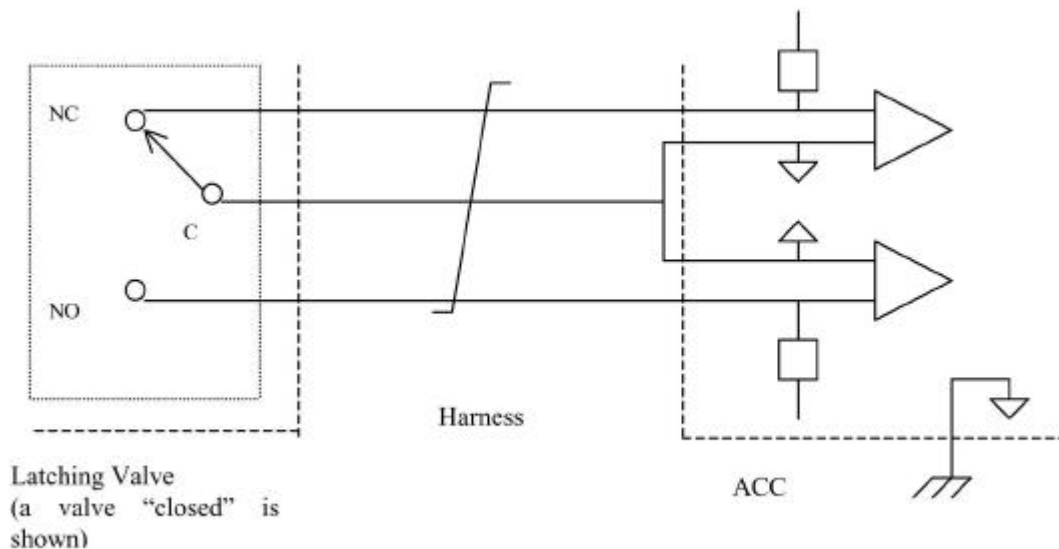
The harness manufacturer shall perform the splicing for the common return at ACC side providing two separated return signal contacts on the plug connector.

The LV μ SWITCH shall be equipped with flying leads which characteristics are listed below:

Line characteristics	CC capacitance (pF/m)	CS capacitance (pF/m)	Length (m)	Resistance (Ω /m)
Flying leads TP AWG22 MIL-W-81381/7	115	N/A	1	0.1 (1)

(1) referred to the line included return

Flying leads identification	Function
Green/White	Microswitch NC (Contact closed =LV OFF status=valve closed)
White	Microswitch NO (Contact closed =LV ON status=valve open)
Red/White	Microswitch Common



5.4.7 STR_HP_Input

Electrical characteristics to be provided

5.4.8 STR_Stim

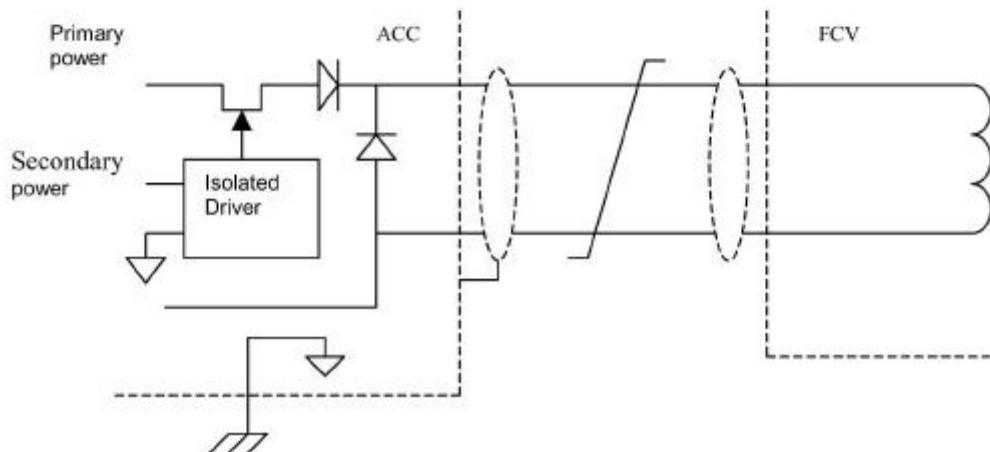
Electrical characteristics to be provided

5.4.9 THR1N_Vlv-Cmd and THR1_Vlv-Cmd_Input

1.0N FCV 28Vdc Pulse Command I/F.

Parameter	SOURCE (ACC)	RECEIVER (Thruster)	Remarks
Circuit type	single ended	Isolated coil	
Transfer	DC coupled	DC coupled	
Voltage active	25 to 29Vdc	24 to 29Vdc	
Voltage quiescent	-0.5 to +2Vdc (4)	-0.5 to +2Vdc (4)	
Current driver capability	>160mA	N/A	
Leakage (quiescent state)	<1mA	N/A	
Pulse width	30ms < t < 60s	N/A	
Rise/Fall time	≤100μs	N/A	
Fault Voltage Emission	+32Vdc (3)	N/A	
Short circuit tolerance	(2)	N/A	
Overvoltage tolerance	(1)	29Vdc (3)	
Coil ohmic resistance	N/A	191 to 212Ω	
Coil inductance (open) (closed)	N/A	3000mH 1700mH	
Switching time	N/A	<20ms	
Drop-out voltage	N/A	1Vdc	
Pull-in voltage	N/A	20Vdc	

- (1) the driver shall be equipped with opportune suppress device
- (2) the value corresponds to the upstream LCL III current limitation specified in GDEL-600.
- (3) 29Vdc for indefinite time, 32Vdc up to 5hrs, 35Vdc up to 4500s, 36Vdc up to 120s.
- (4) With a max provided current 1mA by ACC.

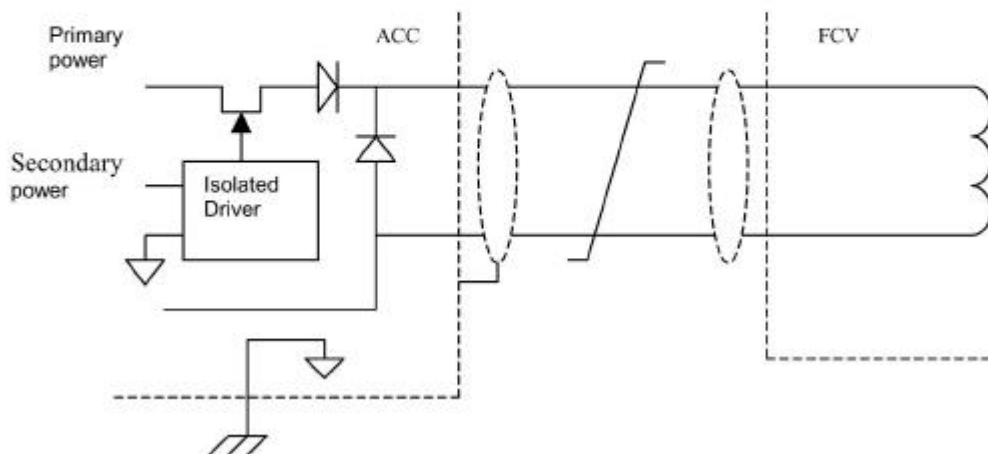


5.4.10 THRDV_Vlv-Cmd & THR20_Vlv-Cmd_Input

20.0N FCV 28Vdc Pulse Command I/F:

Parameter	SOURCE (ACC)	RECEIVER (Thruster)	Remarks
Circuit type	single ended	Isolated coil	
Transfer	DC coupled	DC coupled	
Voltage active	25Vdc to 32Vdc	24Vdc to 32Vdc	
Voltage quiescent	-0.5 to +2Vdc (3)	-0.5 to +2Vdc (3)	
Current driver capability	>500mA	N/A	
Leakage (quiescent state)	<1mA	N/A	
Pulse width (at 28Vdc)	30ms to 2250s	N/A	
Rise/Fall time	≤100μs	N/A	
Fault Voltage Emission	+32Vdc	N/A	
Short circuit tolerance	(2)	N/A	
Overvoltage tolerance	(1)	35Vdc up to TBDms	
Coil ohmic resistance	N/A	59 to 61Ω	
Coil inductance	N/A	540mH	
Switching time	N/A	<20ms	
Drop-out voltage	N/A	1Vdc	
Pull-in voltage	N/A	20Vdc	

- (1) the driver shall be equipped with oportune suppress device
- (2) the value corresponds to the upstream LCL III current limitation specified in GDEL-600.
- (3) with a max provided current 1mA by ACC.



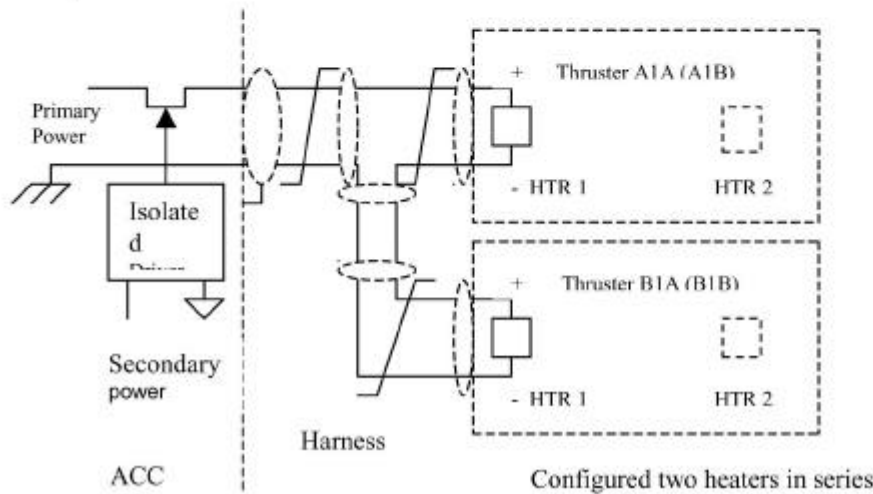
5.4.11 THR1N_Htr-Cmd

1.0N Heater 28Vdc power I/F.

Parameter	SOURCE (ACC)	RECEIVER (Thruster)	Remarks
Heater line Voltage range	25Vdc to 32Vdc	24Vdc to 32Vdc	
Heater Voltage range	N/A	12V to 21Vdc	
Quiescent voltage	-0.5 Vdc to +2Vdc	N/A	
Fault Voltage emission	≤+42Vdc	N/A	
Heater resistance	N/A	nom. 80Ω±5%@20°C min. 79.87Ω±5%@4°C	
Heater line input resistance	N/A	nom. 160Ω±5%@20°C min. 159.74Ω±5%@4°C	configured two heaters in series (2)
Current capability	>200mA	N/A	
Short circuit tolerance	(1)	N/A	

(1) the value corresponds to the upstream LCL III current limitation specified in GDEL-600

(2) cross-coupled heaters of two different thrusters in the same branch.



5.4.12 THRDV_Htr-Cmd

Electrical characteristics to be provided

5.4.13 LOBT_Sync

The Sync line uses the SDBL interfaces :

The electrical characteristics of the Balanced Digital Link source and receiver shall be as listed in the following table:

Parameter	SOURCE (CDMU)	RECEIVER	Remarks
Low Level Output Voltage	$0\text{ V} \leq V_{OL} \leq 0.5\text{ V}$ (1)	N/A	
High Level Output Voltage	$2.5\text{ V} \leq V_{OH} \leq 5.5\text{ V}$ (1)	N/A	
Differential Voltage	$2.0\text{ V} \leq V_{OD} \leq 5.5\text{ V}$	Low : $V_{ID} \leq -1\text{V}$ High $V_{ID} \geq +1\text{V}$	
Rise and Fall Times	$10\text{ns} \leq t_r \leq 58\text{ns}$ (2) $10\text{ns} \leq t_f \leq 58\text{ns}$ (2)	N/A	
Differential Impedance	$107\Omega \leq Z_d \leq 140\Omega$ (4)	DC: $\geq 5\text{ k}\Omega$ (4)	
Common Mode Voltage	N/A	$-2.5\text{ V} \leq V_{CM} \leq +2.5\text{ V}$	
Current Drive and Sink Capability	sufficient to comply with specified t_r & t_f (2)	N/A	
Short Circuit	short circuit proof; current limited to <150 mA	N/A	
Fault Voltage Emission	0V to +5V	0 V to +5V (3)	
Fault Voltage Tolerance	-12 V to +12 V (3)	-0.5 V to +7 V	(5)
Zero Reference	signal ground	N/A	

- (1) non-inverting (true) & inverting (comp) output with ref. to signal ground;
- (2) when loaded with differential 400pF (harness & user input capacitance). Max TM rate is 3441300 Symbol Rate = 290 ns Symbol period).
- (3) with an overvoltage source impedance of 1kOhm.
- (4) Both source and receiver I/Fs shall provide a serial termination on the true and complementary line.
- (5) additional protection components may be added as deemed necessary by the circuit designer.

5.4.14 Sync_in

Electrical characteristics to be provided for :

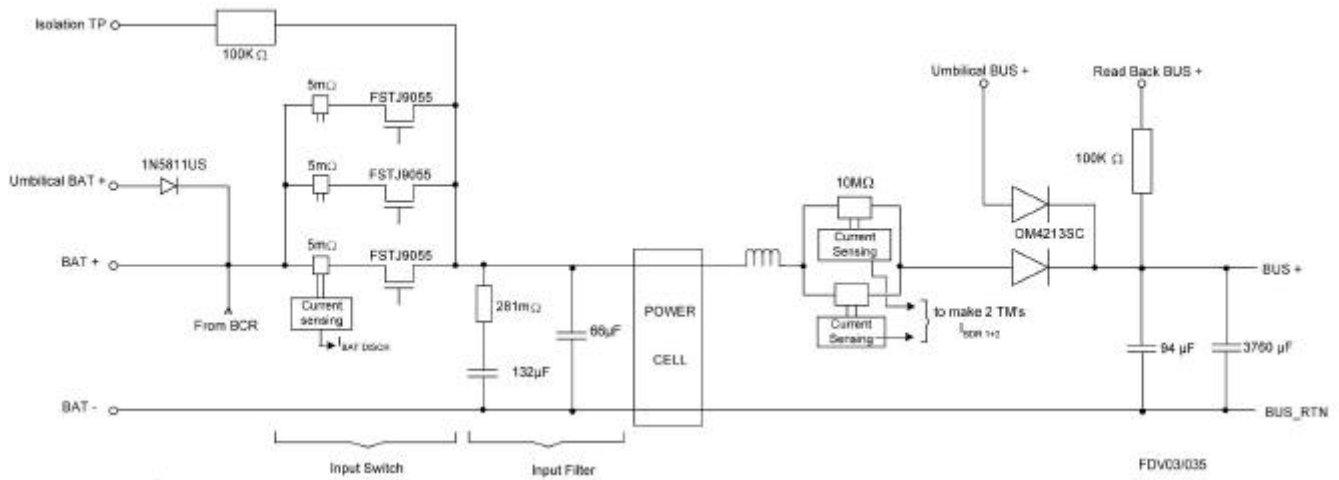
HFI_DPU_Nom
HFI_DPU_Red
LFI_BEU
LFI_REBA_Nom
LFI_REBA_Red
SCE Nom
SCE Red

6. INTERFACE CIRCUIT DIAGRAMS

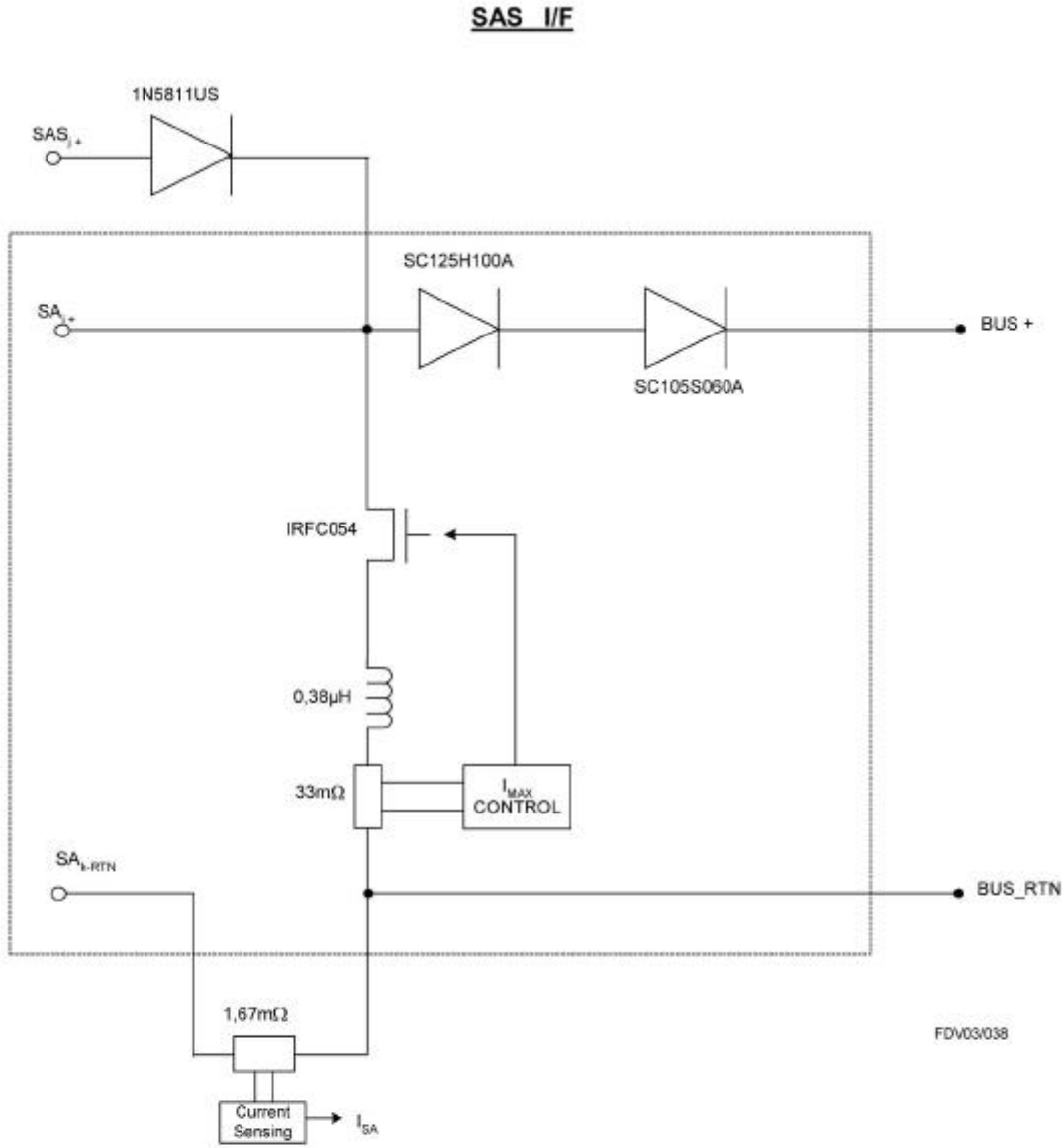
This chapter gives the electrical interface circuits per individual interface. for each interface type referenced in the tables in paragraph 4.

6.1 Power Interfaces

6.1.1 PCDU_Battery_Input



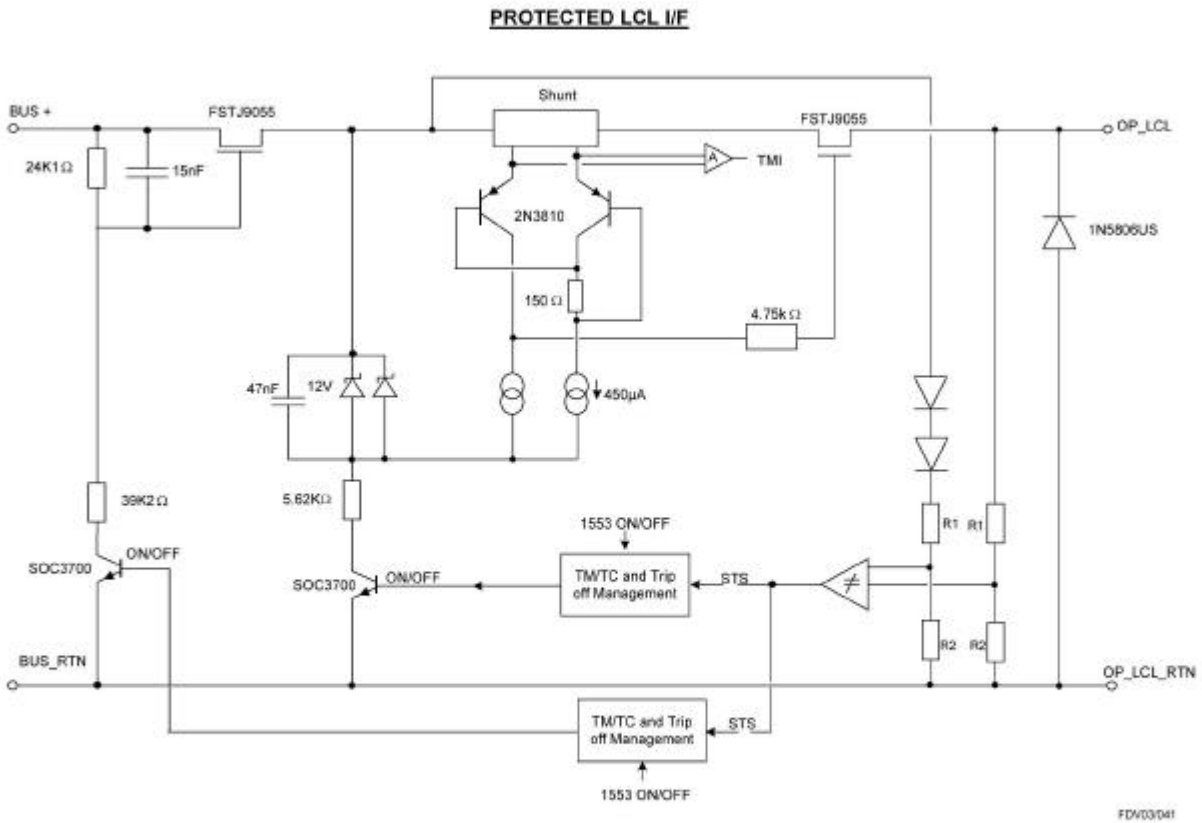
6.1.2 SA_Aux_PWR_in



Module	SAS I/F	Corresponding SA I/F
SUN 1	J = 1	l = 25, k = 1
	J = 2	l = 28, k = 1
SUN 2	J = 3	l = 26, k = 2
	J = 4	l = 29, k = 2
SUN 3	J = 5	l = 27, k = 3
	J = 6	l = 30, k = 3

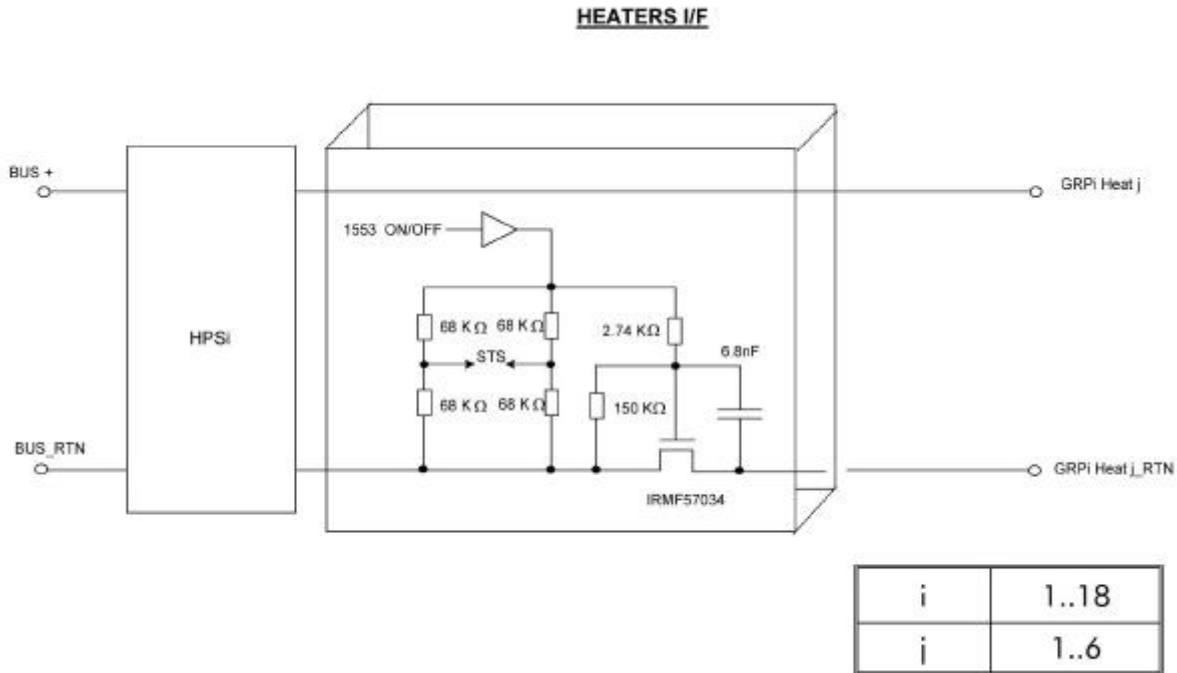
6.1.3 PCDU_LCL

Protected LCL I/F



	Class I	Class II	Class III
Shunt Value	50 mΩ	20 mΩ	10 mΩ

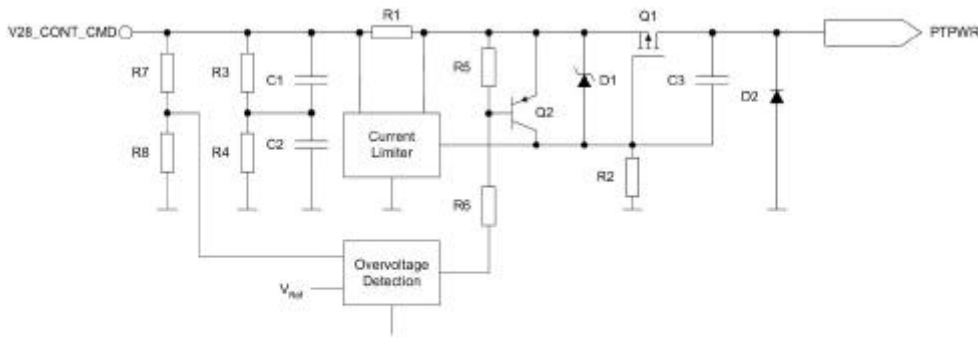
6.1.4 PCDU_Heater_pwr



6.1.5 PT_Input_Pwr

TO BE PROVIDED

6.1.6 PT_Pwr



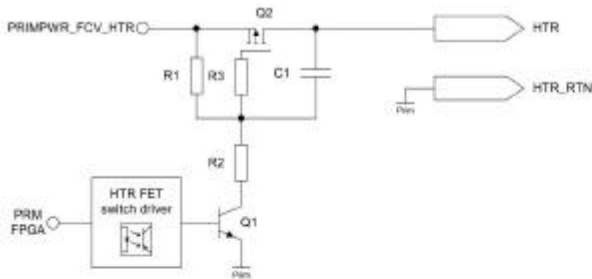
Component	Value
R1	2R49
R2	27k4
R3, R4	1M
R5	10k
R6	68k1
R7	50k9
R8	10k
C1, C2	1µF
C3	470pF
D1	1N4106UR
D2	1N5806US
Q1	IRHNJ597130
Q2	2N2907

6.1.7 PWR_Input

4KC_DriveB_Nom1_Pwr
 4KC_DriveB_Nom2_Pwr
 4KC_DriveB_Red1_Pwr
 4KC_DriveB_Red2_Pwr
 4KCDE_Nom_Pwr
 4KCDE_Red_Pwr
 DAE_Nom_Pwr
 DAE_Red_Pwr
 DPU_Nom_Pwr
 DPU_Red_Pwr
 FOG_Chan1_Pwr
 FOG_Chan2_Pwr
 FOG_Chan3_Pwr
 FOG_Chan4_Pwr
 PHEC_Pwr (DDCU)

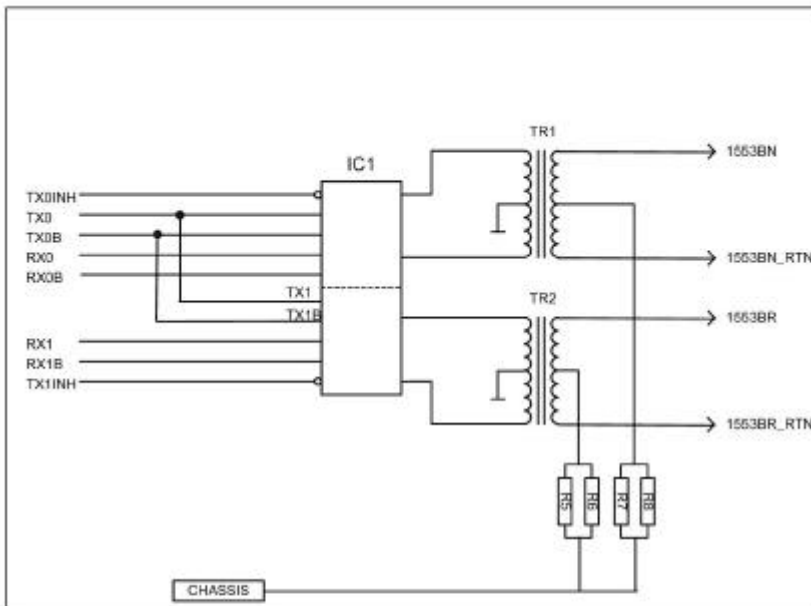
Reba_Nom_Pwr
 Reba_Red_Pwr
 REU_Belts0&1_Pwr
 REU_Belts10&11_Pwr
 REU_Belts2&3_Pwr
 REU_Belts4&5_Pwr
 REU_Belts6&7_Pwr
 REU_Belts8&9_Pwr
 Reu_Nom_Pwr
 Reu_Red_Pwr
 SCE Nom
 SCE Red
 SREM_Pwr

6.1.8 THR_1N_Heater & THR_20N_Heater



Component	Value
R1	3k32
R2	6k81
R3	21R5
C1	1.5nF
D1	1N5806US
Q1	2N3700
Q2	IRH NJ597130

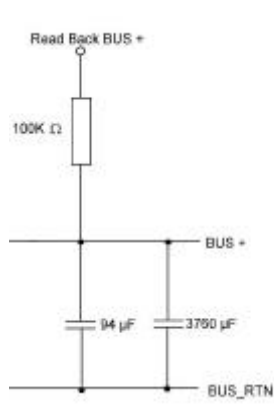
6.2 MIL-1553 Interfaces



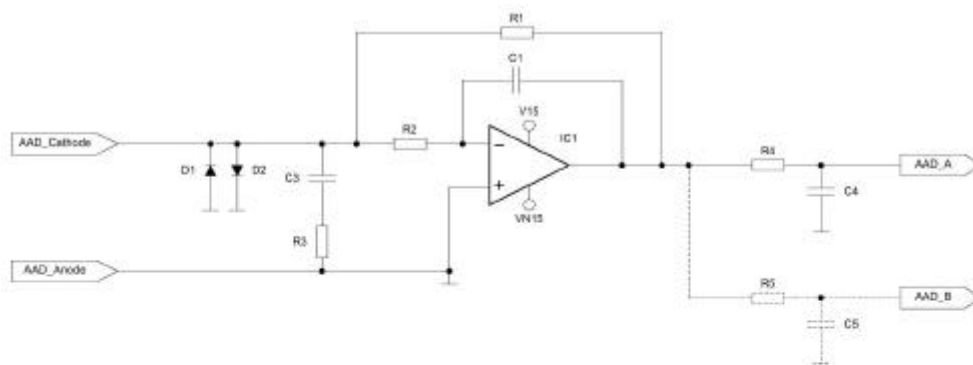
IC1	UT63M147	
TR1	B-3226	
TR2	B-3226	
R5	RM1206	1M Ω
R6	RM1206	1M Ω
R7	RM1206	1M Ω
R8	RM1206	1M Ω

6.3 TM Interfaces

6.3.1 +28V_TM



6.3.2 AAD_Mnt

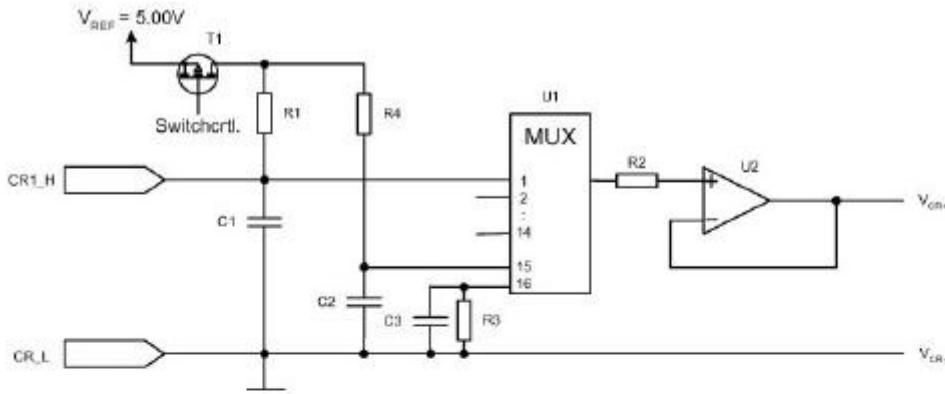


C1	CDR03BP	1.0nF
C3-C5	2220-II	1.0uF
D1-D2	1N3595US-1	
R1	RM1206-P	8.25kohm
R2	RM1206	100ohm
R3	RM1206	2.61kohm
R4-R5	RM1206	3.16kohm
IC1	OP27A	

6.3.3 AAD_Output

TO BE PROVIDED

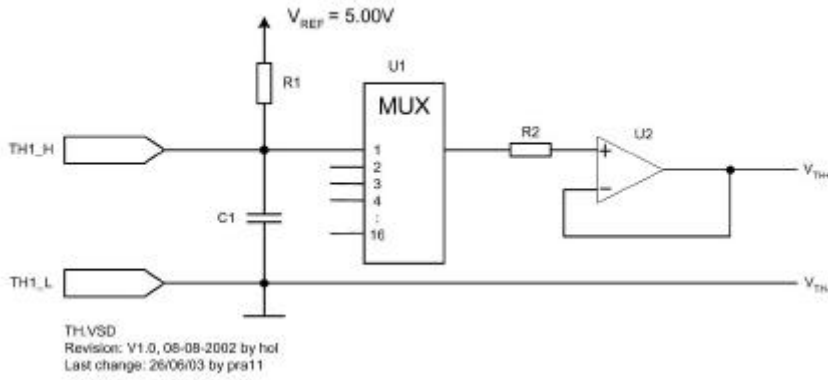
6.3.4 CR-N and CR-W



Component	Value
R1	10k
R2	1k78
R3	1k
R4	1k
C1, C2, C3	560n
U1	HS1840A
U2	AD648
T1	IRHNJ597130

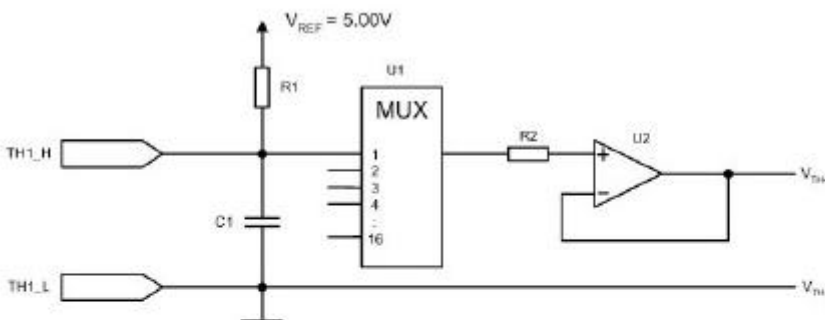
6.3.5 Therm_Mnt

The ACC input :



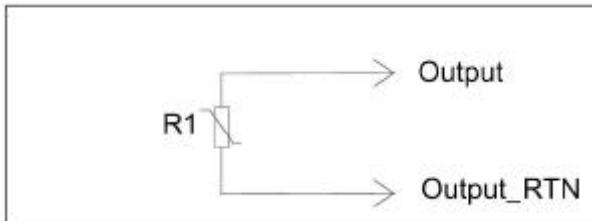
Component	Value
R1	RWL-TH: 10k, CRS-TH, Tank-TH: 26k1 THC-TH: 26k1 (internal)
R2	1k78
C1	560nF
U1	HS1840A
U2	AD648

CDMU Input:



Component	Value
R1	26k1
R2	1k78
C1	560nF
U1	HS1840
U2	AD648

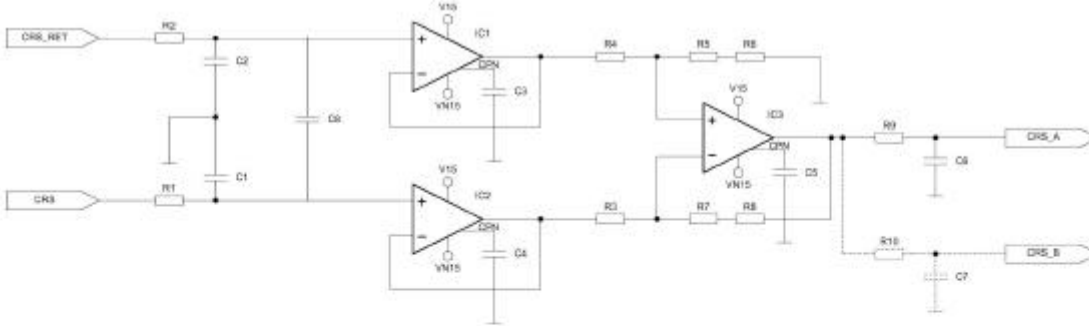
6.3.6 Thermistor



THERM/EGSE_Therm1_Tank
 THERM/EGSE_Therm2_Tank
 THERM/EGSE_Therm3_Tank
 Baffle-1_Front_Nom
 Baffle-1_Front_Red
 Baffle-2_Lateral_Nom
 Baffle-2_Lateral_Red
 Baffle-3_Lateral_Nom
 Baffle-3_Lateral_Red
 Baffle-3_Rear_Nom
 Baffle-3_Rear_Red
 FPU-1_Nom
 FPU-1_Red
 FPU-2_Nom
 FPU-2_Red
 FPU-3_Nom
 FPU-3_Red
 FPU-HFI_Decon_TC1
 FPU-HFI_Decon_TC2
 FPU-HFI_Decon_TC3
 FPU-LFI_Decon_TC1
 FPU-LFI_Decon_TC2
 FPU-LFI_Decon_TC3
 Groove-1_External_Nom
 Groove-1_External_Red
 Groove-1_SC_HeatExc-1_Nom
 Groove-1_SC_HeatExc-1_Red
 Groove-1_SC_HeatExc-2_Nom
 Groove-1_SC_HeatExc-2_Red
 Groove-2_SC_HeatExc-1_Nom

Groove-2_SC_HeatExc-1_Red
 Groove-2_SC_HeatExc-2_Nom
 Groove-2_SC_HeatExc-2_Red
 Groove-3_OpticalCavity_Nom
 Groove-3_OpticalCavity_Red
 Groove-3_SC_HeatExc-1_Nom
 Groove-3_SC_HeatExc-1_Red
 Groove-3_SC_HeatExc-2_Nom
 Groove-3_SC_HeatExc-2_Red
 Groove-3_WaveGuide-1_Nom
 Groove-3_WaveGuide-1_Red
 Groove-3_WaveGuide-2_Nom
 Groove-3_WaveGuide-2_Red
 JFET_Nom
 JFET_Red
 Pri-Refl_Decon_TC1
 Pri-Refl_Decon_TC2
 Pri-Refl_Decon_TC3
 Reflector_PR1_Nom
 Reflector_PR1_Red
 Reflector_PR2_Nom
 Reflector_PR2_Red
 Reflector_SR1_Nom
 Reflector_SR1_Red
 Reflector_SR2_Nom
 Reflector_SR2_Red
 Sec-Refl_Decon_TC1
 Sec-Refl_Decon_TC2
 Sec-Refl_Decon_TC3

6.3.7 CRS_Meas



CRS Coarse (+/-8.5V) (CRS_C)

C1-C2	CDR34BP	10nF
C3-C5	CDR31BP	100pF
C6-C7	CDR04BP	3.3nF
C8	2220-II	1.0μF
R1-R2	RM1206	1.78kohm
R3-R4	RM1206	100kohm
R5, R7	RM1206	1.78kohm
R6, R8	RNC90	56.2kohm
R9-R10	RM1206	5.62kohm
IC1-IC3	LM108A	

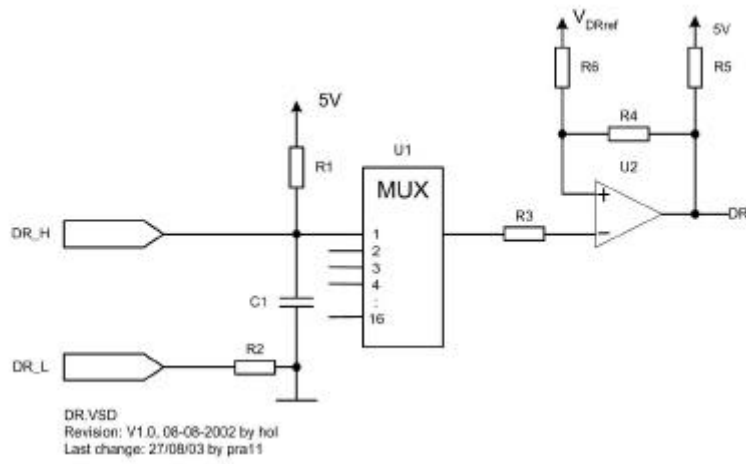
CRS Precision (+/-2V) (CRS_P1/CRS_P2)

C1-C2	CDR34BP	10nF
C3-C5	CDR31BP	100pF
C6-C7	CDR04BP	3.3nF
C8	2220-II	1.0μF
R1-R2	RM1206	1.78kohm
R3-R4	RM1206	28.0kohm
R5, R7	RM1206	100ohm
R6, R8	RNC90	68.1kohm
R9-R10	RM1206	5.62kohm
IC1-IC3	LM108A	

6.3.8 CRS_Output

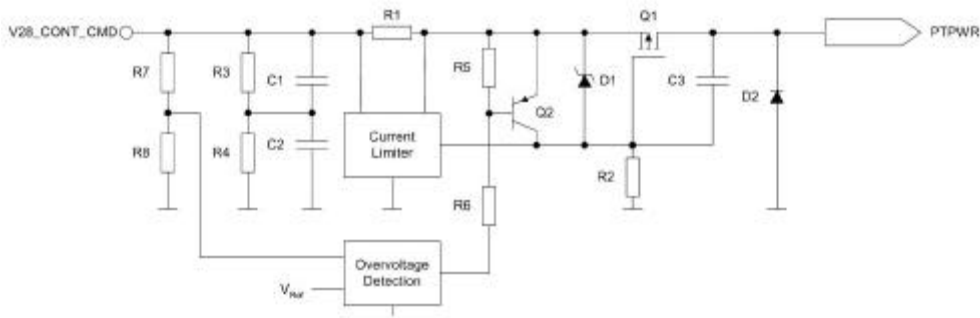
TO BE PROVIDED

6.3.9 DR_Mnt



Component	Value
R1	10k
R2	1k78
R3	1k78
R4	31k6
R5	1k21
R6	1k78
C1	10nF
U1	HS1840A
U2	RH1011

6.3.10 PT_Meas

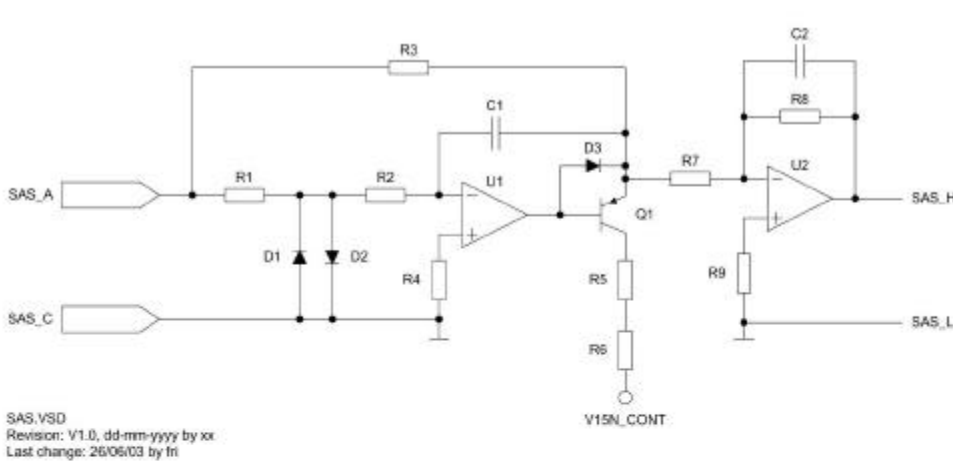


Component	Value
R1	2R49
R2	27k4
R3, R4	1M
R5	10k
R6	68k1
R7	50k9
R8	10k
C1, C2	1μF
C3	470pF
D1	1N4106UR
D2	1N5806US
Q1	IRHNJ597130
Q2	2N2907

6.3.11 PT_Meas_Output

TO BE PROVIDED

6.3.12 SAS_Mnt



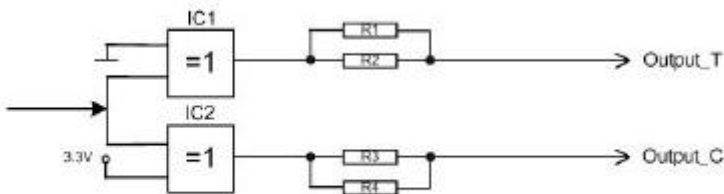
Component	Value
R1	40R2
R2	1k78
R3	121R
R4	1k96
R5, R6	100R
R7, R8	10k
R9	4k87
C1	3.3nF
C2	330nF
D1 – D3	1N6638US
Q1	2N2907
U1, U2	OP400

6.3.13 SAS_Output

TO BE PROVIDED

6.3.14 SBDL_Driver

Interface between Umbilical and CDMU

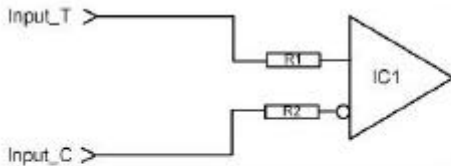


R1	RM1206	90.9Ω
R2	RM1206	100Ω (Trimmed)
R3	RM1206	90.9Ω
R4	RM1206	100Ω (Trimmed)
IC1	AC86	
IC2	AC86	

Telemetry Output Interface Telemetry Encoded

6.3.15 SBDL_Receiver

Interface between Umbilical and CDMU



R1	RM1206	1.78kΩ
R2	RM1206	1.78kΩ
IC1	26CLV32	

Telecommand Link Interface Telecommand

6.3.16 SREM_SBDL_Driver

TO BE PROVIDED

6.3.17 SREM_SBDL_Receiver

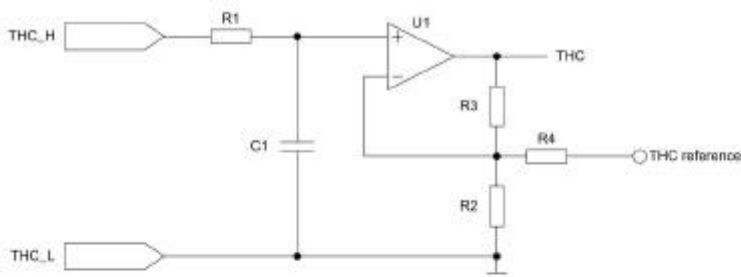
TO BE PROVIDED

6.4 TC Interfaces

6.4.1 CRS_Stim_input

TO BE PROVIDED

6.4.2 THR_TS

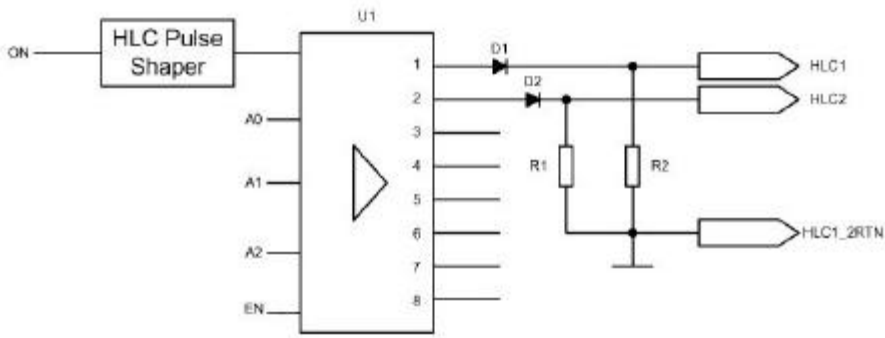


Component	Value
R1	9k09
R2	1k
R3	82k5
R4	100k
C1	1 μ F
U1	OP400

6.4.3 THR_TS_Output

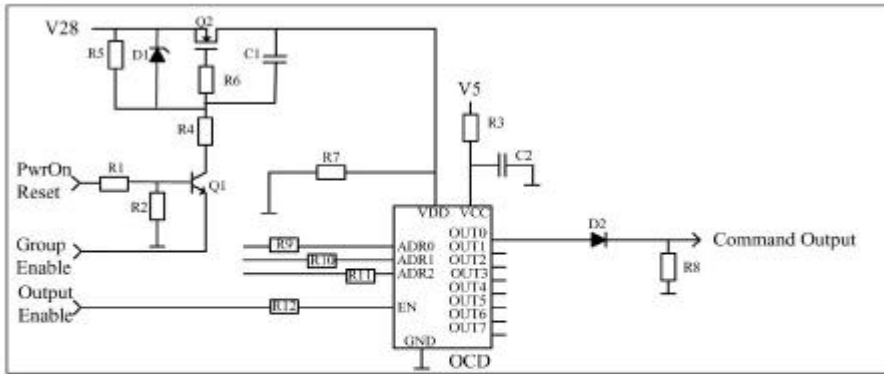
Thruster Thermocouple Output TO BE PROVIDED

6.4.4 HL_Cmd



Component	Value
R1	17.8 k
R2	17.8 k
D1	1N5806US
D2	1N5806US
U1	OCD ASIC

6.4.5 HP_Cmd



R1	RM1206	3.83k Ω
R2	RM1206	38.3k Ω
R3	RM1206	825 Ω
R4	RM1206	14.7k Ω
R5	RM1206	10k Ω
R6	RM1206	38.3 Ω
R7	RM1206	10k Ω
R8	RM1206	14.7k Ω
R9	RM1206	10k Ω
R10	RM1206	10k Ω
R11	RM1206	10k Ω
R12	RM1206	10k Ω
C1	CDR34BP	4.7nF/100V
C2	CDR04BX	150nF/50V
D1	1N4107-1	10V/500mW
D2	1N5806	
Q1	2N3700	
Q2	FSYE913A0	

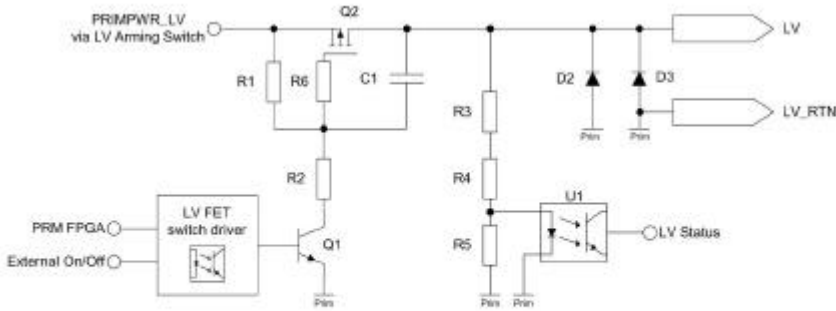
6.4.6 FOG_HP_Cmd_input

TO BE PROVIDED

6.4.7 LV_Cmd_Input

TO BE PROVIDED

6.4.8 LV_DR_Output



Component	Value
R1	3k32
R2	6k81
R3	1k
R4	1k
R5	1k78
R6	21R5
C1	1.5nF
D2, D3	1N5806UR
Q1	2N3700
Q2	IRH NJ597130
U1	66179

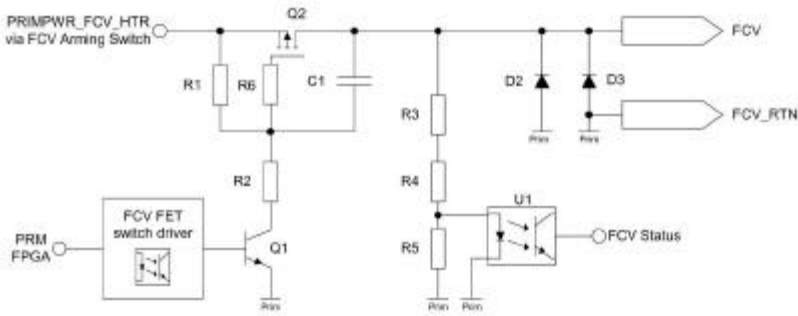
6.4.9 STR_HP_Input

TO BE PROVIDED

6.4.10 THR1_Vlv-Cmd_Input

TO BE PROVIDED

6.4.11 THR1N_Vlv-Cmd and THRDV_Vlv-Cmd

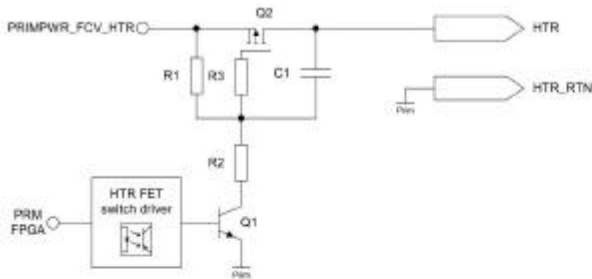


Component	Value
R1	3k32
R2	6k81
R3	1k
R4	1k
R5	1k78
R6	21R5
C1	1.5nF
D2, D3	1N5806US
Q1	2N3700
Q2	IRHJ597130
U1	66179

6.4.12 THR20_Vlv-Cmd_Input

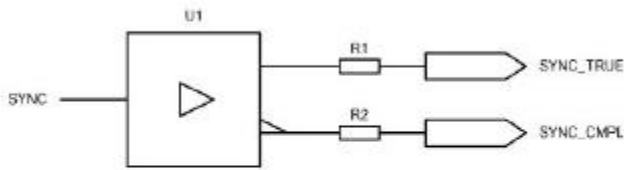
TO BE PROVIDED

6.4.13 THRDV_Htr-Cmd & THR1N_Htr-Cmd



Component	Value
R1	3k32
R2	6k81
R3	21R5
C1	1.5nF
D1	1N5806US
Q1	2N3700
Q2	IRH NJ597130

6.4.14 LOBT_Sync



Component	Value
R1, R2	46R4
U1	26C31

6.4.15 Sync_in

- HFI_DPU_Nom
- HFI_DPU_Red
- LFI_BEU
- LFI_REBA_Nom
- LFI_REBA_Red
- SCE Nom
- SCE Red

7. LIST OF CONNECTOR TYPES

This paragraph to list the connectors which are used as interfaces at the satellite level; i.e. umbilical and skin connectors and at the payload interfaces

7.1.1 Umbilical and Skin Connectors

PU1J01		DBAS 70-61-0SY-090	Umbilical Nominal
PU2J01		DBAS 70-61-0SN-090	Umbilical Redundant
SK01BJ09		340105601B00-25-61SN	PWR Panel (Battery S/A M)
SK01BJ10		340105601B00-25-61SA	PWR Panel (Battery S/A R)
SK01BJ11		340105601B00-09-35SN	PWR Panel (SA EGSE I/F 1)
SK01BJ12		340105601B00-09-35SA	PWR Panel (SA EGSE I/F 2)
SK02J01		340105601B00-09-35SN	PWR Panel (DMS 1553 M)
SK02J02		340105601B00-09-35SA	PWR Panel (DMS 1553 R)
SK02J03		340105601B00-09-35SB	PWR Panel (ACMS 1553 M)
SK02J04		340105601B00-09-35SC	PWR Panel (ACMS 1553 R)
SK02J05		340105601B00-17-35SN	PWR Panel (LV1/FCV 20N/1N CMD S/A M)
SK02J06		340105601B00-17-35SA	PWR Panel (LV2/FCV 20N/1N CMD S/A R)
SK02J07		340105601B00-13-35SN	PWR Panel (RCS Press/Tank Temp/PT Pwr)
SK02J08		340105601B00-17-35SB	PWR Panel (Thruster Temp M/LV1 Sts)
SK02J09		340105601B00-13-35SB	PWR Panel (ACC/CDMU SW Load M)
SK02J10		340105601B00-13-35SA	PWR Panel (ACC/CDMU SW Load R)
SK02J11		340105601B00-17-35SC	PWR Panel (Thruster Temp R/LV2 Sts)
SK02J12		340105601B00-19-35SN	PWR Panel (Thruster C/B Heaters M)
SK02J13		340105601B00-19-35SA	PWR Panel (Thruster C/B Heaters R)
SK02J14		340105601B00-13-35SD	PWR Panel (Str1/2 On/Off Cmd M/Str1 Sts)
SK02J15		340105601B00-13-35SE	PWR Panel (Str1/2 On/Off Cmd R/Str2 Sts)
SK03J01		340105601B00-13-35SN	TTC Panel (XPND1 -EPC1 to EGSE)
SK03J02		340105601B00-13-35SA	TTC Panel (XPND2 -EPC2 to EGSE)
SK05J01		340105601B00-13-35SN	CRS1 AOCS Sgn
SK05J02		340105601B00-13-35SA	CRS2 AOCS Sgn
SK05J03		340105601B00-13-35SB	CRS3 AOCS Sgn
SK05J04		340105601B00-17-35SN	CRS 1/2/3 Stimuli
SK05J05		340105601B00-9-35SN	AAD Sgn M
SK05J06		340105601B00-15-35SN	SAS1/2 Sgn M
SK05J07		340105601B00-15-35SA	SAS1/2 Sgn R
SK05J08		340105601B00-9-35SA	AAD Sgn R
SK06J01		340105601B00-17-35SN	STR1 Stimuli
SK06J02		340105601B00-17-35SA	STR2 Stimuli
CBPLMP01		340100201B DAMA 15S NMB	PLM Dec. Heaters Nom Pwr
CBPLMP02		340100201B DAMA 15S NMB	PLM Dec. Heaters Red Pwr
CBPLMP03		340100201B DEMA 9S NMB	PLM Pri. Refl. Thermistors Nom
CBPLMP04		340100201B DEMA 9S NMB	PLM Pri. Refl. Thermistors Red
CBPLMP05		340100201B DEMA 9S NMB	PLM Sec. Refl. Thermistors Nom
CBPLMP06		340100201B DEMA 9S NMB	PLM Sec. Refl. Thermistors Red
CBPLMP07		340100201B DAMA 15S NMB	PLM FPU Thermistors Nom
CBPLMP08		340100201B DAMA 15S NMB	PLM FPU Thermistors Red
CBPLMP09		340100201B DDMA 50S NMB	PPLM Thermistors Nom
CBPLMP10		340100201B DDMA 50S NMB	PPLM Thermistors Red

7.1.2 PPLM Connectors

The Connectors on the PPLM Side are :

CBPLMJ01	340100201B DAMA 15P NMB	Nominal Decontamination Heaters
CBPLMJ02	340100201B DAMA 15P NMB	Redundant Decontamination Heaters
CBPLMJ03	340100201B DEMA 9S NMB	Nominal Primary Reflector Thermistors
CBPLMJ04	340100201B DEMA 9S NMB	Redundant Primary Reflector Thermistors
CBPLMJ05	340100201B DEMA 9S NMB	Nominal Secondary Reflector Thermistors
CBPLMJ06	340100201B DEMA 9S NMB	Redundant Secondary Reflector Thermistors
CBPLMJ07	340100201B DAMA 15S NMB	Nominal FPU Thermistors
CBPLMJ08	340100201B DAMA 15S NMB	Redundant FPU Thermistors
CBPLMJ09	340100201B DDMA 50S NMB	Nominal PPLM Thermistors
CBPLMJ10	340100201B DDMA 50S NMB	Redundant PPLM Thermistors

7.1.3 LFI Connectors

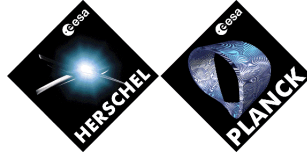
LFI_DAE_power_box	PLAEFJ01	DD 9P	Power Nom
LFI_DAE_power_box	PLAEFJ84	DD 9P	Power Red
LFI_BEU	PLBEUJ09	DD 9P	Sync Nom
LFI_BEU	PLBEUJ83	DD 9P	Sync Red
LFI_REBA_Nom	PLRENJ11	DEM 9P	Primary Power Bus
LFI_REBA_Nom	PLRENJ12	DEM 9S	Satellite Synchronization Nom
LFI_REBA_Nom	PLRENJ41	DEM 9P	1553 TM/TC A
LFI_REBA_Nom	PLRENJ42	DEM 9P	1553 TM/TC B
LFI_REBA_Red	PLRERJ11	DEM 9P	Primary Power Bus
LFI_REBA_Red	PLRERJ12	DEM 9S	Satellite Synchronization Red
LFI_REBA_Red	PLRERJ41	DEM 9P	1553 TM/TC A
LFI_REBA_Red	PLRERJ42	DEM 9P	1553 TM/TC B
SCE_Nom	PSM4J21	DEMA 9S	1553B Bus Nom and OBC/Synch
SCE_Nom	PSM4J22	DEMA 9S	1553B Bus Red
SCE_Nom	PSM4J23	DEMA 9P	Low Power supply
SCE_Nom	PSM4J24	340104401B0316-08PN	High Power supply
SCE_Red	PSR4J21	DEMA 9S	1553B Bus Nom and OBC/Synch
SCE_Red	PSR4J22	DEMA 9S	1553B Bus Red
SCE_Red	PSR4J23	DEMA 9P	Low Power supply
SCE_Red	PSR4J24	340104401B0316-08PN	High Power supply

7.1.4 HFI Connectors

HFI_DPU_Nom	PHBANJ01	DEMA 9S	1553 BUS A TX/RX
HFI_DPU_Nom	PHBANJ02	DEMA 9S	1553 BUS B TX/RX
HFI_DPU_Nom	PHBANJ05	DEMA 9P	Power Nominal PCDU to DPU
HFI_DPU_Nom	PHBANJ141	DAM 15P	Power Nominal REU belts group 1
HFI_DPU_Nom	PHBANJ15	DEMA 9S	On Board Clock /Sync
HFI_DPU_Red	PHBARJ01	DEM 9S	1553 BUS A TX/RX
HFI_DPU_Red	PHBARJ02	DEM 9S	1553 BUS B TX/RX
HFI_DPU_Red	PHBARJ05	DEM 9P	Power Nominal PCDU to DPU
HFI_DPU_Red	PHBARJ142	DAM15P	Power Nominal REU belts group 1
HFI_DPU_Red	PHBARJ15	DEM 9S	On Board Clock /Sync
HFI_REU	PHCBC12J06	DEM 9P	Power Nominal
HFI_REU	PHCBC13J06	DEM 9P	Power Redundant



HFI_4K_CEU	PHDCJ01A	340100201BDAMA15PNMBFO	Input Power (Nominal)
HFI_4K_CEU	PHDCJ01B	340100201BDAMA15PNMBFO	Input Power (Redundant)
HFI_DCCU	PHECJ01	DEMA 9P	Primary power supply



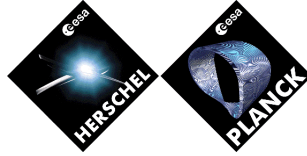
7.1.5 Miscellaneous Connectors

SREM	SREMJ01	3401029001B15SFR115	Power
SREM	SREMJ02	3401029001B15PFR115	ML16 Memory Load Serial Command OBDH
FOG	FOG1J01	DAM 15S	FOG1 1553 bus
FOG	FOG1J04	DAM 15P	FOG1 Power bus
FOG	FOG2J01	DAM 15S	FOG2 1553 bus
FOG	FOG2J04	DAM 15P	FOG2 Power bus
FOG	FOG3J01	DAM 15S	FOG3 1553 bus
FOG	FOG3J04	DAM 15P	FOG3 Power bus
FOG	FOG4J01	DAM 15S	FOG4 1553 bus
FOG	FOG4J04	DAM 15P	FOG4 Power bus

8. UMBILICAL CONNECTORS

Two Umbilical Connectors (1 Nominal + 1 Redundant) each of 61 Pins, of which 37 are routed to the COTE and 24 available for S/C thus:

PU1 - DBAS 70 61 OSY 090					
Pin #	Signal	V max (V)	I Max (A)	Line Start	Line End
1	Shielding COTE Harness	0	0	SVM	COTE #2
29	+28V Aux IN 1	30	3	SVM	COTE #2
50	+28V Aux IN 2	30	3	SVM	COTE #2
28	+28V Aux IN 3	30	3	SVM	COTE #2
49	+28V Aux IN 1 RTN	0	3	SVM	COTE #2
30	+28V Aux IN 2 RTN	0	3	SVM	COTE #2
51	+28V Aux IN 3 RTN	0	3	SVM	COTE #2
40	+28V Monitoring 1	30	<10mA	SVM	COTE #2
41	+28V Monitoring 1 RTN	0	<10mA	SVM	COTE #2
36	TC Clock +	3.6	2mA	SVM	STFO
19	TC Clock -	3.6	2mA	SVM	STFO
35	TC Data +	3.6	2mA	SVM	STFO
18	TC Data -	3.6	2mA	SVM	STFO
34	TC Squelch +	3.6	2mA	SVM	STFO
17	TC Squelch -	3.6	2mA	SVM	STFO
33	TM Clock +	3.6	2mA	SVM	STFO
32	TM Clock -	3.6	2mA	SVM	STFO
31	TM Data +	3.6	2mA	SVM	STFO
52	TM Data -	3.6	2mA	SVM	STFO
8	Charge Array Disable link	0	0	SVM	COTE #2
9	Charge Array Disable link RTN	0	0	SVM	COTE #2
2	Spare				
3	Spare				
5	Spare				
7	Spare				
14	Spare				
20	Spare				
37	Spare				
38	Spare				
39	Spare				
43	Spare				
44	Spare				
45	Spare				
46	Spare				
57	Spare				
60	Spare				
61	Spare				
Total to COTE					37



PU1 - DBAS 70 61 OSY 090 - (cont'd)					
Pin #	Signal	V max (V)	I Max (A)	Line Start	Line End
6	Shielding	0	0		
13	CDMU Separation Strap 1Status	5	1mA	SVM	launcher
27	CDMU Separation Strap 1Status RTN	0	1mA	SVM	launcher
47	CDMU Separation Strap 2 Status	5	1mA	SVM	launcher
48	CDMU Separation Strap 2 Status RTN	0	1mA	SVM	launcher
21	CDMU RMA Separation Strap 5a Status	5	1mA	SVM	launcher
22	CDMU RMA Separation Strap 5a Status RTN	0	1mA	SVM	launcher
25	CDMU RMB Separation Strap 6a Status	5	1mA	SVM	launcher
26	CDMU RMB Separation Strap 6a Status RTN	0	1mA	SVM	launcher
4	Spare				
10	Spare				
11	Spare				
12	Spare				
15	Spare				
16	Spare				
23	Spare				
24	Spare				
42	Spare				
53	Spare				
54	Spare				
55	Spare				
56	Spare				
58	Spare				
59	Spare				
Total to Launcher					24
OVERALL TOTAL					61



PU2 - DBAS 70 61 OSN 090					
Pin #	Signal	V max (V)	I Max (A)	Line Start	Line End
1	Shielding COTE Harness	0	0	SVM	COTE #2
29	+28V Aux IN 4	30	3	SVM	COTE #2
50	+28V Aux IN 5	30	3	SVM	COTE #2
28	+28V Aux IN 6	30	3	SVM	COTE #2
49	+28V Aux IN 4 RTN	0	3	SVM	COTE #2
30	+28V Aux IN 5 RTN	0	3	SVM	COTE #2
51	+28V Aux IN 6 RTN	0	3	SVM	COTE #2
40	+28V Monitoring 2	30	<10mA	SVM	COTE #2
41	+28V Monitoring 2 RTN	0	<10mA	SVM	COTE #2
36	TC Clock +	3.6	2mA	SVM	STFO
19	TC Clock -	3.6	2mA	SVM	STFO
35	TC Data +	3.6	2mA	SVM	STFO
18	TC Data -	3.6	2mA	SVM	STFO
34	TC Squelch +	3.6	2mA	SVM	STFO
17	TC Squelch -	3.6	2mA	SVM	STFO
33	TM Clock +	3.6	2mA	SVM	STFO
32	TM Clock -	3.6	2mA	SVM	STFO
31	TM Data +	3.6	2mA	SVM	STFO
52	TM Data -	3.6	2mA	SVM	STFO
8	Charge Array Disable link	0	0	SVM	COTE #2
9	Charge Array Disable link RTN	0	0	SVM	COTE #2
2	Spare				
3	Spare				
5	Spare				
7	Spare				
14	Spare				
20	Spare				
37	Spare				
38	Spare				
39	Spare				
43	Spare				
44	Spare				
45	Spare				
46	Spare				
57	Spare				
60	Spare				
61	Spare				
Total to COTE					37



PU2 - DBAS 70 61 OSN 090 - (cont'd)					
Pin #	Signal	V max (V)	I Max (A)	Line Start	Line End
6	Shielding	0	0		
13	ACC Separation Strap Status 3b	5	1mA	SVM	launcher
27	ACC Separation Strap Status 3b RTN	0	1mA	SVM	launcher
47	ACC Separation Strap Status 4b	5	1mA	SVM	launcher
48	ACC Separation Strap Status 4b RTN	0	1mA	SVM	launcher
21	ACC RMA Separation Strap Status 7a	5	1mA	SVM	launcher
22	ACC RMA Separation Strap Status 7a RTN	0	1mA	SVM	launcher
25	ACC RMB Separation Strap Status 8a	5	1mA	SVM	launcher
26	ACC RMB Separation Strap Status 8a RTN	0	1mA	SVM	launcher
4	Spare				
10	Spare				
11	Spare				
12	Spare				
15	Spare				
16	Spare				
23	Spare				
24	Spare				
42	Spare				
53	Spare				
54	Spare				
55	Spare				
56	Spare				
58	Spare				
59	Spare				
Total to Launcher					24
OVERALL TOTAL					61

9. 1553 ADDRESSES

Bus Address	Bus Terminal
1	CDMU A
2	CDMU B
3	ACC A
4	ACC B
5	PCDU A
6	PCDU B
7	Spare
8	Spare
9	TRSP A
10	TRSP B
11	Spare
12	Spare
13	Spare
14	Spare
15	Spare
16	HFI A
17	HFI B
18	LFI A
19	LFI B
20	SCE A
21	SCE B
22	Spare
23	Spare
24	Spare
25	Spare
26	Spare
27	Spare
28	Spare
29	Spare
30	Spare
31	Reserved for Broadcast

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