



SAp-SPIRE- HT-0392-06 V1.0 Issue : 1.0 Date : 25/07/2006

SPIRE FCU FM ACCEPTANCE

Vibration Tests Report

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Vibration Tests Report



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1 **Abreviations list**





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2 Applicable documents

[AD1] HERSCHEL/SPIRE DRCU Interface Control Document	SAP-SPIRE-CCa-075-02
[AD2] HERSCHEL/SPIRE DRCU/DPU Interface Control Document	SAP-SPIRE-CCa-076-02
[AD3] Herschel/SPIRE DRCU AIV PLAN	SAp-SPIRE-HT-0082-02
[AD4] HERSCHEL/SPIRE DRCU Subsystem specification	Sap-SPIRE-CCa-0025-00
[AD5] Herschel/Planck Instrument Interface Document IID Part A "	SCI-PT-IIDA/SPIRE-04624
[AD6] Herschel/Planck Instrument Interface Document Part B Instrument "SPIRE"	SCI-PT-IIDB/SPIRE-02124
[AD7] Herschel SPIRE Detector Subsystem Specification Document	SPIRE –JPL-PRJ-000456
[AD8] SPIRE FCU FM Functional test procedure	SAp-SPIRE-HT-0388-06
[AD9] Herschel/SPIRE HSFCU FM	SAp-SPIRE-TT-0401-06
Procédure de vibrations	

3 <u>Reference documents</u>

[RD1] HERSCHEL/SPIRE DCU EM/QM1 Test Report	Sap-SPIRE-CCa-0129-03 Issue 1.0
[RD2] DS-SPIRE development tree	Sap-SPIRE-DS - Version 07/03/2002
[RD3] FIRST/SPIRE DRCU and WIH Development	SAP-SPIRE-JLA-0047-01
Plan	
[RD4] Mechanical test report	Référence Intespace
	E5831.HSFFMMECA
[RD5] HS-FCU QM2 TRR Report	SAp-SPIRE- QA-0419-05
W	





4 Introduction

This technical report applies to the Vibration tests performed on the FCU FM equipment for the HERSCHEL SPIRE Project.

These tests have been performed at Intespace between June 14th 2006 and June 16th 2006.

5 <u>Test Configuration</u>

We use the LTU in its latest software configuration, the fully assembled DCU and FCU, the FPU and the PSU Flight Model (PSU FM).

Before to connect any harness, we place savers on all connectors

Connect all harnesses as described below :

- Between SCU and LTU
- Between DCU and PSU FM
- Between SCU and PSU FM

We place savers on all connectors

We connect all harnesses as described below







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FPU simulator

(Jec = 15 P)	FPU_TS_1_M / TEMP_M	TEMP_M (J21 - 15.8) SCU		1
(Jax = 50 P)	FPU_TS_2_M / TEMP_M	TEMP_M (J03 = 25 S)	CCHK_IF_M / DPU_M (W3)	(J09-25 P)
(Jun = 15 P)	FPU MEC TS M/TEMP M	(J23 = 50 S) TEMP M CCHK_IF_R	CCHK_IF_R / DPU_R (W3)	(J12-25P)
(Jxx = 15 P)	FPU_TS_1_R/TEMP_R	(JJS = 15 %) TEMP_R (JJS6 = 15 P)	1	
(Jxx = 50 P)	FPU_TS_2_R / TEMP_R	(J22= 15 S) SCU Secondary power TEMP_R (J35 = 15 P)		LTU DRCU
(Jxx = 15 P)	FPU_MEC_TS_R/ TEMP_R	(J24 = 50 S) TEMP R (J26 = 15 S)	(J33 - 15 P) (J34 - 15 P)	
(Jex = 25 P)	FPU COOL CAL M/CCHK IF M	CCHK_IF_M	PSU FM	DPU
(Jer = 9 P)	FPU_PH_STIM_M / CCHK_IF_M	(J11 = 25 S) CCHK_IF_M (J13 = 9 S)	(J09-25 P) (J10-25 P)	simulator
(Jax = 25 P)	FPU COOL CAL R/CCHK IF R	CCHK_IF_R		
(Jrr - 9 P)	FPU_PH_STIM_R / CCHK_IF_R	(J12-25.5) (J31-25.7) (J31-25.7) (J14-9.5) MC1) Secondary power (J14-9.5)		
		(J32=25.P) MAC M/DPU M	MAC M/DPU M(W3)	(JUN= 25 P)
		$\begin{array}{c} (J0\overline{I} = 25 S) \\ MAC R / DPU R \\ (J02 = 25 S) \end{array}$	MAC_R / DPU_R (W3)	(JII = 25 P)
(Jax = 9 P)	FPU SMECm 1 M/SMEC M	SMEC_M / FPU_SMECm_I_M		
(Jax = 50 P)	FPU SMECm 2 M/SMEC M	(J17 - 37 S) SMEC_M / FPU_SMECm_2_M		
(Jax = 15 P)	FFU BSM M/BSM M	(J29 – 37 P) RSM_M / FPU_RSM_M (J19 – 37 S)		
(Jac - 9 P)	FPU_SMECm_1_R / SMEC_R	SMEC_R / FPU_SMECm_1_R		
(Jax = 50 P)	FPU SMECm 2 R/SMEC R	(JI8 = 37 S) SMEC R / FPU SMECm 2 R		
(Jax = 15 P)	FPU BSM R/BSM R	(J30 – 37 P) 		

FCU

FCU FLIGHT MODEL TEST CONFIGURATION

FCU FM acceptance tests configuration





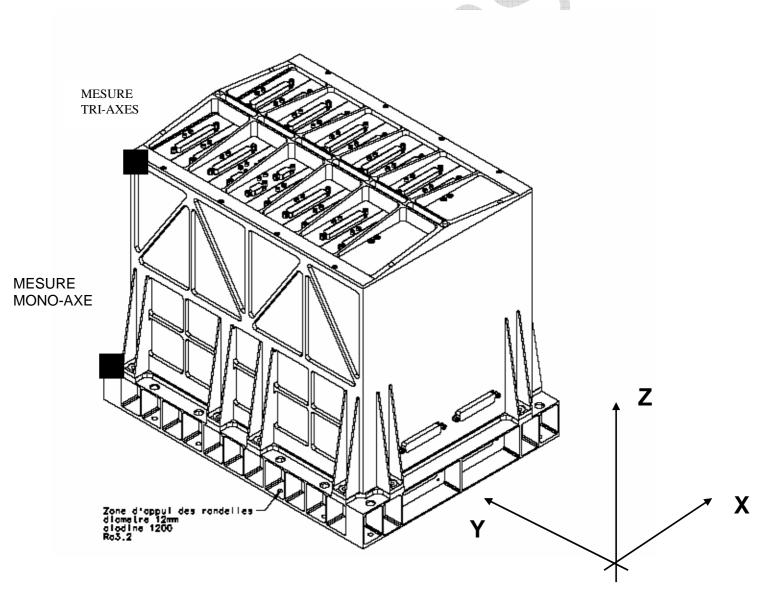
6 Test Program

The description of the vibration test is given in the vibration tests procedure [AD9].

The FCU is mounted on the shaker through a rigid interface (f > 2 kHz). We apply on this interface the specified vibration levels.

Before and after applying these levels, we apply acceptance sinus and random levels that show the FCU eigen frequencies of the structure. By comparison of the dynamic signatures (low level sinus) of the FCU before and after the tests, we check its structural integrity.

The tests are performed on **Y,X,Z** axes successively in the reference frame given in [AD9].







Preliminary tests have been performed on the interface plate of the shaker before mounting the FCU on it.

Before to start the vibration procedure and after the vibrations applied on each axis, we perform a full functional test following the functional test procedures [AD8].

The sequence, which is the same on each axis, is as follows :

Test	
Functional test	Refer to [AD8]
Low level Sinus	Described in § 6.1 of [AD9]
Qualification level Sinus	Described in § 6.2.1 of [AD9]
Random low level	Described in § 6.3 of [AD9]
Random qualification level	Described in § 6.4.1 of [AD9]
Low level Sinus	Described in § 6.1 of [AD9]
Functional test	Refer to [AD8]

The results of the mechanical tests are given in the next chapter.

The results of the functional tests are given in the chapter 8.



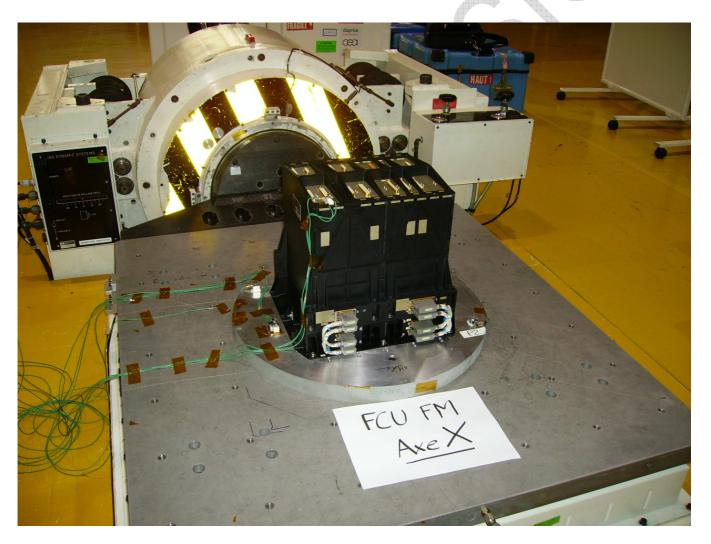


7 Report of the mechanical tests

It consists in the analysis of the response of the structure to the applied vibration levels. All the data relative to these tests are given in the test report from Intespace referenced as [RD4].

The comparison of the low level sinus response before and after the vibration applied on each axis is being analyzed thereafter.

We look for eigen frequencies and associated magnitudes and check their shift following the test (frequency shift should be lower than 5%). We also test that all eigen frequencies are above 150 Hz.



Vibration on X axis





The graphics representing the low level sinus, the qualification level sinus and the random qualification levels on each axis are given in [RD4].

Their analysis shows that the signatures remain the same (with respect to before vibrations) with no significant frequency shift.

The acceleration levels (g_{rms}) , as observed during acceptance ensure the following security margins with respect to qualification levels :

- On X axis : Margin of safety = 1,15
- On Y axis, margin of safety = 1,25
- On Z axis, margin of safety = 1,50





8 Report of the functional tests

The list of the performed test, with reference to [AD8] is :

FCU QM2 tests	it .	(0		
	Preliminary : after FCU integration at INRTESPACE	After X and Y axis	Final (after Z axis)	
	ary : grat AC	/ pu	er A	
	inte ESI	Ха	(aft	
	ICU NRT	vfter	inal	
Consumption SCU redundant	X	X	X	
SCU HouseKeepings	X	X	X	
SCU Frames				
SCU Temp/SubK channels	X	X	X	
SCU Temp/SubK ON/OFF				
SCU Heater/Calibrators channels	X	X	X	
Heater/Calibrators ON/OFF				
SCU currents measured by FPU simulator				
SCU Currents in Stand alone		\forall		
Consumption MCU redundant	X	X	X	
Consumption MCU main	X	x	X	
HouseKeepings	X	x	x	
Configuration 1 Main	Х	x	x	
Configuration 2 Main	Х	x	x	
Configuration 3 Main	Х	x	x	
Configuration 4 Main	Х	x	x	
Configuration 5 Main	X	x	x	
Configuration 1 Redundant	Х	X	x	
Configuration 2 Redundant	Х	X	x	
Configuration 3 Redundant	X	X	x	
Configuration 4 Redundant	X	X	x	
Configuration 5 Redundant	X	X	x	

In this document, only the main results are given.

8.1 Visual inspection

A visual inspection was performed after each axis. It revealed no specific problem.





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8.2 <u>Consumption tests</u>

We thereafter report the consumptions as measured during each functional tests after vibrations.

The MCU configurations testes are the following :

- Configuration 1 : MCU ON, all currents applied to the mechanisms set at 0 level
- Configuration 2 : MCU ON, all currents applied to the mechanisms set at 0 level; latch engaged
- Configuration 3 :MCU ON, all currents applied to the mechanisms set at 10% level;
- Configuration 4 :MCU ON, all currents applied to the mechanisms set at 50% level;
- Configuration 3 :MCU ON, all currents applied to the mechanisms set at 100% level;

Before vibrations

Report of the primary current consumption (mA) at PSU FM level (28V)

MCU Status	Main	Redundant
	100	400
OFF	400	400
MCU ON	810	810
MCU after boot	870	870
Configuration 1	880	870
Configuration 2	880	870
Configuration 3	940	930
Configuration 4	1060	1060
Configuration 5	1220	1210

After Y axis

Report of the primary current consumption at PSU FM level (28V)

MCU Status	Main	Redundant
OFF	400	400
MCU ON	810	810
MCU after boot	870	870
Configuration 1	880	870
Configuration 2	880	870
Configuration 3	940	930
Configuration 4	1060	1050
Configuration 5	1210	1210





After X axis

Report of the primary current consumption at PSU FM level (28V)

MCU Status	Main	Redundant
OFF MCU ON	400 810	400 810
MCU after boot	870	870
Configuration 1	880	870
Configuration 2	880	870
Configuration 3	940	940
Configuration 4	1060	1060
Configuration 5	1220	1210
-		

After Z axis

Report of the primary current consumption at PSU FM level (28V)

MCU Status	Main	Redundant
OFF	400	400
MCU ON	810	810
MCU after boot	870	870
Configuration 1	880	870
Configuration 2	880	870
Configuration 3	940	940
Configuration 4	1060	1060
Configuration 5	1220	1210

The consumptions of SCU and MCU did not move following the vibrations.





8.3 SCU and MCU HouseKeepings

We have started the DCU Monitoring Mode from LTU (1 Hz frequency) after each vibration test (each axis) and the results were consistent with the expected values.

Note that we observe the results over a sufficient time (1 minute).

The housekeepings remained nominal during the whole test, id est after each vibration test.

We thereafter show the results relative the H/K acquisitions performed after the vibrations :



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SCU main side

COLL					
SCU Status					
Nom Parametre	Description			Valeur Lue H	Valeur Nomin
ScuStatus	SCU Status			0000	
•					Þ
- 1					
Temperature:	•				
				1	
Nom Parametre	Description			Valeur Conver	Unite
Csu Temp	CCHK_IF Booard			21.00	°C
Tsu Temp	Temp Board temp			21.00	℃ ℃
Psu Temp1	PSU temperature			24.00	°C
Psu Temp2	PSU temperature	2		23.00	ι.
-					
ا					•
۱					
					•
▲ Reference/V	'oltage				•
	'oltage Description	Valeur Conver	Valeur Lue H	Min	Max
Reference/V Nom Parametre ScuCHTp05	Description +5V DC powe	5.25	697B	4.77	Max 5.28
Reference/V Nom Parametre ScuCHTp05 ScuCHTp09	Description +5V DC powe +9V DC powe	5.25 9.09	697B 68B2		Max
Reference/V Nom Parametre ScuCHTp05	Description +5V DC powe +9V DC powe •9V DC power	5.25 9.09 -9.09	697B 68B2 9757	4.77 8.89 -9.07	Max 5.28 9.07 -8.88
Reference/V Nom Parametre ScuCHTp05 ScuCHTp09 ScuCHTn09 ScuCHTn25	Description +5V DC powe +9V DC powe -9V DC power +2.5V DC inte	5.25 9.09 -9.09 2.52	697B 68B2 9757 408F	4.77 8.89 -9.07 2.47	Max 5.28 9.07 -8.88 2.55
Reference/V Nom Parametre ScuCHTp05 ScuCHTp09 ScuCHTn09 ScuCHT25 ScuCHT25 ScuCHTref	Description +5V DC powe +9V DC powe -9V DC power +2.5V DC inte Internal voltag	5.25 9.09 -9.09 2.52 1.23	6978 6882 9757 408F 3F14	4.77 8.89 -9.07 2.47 1.21	Max 5.28 9.07 -8.88 2.55 1.25
Reference/V Nom Parametre ScuCHTp05 ScuCHTp09 ScuCHTn09 ScuCHTn09 ScuCHT25 ScuCHTref ScuCHTgnd	Description +5V DC powe +9V DC powe -9V DC power +2.5V DC inte Internal voltag Internal Groun	5.25 9.09 -9.09 2.52 1.23 0.00	6978 6882 9757 408F 3F14 0006	4.77 8.89 -9.07 2.47 1.21 -0.04	Max 5.28 9.07 -8.88 2.55 1.25 0.04
Reference/V Nom Parametre ScuCHTp05 ScuCHTp09 ScuCHTn09 ScuCHT09 ScuCHT25 ScuCHT25 ScuCHTgnd ScuCHTgnd ScuTHTref	Description +5V DC powe +9V DC powe -9V DC power +2.5V DC inte Internal voltag Internal Groun Internal voltag	5.25 9.09 -9.09 2.52 1.23 0.00 1.23	6978 6882 9757 408F 3F14 0006 3F39	4.77 8.89 -9.07 2.47 1.21 -0.04 1.21	Max 5.28 9.07 -8.88 2.55 1.25 0.04 1.25
Reference/V Nom Parametre ScuCHTp05 ScuCHTp09 ScuCHTn09 ScuCHTn09 ScuCHT25 ScuCHTref ScuCHTgnd	Description +5V DC powe +9V DC powe -9V DC power +2.5V DC inte Internal voltag Internal Groun	5.25 9.09 -9.09 2.52 1.23 0.00	6978 6882 9757 408F 3F14 0006	4.77 8.89 -9.07 2.47 1.21 -0.04	Max 5.28 9.07 -8.88 2.55 1.25 0.04
Reference/V Nom Parametre ScuCHTp05 ScuCHTp09 ScuCHTn09 ScuCHT09 ScuCHT25 ScuCHT25 ScuCHTgnd ScuCHTgnd ScuTHTref	Description +5V DC powe +9V DC powe -9V DC power +2.5V DC inte Internal voltag Internal Groun Internal voltag	5.25 9.09 -9.09 2.52 1.23 0.00 1.23	6978 6882 9757 408F 3F14 0006 3F39	4.77 8.89 -9.07 2.47 1.21 -0.04 1.21	Max 5.28 9.07 -8.88 2.55 1.25 0.04 1.25
Reference/V Nom Parametre ScuCHTp05 ScuCHTp09 ScuCHTn09 ScuCHT09 ScuCHT25 ScuCHT25 ScuCHTgnd ScuCHTgnd ScuTHTref	Description +5V DC powe +9V DC powe -9V DC power +2.5V DC inte Internal voltag Internal Groun Internal voltag	5.25 9.09 -9.09 2.52 1.23 0.00 1.23	6978 6882 9757 408F 3F14 0006 3F39	4.77 8.89 -9.07 2.47 1.21 -0.04 1.21	Max 5.28 9.07 -8.88 2.55 1.25 0.04 1.25
Reference/V Nom Parametre ScuCHTp05 ScuCHTp09 ScuCHTn09 ScuCHTn09 ScuCHT25 ScuCHT25 ScuCHTgnd ScuCHTgnd ScuTHTref	Description +5V DC powe +9V DC powe -9V DC power +2.5V DC inte Internal voltag Internal Groun Internal voltag	5.25 9.09 -9.09 2.52 1.23 0.00 1.23	6978 6882 9757 408F 3F14 0006 3F39	4.77 8.89 -9.07 2.47 1.21 -0.04 1.21	Max 5.28 9.07 -8.88 2.55 1.25 0.04 1.25



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SCU redundant side

- SCU					
Status					
Nom Parametre	Description			Valeur Lue H	Valeur Nomin
ScuStatus	SCU Status			0000	
•					()
T					
Temperature	S				
Nom Parametre	Description			Valeur Conver	Unite
Csu Temp	CCHK_IF Booard	l temperature		22.00	°C
Tsu Temp	Temp Board temp			22.00	יב יב יב
Psu Temp1	PSU temperature			23.00	°C
Psu Temp2	PSU temperature	2		24.00	°C
L					
•					•
Reference/V	oltage				
Nom Parametre	Description	Valeur Conver	Valeur Lue H	Min	Max
ScuCHTp05	+5V DC powe	5.24	6946	4.77	5.28
ScuCHTp09	+9V DC powe	9.09	68BA	8.89	9.07
ScuCHTn09	-9V DC power	-9.11	9719	-9.07	-8.88
ScuCHT25	+2.5V DC inte	2.52	408E	2.47	2.55
ScuCHTref	Internal voltag	1.23	3F1A	1.21	1.25
ScuCHTgnd	Internal Groun	0.00	000B	-0.04	0.04
ScuTHTref	Internal voltag	1.24	3F4B	1.21	1.25
ScuTHTgnd	Internal Groun	0.00	0001	-0.04	0.04



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MCU main side

Nom Par	Description	Valeur Decimale	Valeur Convertie	Min	Max	Unite
5V	+5V power supply volt	39682	5.06	4.85	5.25	V
P14V	+14V power supply vo	38920	14.01	14.20	14.60	V
M14V	-14V power supply vol	26474	-14.33	-14.60	-14.20	V
P15V	+15V power supply vo	39524	15.39	15.20	15.60	V
M15V	-15V power supply vol	25972	-15.48	-15.60	-15.20	V
MACTemp	MAC temperature probe	38552	13.04	-5.75	30.00	°С
SMECTemp	SMEC temperature pr	38654	18.08	-5.75	30.00	°C °C
BSMTemp	BSM temperature probe	38645	17.64	-5.75	30.00	°С
ErrorCode	errors, warnings occur	0	0.00	0.00	0.00	



MCU Redundant side

Nom Par	Description	Valeur Decimale	Valeur Convertie	Min	Max	Unite
5V	+5V power supply volt	39663	5.05			V
P14V	+14V power supply vo	38909	13.99			V
M14V	-14V power supply vol	26469	-14.35			V
P15V	+15V power supply vo	39507	15.35			V
M15V	-15V power supply vol	25983	-15.45			V
MACTemp	MAC temperature probe	38568	13.83			°C
SMECTemp	SMEC temperature pr	38647	17.74			°C
BSMTemp	BSM temperature probe	38649	17.83			°C
ErrorCode	errors, warnings occur	0	0.00			





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8.4 SCU Temp / SubK Channels

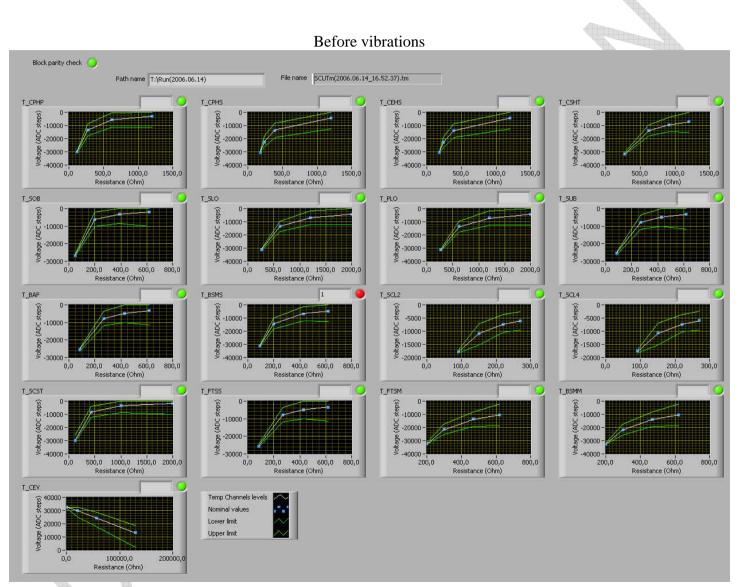
For this test, the FPU simulator is switched ON and runs in slave mode. The TEMP channels are tested with the four set of TEMP resistances that are implemented in the FPU simulator.

The tests have been performed on main and redundant sides. We thereafter show the graphics corresponding to SCU main side :





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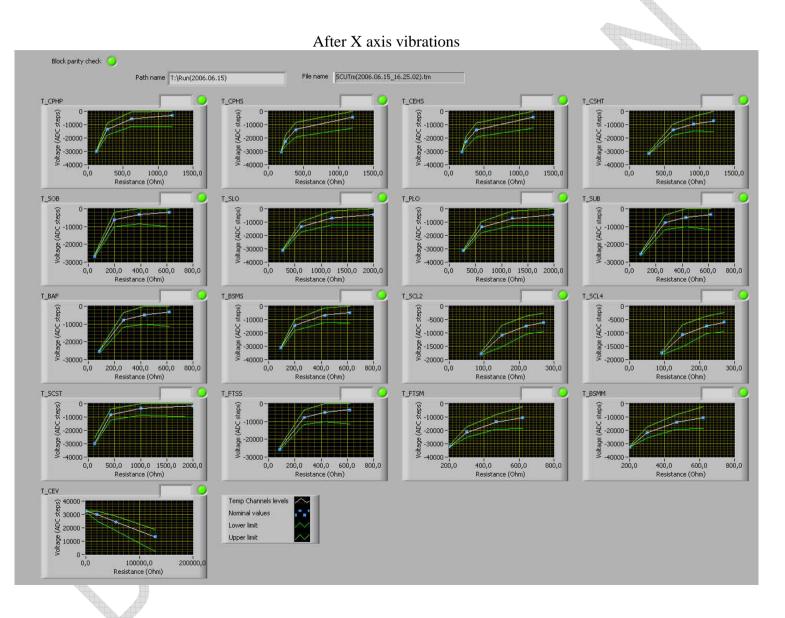
After Y axis vibrations Block parity check File name SCUTm(2006.06.15 12.25.36).tm Path name T:\Run(2006.06.15) T_CEHS T_CPHP T_CPHS T CSHT 0 R -10000 -R -10000 -뽃 -10000 -분 -10000 --20000 -है -20000 -Ê -20000 --20000 -B-30000-₽ -30000 쭑 -30000 ž -30000 -\$ -40000-\$ -40000 -\$ -40000 -\$ -40000 -1000,0 0,0 500.0 1500.0 0,0 500,0 1000,0 1500,0 0,0 500,0 1000,0 1500,0 0,0 500,0 1000,0 1500,0 Resistance (Ohm) Resistance (Ohm) Resistance (Ohm) Resistance (Ohm) T_SLO T_SOB T_PLO T_SUB ₩ -10000 --10000 -ပို -10000 --10000 ğ -20000 -§ -20000--20000 -. 9. -20000 -8 -30000 --30000 -(ol -30000 -40000 -40000 --30000-0,0 200,0 400,0 600,0 800,0 0,0 0,0 0,0 500,0 1000,0 1500,0 2000,0 500,0 1000,0 1500,0 2000,0 200,0 400,0 600,0 800,0 Resistance (Ohm) Resistance (Ohm) Resistance (Ohm) Resistance (Ohm) T_BAF T_BSMS T_SCL2 T_SCL4 Ty. (SC steps) ₩ -10000-He he -5000 --5000 -ပ္ဂ်ိ -10000 -9 -10000 -9 -10000 -§ -20000g -20000 **쌹** -30000 -훓 -15000 -ន្តិ -15000 -2 -40000-\$ -20000 -\$ -20000--30000-0,0 100,0 , o, o 200,0 400,0 600,0 800,0 0,0 200,0 400,0 600,0 800,0 200,0 300,0 0,0 100,0 200,0 300,0 Resistance (Ohm) Resistance (Ohm) Resistance (Ohm) Resistance (Ohm) T_SCST T_FTSS T_FTSM T_BSMM n 0. 0 ₩ -10000-₩ -10000-₩ -10000--10000-Q -20000 ğ -20000 -₹ -20000g -20000 · ੈ -30000 -뿕 -30000 · -30000 -\$ -40000 \$ -30000-\$ -40000-\$ -40000 500,0 1000,0 1500,0 2000,0 200,0 400,0 600,0 800,0 0,0 0,0 200,0 400,0 600,0 800,0 200,0 400,0 600,0 800,0 Resistance (Ohm) Resistance (Ohm) Resistance (Ohm) Resistance (Ohm) T_CEV g 40000 Temp Channels levels Nominal values ₩ 30000 ₹ 20000 · Lower limit Upper limit ්සි 10000 -200000,0 0,0 100000.0 Resistance (Ohm)





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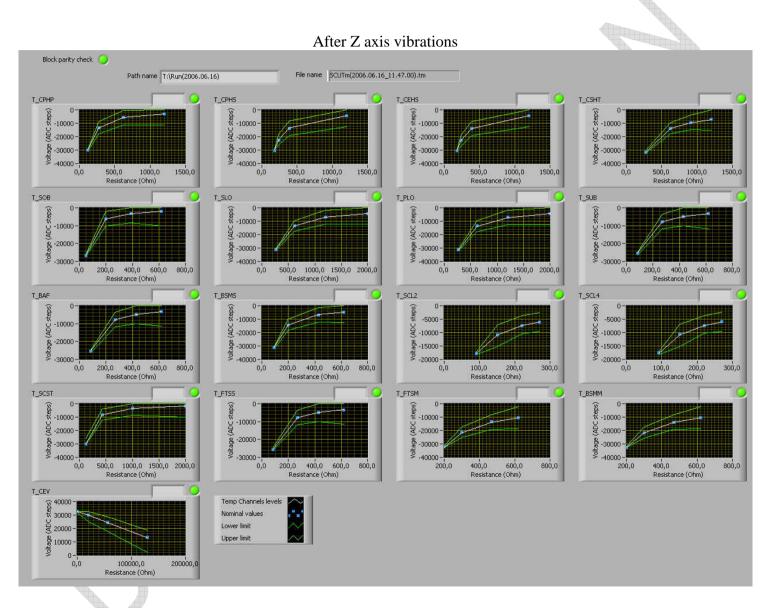








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We notice that the results remain within the specified margins before and after the vibrations on each axis.

To study in detail, we have plotted, for each TEMP channel, the difference of response relatively to prior the vibrations : difference in encoder steps wrt resistance values respectively after the vibrations on X axis (white plot), Y axis (red plot) and Z axis (green plot).

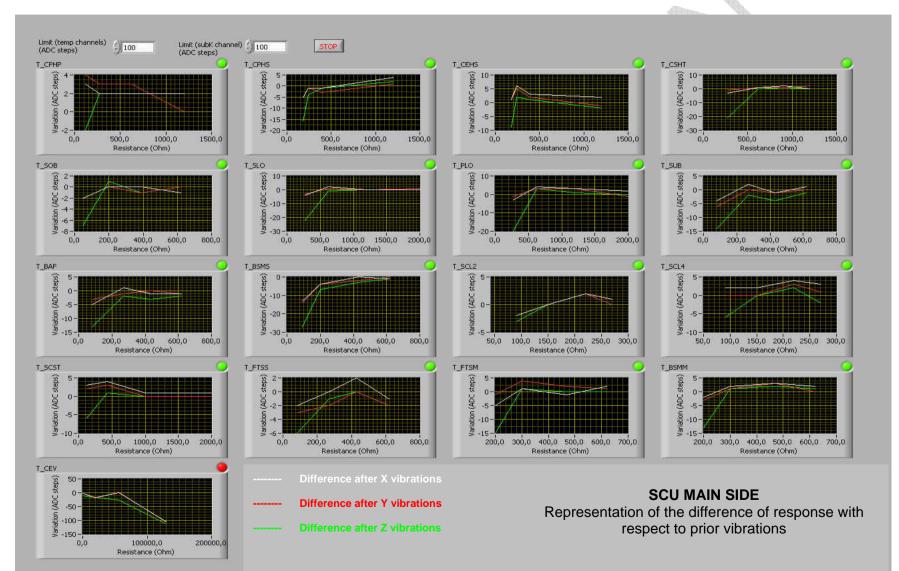
As we notice on the thereafter plots, the results remain within approximately 10 encoder steps (except for the SUBK probe, CEV, that is much more sensitive, especially at high resistance values) which means that the vibrations have no effect on the TEMP channels responses.





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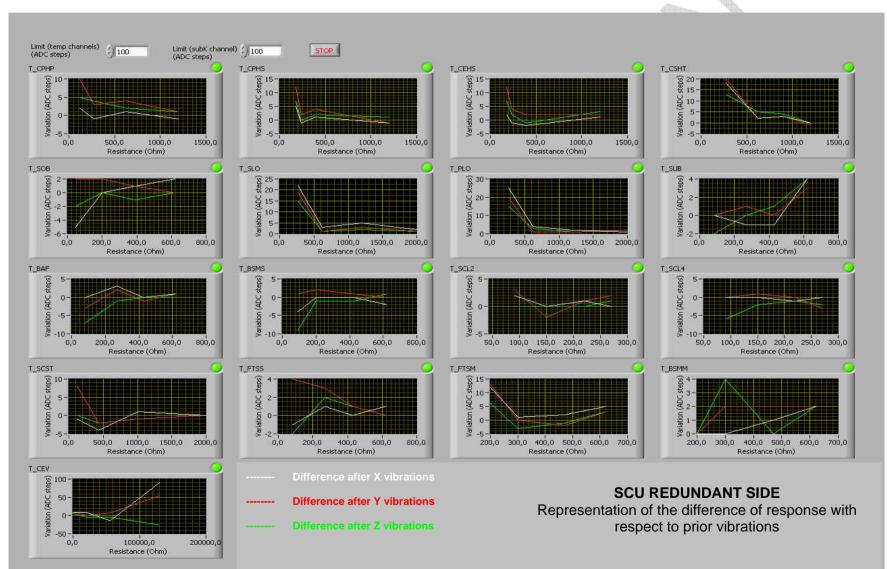








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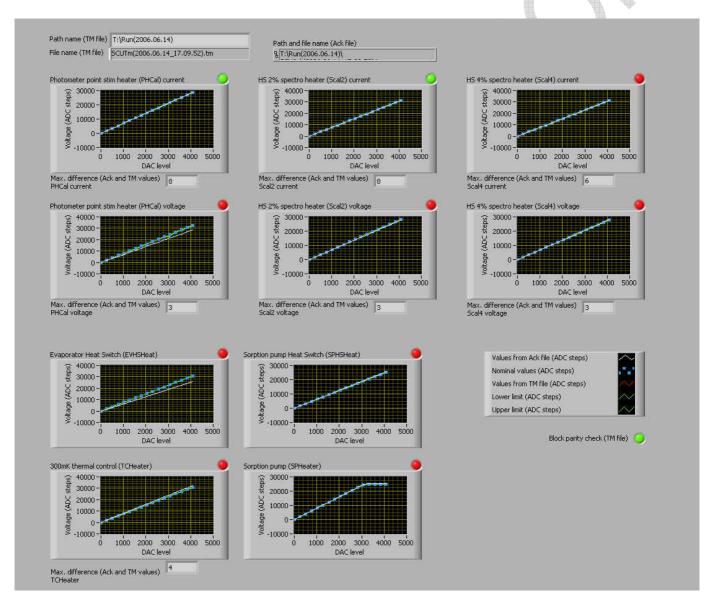
8.5 SCU Heaters Calibrators tests

For this test, the FPU simulator switched ON and runs in stand alone mode. The Heaters/calibrators channels are tested for 17 ADC values over the range [0 - 4095].

The tests have been performed on main and redundant sides. We thereafter show the graphics corresponding to SCU main side :

:

Before vibrations :

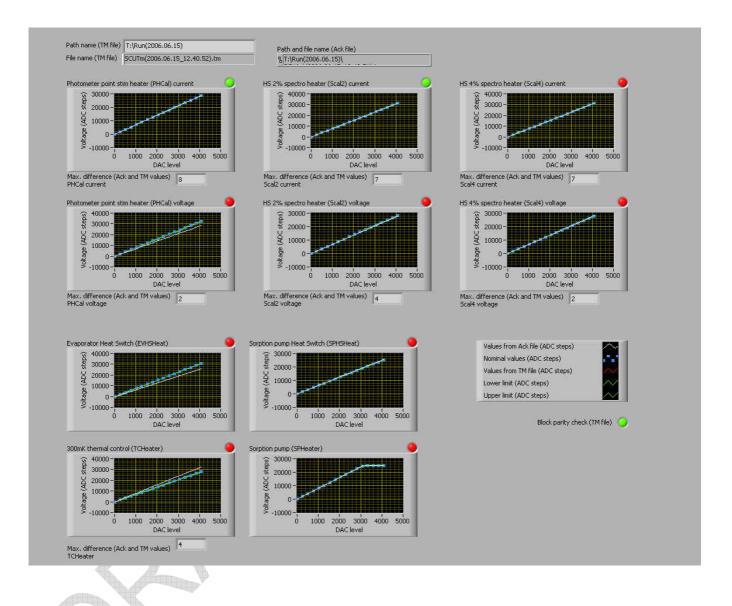






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After Y axis vibrations

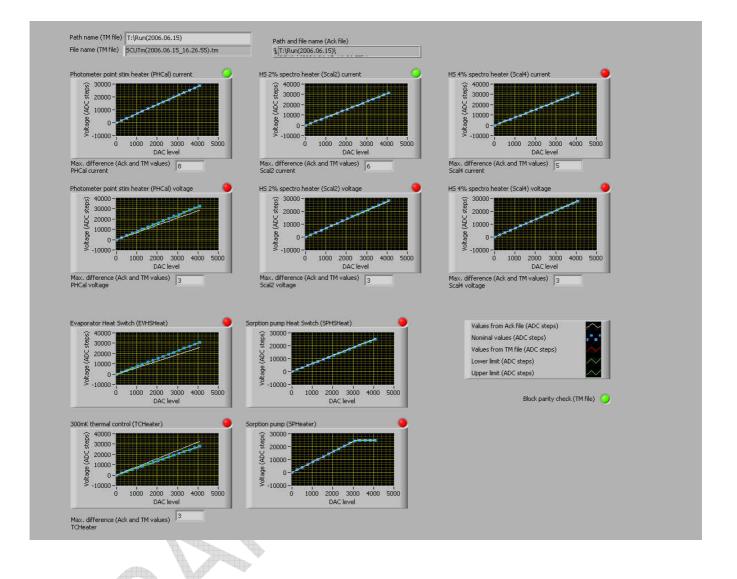






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After X axis vibrations

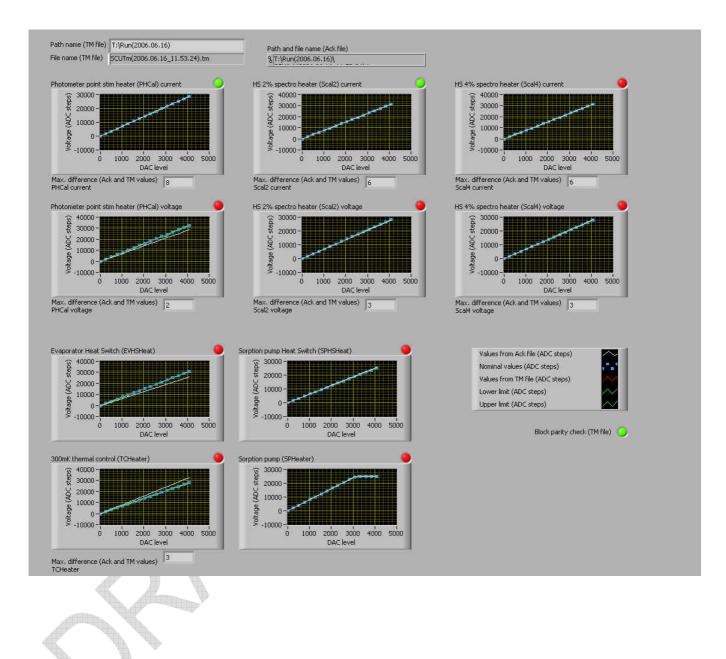






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After Z axis vibrations

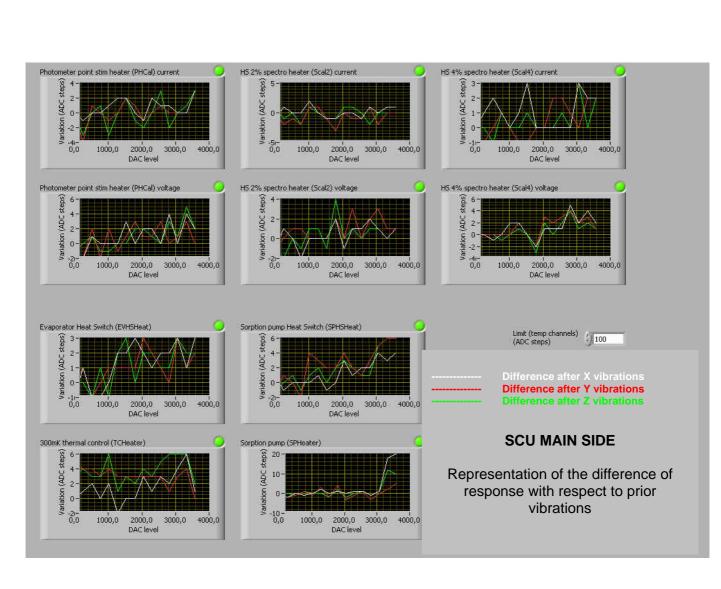






We notice that the results remain within the specified margins before and after the vibrations on each axis.

To study in detail, we have plotted, for each Heater/Calibrator channel, the difference of response relatively to prior the vibrations : difference in encoder steps wrt resistance values respectively after the vibrations on X axis (white plot), Y axis (red plot) and Z axis (green plot).



We notice that the results remain within approximately 5 encoder steps which means that the vibrations have no effect on the Heater/Calibrator channels responses.





8.6 Currents generated by SCU as measured by FPU simulator

In this test, we set the DAC to their maximum and measure the currents generated on Heaters/Calibrators channels at FPU simulator level.

The results, as obtained after the vibrations, are the following :

After Vibrations, Main side

After Vibrations, Redundant side

	Measured	Expected ⁽¹⁾
Sorption pump main :	38.216 E-3 A	38.157E-3 A
Evaporator Switch main :	1.627E-3 A	1.625E-3 A
Sorption pump Switch main :	1.628E-3 A	1.626E-3 A
300mK thermal control main :	4.538E-6 A	4.533E-6 A
HS spect 4% main :	5.610E-3 A	5.597E-3 A
HS spect 2% main :	5.607E-3 A	5.598E-3 A
Photometer point stim main :	7.194E-3 A	7.183E-3 A
-		

The violations, Redaindant side		
	Measured	$Expected^{(1)}$
Sorption pump main :	38.061 E-3 A	38.157E-3 A
Evaporator Switch main :	1.628E-3 A	1.625E-3 A
Sorption pump Switch main :	1.628E-3 A	1.626E-3 A
300mK thermal control main :	4.538E-6 A	4.533E-6 A
HS spect 4% main :	5.610E-3 A	5.597E-3 A
HS spect 2% main :	5.607E-3 A	5.598E-3 A
Photometer point stim main :	7.192E-3 A	7.183E-3 A

⁽¹⁾From tests at saclay

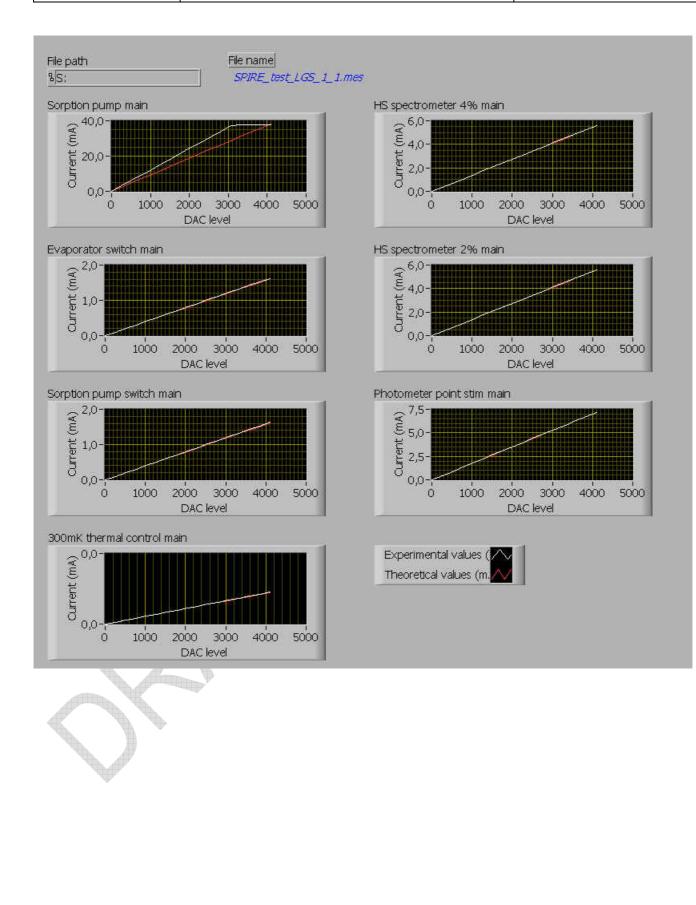
These results are consistent with the specifications. No effect due to the vibrations is observed.

When plotting the evolution wrt DAC level from the FPU simulator data, we get the following representation (where the maximum value correspond to the above "After vibrations" data) :





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8.7 MCU functional parameters

After the vibrations on each axis, the MCU functional parameters, coming from the telemetry messages, have been tested on main and redundant sides and under the following functional configurations :

- Configuration 1 : MCU ON, all currents applied to the mechanisms set at 0 level
- Configuration 2 : MCU ON, all currents applied to the mechanisms set at 0 level; latch engaged
- Configuration 3 :MCU ON, all currents applied to the mechanisms set at 10% level;
- Configuration 4 :MCU ON, all currents applied to the mechanisms set at 50% level;
- Configuration 3 :MCU ON, all currents applied to the mechanisms set at 100% level;

Under each of these configurations, the values of the following parameters :

- Encoder Sinus 0°,
- Encoder Sinus 120°,
- Encoder sinus 240°,
- LVDT AC
- LVDT DC
- SMEC Motor Current,
- SMEC Motor Voltage
- Chopper Sensor
- Chopper current
- Chopper Voltage
- Jiggle sensor
- Jiggle current
- Jiggle voltage

have been compared to expected values. The values of these parameters remained nominal following the vibrations tests.

9 Conclusion

The tests reveal no susceptibility of the FCU FM subsystem (mechanical and functional aspects) to the acceptance vibrations levels.