



### 1. Introduction

This document reports on the activities carried out to verify the procedures and setup of the AVM to be used at FN for the AVM UFTs.

## 1.1 Scope

The results appearing in report are only applicable to the AVM scenario, although the same CCS templates will be used for both the AVM and the FM.

## **1.2 Applicable Documents**

AD#	Title	Reference	Issue#	Date
AD01	SPIRE_WU_INT_Procedures	SPIRE-RAL-PRC-002680	1.3	15 <sup>th</sup> Feb 2007
AD02				

### **1.3 Reference Documents**

RD#	Title	Reference	Issue#	Date
RD01	SPIRE Instrument User Manual	SPIRE-RAL-PRJ-002395	1.0	08/04/2005
RD02	H/P OBT-UTC Time	PT-CMOC-OPS-TN-6604-	1.3	Sep 2004
	Synchronisation Technical Note	OPS- OGH		_
RD03	Spire Instrument Block Diagram	SPIRE-RAL-DWG-000646	6.1	
RD04	DRCU Simulator HW/SW User		1.0	26/11/2003
	Manual			

## 1.4 Change Record

Doc	Issue#	Changes	Date of Change
Issue	1.0		
Issue	1.1	Updated report after using the latest AVM procedures to check the templates.	16/02/07

## 1.5 List of Acronyms

FM	Flight Model
AVM	Avionics Model
OBT	On Board Time
FPU	Focal Plane Unit
CCS	Central Checkout System
FM	Integrated System Test
EGSE	Electrical Ground Support Equipment
DPU	Digital Processing Unit
DRCU	Detector Readout and Control Unit





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### 2. Test Configuration

The following sections describe the hardware and ESGE configuration present at the time of the test. The main differences between the AVM and the FM configurations are with respect to the hardware. On the SPIRE AVM hardware the Warm Units and the FPU are substituted by a DRCU simulator.

## 2.1 AVM (at RAL) Test Configuration

This is the required hw/sw configuration prior to the start of the test:

#### Hardware:

SPIRE WU and CDMS Sim:

- The SPIRE DRCU Simulator PC powered ON and the operating system running.
- The SPIRE DRCU Simulator PC interconnected with the SPIRE FM DPU following RD04.
- The SPIRE AVM1 DPU PRIME 1553 interface to the CDMS Simulator connected (HSDPU J03).
- The SPIRE AVM1 DPU PRIME power interface to power supply connected (HSDPU J01).

#### Software:

#### EGSE:

- HCSS v0.4.1 Build (# 1123)
- Router, gateway, packet display running on chichester
- The SPIRE MIB 2.2.G1 imported on the CCS.
- SCOS2.3eP5 running on Truro
- CCS templates v1.1 from CVS AVM folder
- Using configuration wu\_int\_config on the test\_backup db





## 3. Warm Units Integration Test Procedure Overview

### 3.1 General instructions for executing the test procedures

- Before carrying out the next procedure within the test sequence always ask for the go ahead by the SPIRE staff.
- Section 3.4 of this document specifies the sequence to be executed. Each of the steps in the sequence has a detailed specification later on sections 4.1 and 4.2. The operator should refer to the later in order to execute detailed steps.
- The procedure tables in section 4.1 and 4.2 include blank boxes where the actual values of parameters can be noted. Based on the comparison with the expected values the success or failure of a step should be recorded in the final column of the table.
- The last row in a procedure table should be used to record the overall Pass/Fail result of each test.
- Any text in boldface in the procedural steps generally indicates an action which may have to be performed manually by the CCS staff.

## 3.2 General Pass/Fail Criteria

Consecutive failure of 2 executions of the same procedure is enough to declare the overall test result as failed. If the repetition of a failed test execution is successful this one should be repeated once again as a 'health' check. In case of overall failure of the test procedure the switch off steps 9, 10 and 11 (or 22, 23 and 24) from the next section should be executed.

### 3.3 Constrains

A general constrain (inferred from the test configuration described above) is that the SPIRE DPU and DRCU power interfaces to the Herschel satellite must connected and the DPU and DRCU must be interconnected before carrying out this procedure.

### 3.4 Test Sequence

This section specifies the sequence to be executed with estimated times for each execution. **During AVM** although no redundancy is present the redundant procedures can still be tested with the AVM DPU PRIME as the uplink test sequences do not change, executing the redundant procedures on the PRIME DPU will not result in any harm to the unit.

Step	Procedure Name	Purpose	Duration
#			
1	SPIRE-WU-INT-DPU-ON-P	DPU PRIME Power up and OBS start	5 min
2	SPIRE-WU-INT-DRCU-ON-P	DRCU PRIME Power up	5 min
3	SPIRE-WU-INT-SCU-01-P	SCU Low Speed Link check	5 min
4	SPIRE-WU-INT-SCU-02-P	SCU High Speed Link check	5 min
5	SPIRE-WU-INT-MCU-01-P	MCU Low Speed Link check	5 min
6	SPIRE-WU-INT-MCU-02-P	MCU High Speed Link check	5 min
7	SPIRE-WU-INT-DCU-01-P	DCU Low Speed Link check	5 min
8	SPIRE-WU-INT-DCU-02-P	DCU High Speed Link check	5 min
9	SPIRE-WU-INT-MCU-OFF-P	MCU power off	5 min
10	SPIRE-WU-INT-DRCU-OFF-P	DRCU PRIME power off	5 min
11	SPIRE-WU-INT-DPU-OFF-P	DPU PRIME power off	5 min





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12	Reconfigure the 1553 Spacecraft bus from SPIRE DPU PRIME (RT = 21) to SPIRE DPU REDUNDANT (RT = 22).		5 min
13	Change to SPIRE Redundant MIB on the CCS (If applicable)		5 min
14	SPIRE-WU-INT-DPU-ON-R	DPU RED Power up and OBS start	5 min
15	SPIRE-WU-INT-DRCU-ON-R	DRCU RED Power up	5 min
16	SPIRE-WU-INT-SCU-01-R	SCU Low Speed Link check	5 min
17	SPIRE-WU-INT-SCU-02-R	SCU High Speed Link check	5 min
18	SPIRE-WU-INT-MCU-01-R	MCU Low Speed Link check	5 min
19	SPIRE-WU-INT-MCU-02-R	MCU High Speed Link check	5 min
20	SPIRE-WU-INT-DCU-01-R	DCU Low Speed Link check	5 min
21	SPIRE-WU-INT-DCU-02-R	DCU High Speed Link check	5 min
22	SPIRE-WU-INT-MCU-OFF-R	MCU power off	5 min
23	SPIRE-WU-INT-DRCU-OFF-R	DRCU RED power off	5 min
24	SPIRE-WU-INT-DPU-OFF-R	DPU RED power off	5 min

Total: ~ 120 min





 
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# 4. AVM Database Backup

# 4.1 Backing up the avm database:

Step#	Action	Command line	Result
1	Stopped the db you want to	stopdb –s avm_test@chichester	
	backup		
2	Put the db into single user	stopdb -s avm_test@chichester	
	mode		
3	Backed up the db:		
	Created 2 backups:		
	One is the test backup that will	vbackup –device test_backup.bck –backup	
	be restored here (at RAL).	avm_test@chichester	
		-hadren daries som dard hadren hals hadren	
	that will be taken to EN	vbackup –device avm_test_backup.bck –backup	
	that will be taken to FN.	avm_test@cmcnester	
4	Compressed backups:	gzin test, backun bek	
-	Compressed backups.	gzin avm test backun bck	
5	Uncompressed test backup:	gunzip test backup.bck	
6	Restored test backup:	Sum t us funn t	
-	1. Change dir to dbs	AT RAL : cd /spired/verant/db	
	directory:	AT FN: cd /data/versant/db	
	2. Create folder that will	AT RAL : makedb test backup	
	hold database	AT FN: makedb avm_test	
	volumes:		
	3. Restore database from	AT RAL: vbackup –device	
	backup:	/home/sg55/test_backup.bck -restore	
	_	avm_test@chichester -rename	
		test_backup@chichester	
		AT FN: vbackup –device	
		full path where back up is/avm test backup.bc	
		k –restore avm_test@db_server_name (hos4-d?)	
7	Change the user.props file	Edit the file	
	(var.database.devel) located		
	under \${USER_HOME}/.hcss		
	to refer to the new restored		
	database.		
8	Try to open the cusgui.	cusgui&	
	Note: At RAL got an error		
	complaining about the name of		
	the database so had to do:		
	db_admin –n		
	test_bakcup@chichester in		
1	order to get it to work.		1





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9	Check whether all configurations are there.	missetup –listconfig	
10	Update the configuration for the required test: i.e. for WU INT test will updated the configuration to wu_int_config.	Edit user.props	





# 5. Warm Units Integration Detailed Test Results

## 5.1.1 Prime Results

#### 5.1.1.1 Procedure SPIRE-WU-INT-DPU-ON-P

Version	1.3	
Date	15 <sup>th</sup> February 2007	
Purpose	To switch on the SPIRE DPU PRIME and start generating housekeeping	
Initial configuration	SPIRE DPU and DRCU PRIME are switched off	
Final configuration	SPIRE DPU PRIME is ON and SPIRE HK is being produced, SPIRE DRCU	
	PRIME is OFF	
Preconditions	• SPIRE FM DPU is electrically integrated with the Herschel Satellite	
	• SPIRE MIB PRIME is imported in the CCS database.	
	• CCS is up and running	
	IEGSE is up and running	
	• DPU AND OBS PARAMETERS display is selected on the CCS	
Duration	5 minutes	
Pass/Fail criteria	Nominal and critical HK reports start being generated at their nominal rates of	
	1Hz and 0.5Hz respectively.	





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#### **Procedure Steps for AVM:**

Step	Description	Parameter	Expected Values Before/After	Actual Values Before/After	Pass/ Fail
1	Select DPU AND OBS PARAMETERS display is on the CCS			_	
2	Power ON the SPIRE DPU PRIME		—	—	
	unit using the dedicated spacecraft LCL line and configure 1553 Spacecraft bus for SPIRE DPU PRIME (RT = 21)				*
3	Wait for the boot software to produce at least 2 event packets (5,1)				✓
4	From manual stack sent FORCE_BOOT_PRIMARY, then pressed the reset RED button.			_	~
5	Check that Nominal and Critical HK packets are arriving at the CCS: <b>SPIRE Nominal HK:</b> • (type ,subtype) : (3,25) • APID : 0x502 <b>SPIRE Critical HK:</b> • (type ,subtype) : (3,25) • APID: 0x500				~
6	When the HK requests start being generated several HK parameters will go Out of Limits (Hard). This is a design feature of the DRCU Simulator and cannot be avoided.	PLIAP5V PLIAP9V PLIAM9V SLIAP5V SLIAP9V SLIAM9V LIAPiTEMP LIASiTEMP BIASTEMP DAQTEMP	-/ OOL -/ OOL -/ OOL -/ OOL -/ OOL -/ OOL -/ OOL -/ OOL -/ OOL -/ OOL		~
7	Check that THSK parameter is refreshing every second	THSK	Refreshing @ 1 Hz	_	~
8	Check that TM2N parameter is incrementing by 1 every second	TM2N	Incrementing by 1 @ 1Hz	—	✓
9	Check that TM1N parameter is incrementing by 1 every 2 second	TM1N	Incrementing by 1 @ 0.5Hz		✓
10	On CCS check the consistency of the SPIRE on board time to the HCDMU time and the CCS. *				~
11	On IEGSE check the consistency between SCOS time and THSK and QLA time.	THSK	Incrementing once per second		~
Test F	Result (Pass/Fail): <mark>Pass</mark>				





#### 5.1.1.2 Procedure SPIRE-WU-INT-DRCU-ON-P

Version	1.3	
Date	15 <sup>th</sup> February 2007	
Purpose	To switch on the SPIRE DRCU PRIME and start generating housekeeping	
Initial configuration	SPIRE DPU is ON and the DRCU are switched off	
Final configuration	SPIRE DPU and DRCU are ON and SPIRE HK is being produced	
Preconditions	econditions • SPIRE FM DPU and DRCU are electrically integrated with the Herschel	
	Satellite	
	• SPIRE MIB PRIME is imported in the CCS database.	
	• CCS is up and running	
	IEGSE is up and running	
	• FUNCTIONAL TEST PARAMETERS display is selected on the CCS	
Duration	5 minutes	
Pass/Fail criteria	DRCU housekeeping telemetry shows expected 'ON' voltages	

#### **Procedure Steps for AVM:**

Step	Description	Parameter	Expected Values Before/After	Actual Values Before/After	Success/ Failure
1	Execute TCL script SPIRE-WU-INT- DRCU-START-P-STEP1.tcl		_		✓
2	Check that THSK parameter is not refreshing anymore	THSK	Not refreshing	Not refreshing	~
3	Check that TM2N parameter is not incrementing anymore	TM2N	Not incrementing	Not incrementing	✓
4	Start DRCU simulator application software.*		_		✓
5	Execute TCL script SPIRE-WU-INT- DRCU-START-P-STEP2.tcl		_		✓
6	Check that THSK parameter is again refreshing every second	THSK	Refreshing @ 1Hz	Refreshing @ 1Hz	~
7	Check that TM2N parameter is again incrementing every second	TM2N	Incrementing by 1 @ 1Hz	Incrementing by 1 @ 1Hz	✓
Test I	Result (Pass/Fail): <mark>Pass</mark>				

\* It is assumed that the DRCU simulator PC is already ON. Double click on the Transmit.exe icon on the desktop of the PC to start the application software.





 
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#### 5.1.1.3 Procedure SPIRE-WU-INT-SCU-01-P

Version 1.3 Date 15<sup>th</sup> February 2007 To check the correct functioning of the SCU PRIME Low Speed Link Purpose SPIRE DPU and DRCU PRIME are switched ON, SPIRE HK is being **Initial configuration** produced **Final configuration** Identical • SPIRE-WU-INT-DPU-ON-P and SPIRE-WU-INT-DRCU-ON-P Preconditions procedures have been executed. • SPIRE MIB PRIME is imported in the CCS database. • CCS is up and running • IEGSE is up and running • FUNCTIONAL TEST PARAMETERS display is selected on the CCS 5 minutes Duration SCUTEMPSTAT and SUBKSTAT HK parameters show expected values Pass/Fail criteria

#### **Procedure Steps:**

Step	Description	Parameter	Expected Values Before/ After	Actual Values Before /After	Pass/Fail
1	Execute TCL script SPIRE-WU-INT-	SCUTEMPSTAT	0/0xFFFF	0/0xFFFF	
	SCU-01-P.tcl	SUBKSTAT	0/1	0/1	
Test R	Test Result (Pass/Fail): Pass				





 
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### 5.1.1.4 Procedure SPIRE-WU-INT-SCU-02-P

<u>Next></u>

Version	1.3		
Date	15 <sup>th</sup> February 2007		
Purpose	To check the correct functioning of the SCU PRIME High Speed Link		
Initial configuration	SPIRE DPU and DRCU PRIME are switched ON, SPIRE HK is being		
	produced		
Final configuration	Identical		
Preconditions	• SPIRE-WU-INT-DPU-ON-P and SPIRE-WU-INT-DRCU-ON-P		
	procedures have been executed.		
	• SPIRE MIB PRIME is imported in the CCS database.		
	• CCS is up and running		
	• IEGSE is up and running		
	• FUNCTIONAL TEST PARAMETERS display is selected on CCS		
Duration	n 5 minutes		
Pass/Fail criteria	Two SCU Nominal Science telemetry packets are received at CCS with :		
	• (type,subtype): (21,1).		
	• APID : 0x508		

#### **Procedure Steps:**

Step	Description	Parameter	Expected	Actual	Success/
			Values	Values	Failure
			Before/	Before	
			After	/After	
1	Execute TCL script SPIRE-WU-INT-	SCUFRAMECNT	0/31	0/0	Failure
	SCU-02-P.tcl	TM5N	0x3FFF/1	0x3FFF/1	
2	Verify that two telemetry packets				
	with :				
	• (type,subtype): (21,1).				
	• APID : 0x508				
	have been received at CCS				
Test F	Result (Pass/Fail): DRCU Simulator h	as produced 2 (21,4) tm	packets		

NT





 
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#### 5.1.1.5 Procedure SPIRE-WU-INT-MCU-01-P

Version	1.3		
Date	15 <sup>th</sup> February 2007		
Purpose	To check the correct functioning of the MCU PRIME Low Speed Link		
Initial configuration	SPIRE DPU and DRCU PRIME are switched ON, SPIRE HK is being		
	produced		
<b>Final configuration</b>	SPIRE DPU and DRCU PRIME are switched ON, SPIRE HK is being		
	produced and MCU is booted.		
Preconditions	• SPIRE-WU-INT-DPU-ON-P and SPIRE-WU-INT-DRCU-ON-P		
	procedures have been executed.		
	SPIRE MIB PRIME is imported in the CCS database.		
	• CCS is up and running		
	• IEGSE is up and running		
	<ul> <li>MCU PARAMETERS display is selected on the CCS</li> </ul>		
	• FUNCTIONAL TEST PARAMETERS display is selected on the CCS		
Duration	5 minutes		
Pass/Fail criteria	MCU housekeeping telemetry shows expected 'ON' voltages		

### **Procedure Steps for AVM:**

Step	Description	Parameter - Unit	Expected Values Before/ During/ After	Actual Values Before/ During/ After	Pass/Fail
1	Execute TCL script SPIRE-WU- INT-MCU-01-P.tcl				_
2	Check that the MCU is booted up successfully	MCUBITSTAT	0/-/1	0/-/1	✓
Test F	Test Result (Pass/Fail): Pass				





 
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#### 5.1.1.6 Procedure: SPIRE-WU-INT-MCU-02-P

Version	1.3		
Date	15 <sup>th</sup> February 2007		
Purpose	To check the correct functioning of the MCU PRIME High Speed Link		
Initial configuration	SPIRE DPU and DRCU are switched ON, SPIRE HK is being produced and		
	MCU is booted.		
Final configuration	Identical		
Preconditions	• SPIRE-WU-INT-DPU-ON-P and SPIRE-WU-INT-DRCU-ON-P		
	procedures have been executed.		
	• SPIRE-WU-INT-MCU-01-P has been run successfully		
	• SPIRE MIB PRIME is imported in the CCS database.		
	• CCS is up and running		
	IEGSE is up and running		
	• FUNCTIONAL TEST PARAMETERS display is selected on the CCS		
Duration	5 minutes		
Pass/Fail criteria	The following MCU telemetry packet types are received at CCS with :		
	ENG:		
	- (type,subtype): (21,3).		
	- APID 0x508		
	BSM		
	- (type,subtype): (21,1).		
	- APID 0x508		
	SMEC		
	- (type,subtype): (21,1).		
	- APID 0x508		

### **Procedure Steps:**





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Step	Description	Parameter - Unit	Expected Values Before/ During/ After	Actual Values Before/ During/ After	Success/ Failure
1	Execute TCL script SPIRE-WU- INT-MCU-02-P.tcl	_	_	_	
2	Record the values of MCUFRAMECNT at the start and end of the test	MCUFRAMECNT	<b>FM</b> : 0/297 <b>AVM</b> : 0/~300	0 /~202	Failure
3	Verify that the following type of MCU telemetry packets have been received at the CCS : ENG: - (type,subtype): (21,3). - APID 0x508 BSM - (type,subtype): (21,1). - APID 0x508 SMEC -(type,subtype): (21,1). - APID 0x508				
Test H	Result (Pass/Fail): DRCU Simulator	produces 2 ENG fram	es , then 100 SMI	EC, then 10	0 BSM





 
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### 5.1.1.7 Procedure SPIRE-WU-INT-DCU-01-P

<u>Next></u>

Version	1.3
Date	15 <sup>th</sup> February 2007
Purpose	To check the correct functioning of the DCU PRIME Low Speed Link
Initial configuration	SPIRE DPU and DRCU PRIME are switched ON, SPIRE HK is being
	produced and MCU is booted.
Final configuration	Identical
Preconditions	• SPIRE-WU-INT-DPU-ON-P and SPIRE-WU-INT-DRCU-ON-P
	procedures have been executed.
	• SPIRE MIB PRIME is imported in the CCS database.
	CCS is up and running
	IEGSE is up and running
	<ul> <li>BIAS PARAMETERS display is selected on the CCS</li> </ul>
	• FUNCTIONAL TEST PARAMETERS display is selected on the CCS
Duration	5 minutes
Pass/Fail criteria	PSWBIAS, PMWBIAS and PLWBIAS HK parameters show expected values

### **Procedure Steps:**

Step	Description	Parameter	Expected Values Before/ After	Actual Values Before /After	Success/ Failure
1	Execute TCL script SPIRE-WU-INT-	PSWBIAS	0/0xff/0	0/0xff/0	✓
	DCU-01-P.tcl	PMWBIAS	0/0xff/0	0/0xff/0	
		PLWBIAS	0/0xff/0	0/0xff/0	
Test F	Test Result (Pass/Fail): Pass				





 
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### 5.1.1.8 Procedure SPIRE-WU-INT-DCU-02-P

Version	1.3	
Date	15 <sup>th</sup> February 2007	
Purpose	To check the correct functioning of the DCU PRIME High Speed Link	
Initial configuration	SPIRE DPU and DRCU PRIME are switched ON, SPIRE HK is being	
	produced and MCU is booted.	
Final configuration	Identical	
Preconditions	• SPIRE-WU-INT-DPU-ON-P and SPIRE-WU-INT-DRCU-ON-P	
	procedures have been executed.	
	• SPIRE MIB PRIME is imported in the CCS database.	
	• CCS is up and running	
	• IEGSE is up and running	
	<ul> <li>DCU PARAMETERS display is selected on the CCS</li> </ul>	
	<ul> <li>FUNCTIONAL TEST PARAMETERS display is selected on the CCS</li> </ul>	
Duration	5 minutes	
Pass/Fail criteria	The following DCU telemetry packet types are received at CCS with :	
	Full Photometer:	
	- (type,subtype): (21,1).	
	- APID 0x504	
	PSW	
	- (type,subtype): (21,2).	
	- APID 0x504	
	-(type,subtype): $(21,2)$ .	
	FLW (type subtype): (21.2)	
	- APID 0x504	
	Full Spectrometer:	
	- (type,subtype): (21,1).	
	- APID 0x506	
	SSW	
	- (type,subtype): (21,2).	
	- APID 0x506	
	SLW	
	-(type,subtype): (21,2).	
	- APID 0x506	

#### **Procedure Steps:**





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Step Description Expected Parameter Actual Values Values Success/ **Before**/ Before Failure After /After Execute TCL script SPIRE-WU-INT-DCUFRAMECNT FM: 0/700 0 / 700 1 ✓ DCU-02-P.tcl AVM: 0/~700 2 Verify that the following type of \_\_\_\_ \_\_\_\_ DCU science telemetry packets have been received at the CCS : Full Photometer: - (type,subtype): (21,1). - APID 0x504 **PSW** - (type,subtype): (21,2). - APID 0x504 PMW -(type,subtype): (21,2). - APID 0x504 PLW 1 -(type,subtype): (21,2). - APID 0x504 Full Spectrometer: - (type,subtype): (21,1). - APID 0x506 SSW - (type,subtype): (21,2). - APID 0x506 SLW -(type,subtype): (21,2). - APID 0x506 Test Result (Pass/Fail): Pass



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#### 5.1.1.9 Procedure SPIRE-WU-INT-MCU-OFF-P

<u>Next></u>

Version	1.3		
Date	15 <sup>th</sup> February 2007		
Purpose	To switch OFF the MCU PRIME		
Initial configuration	SPIRE DPU and DRCU PRIME are switched ON, SPIRE HK is being		
	produced and MCU PRIME is booted.		
Final configuration	SPIRE DPU and DRCU PRIME are switched ON, SPIRE HK is being		
	produced and MCU PRIME is OFF.		
Preconditions	litions • SPIRE-WU-INT-DPU-ON-P and SPIRE-WU-INT-DRCU-ON-P		
	procedures have been executed.		
	• SPIRE MIB PRIME is imported in the CCS database.		
	CCS is up and running		
	IEGSE is up and running		
	• FUNCTIONAL TEST PARAMETERS display is selected on the CCS		
Duration	5 minutes		
Pass/Fail criteria	Specified MCU HK Parameter shows expected value.		

Step	Description	Parameter – Unit	Expected Values Before/ During/ After	Actual Values Before/ During/ After	Success/ Failure
1	Execute SPIRE-WU-INT-MCU- OFF-P.tcl				
2	Check that the MCU is switched off	MCUBITSTAT	1/-/0	1/0	✓
Test Result (Pass/Fail): Pass					

**Procedure Steps:** 



Spire Procedure

### 5.1.1.10 Procedure SPIRE-WU-INT-DRCU-OFF-P

<u>Next></u>

Version	1.3	
Date	15 <sup>th</sup> February 2007	
Purpose	Purpose To switch OFF the DRCU PRIME	
Initial configuration	n SPIRE DPU and DRCU PRIME are switched ON, SPIRE HK is being	
	produced and MCU is OFF.	
<b>Final configuration</b>	ion SPIRE DPU PRIME is ON (but no HK is being produced) and DRCU	
	PRIME is switched OFF.	
Preconditions	s • SPIRE-WU-INT-DPU-ON-P and SPIRE-WU-INT-DRCU-ON-P	
	procedures have been executed.	
	• SPIRE MIB PRIME is imported in the CCS database.	
	• CCS is up and running	
	• IEGSE is up and running	
	• FUNCTIONAL TEST PARAMETERS display is selected on the CCS	
Duration	1 5 minutes	
Pass/Fail criteria	THSK and TM2N stop refreshing/incrementing	

Step	Description	Parameter - Unit	Expected Values Before/ During/ After	Actual Values Before/ During/ After	Success/ Failure
1	Execute TCL script SPIRE-WU- INT-DRCU-OFF-P.tcl	_	_		
2	Check that THSK parameter is not refreshing anymore	THSK	Not refreshing	Not refreshing	~
3	Check that TM2N parameter is not incrementing anymore	TM2N	Not incrementing	Not increment ing	<b>√</b>
4	IEGSE staff: Stop DRCU Simulator application software				
Test Result (Pass/Fail), Pass					

**Procedure Steps for AVM:** 



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#### 5.1.1.11 Procedure SPIRE-WU-INT-DPU-OFF-P

Version	1.3		
Date	15 <sup>th</sup> February 2007		
Purpose	To switch OFF the DPU PRIME		
Initial configuration	uration SPIRE DPU PRIME is ON (but no HK is being generated) and the DRCU		
	PRIME is OFF.		
Final configuration	n SPIRE DPU and DRCU PRIME are switched OFF.		
Preconditions	• SPIRE-WU-INT-DRCU-OFF-P procedure has been executed.		
	• SPIRE MIB PRIME is imported in the CCS database.		
	• CCS is up and running		
	IEGSE is up and running		
	• FUNCTIONAL TEST PARAMETERS display is selected on the CCS		
Duration	5 minutes		
Pass/Fail criteria	Power to SPIRE DPU PRIME is OFF		

Step	Description	Parameter – Unit	Expected Values Before/ During/ After	Actual Values Before/ During/ After	Success/ Failure
1	Power OFF the SPIRE DPU PRIME unit.	_			
Test Result (Pass/Fail): Pass					

**Procedure Steps:** 





Spire Procedure

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