

SPIRE Technical Note

Ref: SPIRE-RAL-NOT-002284

Issue: 2.0

Date: 15 April 2005

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SPIRE Integrated Module Test sequence for EQM testing B. Swinyard

Scope

Outline description of the sequence and procedures to be used during the SPIRE CQM Integrated Module Test (IMT) once integrated into the Herschel EQM Payload Module at EADS Astrium in Ottobrun. The pre-requisites for the test are briefly described followed by a table setting out the steps in the test sequence; the names of the procedures to be executed from CCS and the references to any manual procedures required for, for instance, switching on the DRCU; and the estimated duration of each part of the test.

Change notes

0.1	7 January 2005	Draft for checking – procedure names to be added/checked	
1.0	5 April 2005	First issue name corrected to SPECIFIC from SHORT	
	_	Specific procedure names added	
		Manual procedures to be added after discussion with Astrium	
2.0	15 April 2005	Re-titled from Specific Functional Test to Integrated Module Test.	
		EMC tests removed to separate document.	

Applicable Documents

AD1 SPIRE Functional Test Specification - SPIRE-RAL-DOC-001652

AD2 SPIRE COOLER RECYCLING SCOS PROCEDURE - SPIRE-RAL-PRC-002267

Reference Documents

Prerequisites for carry out the IMT

FPU is integrated onto HOB

WE integrated with CCE

WE integrated with harness and FPU

Warm functional test done

Cold functional test done at "4K" and "1.7K" as per AD1

FP is at nominal temperature and left in REDY mode – see transition diagram in AD1

Prerequisites for data analysis

SCOS is running and display screens are available with conversion curves loaded QLA sequences ready for display of data and FITS output IDL V 6.0 or later present on local machine (laptop if necessary) with access to FITS filestore via FTP or other method

Outline Test sequence:

Step	Description	Procedure Name	Estimated			
			Duration			
1	Check the noise in the PLW JFETs	CCS-SPT-NOISE-P	30			
	with shorted inputs versus Vss		minutes			
	(detectors at ~2K)					
2	Noise versus bias using spectrometer	CCS-SPT-NOISEVBIAS-S	30			
	side of instrument and STM JFETS		minutes			
3	Analyse data – verify no excess	BMS IDL code				
	system noise					
Thermal case 1						



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5 R 6 S	Recycle cooler Switch to Photometer Standby Vait until temperature stabilises	CCS-SPT-PDET_OFF CCS-SPT-SDET-OFF CCS-SPT-CREC CCS-SPT-PDET-ON CCS-SPT-RESET-OFFSETS-P Requires manual procedure to set equivalent power in BSM coils	2 hours 10 minutes				
6 S	Switch to Photometer Standby Vait until temperature stabilises	CCS-SPT-CREC CCS-SPT-PDET-ON CCS-SPT-RESET-OFFSETS-P Requires manual procedure to set	10				
6 S	Switch to Photometer Standby Vait until temperature stabilises	CCS-SPT-PDET-ON CCS-SPT-RESET-OFFSETS-P Requires manual procedure to set	10				
	Vait until temperature stabilises	CCS-SPT-RESET-OFFSETS-P Requires manual procedure to set					
7 V		Requires manual procedure to set	minutes				
7 V		<u> </u>					
7 V		equivalent power in BSM coils					
7 V							
		N/A	TBD				
	Ouring stabilisation we can check	CCS-SPT-DNA-P	4 hours				
	oise versus bias level and frequency		max				
	with reduced number of bias levels						
	nd frequencies or it will take all day						
	Analyse data – determine noise is	BMS/TLL IDL code					
	.k. and optimum frequency setting						
	analysis procedure exists						
Evaporator temperature must have stabilised before next test							
	Set for clean bias frequency and	CCS-SPT-BIAS-FREQ	10				
	ominal bias (~15 mV)		minutes				
10 P	Phase up to maximise signal	CCS-SPT-PHASEUP-P	30				
			minutes				
			(TBC)				
	oadcurve at fixed frequency and	CCS-SPT-LC-P	15				
p	bhase		minutes				
10 1	1	CCC CDT I C DI LICON D	(TBC)				
	Loadcurve at fixed frequency and	CCS-SPT-LC-PLUS90-P	15				
p.	hase+90		minutes				
12 I	and arrange of Errad fragrenment and	CCS-SPT-LC-MINUS90-P	(TBC)				
	Loadcurve at fixed frequency and	CCS-SPT-LC-MINUS90-P					
l P	hase-90		minutes (TBC)				
14 A	nalyza data datarmina dataatar	DMC IDL anda	(IBC)				
	Analyse data – determine detector emperature and estimate	BMS IDL code					
	packground loading						
	Set detector for optimum bias setting	CCS-SPT-BIAS-AMPL-P	10				
	nd reset offsets	CCS-SPT-RESET-OFFSETS-P	minutes				
	Rephase detector at optimum bias	CCS-SPT-PHASEUP-P	10				
	etting		minutes				
	Run PCAL static test to check	CCS-SPT-PCAL-STATIC	15				
	alibration against CBB		minutes				
	and an against CDD		(TBC)				
18 -	Analyse data – determine absolute	Analysis code required	(150)				
	ignal versus voltage calibration –	Imalysis concrequired					
	Can now use SPIRE to determine	Analysis code required					
	mbient background for (almost) any	11. arysis concrequired					
	etting of the cryo-cover						
		other thermal cases	1				



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Step	Description	Procedure Name	Estimated Duration			
19	Photometer scan mode	No procedure required				
20	Photometer chop mode	Requires procedure to manually apply power to BSM using external (GSE) supply				
21	Switch photometer to spectrometer	CCS-SPT-PDET-OFF CCS-SPT-SDET-ON				
22	Spectrometer mode	Requires procedure to manually power to SMEC using external (GSE) supply				
Wait for cooler exhaustion approx 30-32 hours after recycle						
	SPIRE/PA	CS parallel mode test				
23	Second Cooler recycle in conjunction with PACS	CCS-SPT-CREC	Start 25 minutes after PACS recycle Then 2 hours			
24	Switch to SPIRE Photometer Standby	CCS-SPT-PDET-ON CCS-SPT-RESET-OFFSETS Requires manual procedure to set equivalent power in BSM coils	10 minutes (TBC)			
25	Switch to SPIRE Parallel (scan)	CCS-SPT-PARALLEL	1 minute			
		peratures have stabilised				
26	Switch to SPIRE Parallel (chop)	Procedure will not be quite flight like – keep 10 Hz continuous sample and manually apply power to BSM coils via external (GSE) supply	10 minutes			
Other PACS modes? Wait for cooler exhaustion						