

SPIRE
Minutes of meeting
FPU and JFET trial integration
9 December 2004.
SPIRE-RAL-MOM-002248

Paul Mack EADS
Eric Sawyer RAL

RAL staff present during all activities

- Eric Sawyer
- Alan Pearce

Activities carried out:

Integration of the SPIRE CQM FPU and JFETS carried out following Integration procedure SPIRE-RAL-PRC-oo1923 Issue 3. Document red lined to highlight deviations from procedure (minor) and additional information.

Main deviation was the integration was carried out with all three L0 straps fitted to the FPU.

Points to note:

The detector box L0 strap is very close to the vent line, SPIRE had to be held away during the lowering. A 45x5mm chamfer on the top edge of the solid part of the strap would aid integration.

The MGSE turnbuckle was used to adjust the angle of the FPU during the final lowering to assist getting the L0 strap under the vent line.

Main FPU fitted ok

JFETS fitted ok

L1 thermal straps fitted ok with RAL supplied bushes, bolts and backing plate.

L3 thermal straps fitted JFETS ok, with RAL supplied bolts, bushes and plates.

A short circuit was discovered between the L1 interface plate and the FPU, this should be insulated by an epoxy joint. SPIRE to investigate.

Investigation into the possibility of exchanging the three L0 straps was carried out. It is possible with the FPU on the OBA.

The cryo sensor harness of the OBA, should be rerouted to the out side of the vent line in the -Z, +Y side (near the L0 straps).

If the short circuit prevents the integration, SPIRE to inform EADS by Monday evening (13/12/04).

Open work from IRR:

Check availability of all items available for integration, all items available.

Fit check of spire carried out.

Remove spire from OBA.