

**SPIRE Critical Design Review (CDR)  
RAL, 19/21 July 2004**

**Agenda**

**Day 1 19th July**

<b>Start Time</b>	<b>End Time</b>	<b>Duration (Min.)</b>		
			<b>Introduction</b>	<b>Chair: King</b>
14:00	14:10	10	(A) Welcome and logistics	M. Griffin
14:10	14:20	10	(B) Purpose and organisation of the review	Sawyer
14:20	14:35	15	(C) IHDR status	Sawyer
			<b>SPIRE System</b>	<b>Chair: King</b>
14:35	14:45	10	(D) Outline of model programme	Sawyer
14:45	15:00	15	(E) The SM and AM programme	Sawyer
15:00	15:10	10	(F) The AVM programme	King
15:10	15:40	30	Coffee	
15:40	16:05	25	(G) The CQM programme	Swinyard
16:05	16:25	20	(H) The PFM programme	Sawyer
16:25	16:45	20	(I) Status of IID-B and Herschel interfaces	Sawyer
16:45	17:15	30	Questions and clarifications	
17:15			<b>End of Day 1</b>	
<b>Evening</b>			<b>TBD</b>	

00:01

## Day 2 20 July

Start Time	End Time	Duration (Min.)		
			<b>AIV and PA</b>	<b>Chair: King</b>
09:00	09:20	20	(J)AIV Facility and Ground Calibraion	Smith
09:20	09:40	20	(K)Thermal test results	Goizel
09:40	10:00	20	(L)Thermal design and modelling	Goizel
10:00	10:30	30	<b>Coffee</b>	
10:30	10:50	20	(M)Thermal hardware implementation and material test results	Winter
10:50	11:10	20	(N)CQM Functional test results	Sidher
11:10	11:40	30	(O)CQM Performance test results	Swinyard/Lim
11:40	11:55	15	(P)Verification status	Swinyard
11:55	12:25	30	(Q)PA, status of NCRs, RFWs, ECRs	Clark
12:25	12:55	30	Questions and clarifications	
12:55	14:10	75	<b>Lunch</b>	
			<b>Major Subsystems and their interfaces</b>	<b>Chair: Sawyer</b>
14:10	14:40	30	(R)DPU	Orfei
14:40	15:10	30	(S)On-Board software	Orfei
15:10	15:30	20	(T)FMECA	Swinyard
15:30	15:45	15	(U)FDIR and HW-SW interaction	King
15:45	16:15	30	<b>Coffee</b>	
16:15	16:35	20	(V)FPU structure	Winter
16:35	16:55	20	(W)Vibration test results	Winter
16:55	17:25	30	Questions and clarifications	
17:25	17:55	30	Review Board meeting	

**Evening**

**Relaxing social event**

## Day 3 21 July

Start Time	End Time	Duration (Min.)		Chair:
			<b>Major Subsystems and their interfaces (cont)</b>	<b>Swinyard</b>
09:00	09:20	20	(a)Optics	Ferlet
09:20	09:35	15	(b)Stray light control	Griffin D
09:35	09:55	20	(c)MCU	Baluteau
09:55	10:35	40	(d)BDAs and JFETs	Bock
10:35	11:05	30	Coffee	
11:05	11:20	15	(e)PTC	Griffin D
11:20	12:00	40	(f)DCU and FCU	Cara
12:00	12:20	20	(g)BSM	Stobie
12:20	12:40	20	(h)SMEC	Dargent
12:40	13:55	75	lunch	
			<b>Major Subsystems and their interfaces (cont)</b>	<b>King</b>
13:55	14:15	20	(i)cooler	Griffin D
14:15	14:35	20	(j)Filters, beam splitters and Dichroics	Hargrave
14:35	14:55	20	(k)Calibrators	Hargrave
14:55	15:15	20	Coffee	
15:15	15:30	15	questions and clarifications	
15:30	15:40	10	Summing up	Sawyer
15:40	16:10	30	Review board meeting	
16:10	16:40	30	Review board feed back	
16:40			End of meeting	