

## Minutes : Common EGSE meeting #36

Present	: PACS SPIRE HIFI LFI ESTeC Alcatel Astrium		
Date	: 19 Feb 2	19 Feb 2004	
Place	: Telcon		

#### Agenda

- 1. Minutes and Action Items from last meeting
- 2. DPU/ICU status
- 3. Status of IEGSE hardware
  - Payment
  - Configuration plan
- 4. TOPE status
- 5. 1553 tests: status
  - Low level tests (MIL 1553B) at MPE
  - CCS-Light CDMU simulator and real DPU: tests at Astrium
    - + Integration and Test Plan
    - + HPSDB and MIB
    - + Command sequences
    - + Confirmation of date, participants
- 6. Next meeting/telecon
- 7. AOB

### 1. Minutes and Action Items from last meeting

Action	Actionee	Description	Due Date
EGSE#32-12	Markus	Astrium to draft IEGSE H/W integration plan	Obsolete
EGSE#32-14	Erich	To provide validation plan for Telecommand Sequence I/F	8-Mar-04
EGSE#32-15	Felix, Markus, Ken, Luc, Otto	To comment on Erich's validation plan	19-Mar-04
EGSE#33-02	Serge	Serge will write and distribute a TN containing the differences between TOPE and TOPE p4	Closed
EGSE#34-04	Otto	to provide the inputs necessary to create the test procedures to be carried out in the	20-Jan-04



		CCS-DPU tests	
EGSE#35-01	Johannes	To send draft procedure for HCSS update to Otto	Closed
EGSE#35-02	Otto	To discuss proposal with Ken and Luc and submit common proposal to Industry	Closed
EGSE#35-03	Felix	To make sure that SSBV is aware of delay of test and training at Astrium	Closed
EGSE#35-04	Felix	To arrange new date for test and training at Astrium	Closed
EGSE#35-05	Otto	To provide input needed for OBSM SoW to ESOC	20-Feb-04

#### Notes after the Als

EGSE#32-12 There is a short discussion about the question what we need for integration plan. Notice that we have H-P-ASP-PL-0556 PIPE Protocol validation plan for Herschel I.EGSE, apart from the documentation that comes with the PIPE-GW.

This, combined with common sense, should be enough for an efficient integration.

#### 2. DPU/ICU status

#### PACS

Otto reports that the 1355 problem is solved.

The DPU test has started. It has run for 11h before the first CRC problem occurred.

#### SPIRE

DPU is used now for 1 month. There are no problems with the High-speed link. There are communication-problems with the low-speed link. (at a rate of 40 errors during 30 days)

The DPU rejects proper TC complaining about CRC-errors.

#### HIFI

The connection problems between FPGA and FIFO are now understood. On the current models, some extra added components establish a work-around. A redesign of the interface board is required.

#### All models

After new software is loaded the DPU can not boot properly. Therefore the AVMs are equipped with a reset-button, located at the unused redundant connector opening. The request to Astrium is to make sure that this little button stays within reach for rare occasions.

The button is not used when the DPU powers up and boots on the SW resident in the EEPROM.

The meeting of ASI, CGS and IFSI originally planned for on Feb 25 is postponed to March.

There is a proposal from instruments to ASI to split up the ASI-CSG contract into two parts: ASI-IFS for the Software and ASI-CGS for the hardware.

#### 3. Status of IEGSE hardware

- Payment
- Configuration plan



There is a Dell-quotation for the FEGSE setup.

For the installation and configuration details a separate telecon will be organized. The installation will be done at MPE-location, supported by SPIRE/HIFI experts.

# 4. TOPE status

Tope delivery is expected on 8 March.

The test-procedures are not written or delivered in MOIS format. Félix will distribute a note about test-sequence generation.

#### 5. 1553 tests: status

- Low level tests (MIL 1553B) at MPE

Due to the organizational changes in the Italian situation, the instruments have the opinion that Industry (CGS) is responsible for these tests.

- CCS-Light CDMU simulator and real DPU: tests at Astrium
  - + Integration and Test Plan
  - + HPSDB and MIB
  - + Command sequences
  - + Confirmation of date, participants

In week 10 there is a SSBV organized training with PLM-EGSE In week 11 the integration of the PACS DPU takes place. Start on Monday noon with TRR. In principle the tests will be witnessed by SPIRE and HIFI.

AI EGSE#36-1Félix checks the availability of SSBV during this testAI EGSE#36-2Christian to distribute the integration plan.

Concerning HPSBD: there are a few comments about the PACS files. **AI EGSE#36-3** PACS will provide a new MIB on 25 February. The files of the other instruments may be ingested mid March.

Félix can mail the bridge file to Johannes.

Félix announce a change in the naming conventions. The number of allowed boxes in pseudo-elements is restricted to one. If Instruments suffer heavy under the impact, this restriction will be relaxed.

A proposal to update the FEGSE SW has been distributed by Otto (AI EGSE-WG#35-02) AI EGSE#36-4 Alcatel and Astrium will comment on this I\_EGSE SW update proposal.

#### 6. Next meeting/telecon

March 25, 11h00

7. AOB



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EGSE#32-14	Erich	To provide validation plan for Telecommand Sequence I/F	8-Mar-04
EGSE#32-15	Felix, Markus, Ken, Luc, Otto	To comment on Erich's validation plan	19-Mar-04
EGSE#34-04	Otto	to provide the inputs necessary to create the test procedures to be carried out in the CCS-DPU tests	20-Jan-04
EGSE#35-05	Otto	To provide input needed for OBSM SoW to ESOC	20-Feb-04
EGSE#36-01	Félix	To check the availability of SSBV during this test	27-Feb-04
EGSE#36-02	Christian	to distribute the integration plan.	27-Feb-04
EGSE#36-03	Otto	provide a new MIB	25-Feb-04
EGSE#36-04	Alcatel Astrium	comment on Otto's I_EGSE SW update proposal	5-Mar-04

# Status AI at the end of the meeting