



**MINUTES OF MEETING
(MoM)**

**PRODUCT ASSURANCE
Space Science and Technology Department**

Spacecraft/Project	HERSCHEL	Document No	SPIRE-RAL-MoM-001903	
Instrument/Model	SPIRE/STM	Issue No / Rev	1 / 0	Page Page 1 of 3
Subsystem	JFET's	Date	16 th December 2003	

Title / Subject	MRB STM JFETs Problem	Chairman	Eric Sawyer
Meeting Place	SPIRE Project Room	Secretary	Eric Clark

Participants		Agenda
(Print Name)	(Signature Required)	<ul style="list-style-type: none"> • State Problem with JFETs • Are other models effected • Discuss Options • Suggest Fix • Contact JPL for Comments / agreement to Fix.
Eric Sawyer		
John Delderfield		
Bruce Swinyard		
Doug Griffin		
Dave Smith		
Eric Clark		
Additional Distribution		Attachments
Judy Long		

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Action No	Title and Description	Action Responsibility	Action Deadline
	<p>Problem. Pin 19 of 51 way connectors and corresponding pin on 15way connector on each of the STM JFETs is connected to Ground. This is not correct, (although it was the case for an earlier design but was changed for the STM)</p> <p>The CQM JFETS do not appear to have this problem.</p> <p>OPTIONS.</p> <ol style="list-style-type: none"> 1. Modify Existing JFETs. 2. Do Nothing 3. Modify Harnesses. <p>OPTION 1 Modify Existing JFETs</p> <ol style="list-style-type: none"> a) We Modify JFETs at RAL b) JPL Modify JFETs at RAL. c) Return JFETs to JPL for modification <p>OPTION 2. Do Nothing</p> <ul style="list-style-type: none"> • Very Risky: - system may run but if excessive noise is discovered no conclusion. 		

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A01	<p>OPTION 3 Modify Harnesses</p> <ul style="list-style-type: none"> • Remove pins Not a practical solution as these are Flight Harnesses • If Harnesses are not connected, Thermal connections will have to be devised to enable thermal test to be performed • <p>OPTION.</p> <ul style="list-style-type: none"> a) Modify JFETs at RAL is preferred b) Not Likely to happen in the required Time c) Not Likely to happen in the required Time <p>Look at spare STM JFET by removing side covers and investigate how it could be modified.</p> <p>Re convene Meeting 1530 to decide further actions. EC ECS & JD</p> <p>Links found on JFETs If Link 1 removed appears to correct problem JPL to confirm this is OK Links 2 to 5 information required regarding what they do. Note Links will have to be secured on all units before Vibration</p>	Eric Sawyer	16/12/03
A02	<p>Email copy of this report to Marty & Tim fore Telecon At Telecon JPL agreed to report back by 0900-17/12/03 re Questions above. Refer to Telecon minutes for more detail</p>	Eric Clark	1550 on 16/12/03