## DPU Work Package:

2. Subsystem Progress This Month

## 1. Subsystem Progress Since Project Inception

## SPIRE-IFS-REP-001786

None			
13. Schedule	Changes	I	
15/11/01 22/4/02 2/1/03 20/12/02 15/9/03 1/3/04 24/2/04	QM author. To proceed DPU-QM Board at IFSI DPU-QM delivery to RAL FM author. To proceed DPU-FM Board at IFSI DPU-FM delivery to RAL FS ready for delivery	On On On On	te - new date 15/02/02 schedule schedule schedule schedule schedule schedule
<b>11. Milestones</b> 28/06/01 15/6/01 28/6/01 1/10/01 2/4/02	S DRCU simulator at IFSI CDMS (S/C I/F) at IFSI EGSE at IFSI DPU-AVM Board at IFSI DPU-AVM delivery to RAL	Lat Lat Lat Lat	atus te - expected completion 30/2/02 te - Proto SW arrived on 21/8/01 te - SCOS 2000 arrived on 20/8/01 te - arrived on 15/04/02 te - new date 1/07/02
	revious Actions Yet to be Achieved		
6. PA/QA Activities  7. Subsystem Management Issues  8. Actions Requiring Immediate Attention  None			
5. Design Changes			
4. Engineerin	g Activities		
High Speed serial link: we asked CGS to modify the HW in order to accept marginal timing on clock and sampling line			
3. Problem Areas EEPROM boot procedure: performed many tests which seems to point to an HW problem		Remedial Action Under investigation in CGS	
Software: Multiple h HS link cor seems OBS u Multiple No TeleCommand	k_asking: completed , under t mmunication task: new DRCU_SI s that there are bugs; under updating to new DRCU IC on Real Time-VM and RT-VM: co	M SW arriv D; mpleted, f action wit	ved on (11th dec '02): it test procedure started; th HK's and VM's under testing