NOTES:

TSS901:
powered by
+VSMCS line

IDT7025:
powered by VCC line

For EM model mount IDT7025
For FM model mount Temic M67205
The shape is the same but not the numbering of the pins!!

U5

TSS901

U6

IDT7025L/FP84

U7

IDT7025L/FP84

Busy flags are not monitored because the memory is divided in monodirectional buffers

FILTER CAPACITORS:
TSS901: 12 capacitors

FILTER CAPACITORS:
IDT7025: 1 capacitor per side

REV.	DATE	DRAWN	CHECK	ENG.	PR.ASS	C. dIC.	CHANGE/AUTORITY
/							

CUSTOMER/ID: EFFECTIVITY: CARLO GAVAZZI SPACE

DRAWING TITLE: **HSO/FIRST-DPU**
CPU PIGGYBACK
SMCS332

SCALE: GEN. TOL. GEN. PA

HIGHER ASSEMBLY: DPU-EM-120.000

DRAWING NUMBER: DPU-EM-120.020

SHEET OF: 3 3