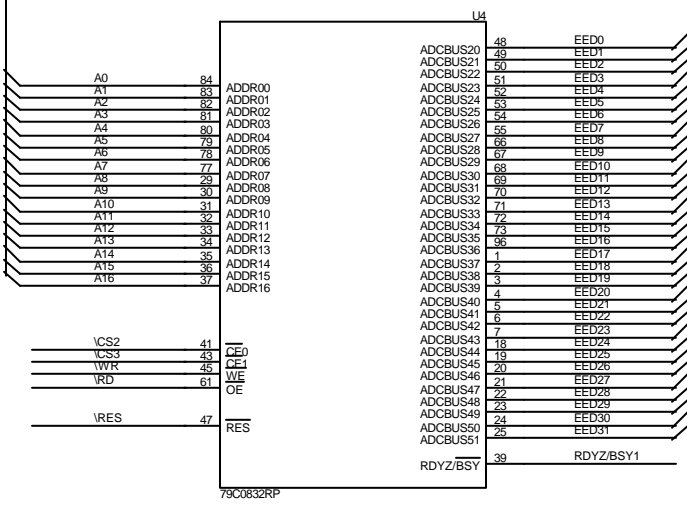
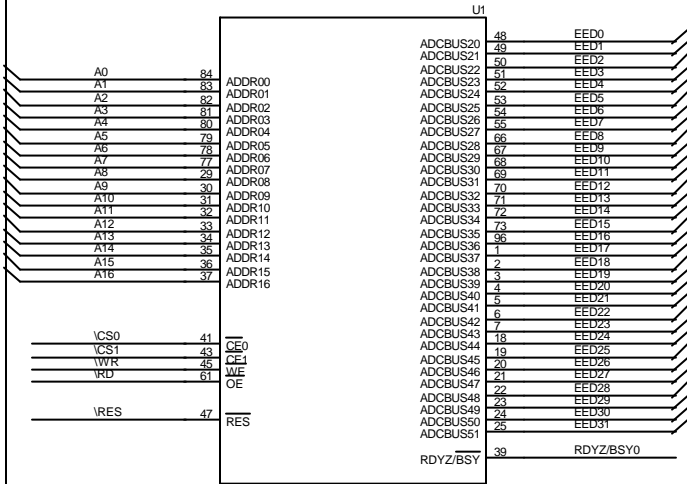
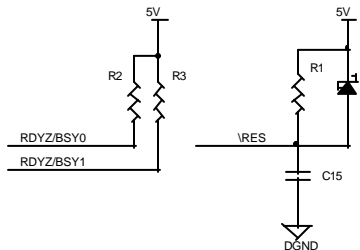
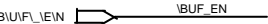
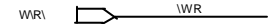
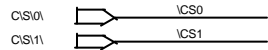
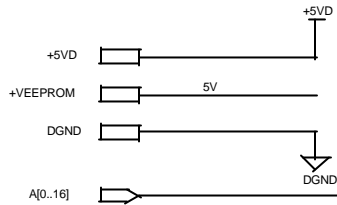


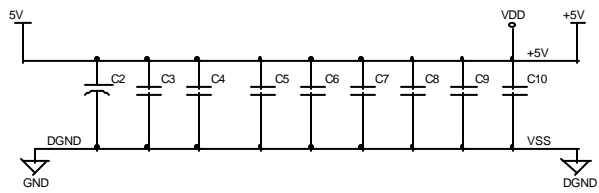
NOTES:

79C0832RP:  
powered by  
+VEEPROM  
line

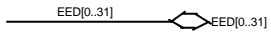
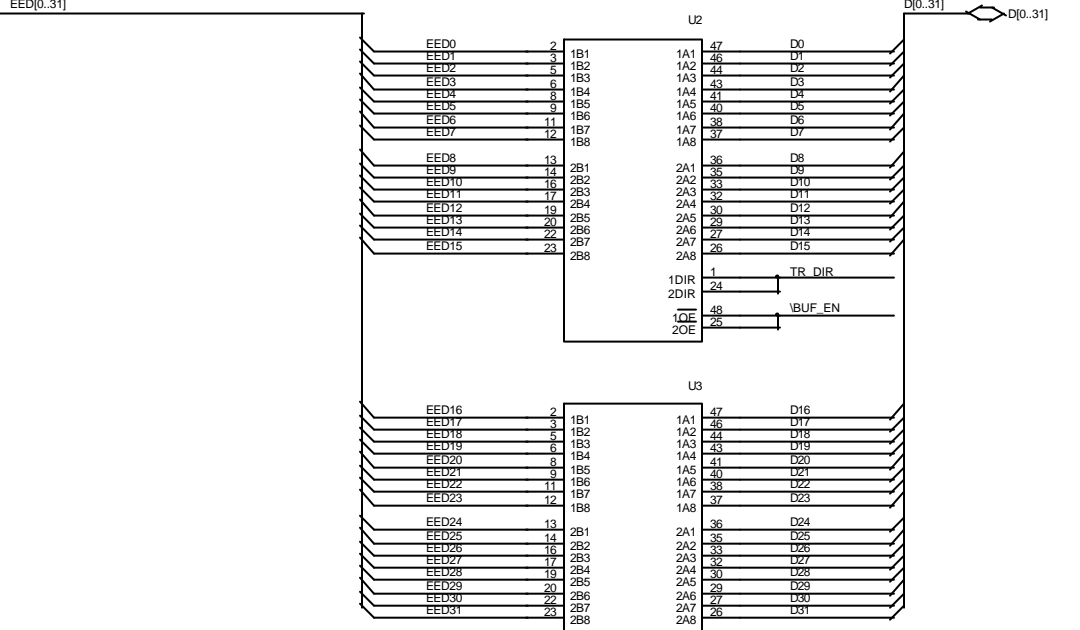
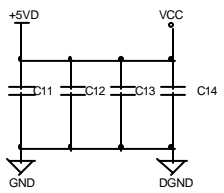
54ACTQ16245:  
+5V: 10, 17, 28, 40, 44, 58, 65, 76, 87, 93  
GND: 8, 9, 11, 12, 13, 14, 15, 16, 26, 27, 38, 42,  
46, 56, 57, 59, 60, 62, 63, 64, 74, 75, 85, 86, 88,  
89, 90, 91, 92, 94, 95




FILTER CAPACITORS:  
79C032RP  
1 capacitor per side



FILTER CAPACITORS:  
54ACTQ16245  
2 capacitor each



/							
REV.	DATE	DRAWN	CHECK	ENG.	FRASS	C. d.C.	CHANGE AUTHORITY
CUSTOMER				EFFECTIVITY		CARLO GAVAZZI SPACE	
DRAWING TITLE  HSO/FIRST-DPU CPU PIGGYBACK EEPROM Block				SCALE		GEN. TEL.	
						GEN. RA.	
HIGHER ASSEMBLY				DRAWING NUMBER		SHEET OF	
DPU-EM-120.000				DPU-EM-120.020		2 3	