SCMS LOCAL ADDRESS

SMCSID[0..3]

JP1
SMCSID0   1 2
SMCSID1   3 4
SMCSID2   5 6
SMCSID3   7 8
GND

SCMS CONTROL BY HOST (jump 5-6) / LINK
(jump 1-2)

VCC
JP2
BOOTLINK   1 2
           3 4
           5 6
GND

SCMS BIG (jump 1-2) /
LITTLE (jump 5-6) INDIAN
MODE

VCC
JP3
HOSTBIGE   1 2
           3 4
           5 6
GND

P1
AMP 536279-2

A[0..16]   \IRQ0
                A0   1   2   D0   D[0..31]
                A1   3   4   D1
                A2   5   6   D2
                A3   7   8   D3
                A4   9 10   D4
                A5   11 12   D5
                A6   13 14   D6
                A7   15 16   D7
                A8   17 18   D8
                +VSMCS 19 20 D8
                A9   61 62   D9
                A10 21 22   D10
                A11 23 24   D11
                A12 25 26   D12
                A13 27 28   D13
                A14 29 30   D14
                A15 31 32   D15
                A16 33 34   D16
                     35 36   D17
                     37 38   D18
D[0..31]   39 40   D18
                D29 41 42   +VEEPROM
                D30 43 44   D20
                D31 45 46   D21
                     47 48   D22
                \RD 49 50   D23
                \WR 51 52   D24
                \RESET_IN 53 54 D25
                CLK 55 56   D26
                ACK 57 58   D27
                \STD_BUF_EN 59 60 D28
                             63

+5VD
                             B1
                +5VD   +5VD
                +VEEPROM +VEEPROM
A[0..16]   A[0..16]   D[0..31]   D[0..31]
\EE_CS0   CS0
\EE_CS1   CS1   EED[0..31] TR[0..31]
\EE_CS2   CS2
\EE_CS3   CS3
\EE_CS[0..3]
\EE_WR   \EE_WR   WR
\EE_RD   \EE_RD   RD
\STD_BUF_EN   BUF_EN
\RD   TR_DIR

                             DGND
                EEPROM   DGND

+5VD
                             B2
                +5VD   +5VD
                +VSMCS +VSMCS
A[0..12]   A[0..12]   SMCSD[0..31]
\SMCS_CS   SMCS_CS   D[0..31]   D[0..31]
\DPR_CS0   DPR_CS0   IRQ   \IRQ0
\DPR_CS1   DPR_CS1   ACK   ACK
\WR   \WR   \SES \SES[0..3]
\RD   RD
                LODI   LODO   LOSO   LODO LOSO
                LOSI   LOSO   LODO   LOSO LODO
                L1DI   L1DI   L1SO   L1DO L1SO
                L1SI   L1SI   L2SO   L1SO L2SO
                L2DI   L2DI   L2DO   L2DO L2DO
                L2SI   L2SI   L3SO   L2SO L3SO
                L3DI   L3DI   L3DO   L3DO L3DO
                L3SI   L3SI   L4SO   L3SO L4SO
                L4DI   L4DI   L4DO   L4DO L4DO
                L4SI   L4SI   L5SO   L4SO L5SO
                L5DI   L5DI   L5DO   L5DO
                L5SI   L5SI   L5SO   L5SO
                LOEN   LOEN   L1EN   L0EN L1EN
SMCSID[0..3]   SMCSID[0..3] L1EN   L2EN
BOOTLINK   BOOTLINK   L2EN   L3EN
HOSTBIGE   HOSTBIGE   L3EN   L4EN
                             L4EN   L5EN
                             L5EN
\RESET_IN   RESET_IN   \BUSY0 \BUSY0
                             BUSY1 \BUSY1
CLK   CLK
CLK10M   CLK10M   RESET_OUT \RESET_OUT
                             DGND
                SMCS332   DGND

P2
AMP 536279-2

\EE_CS[0..3]
                \EE_CS0   1   2
                \EE_CS1   3   4
                \EE_CS2   5   6
                \EE_CS3   7   8
                \EE_WR   9 10
                \EE_RD   11 12
                         13 14
                LODI   15 16   L0DO
                LOSI   17 18   LOSO   +5VD
                         19 20
                L1DI   61 62   L1DO
                L1SI   21 22   L1SO
                L2DI   23 24   L2SO
                L2SI   25 26   L2DO
                L3DI   27 28   L3DO
                L3SI   29 30   L3SO   DGND
                L4DI   31 32   L4DO
                L4SI   33 34   L4SO
                L5DI   35 36   L5DO
                L5SI   37 38   L5SO
                         39 40
                \BUSY0   41 42   \SES0
                \BUSY1   43 44   \SES1
                \DPR_CS0 45 46   \SES2   DGND
                \DPR_CS1 47 48   \SES3
                \SMCS_CS 49 50   L0EN   \SES[0..3]
                \RESET_OUT 51 52   L1EN
                         53 54   L2EN
                CLK10M   55 56   L3EN
                         57 58   L4EN
                         59 60   L5EN

+5VD   VCC   VDD   +5V
+5VD   +5V
        C1
DGND   VSS
GND   DGND

REV. DATE DRAWN CHECK ENG. PRASS C. ofC. CHANGE AUTHORITY
CUSTOMER ID   EFFECTIVITY
                                                CARLO GAVAZZI SPACE
DRAWING TITLE   SCALE   GEN. TOL.
        HSO/FIRST-DPU   GEN. RA
        CPU PIGGYBACK
        Electrical Block Diagram
HIGHER ASSEMBLY   DRAWING NUMBER   SHEET OF
    DPU-EM-120.000   DPU-EM-120.020   1   3