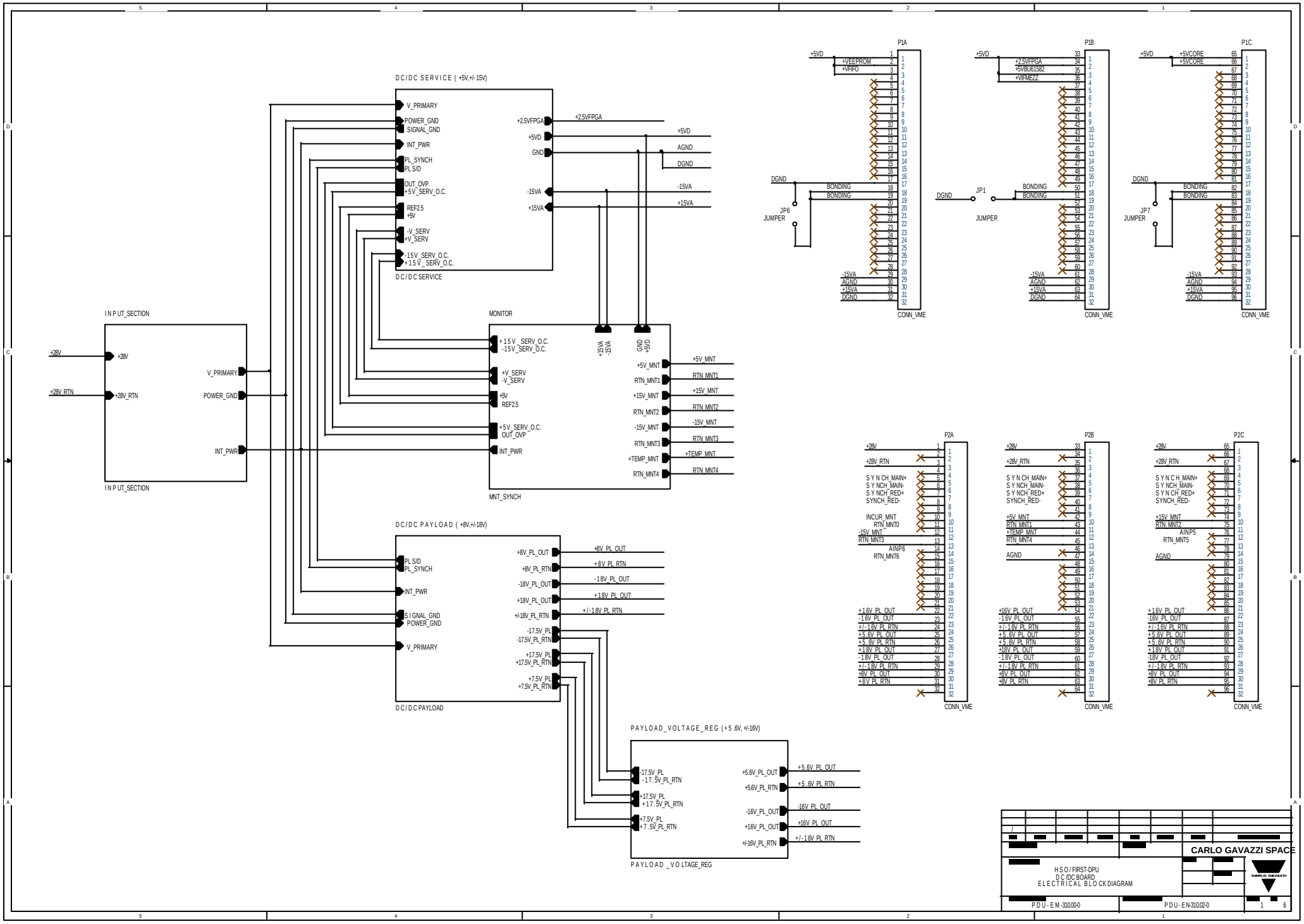
DC/DC SERVICE (+5V,+/-15V)
V_PRIMARY
POWER_GND
SIGNAL_GND
INT_PWR
PL_SYNCH
PL S/D
OUT_OVP
+5V_SERV_O.C.
REF2.5
+5V
-V_SERV
+V_SERV
-15V_SERV_O.C.
+15V_SERV_O.C.
+2.5VFPGA
+5VD
GND
-15VA
+15VA
AGND
DGND
DC/DC SERVICE
INPUT_SECTION
+28V
+28V_RTN
V_PRIMARY
POWER_GND
INT_PWR
INPUT_SECTION
MONITOR
+15V_SERV_O.C.
-15V_SERV_O.C.
+V_SERV
-V_SERV
+5V
REF2.5
+5V_SERV_O.C.
OUT_OVP
INT_PWR
+15VA
-15VA
GND
+5VD
+5V_MNT
RTN_MNT1
+15V_MNT
RTN_MNT2
-15V_MNT
RTN_MNT3
+TEMP_MNT
RTN_MNT4
MNT_SYNCH
DC/DC PAYLOAD (+8V,+/-18V)
PL S/D
PL_SYNCH
INT_PWR
SIGNAL_GND
POWER_GND
V_PRIMARY
+8V_PL_OUT
+8V_PL_RTN
-18V_PL_OUT
+18V_PL_OUT
+/-18V_PL_RTN
-17.5V_PL
-17.5V_PL_RTN
+17.5V_PL
+17.5V_PL_RTN
+7.5V_PL
+7.5V_PL_RTN
DC/DC PAYLOAD
PAYLOAD_VOLTAGE_REG (+5.6V,+/-16V)
-17.5V_PL
-17.5V_PL_RTN
+17.5V_PL
+17.5V_PL_RTN
+7.5V_PL
+7.5V_PL_RTN
+5.6V_PL_OUT
+5.6V_PL_RTN
-16V_PL_OUT
+16V_PL_OUT
+/-16V_PL_RTN
PAYLOAD_VOLTAGE_REG
P1A
P1B
P1C
CONN_VME
JP6
JP1
JP7
JUMPER
BONDING
P2A
P2B
P2C
SYNCH_MAIN+
SYNCH_MAIN-
SYNCH_RED+
SYNCH_RED-
INCUR_MNT
RTN_MNT0
AINP6
RTN_MNT6
AINP5
RTN_MNT5
CARLO GAVAZZI SPACE
HSO/FIRST-DPU
DC/DC BOARD
ELECTRICAL BLOCK DIAGRAM
PDU-EM-310.00-0
PDU-EN-310.02-0
1 6