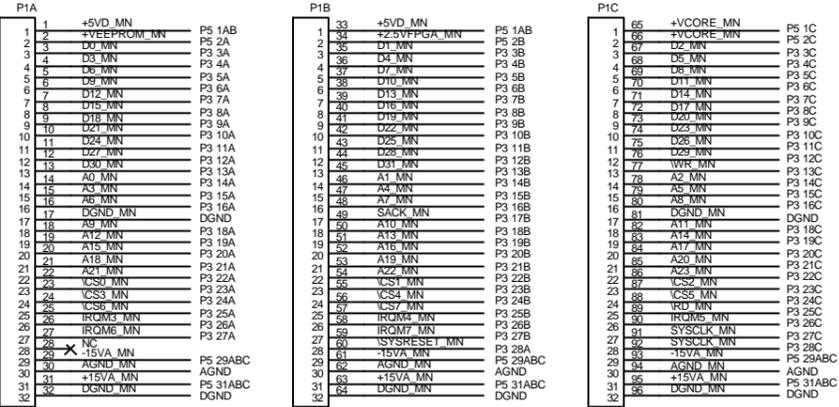
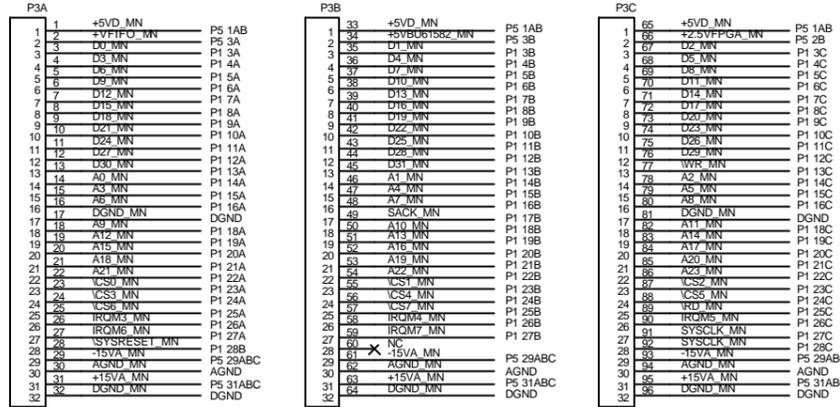


MAIN

SLOT 1 CPU



SLOT 2 PL/IF



SLOT 3 DC/DC

