

SPIRE-RAL-COM-0001480

From: Doug Griffin

To: Gary Parks

CC: John Delderfield, Bruce Swinyard, Eric Sawyer

Date: Tuesday, 07 January 2003 (Draft 0.1)

Tuesday, 14 January 2003 (Issue 1.0) – Amendments in [Blue](#)

Issue: 1.0

Dear Gary,

As promised last week, please find below a detailed list of the issues with the JPL documentation that we have found. Some of these issues were detected after the Dec. 2002 Saclay meeting.

John and I have extensively discussed these comments. None of them imply changes to the hardware, but they do impact on the Astrium S/C I/F and on the order of the pixels on the DCU connectors.

What we have proposed does not necessarily represent the only documentation corrections compatible with existing hardware.

We await the update to the Wiring Schematic that details the changes to the hardware that you informed me of during the phone conversation yesterday.

Would you please respond to the comments referring to the number in the left hand column? I will fill out the Closure column in due course and re-issue.

Cheers,

Doug.

p.s. Sorry that I did not get it to you yesterday. The task was more complex than I had anticipated

Comment Number	Drawing number and issue	Sheet # / Grid reference	RAL Comment	Closure
1	10209725-X231	Sheet 1 Grid L2	<ul style="list-style-type: none"> • There is disagreement between the BDA connectors indicated as being populated on the SPIRE Wiring Diagram and the BDA ICDwg. • The correct allocation is: • 10209725-X231 <ul style="list-style-type: none"> ○ PLW J05, J06 ○ PMW J01, J02, J03, J04 ○ PSW J01, J02, J03, J04, J05, J06 ○ SLW J05 ○ SSW J05, J06 	JPL agreement
2	10209721-A	Sheet 4, Grid G11	<ul style="list-style-type: none"> • Reference: Comment 1 • PLW Should be: J05, J06 (John Coker has signed this issue, I understand that it is OK from a mechanical point of view to move it from J01, J02 but MSSSL need to sign off on this) 	Comment agreed, amendment in hand. Need MSSSL agreement and signature.
3	10209721-A	Sheet 7, Grid G11	<ul style="list-style-type: none"> • Reference: Comment 1 • SLW Should be: J05 (John Coker has signed this issue, I understand that it is OK from a mechanical point of view to move it from J01 but MSSSL need to sign off on this) 	Comment agreed, amendment in hand. Need MSSSL agreement and signature.
4	10209722-A Prerelease 6	Sheet 1, Grid D2, H8, G7	<ul style="list-style-type: none"> • View of 51-Way MDM connectors (J05 and J08) shown at D2 as Sockets; should be Pins • View of 51-Way MDM Connectors (J05, J08) are drawn as sockets (double line around the “D” profile instead of a single line); should be Pins • See “JD Annotated 10209722.pdf” 	JPL agreement
5	10209722-A Prerelease 6	Sheet 1, Various Grid	<ul style="list-style-type: none"> • To have agreement in nomenclature between 10209722 and 10209725, the connector labels should be changed on 10209722 as follows: 	JPL agreement

Comment Number	Drawing number and issue	Sheet # / Grid reference	RAL Comment	Closure
			<ul style="list-style-type: none"> o J01 -> J02 (JBB) (Grid C9) o J02 -> J01 (JAA) (Grid C7) o J03 -> J03 (JAA') (Grid C9) o J04-> J04 (JBB') (Grid C7) o J05 -> J05 (JCC) (Grid G7) o J06 -> J06 (JDD) (Grid G9) o J07 -> J07 (JDD') (Grid H7) o J08 – J08 (JCC') (Grid H9) • See “JD Annotated 10209722.pdf” • Note: Both John and I agree that it would in fact be better if the labels J01 through J08 were removed from the drawing and replaced simply with JAA, JBB, JAA', JBB' etc. as it has been agreed that the units will be supplied without J numbers inscribed next to the connectors. The Jxx labels will be added by RAL after JPL delivery. 	
6	10209722-A Prerelease 6	Sheet 1, Various Grid	All connectors labelled as being x2 (i.e. Two off). This is incorrect. They are all one off.	Reference is to the jack posts. These are two off for each connector.
7	10209722-A Prerelease 6	Sheet 2, Various Grid	<ul style="list-style-type: none"> • J01 should be signals A-L • J02 should be signals M-Z • J03, J04 and J08 should be suffixed with an apostrophe • See “JD Annotated 10209722.pdf” 	JPL agreement