

SPIRE (IFSI)	Monthly Report –August 2002	Date: 9/9/2002
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Work Package: DPU

SPIRE-IFS-REP-001416

1. Subsystem Progress Since Project Inception		
2. Subsystem Progress This Month		
<p>After the ESA meeting on 19th of June and ESA statement to pay all CPPA components the ICC funding problem for years 2004-2006 looks to be relieved.</p> <p>We are still waiting for a formal ESA document in order to define activities with ASI about the available FM components money.</p> <p>The FPGA hardware bug, that was giving random errors in the DPU analogue channels reading, has been amended. A new FPGA was tested and found performing well.</p>		
3. Problem Areas		Remedial Action
4. Engineering Activities		
5. Design Changes		
6. PA/QA Activities		
7. Subsystem Management Issues		
8. Actions Requiring Immediate Attention		
None		
9. Status of Previous Actions		
10. Activities Yet to be Achieved		
11. Milestones		Status
28/06/01	DRCU simulator at IFSI	Late - expected completion 30/2/02
15/6/01	CDMS (S/C I/F) at IFSI	Late - Proto SW arrived on 21/8/01
28/6/01	EGSE at IFSI	Late - SCOS 2000 arrived on 20/8/01
1/10/01	DPU-AVM Board at IFSI	Late - arrived on 15/04/02
2/4/02	DPU-AVM delivery to RAL	Late - new date 1/07/02
15/11/01	QM author. To proceed	Late - new date 15/02/02
22/4/02	DPU-QM Board at IFSI	On schedule
2/1/03	DPU-QM delivery to RAL	On schedule
20/12/02	FM author. To proceed	On schedule
15/9/03	DPU-FM Board at IFSI	On schedule
1/3/04	DPU-FM delivery to RAL	On schedule
24/2/04	FS ready for delivery	On schedule
13. Schedule Changes		
None		