

## **HERSCHEL**

# **SPIRE On-Board Software Verification and Validation Plan/Acceptance Test Plan**

**Document Ref.: SPIRE-IFS-DOC-001392**

**Issue: 1.1**

Prepared by: Sergio Molinari

---

**Distribution List:**

<b>RAL</b>	<b>K. King</b>
	<b>B. Swinyard</b>
	<b>S. "Duke" Sidher</b>
<b>IFSI</b>	<b>R. Cerulli</b>
	<b>R. Orfei</b>
	<b>J. Liù</b>



<b>1</b>	<b>INTRODUCTION</b>	<b>4</b>
<b>1.</b>		<b>4</b>
1.1	PURPOSE OF THE DOCUMENT	4
1.2	ACRONYMS AND GLOSSARY	4
1.3	DOCUMENT LIST	4
1.3.1	<i>Applicable Documents</i>	5
1.3.2	<i>Reference Documents</i>	5
<b>2</b>	<b>TEST PLAN</b>	<b>5</b>
2.1	TEST ITEMS	5
2.1.1	<i>Unit level</i>	5
2.1.2	<i>Integration level</i>	6
2.1.3	<i>System level</i>	6
2.2	TEST DELIVERABLES	6
2.3	TESTING TASKS	7
2.4	ENVIRONMENTAL NEEDS	7
2.5	TEST CASE PASS/FAIL CRITERIA	7
<b>3</b>	<b>TEST CASE SPECIFICATIONS</b>	<b>8</b>
3.1	SWITCH_ON	8
3.1.1	<i>Test Items</i>	8
3.1.2	<i>Input Specifications</i>	8
3.1.3	<i>Output specifications</i>	8
3.1.4	<i>Environmental needs</i>	8
3.2	DPU_COMMAND_EXEC	8
3.2.1	<i>Test Items</i>	9
3.2.2	<i>Input specifications</i>	9
3.2.3	<i>Output specifications</i>	9
3.2.4	<i>Environmental needs</i>	9
3.3	DPU_MEM	9
3.3.1	<i>Test Items</i>	9
3.3.2	<i>Input specifications</i>	9
3.3.3	<i>Output specifications</i>	11
3.3.4	<i>Environmental needs</i>	11
3.4	HK_COLLECT	11
3.4.1	<i>Test Items</i>	11
3.4.2	<i>Input specifications</i>	11
3.4.3	<i>Output specifications</i>	12
3.4.4	<i>Environmental needs</i>	12
3.5	VM	12
3.5.1	<i>Test Items</i>	12
3.5.2	<i>Input Specifications</i>	12
3.5.3	<i>Output specifications</i>	14
3.5.4	<i>Environmental needs</i>	14
3.6	COMMAND_LIST_EXEC	15
3.6.1	<i>Test Items</i>	15



---

3.6.2	<i>Input specifications</i> .....	15
3.6.3	<i>Output specifications</i> .....	20
3.6.4	<i>Environmental needs</i> .....	20
<b>4</b>	<b>TEST PROCEDURES</b> .....	<b>20</b>
4.1	TP0.....	20
4.2	TP1.....	21
4.3	TP2.....	23
4.4	TP3.....	26
4.5	TP4.....	28



## 1 Introduction

### 1.1 Purpose of the document

This document presents the test plan and procedures for the verification and validation of the On-Board Software of the SPIRE instrument at the unit, integration and system level. This test plan deals with all SPIRE OBS components as specified in AD2, except for the Handler of the interface to the Spacecraft CDMS, which is tested under a separate plan (RD1). A subset of this plan will constitute the SPIRE OBS acceptance test plan.

### 1.2 Acronyms and Glossary

AVM	Avionic Model
BC	Bus Controller
BP	BreakPoint
CDMS	Command and Data Management System
DM	Data Memory (DSP)
DPU	Digital Processing Unit
DSP	Digital Signal Processor
DTST	Dedicated Test Software Tools
EGSE	Electrical Ground Support Equipment
ESA	European Space Agency
HERSCHEL	Herschel Space Observatory
HK	Housekeeping
HW	Hardware
ICE	DSP In-Circuit Emulator
I/F	Interface
IFSI	Istituto di Fisica dello Spazio Interplanetario
NA	Not Applicable
OBS	On-Board Software
PM	Program Memory (DSP)
RAM	Random Access Memory
S/C	Spacecraft
S/S	Subsystem
SUT	Software Under Test
TBC	To Be Confirmed
TBD	To Be Defined
TBW	To Be Written
TC	Telecommand
TM	Telemetry
VME	Virtual Machine Executable Code

### 1.3 Document List



### 1.3.1 Applicable Documents

Document Reference	Name	Number/version/date
AD1	SPIRE OBS User Requirements Document	SPIRE-IFS-PRJ-000444 Issue 1.1
AD2	SPIRE OBS Software Specifications Document	SPIRE-IFS-PRJ-001036 Draft 0.9
AD3	Packet Structure Interface Control Document	SCI-PT-ICD-7527 Issue 3.2
AD4	Herschel/Planck Instrument Data Rates	H-P-1-ASPI-TN-0204 Issue: 1
AD5	DPU Switch-on procedure	

### 1.3.2 Reference Documents

Document Reference	Name	Number/version
RD1	DPU/ICU Spacecraft Interface Test Plan	CNR.IFSI.2001TR04 Issue 1.1
RD2	SPIRE Data ICD	SPIRE-RAL-PRJ-001078 Issue 1.0
RD3	DRCU/DPU ICD	Sap-SPIRE-CCa-076-02 Draft 0.7
RD4	Virtual Machine Compiler and Simulator	CNR.IFSI.2003.TR01 Issue 1.0
RD5	MCU/SCU Command List	LAM/ELE/SPI/011011 Issue 2.0
RD6	SPIRE OBS User Manual	SPIRE-IFS-PRJ-001391 Draft 0.5

## 2 Test Plan

### 2.1 Test Items

We identify Test Items at the unit, integration and system level. All test items will be tested in the software verification and validation phase, while only a set of items at the system level will be tested in the acceptance phase.

#### 2.1.1 Unit level

At the unit level we identify a test item as a routine, or a group of routines, that perform a specific and self-contained function. A list is given below:

- TIUL1. TC reception from the CDMS, verification and generation of the relative acceptance report.
- TIUL2. Dispatch of TM packets to the CDMS
- TIUL3. Identification and execution of DPU commands.
- TIUL4. Transmission of commands to the S/Ss via the Low-Speed link.
- TIUL5. Reception of S/S HK parameters via the Low-Speed link.
- TIUL6. Reception of Science Frames from S/Ss.
- TIUL7. Autonomy Functions.
- TIUL8. Temperature Controls.
- TIUL9. Event generation (including execution reports).
- TIUL10. Virtual Machine (execution of command lists).



## 2.1.2 Integration level

At the integration level we identify a test item as a specific task; a task handles different functions. A list is given below:

- TIIL1. Command sequencing.
- TIIL2. Request, reception and packing of HK parameters.
- TIIL3. Reception and packing of Science data.
- TIIL4. HK monitoring.

## 2.1.3 System level

The first item to be tested at system level is the ability of the DPU to switch-on and the OBS to start under two different conditions. The first one is to load the OBS from the EEPROM, the second one is to load the OBS via TCs from SCOS2000.

- TISL1. Switch-on

At the system level we also have a set of test items that deal with the correct inter-task communication (Data & Controls flow). Specifically:

- TISL2. TMTC  $\leftrightarrow$  CMD\_SEQ
- TISL3. CMD\_SEQ  $\leftrightarrow$  LS
- TISL4. HK\_ASK  $\leftrightarrow$  LS
- TISL5. HK\_ASK  $\leftrightarrow$  TMTC
- TISL6. HS  $\leftrightarrow$  TMTC
- TISL7. HK\_MONITOR  $\leftrightarrow$  Autonomy\_Funct
- TISL8. Autonomy\_Funct  $\leftrightarrow$  LS
- TISL9. PID\_Controls  $\leftrightarrow$  LS

At the system level we also identify as a test item the ability to perform the following services specified in AD3 and required from the OBS according to AD1. The following set of system level test covers the full list of services required by AD3:

- TISL10. Telecommand Verification
- TISL11. Housekeeping & Diagnostic Data Reporting
- TISL12. Memory Management
- TISL13. Function Management
- TISL14. Event Reporting
- TISL15. Packet Transmission Control
- TISL16. Time management
- TISL17. Science Data Transfer
- TISL18. Test service

## 2.2 Test Deliverables

The items that will be delivered at the end of tests are:



1. Test procedures
2. Test report

## 2.3 Testing Tasks

These are the tasks needed to prepare and carry out the tests:

1. Preparation of a SPIRE specific MIB for SCOS2000 to be able to a) generate all TC packets needed for the OBS tests, and b) open and interpret HK and Event TM packets
2. Upload the compiled OBS to the DPU
3. Prepare TBD dedicated test SW tools (DTST) to open science TM packets
4. Execute the tests and compile the test report

## 2.4 Environmental Needs

The following equipment must be installed at IFSI in order for the tests to be carried out

1. DRCU SW simulator
2. EGSE(SCOS2000+Router+CDMS simulator)

It is planned that a small subset of the tests, identified in boldface in the test procedures (see 4), and which involve testing of the TFL protocol, be also executed with the ESA CDMS Testbed. Pass criteria in this case will simply be the correct reception of TC packets by the DPU (verifying that the DPU reacts as expected), and the correct dispatch of TM packets (verifying that SCOS2000 accepts and, in case of events and HK packets, correctly interprets the packets). The functionality required for this Testbed is the ability to run pre-defined 1553 bus profiles according to AD3 and AD4.

For these tests, it is required that the following equipment is available at the test site:

- 1) ESA CDMS Testbed
- 2) DPU
- 3) OBS development system (hosting a licensed copy of the VIRTUOSO operating system)

The ESA CDMS Testbed should provide these functionalities:

- Run pre-defined buslists, handling the exchange of TM and TC packets
- Be able to switch automatically between normal/burst modes bus lists
- Be able to handle the “retry-at-packet-level”

Based on these needs, the instruments required that the ESA CDMS Testbed is made available in a suitcase for shipping to the test site. In case, following the tests with the ESA CDMS Testbed, it is necessary to debug and update the OBS, the instruments anticipate that they will not be able to support such activities on any site other than IFSI.

## 2.5 Test case pass/fail criteria



Test criteria are based on the direct inspection Science, Event and HK TM Packets received by the EGSE. DTST will be used to inspect Science TM packets which SCOS2000 does not open. In case a test item has to be verified before the transmission of a TM packet, the evaluation criteria will be based on the direct inspection of the DSP DM.

### 3 Test case specifications

#### 3.1 SWITCH\_ON

The purpose is to demonstrate the ability of the OBS to correctly initialize and start-up under all foreseen conditions according to the procedure outlined in AD5.

##### 3.1.1 Test Items

TISL1.

##### 3.1.2 Input Specifications

The input to this test case will be two TC(8,4) specified in AD5 to tell the Boot Software to start the OBS. These commands are:

TC Code	Description
TC8.4.BSW.1	<i>Force_Boot</i> TC. This TC is interpreted by the Boot Software; it forces the OBS image currently in PM to start.
TC8.4.BSW.2	<i>Load_Boot</i> TC. This TC is interpreted by the Boot Software; it copies the OBS image from DM to PM and starts it.

In addition, the OBS executable shall be available on SCOS2000 in form of a set of standard TCs (6,2).

##### 3.1.3 Output specifications

None

##### 3.1.4 Environmental needs

The procedure to load the OBS via TCs from SCOS2000, as described in RD6, will be available.

#### 3.2 DPU\_COMMAND\_EXEC

The purpose is to demonstrate the link S/C-DPU by verifying the:

- Reception, validation and interpretation of TCs
- Command identification and execution
- Verification reporting





### 3.2.1 Test Items

TIUL1, TIUL2, TIUL3, TIUL9, TISL1, TISL10, TISL16p, TISL18

### 3.2.2 Input specifications

The input to this test case is a set of TCs built according to AD3, and requiring specific functions to be performed by the DPU. For this first test case this set shall be limited to self-contained commands that do not affect units not tested in this test case. Some of the TCs will contain invalid fields (e.g., APID etc.); if SCOS2000 is unable to send invalid packets, those packets will have to be available as HEX text files in the CDMS simulator. The set of TCs is specified below:

TC Code	Description
TC17.1.1	<i>Perform Connection Test</i> standard TC
TC17.1.2	Same as TC17.1.1, but with an incorrect APID of 0x400
TC17.1.3	Same as TC17.1.1, but with an incorrect packet length of 0xA
TC17.1.4	Same as TC17.1.1, but with an incorrect checksum of 0x1111
TC17.1.5	Same as TC17.1.1, but with an incorrect packet type of 0x1
TC17.1.6	Same as TC17.1.1, but with an incorrect packet subtype of 0xA
TC9.7.1	<i>Enable Time Verification</i> standard TC
TC14.3.1	<i>Report Enabled Telemetry Packets</i> standard TC

### 3.2.3 Output specifications

The output for this test case will consist in TM packets normally expected for the input TCs.

### 3.2.4 Environmental needs

The required set of input TCs will reside in SCOS2000 or on the CDMS Simulator. At the OBS start-up, packets generation will be enabled for all APIDs.

## 3.3 DPU\_MEM

The purpose is to demonstrate the ability to load, check and dump memory areas resident on the DPU. This will be done by absolute (via Service 6) and relative (via dedicated functions with Service 8) addresses in memory.

### 3.3.1 Test Items

TIUL2, TISL12, TISL13p. Test item TIUL2 is verified again here because SCIENCE packets (like the memory dump TM packets) are managed via a different memory pool with respect to EVENT packets (like the TC verification reports, see 3.2.1).

### 3.3.2 Input specifications

A set of TCs will be available:



<b>TC Code</b>	<b>Description</b>
TC6.2.1	<i>Memory Load</i> standard TC. Application data is structured according to RD2 with the following parameter values: <ul style="list-style-type: none"> <li>• Memory_ID = 1 (DM)</li> <li>• Start_Address = 0x2FF00</li> <li>• Length = 0x64</li> <li>• 100 data words all = 0xA5A5</li> </ul>
TC6.2.2	Same as TC6.2.1, but an incorrect Memory_ID = 4
TC6.2.3	Same as TC6.2.1, but an incorrect Start_Address = 0x80000
TC6.2.4	Same as TC6.2.1, but with Start_Address = 0x7FFF0, length = 0x10 and 32 data words
TC6.2.5	Same as TC6.2.1, but with an incorrect number of 20 data words
TC6.2.6	Same as TC6.2.1, but an incorrect Application Data CRC of 0x1111
TC6.5.1	<i>Memory Dump</i> standard TC. Application data is structured according to RD2 with the following parameter values: <ul style="list-style-type: none"> <li>• Memory_ID = 1 (DM)</li> <li>• Start_Address = 0x2FF00</li> <li>• Length = 0x64</li> </ul>
TC6.9.1	<i>Memory Check</i> standard TC. Application data will be as in RD2
TC8.4.1-1.1	<i>Set Table</i> standard TC. Application data will be structured as specified in RD2 and will contain the following parameters: <ul style="list-style-type: none"> <li>• Function_ID = 0x01</li> <li>• Activity_ID = 0x01</li> <li>• Table_ID = 0x40</li> <li>• Length = 0x32 (in units of 32-bit words)</li> </ul>
TC8.4.1-1.2	Same as TC8.4.1-1.1, but with Function_ID of 0xE0
TC8.4.1-1.3	Same as TC8.4.1-1.1, but with Activity_ID of 0xA
TC8.4.1-1.4	Same as TC8.4.1-1.1, but with SID of 0x902
TC8.4.1-1.5	Same as TC8.4.1-1.1, but with Table_ID of 0x200
TC8.4.1-1.6	Same as TC8.4.1-1.1, but with Length = 0x4E20
TC8.4.1-2.1	<i>Report Table</i> standard TC. Application data will be structured as specified in RD2 and will contain the following parameters: <ul style="list-style-type: none"> <li>• Function_ID = 0x01</li> <li>• Activity_ID = 0x02</li> <li>• Table_ID = 0x40</li> <li>• Index = 0</li> <li>• Length = 0x32</li> </ul>
TC8.4.1-3.1	<i>Update Table</i> standard TC. Application data will be structured as specified in RD2 and will contain the following parameters: <ul style="list-style-type: none"> <li>• Function_ID = 0x01</li> <li>• Activity_ID = 0x03</li> <li>• Table_ID = 0x40</li> <li>• Index = 0</li> <li>• Length = 0x32</li> <li>• 50 16-bit words with pattern 0x5A5A</li> </ul>
TC8.4.1-3.2	Same as TC8.4.1-3.1, but with Table_ID = 0x30
TC8.4.1-3.3	Same as TC8.4.1-3.1, but with Index = 0x64



TC8.4.1-3.4	Same as TC8.4.1-3.1, but with Length = 0x40
TC8.4.1-3.5	Same as TC8.4.1-3.1, but with only 40 16-bit data words

### 3.3.3 Output specifications

The output will consist of the set of TM packets expected in response to input TCs.

### 3.3.4 Environmental needs

The required set of input TCs will reside in SCOS2000 or on the CDMS Simulator. At the OBS start-up, packets generation will be enabled for all APIDs.

## 3.4 HK\_COLLECT

The purpose is to test the DPU-S/S chain by demonstrating the collection and transmission of HK packets. The ability to support the TM transmission retry at packet level will also be tested here.

### 3.4.1 Test Items

TIUL2, TIUL4, TIUL5, TIIL2, TISL4, TISL5, TISL11. Test item TIUL2 is verified again here because HK packets are managed via a different memory pool with respect to EVENT packets (like the TC verification reports, see 3.2.1) and SCIENCE packets (like memory dump packets, see 3.3.1).

### 3.4.2 Input specifications

A set of TCs will be available:

TC Code	Description
TC8.4.1-1.10	<i>Set Table</i> standard TC. Application data will be structured as specified in RD2 and will contain the following parameters: <ul style="list-style-type: none"> <li>• Function_ID = 0x01</li> <li>• Activity_ID = 0x01</li> <li>• Table_ID = 2</li> <li>• Length = 0x14</li> </ul>
TC8.4.1-1.11	Same as TC8.4.1-1.10 but with Table_ID = 3
TC8.4.1-3.10	<i>Update Table</i> standard TC. Application data will be structured as specified in RD2 and will contain the following parameters: <ul style="list-style-type: none"> <li>• Function_ID = 0x01</li> <li>• Activity_ID = 0x03</li> <li>• Table_ID = 2</li> <li>• Index = 0</li> <li>• Ndata = 0x14 (in units of 32-bit words)</li> <li>• 40 16-bit data words which will represent 20 HK collection commands (TBD)</li> </ul>
TC8.4.1-3.11	Same as TC8.4.1-3.10 but with Table_ID = 3 and a different set of HK collec-



	tion commands (TBD)
TC3.2.1	<i>Define New Diagnostic Parameter Report</i> standard TC. Application data will be structured as specified in RD2 and will contain the following parameters: <ul style="list-style-type: none"> <li>• HKPCKTID = 2</li> <li>• Least significant octet of HKSID = 0x02</li> <li>• HKINTERVAL = 100</li> </ul>
TC3.2.2	Same as TC3.2.1, but with HKPCKTID = 3 and LSO of HKSID = 0x03
TC3.2.3	Same as TC3.2.1, but with HKPCKTID = 4
TC3.2.4	Same as TC3.2.1, but with HKINTERVAL = 5
TC3.2.5	Same as TC3.2.1, but with LSO of HKSID = 0x28
TC3.4.1	<i>Clear Diagnostic Parameter Report Definition</i> standard TC. Application data will be structured as specified in RD2 and will contain the following parameters: <ul style="list-style-type: none"> <li>• HKPCKTID = 2</li> </ul>
TC3.4.2	Same as TC3.4.1, with HKPCKTID = 3
TC3.9.1	<i>Report Housekeeping Parameter Report Definition</i> standard TC
TC3.11.1	<i>Report Diagnostic Parameter Report Definition</i> standard TC
TCTest.1	<i>Perform Activity of Function</i> standard TC with function ID = 0xCB and activity ID = 0x01. This TC is used to force a wrong CRC to be attached to a TM packet being dispatched.

### 3.4.3 Output specifications

The output for this test case will consist in TM packets containing the HK data.

### 3.4.4 Environmental needs

The DRCU Simulator will be connected to the DPU. The structure of the HK packets will be defined in SCOS2000 so that the packets can be opened and checked. Alternatively, DTSTs will have to be used. At the OBS start-up, packets generation will be enabled for all APIDs, and the default HK and Diagnostic packet structure will be defined on-board. It is assumed that the DRCU simulator will conform to RD3 in its ability to identify and execute commands. The DRCU simulator will allow on-the-fly modification of any HK parameter, without having to stop and restart its software.

## 3.5 VM

The purpose is to demonstrate that all Virtual Machines available in the OBS as specified in AD2 can run in parallel without interfering with one another; this is a potential risk since all VMs use the same interface to send commands and receive parameters from the DRCU.

### 3.5.1 Test Items

TIUL10

### 3.5.2 Input Specifications



The following set of TCs will be available:

TC Code	Description
TC8.4.1-1.20	<i>Set Table</i> standard TC. Application data will be structured as specified in RD2 and will contain the following parameters: <ul style="list-style-type: none"> <li>Function_ID = 0x01</li> <li>Activity_ID = 0x01</li> <li>Table_ID = 0x20</li> <li>Length = <i>length of GET_HK_PAR1</i></li> </ul>
TC8.4.1-1.21	Same as TC8.4.1-1.20, but with: <ul style="list-style-type: none"> <li>Table_ID = 0x21</li> <li>Length = <i>length of GET_HK_PAR2</i></li> </ul>
TC8.4.1-1.22	Same as TC8.4.1-1.20, but with: <ul style="list-style-type: none"> <li>Table_ID = 0x22</li> <li>Length = <i>length of GET_HK_PAR3</i></li> </ul>
TC8.4.1-1.23	Same as TC8.4.1-1.20, but with: <ul style="list-style-type: none"> <li>Table_ID = 0x28</li> <li>Length = <i>length of GET_HK_PAR4</i></li> </ul>
TC8.4.1-3.20	<i>Update Table</i> standard TC. Application data will be structured as specified in RD2 and will contain the following parameters: <ul style="list-style-type: none"> <li>Function_ID = 0x01</li> <li>Activity_ID = 0x03</li> <li>Table_ID = 0x20</li> <li>INDEX = 0</li> <li>NDATA = <i>length of VME GET_HK_PAR1</i></li> <li>DATA = VME GET_HK_PAR1</li> </ul>
TC8.4.1-3.21	Same as TC8.4.1-3.20, but with: <ul style="list-style-type: none"> <li>Table_ID = 0x21</li> <li>NDATA = <i>length of VME GET_HK_PAR2</i></li> <li>DATA = VME GET_HK_PAR2</li> </ul>
TC8.4.1-3.22	Same as TC8.4.1-3.20, but with: <ul style="list-style-type: none"> <li>Table_ID = 0x22</li> <li>NDATA = <i>length of VME GET_HK_PAR3</i></li> <li>DATA = VME GET_HK_PAR3</li> </ul>
TC8.4.1-3.23	Same as TC8.4.1-3.20, but with: <ul style="list-style-type: none"> <li>Table_ID = 0x28</li> <li>NDATA = <i>length of VME GET_HK_PAR4</i></li> <li>DATA = VME GET_HK_PAR4</li> </ul>
TC8.1.2.1	<i>Start Cooler Control</i> standard TC as specified in RD2, with all parameters set to 0
TC8.1.3.1	<i>Start SCAL Control</i> standard TC as specified in RD2, with all parameters set to 0
TC8.1.4.1	<i>Start 300mK Control</i> standard TC as specified in RD2, with all parameters set to 0
TC8.2.2.1	<i>Stop Cooler Control</i> standard TC as specified in RD2
TC8.2.3.1	<i>Stop SCAL Control</i> standard TC as specified in RD2
TC8.2.4.1	<i>Stop 300mK Control</i> standard TC as specified in RD2



TC8.4.C0-2.1	<p><i>Start Command List</i> standard TC. Application data will be structured as specified in RD2 and will contain the following parameters:</p> <ul style="list-style-type: none"> <li>• Function_ID = 0xC0</li> <li>• Activity_ID = 0x02</li> <li>• Table_ID = 0x28</li> <li>• Index = 0</li> <li>• N = 0</li> </ul>
--------------	--

The following set of VMEs will be available:

VME Code	Pseudo-Code
GET_HK_PAR1	<ul style="list-style-type: none"> <li>• <i>While (1)</i> <ul style="list-style-type: none"> <li>○ <i>For I=0,79</i> <ul style="list-style-type: none"> <li>▪ <i>GetTestPar1</i> (DCU Get command, CID 0x7FA)</li> <li>▪ <i>If (parameter != 0x7FA) Generate_Event (5,1)</i></li> </ul> </li> <li>○ <i>Wait (1 second)</i></li> </ul> </li> </ul>
GET_HK_PAR2	<ul style="list-style-type: none"> <li>• <i>While (1)</i> <ul style="list-style-type: none"> <li>○ <i>For I=0,79</i> <ul style="list-style-type: none"> <li>▪ <i>GetTestPar2</i> (MCU Get command, CID 0x7FB)</li> <li>▪ <i>If (parameter != 0x7FB) Generate_Event (5,1)</i></li> </ul> </li> </ul> </li> <li><i>Wait (1 second)</i></li> </ul>
GET_HK_PAR3	<ul style="list-style-type: none"> <li>• <i>While (1)</i> <ul style="list-style-type: none"> <li>○ <i>For I=0,79</i> <ul style="list-style-type: none"> <li>▪ <i>GetTestPar3</i> (SCU Get command, CID 0x7FC)</li> <li>▪ <i>If (parameter != 0x7FC) Generate_Event (5,1)</i></li> </ul> </li> </ul> </li> <li>• <i>Wait (1 second)</i></li> </ul>
GET_HK_PAR4	<ul style="list-style-type: none"> <li>• <i>While (1)</i> <ul style="list-style-type: none"> <li>○ <i>For I=0,79</i> <ul style="list-style-type: none"> <li>▪ <i>GetTestPar4</i> (DCU Get command, CID 0x7FD)</li> <li>▪ <i>If (parameter != 0x7FD) Generate_Event (5,1)</i></li> </ul> </li> </ul> </li> <li>• <i>Wait (1 second)</i></li> </ul>

The DRCU commands *GetTestPar1*, *GetTestPar2* and *GetTestPar3* will be custom generated on the DRCU simulator. The CIDs listed in the table above are not used for any of the commands specified in RD3 and RD5. The output buffers of the DRCU simulator will be configured so that the parameters sent in response to the above commands will be identical to the CID; no HK parameter returned in response to standard HK requests will contain any of those values.

### 3.5.3 Output specifications

Output for this test case will consist of standard HK packets.

### 3.5.4 Environmental needs

The required set of input TCs will reside in SCOS2000 or on the CDMS Simulator. The DRCU Simulator will be connected to the DPU. A Logic State Analyser will also be used to monitor the



GATE lines of the three cables going from the DPU to the DRCU simulator; this will provide evidence of the HK parameter requests traffic on the LS port.

### 3.6 COMMAND\_LIST\_EXEC

The purpose is to demonstrate the ability to execute in a timely fashion command lists, both resident on-board and uplinked as part of a TC, via a Virtual Machine as specified in RD4. The command lists will implement simulated data acquisition so that the reception (from S/Ss), control, packing and transmission (to S/C) of science frames will also be tested here. The execution of particular commands or command lists will allow testing of other OBS features like the Time Management, and the Information Distribution Service.

#### 3.6.1 Test Items

TIUL6, TIUL9, TIIL1, TIIL3, TISL3, TISL6, TISL14, TISL15, TISL16, TISL17.

#### 3.6.2 Input specifications

The following set of TCs will be available:

TC Code	Description
TC8.4.C0-1.1	<i>Execute Command List</i> standard TC. Application data will be structured as specified in RD2 and will contain the following parameters: <ul style="list-style-type: none"> <li>• Function_ID = 0xC0</li> <li>• Activity_ID = 0x01</li> <li>• Length = <i>length of VME ACQ_PHT</i></li> <li>• Data field contains VME ACQ_PHT</li> </ul>
TC8.4.C0-3.1	<i>Stop Command List</i> standard TC. Application data will be structured as specified in RD2 and will contain the following parameters: <ul style="list-style-type: none"> <li>• Function_ID = 0xC0</li> <li>• Activity_ID = 0x03</li> </ul>
TC8.4.1-1.30	<i>Set Table</i> standard TC. Application data will be structured as specified in RD2 and will contain the following parameters: <ul style="list-style-type: none"> <li>• Function_ID = 0x01</li> <li>• Activity_ID = 0x01</li> <li>• Table_ID = 0x30</li> <li>• Length = <i>length of VME ACQ_PHT</i></li> </ul>
TC8.4.1-1.31	Same as TC8.4.1-1.30, but with: <ul style="list-style-type: none"> <li>• Table_ID = 0x31</li> <li>• Length = <i>length of VME ACQ_SPT</i></li> </ul>
TC8.4.1-1.32	Same as TC8.4.1-1.30, but with: <ul style="list-style-type: none"> <li>• Table_ID = 0x32</li> <li>• Length = <i>length of VME P-SW_ACQ</i></li> </ul>
TC8.4.1-1.33	Same as TC8.4.1-1.30, but with: <ul style="list-style-type: none"> <li>• Table_ID = 0x33</li> <li>• Length = <i>length of VME P-MW_ACQ</i></li> </ul>
TC8.4.1-1.34	Same as TC8.4.1-1.30, but with:



	<ul style="list-style-type: none"> <li>• Table_ID = 0x34</li> <li>• Length = <i>length of VME P-LW_ACQ</i></li> </ul>
TC8.4.1-1.35	Same as TC8.4.1-1.30, but with: <ul style="list-style-type: none"> <li>• Table_ID = 0x35</li> <li>• Length = <i>length of VME S-SW_ACQ</i></li> </ul>
TC8.4.1-1.36	Same as TC8.4.1-1.30, but with: <ul style="list-style-type: none"> <li>• Table_ID = 0x36</li> <li>• Length = <i>length of VME S-LW_ACQ</i></li> </ul>
TC8.4.1-1.37	Same as TC8.4.1-1.30, but with: <ul style="list-style-type: none"> <li>• Table_ID = 0x37</li> <li>• Length = <i>length of VME PH-TEST_ACQ</i></li> </ul>
TC8.4.1-1.38	Same as TC8.4.1-1.30, but with: <ul style="list-style-type: none"> <li>• Table_ID = 0x38</li> <li>• Length = <i>length of VME SP-TEST_ACQ</i></li> </ul>
TC8.4.1-1.39	Same as TC8.4.1-1.30, but with: <ul style="list-style-type: none"> <li>• Table_ID = 0x39</li> <li>• Length = <i>length of VME PH-OFF_ACQ</i></li> </ul>
TC8.4.1-1.40	Same as TC8.4.1-1.30, but with: <ul style="list-style-type: none"> <li>• Table_ID = 0x3A</li> <li>• Length = <i>length of VME SP-OFF_ACQ</i></li> </ul>
TC8.4.1-1.41	Same as TC8.4.1-1.30, but with: <ul style="list-style-type: none"> <li>• Table_ID = 0x3B</li> <li>• Length = <i>length of VME CONF_PHT</i></li> </ul>
TC8.4.1-1.42	Same as TC8.4.1-1.30, but with: <ul style="list-style-type: none"> <li>• Table_ID = 0x3C</li> <li>• Length = <i>length of VME CONF_SPT</i></li> </ul>
TC8.4.1-1.43	Same as TC8.4.1-1.30, but with: <ul style="list-style-type: none"> <li>• Table_ID = 0x3D</li> <li>• Length = <i>length of VME ACQ_C_PHT</i></li> </ul>
TC8.4.1-3.30	<i>Update Table</i> standard TC. Application data will be structured as specified in RD2 and will contain the following parameters: <ul style="list-style-type: none"> <li>• Function_ID = 0x01</li> <li>• Activity_ID = 0x03</li> <li>• Table_ID = 0x30</li> <li>• INDEX = 0</li> <li>• NDATA = <i>length of VME ACQ_PHT</i></li> <li>• DATA = VME ACQ_PHT</li> </ul>
TC8.4.1-3.31	Same as TC8.4.1-3.30, but with: <ul style="list-style-type: none"> <li>• Table_ID = 0x31</li> <li>• NDATA = <i>length of VME ACQ_SPT</i></li> <li>• DATA = VME ACQ_SPT</li> </ul>
TC8.4.1-3.32	Same as TC8.4.1-3.30, but with: <ul style="list-style-type: none"> <li>• Table_ID = 0x32</li> <li>• NDATA = <i>length of VME P-SW_ACQ</i></li> <li>• DATA = VME P-SW_ACQ</li> </ul>
TC8.4.1-3.33	Same as TC8.4.1-3.30, but with: <ul style="list-style-type: none"> <li>• Table_ID = 0x33</li> </ul>





	<ul style="list-style-type: none"> <li>• NDATA = <i>length of VME P-MW_ACQ</i></li> <li>• DATA = VME P-MW_ACQ</li> </ul>
TC8.4.1-3.34	Same as TC8.4.1-3.30, but with: <ul style="list-style-type: none"> <li>• Table_ID = 0x34</li> <li>• NDATA = <i>length of VME P-LW_ACQ</i></li> <li>• DATA = VME P-LW_ACQ</li> </ul>
TC8.4.1-3.35	Same as TC8.4.1-3.30, but with: <ul style="list-style-type: none"> <li>• Table_ID = 0x35</li> <li>• NDATA = <i>length of VME S-SW_ACQ</i></li> <li>• DATA = VME S-SW_ACQ</li> </ul>
TC8.4.1-3.36	Same as TC8.4.1-3.30, but with: <ul style="list-style-type: none"> <li>• Table_ID = 0x36</li> <li>• NDATA = <i>length of VME S-LW_ACQ</i></li> <li>• DATA = VME S-LW_ACQ</li> </ul>
TC8.4.1-3.37	Same as TC8.4.1-3.30, but with: <ul style="list-style-type: none"> <li>• Table_ID = 0x37</li> <li>• NDATA = <i>length of VME PH-TEST_ACQ</i></li> <li>• DATA = VME PH_TEST_ACQ</li> </ul>
TC8.4.1-3.38	Same as TC8.4.1-3.30, but with: <ul style="list-style-type: none"> <li>• Table_ID = 0x38</li> <li>• NDATA = <i>length of VME SP-TEST_ACQ</i></li> <li>• DATA = VME SP_TEST_ACQ</li> </ul>
TC8.4.1-3.39	Same as TC8.4.1-3.30, but with: <ul style="list-style-type: none"> <li>• Table_ID = 0x39</li> <li>• NDATA = <i>length of VME PH-OFF_ACQ</i></li> <li>• DATA = VME PH_OFF_ACQ</li> </ul>
TC8.4.1-3.40	Same as TC8.4.1-3.30, but with: <ul style="list-style-type: none"> <li>• Table_ID = 0x3A</li> <li>• NDATA = <i>length of VME SP-OFF_ACQ</i></li> <li>• DATA = VME SP_OFF_ACQ</li> </ul>
TC8.4.1-3.41	Same as TC8.4.1-3.30, but with: <ul style="list-style-type: none"> <li>• Table_ID = 0x3B</li> <li>• NDATA = <i>length of VME CONF_PHT</i></li> <li>• DATA = VME CONF_PHT</li> </ul>
TC8.4.1-3.42	Same as TC8.4.1-3.30, but with: <ul style="list-style-type: none"> <li>• Table_ID = 0x3C</li> <li>• NDATA = <i>length of VME CONF_SPT</i></li> <li>• DATA = VME CONF_SPT</li> </ul>
TC8.4.1-3.43	Same as TC8.4.1-3.30, but with: <ul style="list-style-type: none"> <li>• Table_ID = 0x3D</li> <li>• NDATA = <i>length of VME ACQ_C_PHT</i></li> <li>• DATA = VME ACQ_C_PHT</li> </ul>
TC8.4.C0-2.10	<i>Start Command List</i> standard TC. Application data will be structured as specified in RD2 and will contain the following parameters: <ul style="list-style-type: none"> <li>• Function_ID = 0xC0</li> <li>• Activity_ID = 0x02</li> </ul>



	<ul style="list-style-type: none"> <li>• Table_ID = 0x30</li> <li>• Index = 0</li> <li>• N = 0</li> </ul>
TC8.4.C0-2.11	Same as TC8.4.C0-2.10, but with Table_ID = 0x31
TC8.4.C0-2.12	Same as TC8.4.C0-2.10, but with Table_ID = 0x32
TC8.4.C0-2.13	Same as TC8.4.C0-2.10, but with Table_ID = 0x33
TC8.4.C0-2.14	Same as TC8.4.C0-2.10, but with Table_ID = 0x34
TC8.4.C0-2.15	Same as TC8.4.C0-2.10, but with Table_ID = 0x35
TC8.4.C0-2.16	Same as TC8.4.C0-2.10, but with Table_ID = 0x36
TC8.4.C0-2.17	Same as TC8.4.C0-2.10, but with Table_ID = 0x37
TC8.4.C0-2.18	Same as TC8.4.C0-2.10, but with Table_ID = 0x38
TC8.4.C0-2.19	Same as TC8.4.C0-2.10, but with Table_ID = 0x39
TC8.4.C0-2.20	Same as TC8.4.C0-2.10, but with Table_ID = 0x3A
TC8.4.C0-2.21	Same as TC8.4.C0-2.10, but with Table_ID = 0x3B
TC8.4.C0-2.22	Same as TC8.4.C0-2.10, but with Table_ID = 0x3C
TC8.4.C0-2.23	Same as TC8.4.C0-2.10, but with Table_ID = 0x3D
TC8.4.C1-1.1	<i>Set Observation ID</i> standard TC. Application data will be structured as specified in RD2 and will contain the following 2 data words: 0xA5A5, 0x5A5A
TC8.4.C1-2.1	<i>Set Building Block ID</i> standard TC. Application data will be structured as specified in RD2 and will contain the following 2 data words: 0x1212, 0x2121
TC8.4.C1-3.1	<i>Set Observing Mode</i> standard TC. Application data will be structured as specified in RD2 and will contain the data words: 0xC1C1
TC8.4.C1-4.1	<i>Set Observation Step</i> standard TC. Application data will be structured as specified in RD2 and will contain the data word: 0x1
TC8.4.C1-4.2	<i>Set Observation Step</i> standard TC. Application data will be structured as specified in RD2 and will contain the data word: 0x2
TC8.4.C1-4.3	<i>Set Observation Step</i> standard TC. Application data will be structured as specified in RD2 and will contain the data word: 0x3
TC8.4.C1-4.4	<i>Synchronize DRCU Counters</i> standard TC as in RD2
TC14.1.1	<p><i>Enable Generation of Telemetry Packets</i> standard TC. Application data will as specified in AD3 with the following parameters:</p> <ul style="list-style-type: none"> <li>• N=2</li> <li>• 1<sup>st</sup> block <ul style="list-style-type: none"> <li>○ Type = 21</li> <li>○ Subtype = 1</li> <li>○ SID = 0x200</li> </ul> </li> <li>• 2<sup>nd</sup> block <ul style="list-style-type: none"> <li>○ Type = 3</li> <li>○ Subtype = 25</li> <li>○ SID = 0x300</li> </ul> </li> </ul>
TC14.2.1	<p><i>Disable Generation of Telemetry Packets</i> standard TC. Application data will as specified in AD3 with the following parameters:</p> <ul style="list-style-type: none"> <li>• N = 1</li> <li>• Type = 21</li> <li>• Subtype = 1</li> <li>• SID = 0x200</li> </ul>
TC14.2.2	Same as TC14.2.1, but with:



	<ul style="list-style-type: none"> <li>• N=1</li> <li>• Type = 3</li> <li>• Subtype = 25</li> <li>• SID = 0x300</li> </ul>
TC14.3.1	<i>Report Enabled Telemetry Packets</i> standard TC.

A list of VMEs will also be available:

VME Code	Pseudo-Code
FRAME_ACQ	<ul style="list-style-type: none"> <li>• <i>SetFrameNber (0xFF)</i></li> <li>• <i>SetStartFrame (1)</i></li> <li>• <i>Wait (5 seconds)</i></li> <li>• <i>SetStartFrame (0)</i></li> <li>• <i>Flush FIFOs</i></li> </ul>
CONT_ACQ	<ul style="list-style-type: none"> <li>• <i>SetFrameNber (0)</i></li> <li>• <i>SetStartFrame (1)</i></li> </ul>
ACQ_PHT	<ul style="list-style-type: none"> <li>• <i>SetDataMode (00000)</i></li> <li>• <i>CALL FRAME_ACQ</i></li> </ul>
ACQ_C_PHT	<ul style="list-style-type: none"> <li>• <i>SetDataMode (00000)</i></li> <li>• <i>CALL CONT_ACQ</i></li> </ul>
ACQ_SPT	<ul style="list-style-type: none"> <li>• <i>SetDataMode (00100)</i></li> <li>• <i>CALL FRAME_ACQ</i></li> </ul>
CONF_PHT	<ul style="list-style-type: none"> <li>• <i>SetPhotoSampFreq (0x96)</i></li> <li>• <i>SetPhotoBiasFreq (0x190)</i></li> <li>• <i>SetPhotoBiasMode (0xFF)</i></li> <li>• <i>SetPhotoDemodPh (0x40, 0)</i></li> <li>• <i>SetPhotoDemodPh (0x50, 1)</i></li> <li>• <i>SetPhotoDemodPh (0x60, 2)</i></li> <li>• <i>SetPhotoDemodPh (0x70, 3)</i></li> <li>• <i>SetPhotoBiasAmpl (0x45, 0)</i></li> <li>• <i>SetPhotoBiasAmpl (0x55, 1)</i></li> <li>• <i>SetPhotoBiasAmpl (0x65, 2)</i></li> <li>• <i>SetPhotoBiasAmpl (0x75, 3)</i></li> </ul>
CONF_SPT	<ul style="list-style-type: none"> <li>• <i>SetSpectroSampFreq (0x26)</i></li> <li>• <i>SetSpectroBiasFreq (0xC8)</i></li> <li>• <i>SetSpectroBiasMode (0xFF)</i></li> <li>• <i>SetSpectroDemodPh (0xA, 0)</i></li> <li>• <i>SetSpectroDemodPh (0x1A, 1)</i></li> <li>• <i>SetSpectroBiasAmpl (0xAA, 0)</i></li> <li>• <i>SetSpectroBiasAmpl (0xBB, 1)</i></li> </ul>
P-SW_ACQ	<ul style="list-style-type: none"> <li>• <i>SetDataMode (00001)</i></li> <li>• <i>CALL FRAME_ACQ</i></li> </ul>
P-MW_ACQ	<ul style="list-style-type: none"> <li>• <i>SetDataMode (00002)</i></li> <li>• <i>CALL FRAME_ACQ</i></li> </ul>
P-LW_ACQ	<ul style="list-style-type: none"> <li>• <i>SetDataMode (00003)</i></li> <li>• <i>CALL FRAME_ACQ</i></li> </ul>



S-SW_ACQ	<ul style="list-style-type: none"> <li>• <i>SetDataMode (00005)</i></li> <li>• <i>CALL FRAME_ACQ</i></li> </ul>
S-LW_ACQ	<ul style="list-style-type: none"> <li>• <i>SetDataMode (00006)</i></li> <li>• <i>CALL FRAME_ACQ</i></li> </ul>
PH-TEST-ACQ	<ul style="list-style-type: none"> <li>• <i>SetDataMode (01000)</i></li> <li>• <i>CALL FRAME_ACQ</i></li> </ul>
SP-TEST_ACQ	<ul style="list-style-type: none"> <li>• <i>SetDataMode (01100)</i></li> <li>• <i>CALL FRAME_ACQ</i></li> </ul>
PH-OFF_ACQ	<ul style="list-style-type: none"> <li>• <i>SetDataMode (11000)</i></li> <li>• <i>CALL FRAME_ACQ</i></li> </ul>
SP-OFF_ACQ	<ul style="list-style-type: none"> <li>• <i>SetDataMode (11100)</i></li> <li>• <i>CALL FRAME_ACQ</i></li> </ul>
	<ul style="list-style-type: none"> <li>• <i>TO BE COMPLETED WHEN I UNDERSTAND HOW TO GENERATE ALL OTHER FRAME TYPES</i></li> </ul>

### 3.6.3 Output specifications

The output for this test case will consist of science and event TM packets that will be opened using a DTST. HK Packets will be accessed via SCOS2000 to check, via inspection of relevant HK parameters that the expected sequence of actions has been performed as expected.

### 3.6.4 Environmental needs

The required set of input TCs will reside in SCOS2000 or on the CDMS Simulator. The DRCU Simulator will be connected to the DPU. At the OBS start-up, packets generation will be enabled for all APIDs, and the default HK packet structure will be defined on-board.

## 4 Test Procedures

The start/stop/debug functionalities for the OBS on the DPU during these tests are managed from a PC using the DSP In-Circuit Emulator software. In case SCOS2000 can be used to send TC packets, it is assumed that full chain SCOS2000+Router+CDMS Simulator is operational.

### 4.1 TP0

This procedure executes test case SWITCH\_ON. Procedure steps which will be repeated as part of the acceptance tests are lightly shaded.

Step #	Action	Pass/Fail	Test Item
1	<b>Switch-on the DPU. At this point the Boot Software loads the OBS image from the EEPROM to PM. After completion, the Boot SW stops.</b>	No event TM (5,4) should be received by SCOS2000.	
2	<b>Send TC8.4.BSW.1 to start the OBS.</b>	The standard HK TM packets TM (3,19) should be received by SCOS2000	



3	Switch-off the DPU.		
4	Switch-on the DPU.		
5	Run the procedure resident on the SCOS2000 router to load the image of the OBS directly onto the DPU DM.		
6	Send TC8.4.BSW.2 to copy the OBS image from DM to PM and start it.	The standard HK TM packets TM (3,19) should be received by SCOS2000	TISL1

## 4.2 TP1

This procedure executes the test cases DPU\_COMMAND\_EXEC and DPU\_MEM. Procedure steps which will be repeated as part of the acceptance tests are lightly shaded. The TCs are identified by their codes as specified in 3.2.2 and 3.3.2. OBS loading is performed via ICE; OBS run/stop/restart functions are performed in ICE in CBUG mode.

Step #	Action	Pass/Fail	Test Item
7	Open VIRTUOSO project file in directory where the code resides.		
8	Assign the HK_ASK task to the EXE_NOBOOT group and compile the OBS		
9	Load the OBS in the DPU.		
10	Set a BP in OBS where the TC acceptance report is generated.		
11	Start the OBS.		
12	Start the CDMS Simulator.		
13	Send TC17.1.1	OBS stops at BP	TISL2
14	Inspect the location in the DM where the report TM packet has been written.	Verify format in conformity with AD3. Verify content of packet to reflect TC type (valid/invalid)	TIUL1
15	Remove BP. Restart OBS.		
16	Send TC17.1.1	Verify reception of: TM (17,2), TM (1,1)	TIUL2, TISL2, TISL18
17	Send TC14.3.1	Verify reception of: TM (14,4), TM (1,1) Verify that the list of SIDs in TM (14,4) matches the list of enabled TM packets (all of them are enabled by defaults at start-up).	
18	Send TC9.7.1	Verify reception of: TM (9,9), TM (1,1)	TISL16p



19	Stop OBS. Stop CDMS. Open CDMS file APID2RT.txt and associate SPIRE with APID 0x400; this is needed to force the CDMS to send TCs with wrong APID to SPIRE.		
20	Start OBS. Start CDMS.		
21	Send TC17.1.2	Verify reception of TM (1,2) with failure code 0	
22	Stop OBS. Stop CDMS. Open CDMS file APID2RT.txt and change SPIRE's APID back to nominal.		
23	Start OBS. Start CDMS.		
24	Send TC17.1.3	Verify reception of TM (1,2) with failure code 1	
25	Send TC17.1.4	Verify reception of TM (1,2) with failure code 2	
26	Send TC17.1.5	Verify reception of TM (1,2) with failure code 3	
27	Send TC17.1.6	Verify reception of TM (1,2) with failure code 4	TISL10
28	Send TC6.5.1	Verify reception of TM (1,1) and TMs (6,6). Check that the received words are different from the pattern contained in TC6.2.1	TIUL3
29	Send TC6.2.1	Verify reception of TM (1,1)	
30	Send TC6.5.1	Verify reception of TM (1,1) and TMs (6,6). Compare received data words to the pattern uplinked in TC6.2.1. Store received words into a text file on the CDMS computer. Run program CRC on this file and record the computed CRC.	
31	Send TC6.9.1	Verify reception of TM (1,1) and TM (6,10). Verify that the received Checksum is identical to CRC computed in the previous step.	
32	Send TC6.2.2	Verify reception of TM (1,8) with error code 0x601	
33	Send TC6.2.3	Verify reception of TM (1,8) with error code 0x602	
34	Send TC6.2.4	Verify reception of TM (1,8) with error code 0x603	



35	Send TC6.2.5	Verify reception of TM (1,8) with error code 0x604	
36	Send TC6.2.6	Verify reception of TM (1,8) with error code 0x605	TISL12
37	Send TC8.4.1-2.1	Verify reception of TM (1,8) with error code TBD	
38	Send TC8.4.1-1.1	Verify reception of TM (1,1)	
39	Send TC8.4.1-2.1	Verify reception of TM (1,1) and TM (21,1). Verify that the received patter is different from the one contained in TC8.4.1-1.1	
40	Send TC8.4.1-3.1	Verify reception of TM (1,1)	
41	Send TC8.4.1-2.1	Verify reception of TM (1,1) and TM (21,1). Verify that the received pattern is identical to that uplinked in TC8.4.1-1.1	
42	Send TC8.4.1-1.2	Verify reception of TM (1,8) with error code 0x801	
43	Send TC8.4.1-1.3	Verify reception of TM (1,8) with error code 0x802	
44	Send TC8.4.1-1.4	Verify reception of TM (1,8) with error code 0x804	
45	Send TC8.4.1-1.5	Verify reception of TM (1,8) with error code 0x805	
46	Send TC8.4.1-1.6	Verify reception of TM (1,8) with error code 0x806	
47	Send TC8.4.1-3.2	Verify reception of TM (1,8) with error code TBD	
48	Send TC8.4.1-3.3	Verify reception of TM (1,8) with error code 0x806	
49	Send TC8.4.1-3.4	Verify reception of TM (1,8) with error code 0x807	
50	Send TC8.4.1-3.5	Verify reception of TM (1,8) with error code 0x808	

### 4.3 TP2

### 4.4 This procedure executes test case HK\_COLLECT. It is assumed at this stage that procedure TP0

This procedure executes test case SWITCH\_ON. Procedure steps which will be repeated as part of the acceptance tests are lightly shaded.

Step #	Action	Pass/Fail	Test Item
--------	--------	-----------	-----------



51	Switch-on the DPU. At this point the Boot Software loads the OBS image from the EEPROM to PM. After completion, the Boot SW stops.	No event TM (5,4) should be received by SCOS2000.	
52	Send TC8.4.BSW.1 to start the OBS.	The standard HK TM packets TM (3,19) should be received by SCOS2000	
53	Switch-off the DPU.		
54	Switch-on the DPU.		
55	Run the procedure resident on the SCOS2000 router to load the image of the OBS directly onto the DPU DM.		
56	Send TC8.4.BSW.2 to copy the OBS image from DM to PM and start it.	The standard HK TM packets TM (3,19) should be received by SCOS2000	TISL1

**TP1**

has been executed successfully. The DPU-S/C interface and the capability of the OBS to receive, interpret and execute commands should have been successfully tested. Procedure steps, which will be repeated as part of the acceptance tests, are lightly shaded.

Step #	Action	Pass/Fail	Test Item
1	Open VIRTUOSO project file in directory where the code resides.		
2	Assign the HK_ASK task to the EXE group and compile the OBS		
3	Configure DRCU Simulator to assign pre-defined values to the set HK parameters that will be sent to the DPU.		
4	Load the OBS in the DPU.		
5	Set a BP in the OBS where task LS reads the commands stored in the low priority command queue, after the commands are actually sent to the S/Ss.		
6	Start OBS	OBS stops at BP about one second after start, at the first periodic request of HK parameters	TIUL3
7	Remove previous BP. Set a new BP in the OBS where the LS task receives the HK parameters from the S/S.		
8	Start DRCU simulator.		
9	Start OBS.	OBS stops at BP as in step 6+ 2 msec. Verify that the value of the received pa-	TIUL5 TISL4





		parameter matches the input value pre-defined in the DRCU Simulator.	
10	Remove previous BP. Set a new BP in OBS when the notification of complete HK packet is sent to TMTC.		
11	Start OBS.	When the OBS stops, examine the DM area where the HK packet has been stored and inspect its integrity.	TIIL2
12	Remove previous BP.		
13	<b>Start OBS. Start CDMS Simulator.</b>	Verify periodic (1/sec) reception of TM (3,25) HK packets with SID 0x300. Verify periodic (0.5/sec) reception of TM (3,26) diagnostic packets with SID 0x301	TIUL2, TISL5
14	<b>Send TCTest.1 10 times, spaced by at least 3 seconds</b>	Verify that no TM (3,25) or TM (3,26) packets are lost by checking that the received packet counter in the CDMS log window shows no jumps	TIUL2
15	Send TC8.4.1-1.10		
16	Send TC8.4.1-1.11		
17	Send TC8.4.1-3.10		
18	Send TC8.4.1-3.11		
19	Send TC3.2.1	Verify periodic (10/sec) reception of additional TM (3,26) diagnostic packets with SID 0x302	
20	Send TC3.2.2	Verify periodic (10/sec) reception of additional TM (3,26) diagnostic packets with SID 0x303	
21	Send TC3.9.1	Verify reception of TM (3,10) with HKPCKTID = 0	
22	Send TC3.11.1	Verify reception of TM (3,12) with HKPCKTID = 1,2,3	
23	Send TC3.4.1	Verify that reception of TM (3,26) with SID 0x302 has stopped	
24	Send TC3.11.1	Verify reception of TM (3,10) with HKPCKTID = 1,3	



25	Send TC3.4.2	Verify that reception of TM (3,26) with SID 0x303 has stopped	
26	Send TC3.11.1	Verify reception of TM (3,10) with HKPCKTID = 1	TISL11

## 4.5 TP3

### 4.6 This procedure executes test case VM. It is assumed at this stage that procedures TP0

This procedure executes test case SWITCH\_ON. Procedure steps which will be repeated as part of the acceptance tests are lightly shaded.

Step #	Action	Pass/Fail	Test Item
57	Switch-on the DPU. At this point the Boot Software loads the OBS image from the EEPROM to PM. After completion, the Boot SW stops.	No event TM (5,4) should be received by SCOS2000.	
58	Send TC8.4.BSW.1 to start the OBS.	The standard HK TM packets TM (3,19) should be received by SCOS2000	
59	Switch-off the DPU.		
60	Switch-on the DPU.		
61	Run the procedure resident on the SCOS2000 router to load the image of the OBS directly onto the DPU DM.		
62	Send TC8.4.BSW.2 to copy the OBS image from DM to PM and start it.	The standard HK TM packets TM (3,19) should be received by SCOS2000	TISL1

## TP1

and TP2 have been successfully executed. We will progressively flood the LS port with HK parameter requests to the DRCU simulator up and beyond the maximum number of requests that can be handled in 1 second (about 500). The measurement with the oscilloscope will be used to verify that this is actually happening.

The requests will come from the HK\_ASK task, from the H/W VM and from the 3 S/W VMs that should nominally run the PID controls. Each request source expects different parameter values; the test will be passed if each source receives exactly the expected parameter values without any response mixing.

Step #	Action	Pass/Fail	Test Item



1	Create custom commands in the DRCU simulator, one per subsystem, with CIDs 0x7FA, 0x7FB and 0x7FC. Set the returned parameter to be equal to CID and make sure these values are not returned by DRCU simulator for any of the standard DRCU commands.		
2	Start OBS. Start CDMS simulator. <i>At this stage, about 220 HK parameter requests are sent to the DRCU simulator. Each request requires 2msec to be served.</i>	Verify the periodic reception of HK (1/sec) and diagnostic (1/2 sec) packets.	
3	Send TC8.4.1-1.20		
4	Send TC8.4.1-1.21		
5	Send TC8.4.1-1.22		
6	Send TC8.4.1-1.23		
7	Send TC8.4.1-3.20		
8	Send TC8.4.1-3.21		
9	Send TC8.4.1-3.22		
10	Send TC8.4.1-3.23		
11	Send TC8.4.C0-2.1 <i>Now there are 80 additional HK parameter requests going to the LS port.</i>	Verify that no TM (5,1) events are received Verify increase of traffic with the DCU on the LSA display.	
12	Send TC8.1.2.1 <i>Now there are 80 additional HK parameter requests going to the Ls port</i>	Verify that no TM (5,1) events are received Verify increase of traffic with the DCU on the LSA display.	
13	Send TC8.1.3.1 <i>Now there are 80 additional HK parameter requests going to the Ls port</i>	Verify that no TM (5,1) events are received Verify increase of traffic with the MCU on the LSA display.	
14	Send TC8.1.4.1 <i>Now there are 80 additional HK parameter requests going to the Ls port. At this point we have passed the number of total requests (about 500) that can go through the LS port each second: we might be losing some HK packets, but this is no problem for the current tests.</i>	Verify that no TM (5,1) events are received Verify increase of traffic with the SCU on the LSA display.	
15	Wait 10 seconds, then stop the OBS		
16	Open the file TelemetryA.txt resident on the CDMS simulator		
17	Perform a search for the values 0x7FA, 0x7FB, 0x7FC and 0x7FD	Verify that search produced negative results.	TIUL10



## 4.7 TP4

## 4.8 This procedure executes test case **COMMAND\_LIST\_EXEC**. It is assumed at this stage that procedures **TP0**

This procedure executes test case SWITCH\_ON. Procedure steps which will be repeated as part of the acceptance tests are lightly shaded.

Step #	Action	Pass/Fail	Test Item
63	Switch-on the DPU. At this point the Boot Software loads the OBS image from the EEPROM to PM. After completion, the Boot SW stops.	No event TM (5,4) should be received by SCOS2000.	
64	Send TC8.4.BSW.1 to start the OBS.	The standard HK TM packets TM (3,19) should be received by SCOS2000	
65	Switch-off the DPU.		
66	Switch-on the DPU.		
67	Run the procedure resident on the SCOS2000 router to load the image of the OBS directly onto the DPU DM.		
68	Send TC8.4.BSW.2 to copy the OBS image from DM to PM and start it.	The standard HK TM packets TM (3,19) should be received by SCOS2000	TISL1

## TP1

, TP2 and TP3 have been successfully executed. The DPU correctly interfaces with the CDMS simulator and the DRCU simulator. Procedure steps that will be repeated as part of the acceptance tests are lightly shaded.

Step #	Action	Pass/Fail	Test Item
18	Configure DRCU Simulator to assign pre-defined values to the set HK parameters and the science data that will be sent to the DPU.		
19	Set a BP in the OBS soon after reception of Half-FIFO-Full interrupt.		
20	Start OBS. Start CDMS Simulator.		
21	Send TC8.4.C0-1.1	OBS should stop at BP.	
22	Using the ICE GUI proceed step-by-step in the code to read the science data present on the FIFOs	Verify correct reception and interpretation of science frames.	TIUL6
23	Remove previous BP. Set new BP where a complete science TM packet is ready to be sent and		



	the notification from HS is received by TMTC.		
24	Restart OBS		
25	Send TC8.4.C0-1.1	OBS stops at BP. Using the ICS GUI check the locations of DM where the built packet is held and inspect integrity of header (APID, counter, type and subtype) and content (compare to input data from DRCU Simulator).	TISL6
26	Remove BP.		
27	Restart OBS		
28	Send TC8.4.C0-1.1	Verify reception of TM (21,1) science packets containing 255 Frames. Check correctness of APID, count, type and subtype as packets appear on the CDMS Simulator GUI. Use a DTST to inspect and verify received packets against pattern sent by DRCU	TIUL2, TIIL3, TISL17
29	Send TC8.4.1-1.43		
30	Send TC8.4.1-3.43		
31	Send TC8.4.C0-2.23. After 5 seconds execute next step.	Verify reception of incoming TM (21,1) science packets	
32	Send TC8.4.C0-3.1	Verify that TM (21,1) transmission stops	
33	Send TC8.4.1-1.30		
34	Send TC8.4.1-3.30		
35	Send TC8.4.C1-1.1	Verify that OBSID value has been updated in HK packet	
36	Send TC8.4.C1-2.1	Verify that BBID value has been updated in HK packet	
37	Send TC8.4.C1-3.1	Verify that MODE value has been updated in HK packet	
38	Send TC8.4.C1-4.1	Verify that STEP value has been updated in HK packet	
39	Send TC8.4.C1-4.4	Verify that time of last DRCU sync has be reset in the HK packet	TISL16
40	Send TC8.4.C0-2.10	Verify reception of TM (21,1) science packets containing 255 Frames. TM packets will have APID = 0x504 and SID = 0x200	



		Content of Science Data should be checked against DRCU input data.	
41	Send TC8.4.1-1.31		
42	Send TC8.4.1-3.31		
26	Send TC8.4.C0-2.11	Verify reception of TM (21,1) science packets containing 255 Frames.	
		TM packets will have APID = 0x505 and SID = 0x201	
		Content of Science Data should be checked against DRCU input data.	
27	Send TC8.4.1-1.32		
28	Send TC8.4.1-3.32		
29	Send TC8.4.C0-2.12	Verify reception of TM (21,2) science packets containing 255 Frames.	
		TM packets will have APID = 0x504 and SID = 0x102	
		Content of Science Data should be checked against DRCU input data.	
30	Send TC8.4.1-1.33		
31	Send TC8.4.1-3.33		
32	Send TC8.4.C0-2.13	Verify reception of TM (21,2) science packets containing 255 Frames.	
		TM packets will have APID = 0x504 and SID = 0x103	
		Content of Science Data should be checked against DRCU input data.	
33	Send TC8.4.1-1.34		
34	Send TC8.4.1-3.34		
35	Send TC8.4.C0-2.14	Verify reception of TM (21,2) science packets containing 255 Frames.	
		TM packets will have APID = 0x504 and SID = 0x104	
		Content of Science Data should be checked against DRCU input data.	
36	Send TC8.4.1-1.35		
37	Send TC8.4.1-3.35		
38	Send TC8.4.C0-2.15	Verify reception of TM (21,2) science packets containing 255 Frames.	



		TM packets will have APID = 0x505 and SID = 0x105	
		Content of Science Data should be checked against DRCU input data.	
39	Send TC8.4.1-1.36		
40	Send TC8.4.1-3.36		
41	Send TC8.4.C0-2.16	Verify reception of TM (21,2) science packets containing 255 Frames.	
		TM packets will have APID = 0x505 and SID = 0x106	
		Content of Science Data should be checked against DRCU input data.	
42	Send TC8.4.1-1.37		
43	Send TC8.4.1-3.37		
44	Send TC8.4.C0-2.17 (PH_TEST)	Verify reception of TM (21,3) science packets containing 255 Frames.	
		TM packets will have APID = TBD and SID = TBD	
		Content of Science Data should be checked against DRCU input data.	
45	Send TC8.4.1-1.38		
46	Send TC8.4.1-3.38		
47	Send TC8.4.C0-2.18 (SP-TEST)	Verify reception of TM (21,3) science packets containing 255 Frames.	
		TM packets will have APID = TBD and SID = TBD	
		Content of Science Data should be checked against DRCU input data.	
48	Send TC8.4.1-1.39		
49	Send TC8.4.1-3.39		
50	Send TC8.4.C0-2.19	Verify reception of TM (21,4) science packets containing 255 Frames.	
		TM packets will have APID = 0x504 and SID = 0x208	
		Content of Science Data should be checked against DRCU input data.	
51	Send TC8.4.1-1.40		
52	Send TC8.4.1-3.40		



53	Send TC8.4.C0-2.20	Verify reception of TM (21,4) science packets containing 255 Frames.	
		TM packets will have APID = 0x505 and SID = 0x209	
		Content of Science Data should be checked against DRCU input data.	TISL17
54	Send TC8.4.1-1.41		
55	Send TC8.4.1-3.41		
56	Send TC8.4.C0-2.21	Verify that configured parameters are reflected in HK packets	
57	Send TC8.4.1-1.42		
58	Send TC8.4.1-3.42		
59	Send TC8.4.C0-2.22	Verify that configured parameters are reflected in HK packets	
60	Send TC14.3.1	Verify reception of a TM (14,4) packet. Transmission of all packets should be enabled.	
61	Send TC8.4.C0-2.23	Verify continuous reception of TM (21,1) packets	
62	Send TC14.2.1	Verify that TM (21,1) are no longer received	
63	Send TC14.2.2	Verify that TM (3,25) HK Packets are no longer received	
64	Send TC14.3.1	Verify reception of TM (14,4) packet. Generation for TM (21,1,0x200) and TM (3,25,0x300) should not be present in the report.	
65	Send TC14.1.1	Verify that TM (21,1,0x200) and TM (3,25,0x300) are again received	
66	Send TC14.3.1	Verify reception of a TM (14,4) packet. Transmission of all packets should be enabled.	TISL15