SPIRE (IFSI)	Monthly Report –June 2002	**Date:** 2/7/2002

Work Package: **DPU**   **SPIRE-IFS-REP-001351**

1. Subsystem Progress Since Project Inception

2. Subsystem Progress This Month

- After the ESA meeting on 19th of June and ESA statement to pay all CPPA components the ICC funding problem for years 2004-2006 looks to be relieved.
- Windows version of VM compiler simulator, and draft document delivered.

3. Problem Areas	Remedial Action
The tests have shown that there is a problem in the reading of the analogue DPU channels. The hardware bug (probably in FPGA) is under investigation at CGS; at least 3 weeks are estimated for repair	

4. Engineering Activities

Mr A. Bertoli, CGS SW expert, spent 1,5 week at IFSI: boot SW completed and tested; EEPROM writing/reading performing well.

5. Design Changes

6. PA/QA Activities

7. Subsystem Management Issues

8. Actions Requiring Immediate Attention

None

9. Status of Previous Actions

10. Activities Yet to be Achieved

11. Milestones		Status
28/06/01	DRCU simulator at IFSI	Late – expected completion 30/2/02
15/6/01	CDMS (S/C I/F) at IFSI	Late – Proto SW arrived on 21/8/01
28/6/01	EGSE at IFSI	Late – SCOS 2000 arrived on 20/8/01
1/10/01	DPU-AVM Board at IFSI	Late – arrived on 15/04/02
2/4/02	DPU-AVM delivery to RAL	Late – new date 1/07/02
15/11/01	QM author. To proceed	Late - new date 15/02/02
22/4/02	DPU-QM Board at IFSI	On schedule
2/1/03	DPU-QM delivery to RAL	On schedule
20/12/02	FM author. To proceed	On schedule
15/9/03	DPU-FM Board at IFSI	On schedule
1/3/04	DPU-FM delivery to RAL	On schedule
24/2/04	FS ready for delivery	On schedule

13. Schedule Changes

None