

## SPIRE-RAL-COM-001338

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**From:** John Delderfield [j.delderfield@rl.ac.uk]  
**Sent:** 02 July 2002 11:12  
**To:** AUGUERES Jean-Louis DAPNIA; Laurent Vigroux; 'Griffin Matt'; Victor Hristov; Gerald Lilienthal; John Delderfield; CARA Christophe SMTP; SCHMITT Dominique SMTP; 'Swinyard Bruce'; Frederic Pinsard; 'Sawyer, EC (Eric)'  
**Cc:** J.A.Long@rl.ac.uk; D.K.Griffin@rl.ac.uk  
**Subject:** Input to SPIRE Grounding Tiger Team



CEA\_philo\_GND\_D  
RCU.pdf

Dear all,

Please find attached information from CEA. Apologies if you've registered this already.

It extends a previous similar drawing from Dominique.

It defines that the analogue and digital parts of both the FCU and DCU backplanes are well separated, which is good.

There is a simplification in that the feeds from the power supply to the other parts of the FCU exit the unit and re-enter it again.

I don't know whether I'm reading too much into the drawing, but one could imply that both analogue and digital grounds go into all modules, join therein, and then have common multiple links to chassis. There is also no sign of any h.f. common-mode filtering in a controlled way, but maybe this is considered a detail.

Could CEA please build on this by expanding it to show the level of detail already shown in the SPIRE Grounding Philosophy document.

Best regards

John

# SPIRE PSU\_DRCU Power/Grounding Implementation

