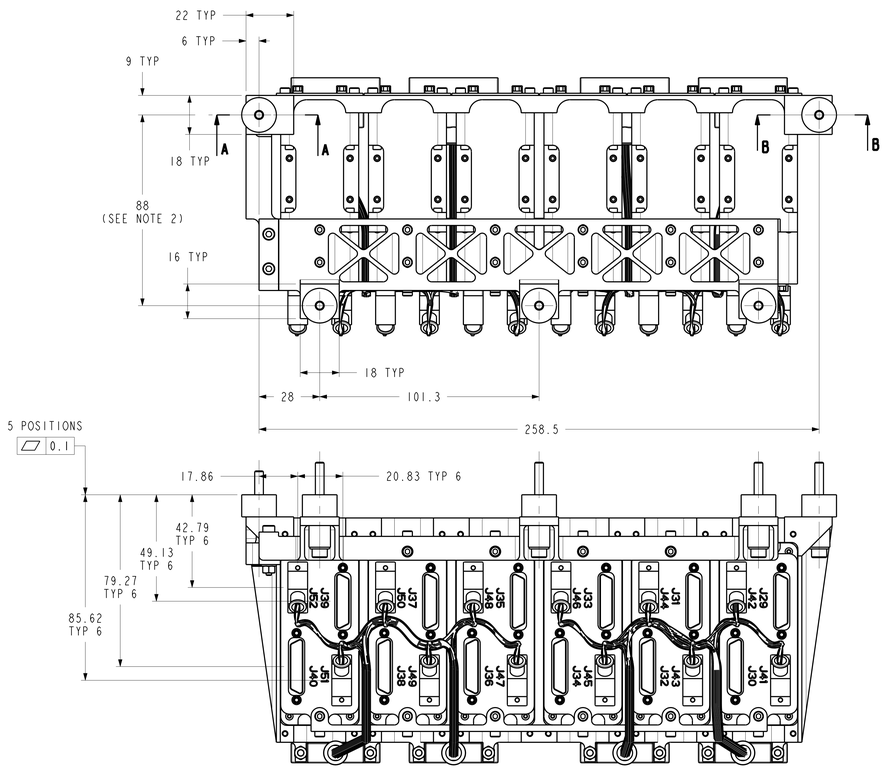
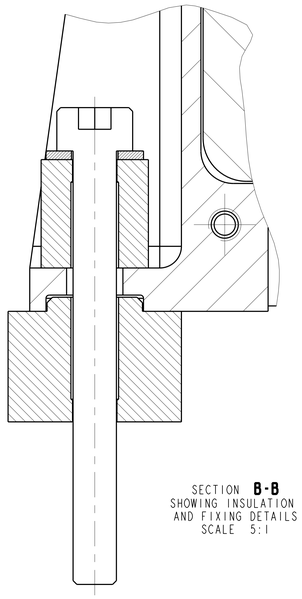
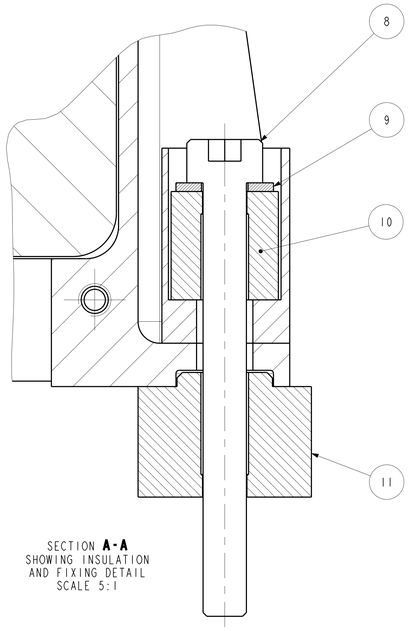
A0-KE-0104-350-B | PROJECTION | THIS DRAWING CONFORMS TO B.S.308





SECTION **B-B**
SHOWING INSULATION
AND FIXING DETAILS
SCALE 5:1

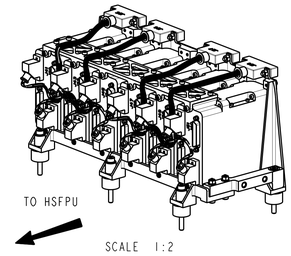


SECTION **A-A**
SHOWING INSULATION
AND FIXING DETAIL
SCALE 5:1

No.	DESCRIPTION	QTY	MASS (g) / ITEM	MASS (g) TOTAL	REMARKS
1	FRONT PLATE	1	128.0	128.0	
2	LOWER REAR PLATE	1	68.0	68.0	
3	UPPER REAR PLATE	1	32.6	32.6	
4	JFET MODULE	6	305	1830	
5	37 WAY MICRO-D ASSY	4	17.0	68.0	CONNECTOR AND BACKSHELL
6	15 WAY MICRO_D ASSY	12	8.3	99.6	CONNECTOR AND BACKSHELL
7	SCREW M2.5 x 8 LONG	60	0.6	37.2	
8	SCREW M4 x 40 LONG	5	5.0	25.0	
9	WASHER M4	5	3.1	15.5	
10	TOP INSULATOR	5	0.04	0.20	
11	BOTTOM INSULATOR	5	0.09	0.45	
GRAND TOTAL				2305	

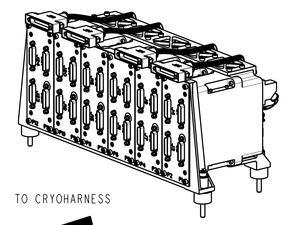
CONNECTOR TABLE

LABEL	TYPE	FUNCTION
J1	ALL MDM25P	ALL SIGNAL FEEDS TO CRYOHARNESS
J2		
J3		
J4		
J5		
J6		
J7		
J8		
J9		
J10		
J11		
J12		
J13		
J14		
J15		
J16		
J17		
J18		
J19		
J20		
J21		
J22		
J23		
J24		
J25	MDM37S	BIAS WIRES FROM CRYOHARNESS
J26		
J27		
J28		
J29	ALL MDM51P	SIGNALS IN FROM DETECTORS
J30		
J31		
J32		
J33		
J34		
J35		
J36		
J37		
J38		
J39		
J40		
J41	ALL MDM15P	BIAS FEEDS INTO MODULES
J42		
J43		
J44		
J45		
J46		
J47		
J48		
J49		
J50		
J51		
J52		



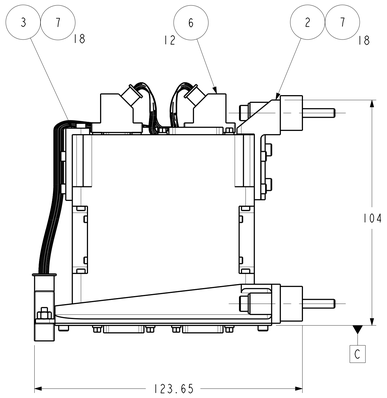
TO HSFPU

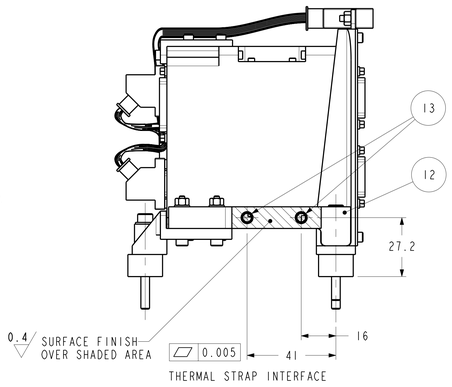
SCALE 1:2



TO CRYOHARNESS

SCALE 1:2





VIEW ON X

NOTE

1. ITEMS 10 & 11 TO BE PERMANENTLY BONDED TO MATING FACES.
2. TO ATTAIN THE CORRECT MOUNTING INTERFACE DIMENSION, THE FOLLOWING PROCEDURE MUST BE FOLLOWED:
   PARTS 4 ARE TO BE MOUNTED TO PART 1. MEASURE FROM THE TOP OF PARTS 4 SHOWN AS PLANE 'C' TO THE TAIL END FACE OF PARTS 4, NOTING THE SIX VALUES.
   MACHINE RAISED PADS ON PART 2 TO REMOVE (VALUE - 87.7).
3. ITEM 8 TO BE TORQUED TO 1Nm AND LOCKED.
4. UNIT SHOWN FITTED WITH BACK-HARNESS MATING TO J25-28 & J41-52 BECAUSE THIS WILL BE FITTED BEFORE ITEM IS INTEGRATED TO HOB. HOWEVER, THIS HARNESS AND ITS CONNECTORS ARE NOT PART OF THE 6 JFET RACK.

SPIRE MASTER DRAWING	
PROJECT MEMBER	APPROVED
PROJECT MANAGER	
SYSTEM ENG	2002.06.24 11:42:12 +01'00'
ELECTRONICS ENG	
PA GROUP	
STRESS ENG	
OPTICAL ENG	
THERMAL ENG	
MECHANICAL ENG	

ISSUE	DATE	MOD. No.	DRN. BY	CHKD.	APPD.	STATUS
B	21-06-02	KE-2953.	D.SMART			MANUF

TOLERANCES UNLESS STATED ±0.2 mm ±0.3 °
FINISH CLEAN REMOVE ALL BURRS
ORIGINAL SCALE 1:1 DO NOT SCALE
MATERIAL & SPEC. SEE DETAILS
SURFACE TEXTURE µM SEE DETAILS ✓UNLESS STATED
0 50mm

USED ON

CENTRAL LABORATORY OF THE RESEARCH COUNCILS

TITLE

6 JFET RACK INTERFACE DRAWING

SPIRE

A | 0-KE-0104-350-B | 1 OF 1