# PACS Status PA/QA at MPE

- Results from IBDR (Board Report)

Open: Derating, Worst Case, FMECA, FDIR, HSIA (System analyses)

Comments: 1) There is no final agreement between institutes, and industry as a level where PA/QA has to be done in accordance with ESA requirements.
There is a difference between engineering tasks (system analyses see above) and PA tasks.
2) Direct contacts from PA MPE to industry must be developed in accordance with institutes.
3) It is not seen that system analyses have to be performed completely before producing hardware. ( depend on the contracts between industry and institutes.)
4) Even if AVM must not be checked completely with FM level MPE recommend to see it as test for further PA activities.

5) Final comment:

Only industry (or skilled people of the bigger institutes) are able to perform system analyses. ESA requirements have to be met at least at delivery of QM.

The requested issues has to be done on the own responsibility of institutes with industry.

# Status of PA/QA of DPU / SPU / DECMEC

- Manufacturing flow charts with MIP/KIP
- Incoming inspection
- Storage, handling, transportation, preservation
- ADP / EIDP
- Test procedures
- Specification of hardware and subsystems

are proceeding and will be formed out during handling the real parts. It seems to be easier to develop understanding for PA/QA by handling with real processes.

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