

SPIRE (JPL)	Monthly Report – For January 2002	Date: 15/03/02
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Work Package: **Detectors and Cold Electronics** **SPIRE-JPL-REP-001213**

1. Subsystem Progress Since Project Inception		
Subsystem has progressed through the CDR stage successfully this summer. We have received approval for moving forward into the implementation phase. Action items from the CDR will be addressed individually and closed out thorough memos distributed to the originators and the project files. We are in the process of building our CQM and STM hardware.		
2. Subsystem Progress This Month		
Completed PLW array lithography. Lost our JFET fabrication engineer (left JPL). We have embarked on the process of training a new one. Began assembly of the QM BDA. We have received the Thermal Cycle Dewar and are in the process of installing it. Finished 4K measurements of the load resistor module successfully.		
3. Problem Areas	Remedial Action	
We have found it necessary to modify the design of the capstan which is used to preload and secure the kevlar suspension bands. Although the kevlar is being pretensioned, the final adjustment length needed is longer than expected.	Redesigned capstan to be installed. A design review is scheduled to cover this and some other minor changes to the BDA design. This review will take place on the 25 th of February.	
4. Engineering Activities		
5. Design Changes		
As described above.		
6. PA/QA Activities		
Completed the FMECA.		
7. Subsystem Management Issues		
None		
8. Actions Requiring Immediate Attention		
ICD for wiring harness is critical.		
9. Status of Previous Actions		
None		
10. Activities Yet to be Achieved		
Completion of the assembly of the first BDA units (QM and CQM).		
11. Milestones		Status
March 1, 02	QM BDA Assembled	
13. Schedule Changes		
Pending with objective being no changes to delivery schedule.		

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