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| SPIRE (IFSI) | Monthly Report –November 2001 | Date: 3/11/2001 |
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Work Package: DPU

SPIRE-IFS-REP-001205

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| 1. Subsystem Progress Since Project Inception | | |
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| 2. Subsystem Progress This Month | | |
| <p>The first set of DPU boards have been delivered by CGS to IFSI after a partial acceptance test at CGS due to a failure in the IFSI provided SS I/F simulators. At IFSI the problem with the SS simulator have been fixed and a number of extensive tests have been performed on the boards. During our tests a number of problems, possibly located in the socket mounted FPGA, have arisen and we are in close contact with CGS in order to solve it.</p> <p>DPU SPIRE documentation have been written/updated and provided to the project for the December DPU DDR.</p> | | |
| 3. Problem Areas | | Remedial Action |
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| 4. Engineering Activities | | |
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| 5. Design Changes | | |
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| 6. PA/QA Activities | | |
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| 7. Subsystem Management Issues | | |
| None | | |
| 8. Actions Requiring Immediate Attention | | |
| None | | |
| 9. Status of Previous Actions | | |
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| 10. Activities Yet to be Achieved | | |
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| 11. Milestones | | Status |
| 28/06/01 | DRCU simulator at IFSI | Late - expected completion 30/9/01 |
| 15/6/01 | CDMS (S/C I/F) at IFSI | Late - Proto SW arrived on 21/8/01 |
| 28/6/01 | EGSE at IFSI | Late - SCOS 2000 arrived on 20/8/01 |
| 1/10/01 | DPU-AVM Board at IFSI | Late - new date 15/01/02 |
| 2/4/02 | DPU-AVM delivery to RAL | Late - new date 16/04/02 |
| 15/11/01 | QM author. To proceed | Late - new date 15/01/01 |
| 22/4/02 | DPU-QM Board at IFSI | On schedule |
| 2/1/03 | DPU-QM delivery to RAL | On schedule |
| 20/12/02 | FM author. To proceed | On schedule |
| 15/9/03 | DPU-FM Board at IFSI | On schedule |
| 1/3/04 | DPU-FM delivery to RAL | On schedule |
| 24/2/04 | FS ready for delivery | On schedule |
| 13. Schedule Changes | | |
| None | | |