

<b>SPIRE (IFSI)</b>	<b>Monthly Report –December 2001</b>	<b>Date:</b> 7/12/2001
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**Work Package: DPU**

**SPIRE-IFS-REP-001162**

<b>1. Subsystem Progress Since Project Inception</b>		
<b>2. Subsystem Progress This Month</b>		
<p>At the beginning of December the DPU-SPIRE DDR took place at IFSI, official report and RIDs not received yet.</p> <p>A 3 days test has been performed at Gavazzi premises in order to try to solve the HW problems discovered on the first set of boards during tests at IFSI. The 1553 problems have been solved (SW development/test in progress at IFSI), problems with slow/high I/F still under investigation.</p> <p>ASI project directive 3 and CGS ECP 3 (QM components quality level &gt;= Mil-C) nearly completed.</p>		
<b>3. Problem Areas</b>	<b>Remedial Action</b>	
<b>4. Engineering Activities</b>		
<b>5. Design Changes</b>		
<b>6. PA/QA Activities</b>		
<b>7. Subsystem Management Issues</b>		
None		
<b>8. Actions Requiring Immediate Attention</b>		
None		
<b>9. Status of Previous Actions</b>		
<b>10. Activities Yet to be Achieved</b>		
<b>11. Milestones</b>		<b>Status</b>
28/06/01	DRCU simulator at IFSI	Late - expected completion 30/1/02
15/6/01	CDMS (S/C I/F) at IFSI	Late - Proto SW arrived on 21/8/01
28/6/01	EGSE at IFSI	Late - SCOS 2000 arrived on 20/8/01
1/10/01	DPU-AVM Board at IFSI	Late - new date 15/01/02
2/4/02	DPU-AVM delivery to RAL	Late - new date 16/04/02
15/11/01	QM author. To proceed	Late - new date 15/01/01
22/4/02	DPU-QM Board at IFSI	On schedule
2/1/03	DPU-QM delivery to RAL	On schedule
20/12/02	FM author. To proceed	On schedule
15/9/03	DPU-FM Board at IFSI	On schedule
1/3/04	DPU-FM delivery to RAL	On schedule
24/2/04	FS ready for delivery	On schedule
<b>13. Schedule Changes</b>		
None		