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**Herschel SPIRE DPU  
AVM Configuration Status  
List**

Ref.: SPIRE-IFS-DOC-001033

Issue: Issue 1.5

Date: 3/06/2003

**SPIRE**  
**DPU AVM Configuration Status List**

Document Ref: SPIRE-IFS-DOC-001033

Issue 1.5

**Distribution List :**

K. King		
B. Swinyard		
J. Delderfield		
E. Sawyer		
R. Cerulli		
S. Molinari		
G. Liu'		

 <b>IFSI CNR</b>	<b>Herschel SPIRE DPU AVM Configuration Status List</b>	<b>Ref.:</b> SPIRE-IFS-DOC-001033 <b>Issue:</b> Issue 1.5 <b>Date:</b> 3/06/2003
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## Document Status Sheet

Issue	Revision	Date	Reason for Change
Issue 1		23/11/2001	First Issue
Issue 1	1	11/02/2002	Updating for IBDR
	2	11/02/2002	More documents added
	3	23/7/2002	More documents added
	4	10/10/2002	Introduced the new document SPIRE DPU CIDL
	5	3/6/2003	Added NCRs

## Document Change Record

<b>Document Title:</b> SPIRE DPU Configuration Status List	
<b>Document Reference Number:</b> SPIRE-IFS-DOC-001033	
<b>Document Issue/Revision Number:</b> Issue 1.5	
Section	Reason For Change
All	<b>Issue 1.0</b>
	<b>ISSUE 1.1</b>
	Added CGS Test Procedures
	<b>Issue 1.2</b>
	Added Derating and Worst case Analysis
	Added DPU/ICU Spacecraft Interface Acceptance Test Plan
	<b>Issue 1.3</b>
	Added a sequence of numbers
	Added Test Reports
	Added 1553B Test Report
	<b>Issue 1.4</b>
	Introduced the new document "SPIRE DPU Configuration Item Data List" and deeply changed the present document, including the title



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**Issue 1.5**

Added NCRs # 18, 19, 21, 22



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### Acronyms

AD	Architectural Design
ATP	Acceptance Test Plan
AVM	Avionic Model
BSW	Basic SW
CGS	Carlo Gavazzi Space
CIDL	Configuration Item Data List
CSL	Configuration Status List
CNR	Consiglio Nazionale delle Ricerche
CPP	Coordinated Parts Procurement
CPP	Coordinated Parts Procurement Board
CPU	Control Processing Unit
CDMS	Central Data Management System
CDMU	Central Data Management Unit
CQM	Cryogenic Qualification Model
DCU	Detector Control Unit
DDD	Detailed Design Document
DPU	Digital Processing Unit
EEPROM	Electrically Erasable Programmable Read Only Memory
EIDP	End Item Data Package
EMC	Electro Magnetic Compatibility
EMI	Electro Magnetic Interference
ESA	European Space Agency
FIRST	Far InfraRed and Submillimeter Telescope
HK	HouseKeeping



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HW	HardWare
IBDR	Instrument Baseline Design Review
ICD	Interface Control Document
ICDR	Instrument Critical Design Review
ICU	Instrument Control Unit
IHDR	Instrument Hardware Design Review
IFSI	Istituto di Fisica dello Spazio Interplanetario
ISVR	Instrument Science Verification Review
MCU	Mechanism Control Unit
NA	Not Applicable
OBS	On-Board Software
PA	Product Assurance
PDU	Power Distribution Unit
PROM	Programmable Read Only Memory
S/C	SpaceCraft
SCC	SpaceCraft Components
SCU	S..... Control Unit
SEU	Single Event Upset
SPIRE	Spectral and Photometric Imaging Receiver
S/S	SubSystem
SVM	Service Module
SW	Software
TBC	To Be Confirmed
TBD	To Be Defined
TBW	To Be Written
TV	Thermal Vacuum
WBS	Work Breakdown Structure

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## 1 INTRODUCTION

### 1.1 SCOPE

The purpose of this document is to define the current status of the AVM DPU development in view of its delivery and is part of the EIDP and of the IHDR.

The various NCR, RFW, CN will be listed and their status recorded.

## 2 DOCUMENTS

### 2.1 APPLICABLE DOCUMENTS

AD	Name
01	Herschel/Planck Instrument Interface Document, part A
02	Herschel/Planck Instrument Interface Document, part B-Instrument SPIRE
04	SPIRE Instrument Specification
07	DPU/ICU P.A.Plan
10	DPU/ICU OBS PA Plan

### 2.2 Reference documents

Reference Document	Name

 <b>IFSI CNR</b>	<b>Herschel SPIRE DPU AVM Configuration Status List</b>	Ref.: SPIRE-IFS-DOC-001033
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### 3 LIST OF NON CONFORMANCE REPORTS (NCR)

The following NCRs have been reported and the appropriate modifications have all been implemented in order to correct random problems become apparent during unit testing.

#### 3.1 NCR-DPU-CGS-B-018 REV 1 DATE 20/02/03

2 <b>NCR Title: Noise on FIFO read signal on PL IF S/N 05, 06</b>						
IDENTIFICATION	3 <b>Supplier</b> CGS	4 <b>Purchase Order N°.</b> N.A.	5 <b>Model</b> EM	6 <b>Subsystem</b> N.A.	7 <b>Procedure/Work Item N°</b> N.A.	
	8 <b>NC ITEM Identification</b> PL IF Electrical Assy	9 <b>Drawing N°.</b> Rev. DPU-EM-210.00-0 Rev. /	10 <b>P.N. / C.I. N°</b> DPU-EM-210.00-0	11 <b>Serial N°</b> 05, 06		
	12 <b>Next Higher Unit Id.</b> PL IF Mechanical Assy	13 <b>Drawing N°.</b> Rev. DPU-EM-200.00-0 Rev. /	14 <b>P.N. / C.I. N°</b> DPU-EM-200.00-0	15 <b>Serial N°.</b> 05, 06		
	16 <b>NON CONFORMANCE Detected During:</b> RECEIVING INSP. <input type="checkbox"/> MANUFACT. <input type="checkbox"/> ASSEMBLY/INTEGRATION <input type="checkbox"/> FINAL INSPECTION <input type="checkbox"/> TEST <input checked="" type="checkbox"/> OTHER..... <input type="checkbox"/>					
DESCRIPTION	17 <b>Initiator, Dept., Date, Signature</b> A. Sciortino, Electrical Engineering, 20/02/03,					
	18 <b>Description of NON CONFORMANCE</b> <ul style="list-style-type: none"> <li>➤ Noise is present on read signal when a FIFO reading cycle is in execution;</li> <li>➤ Third empty FIFO flag of channels 0, 1, 2 is floating in SPIRE configuration (S/N 05 and 06).</li> </ul>			19 <b>Requirements violated</b>		
INTERNAL NRB DISPOSITIONS	20 <b>INTERNAL NRB Dispositions:</b> <ul style="list-style-type: none"> <li>➤ Mount a 100pF capacitor between pin 15 and pin 14 of U16, U17, U24 as indicated in the Annex A and glue with EC 2216 B/A;</li> <li>➤ By use of AWG30 connect pins 21 of U21 and U19, pins 21 of U20 and U18, pins 21 of U28 and U26 and glue with EC 2216 B/A (see Annex A).</li> </ul>				21 <b>Verifications</b>	
	22 <b>Suspected cause of</b> OPERATOR/PROCEDURE ERROR <input type="checkbox"/> HANDLING <input type="checkbox"/> TRANSPORTATION <input type="checkbox"/> TEST EQUIPMENT <input type="checkbox"/> TOOLS <input type="checkbox"/> SW <input type="checkbox"/> DESIGN <input type="checkbox"/> PART <input type="checkbox"/> MATERIAL <input type="checkbox"/> PROCESS <input checked="" type="checkbox"/> TEST <input type="checkbox"/> OTHER..... <input type="checkbox"/>					
	23 <b>Classification</b> MINOR <input checked="" type="checkbox"/> MAJOR <input type="checkbox"/>		24 <b>Corrective/Preventive Actions:</b> For EQM and FM, Designer will take account of this NCR.			
	25 <b>REQUEST FOR WAIVER</b> YES <input type="checkbox"/> NO <input checked="" type="checkbox"/> N°		26 <b>Analysis Required</b> YES <input type="checkbox"/> NO <input checked="" type="checkbox"/> N°		27 <b>Other related documents:</b>	
Department: 28 P.A. 29 Syst. Engineering 30 ENG 30 31 C.C.						



 <b>IFSI CNR</b>	<b>Herschel SPIRE DPU AVM Configuration Status List</b>			<b>Ref.:</b> SPIRE-IFS-DOC-001033 <b>Issue:</b> Issue 1.5 <b>Date:</b> 3/06/2003	
	<b>Name:</b>	E. FRANCINI	A. SCIORTINO	R. GROSSI	L. DI GIOIA
	<b>Signature:</b>				
<b>Date:</b>					

<b>CUSTOMER/HIGHER LEVEL CONTRACTOR NRB DISPOSITIONS</b>	<b>32 CUSTOMER/HIGHER LEVEL CONTRACTOR NRB Dispositions (Class Major Only):</b> N.A.				<b>21 Verifications</b> N.A.		
	<b>33 Finally determined Cause of NC</b> N.A.			<b>34 Corrective/Preventive Actions:</b> N.A.			
	<b>35 Customer/HLContractor Approval:</b>					<b>36 CLOSE OUT CERTIFICATION</b>	
	<b>Department:</b>					<b>CGS PA/QA</b>	
	<b>Name:</b>					<b>PA/QA Stamp</b>	
<b>Signature:</b>							
<b>Date:</b>							

**3.2 NCR-DPU-CGS-B-019 REV 1 DATE 10/03/03**

<b>2 NCR Title: Substitution of FPGA on PCBs DPU-EM-200.00-0 S/N 01, 02, 05 and 06.</b>							
<b>IDENTIFICATION</b>	<b>3 Supplier</b> CGS		<b>4 Purchase Order N°.</b> N.A.		<b>5 Model</b> EM	<b>6 Subsystem</b> N.A.	<b>7 Procedure/Work Item N°</b> DPU-PR-CGS-002 Issue 3
	<b>8 NC ITEM Identification</b> PL IF Electrical Assy		<b>9 Drawing N°.</b> Rev. DPU-EM-210.00-0 Rev. /		<b>10 P.N. / C.I. N°</b> DPU-EM-210.00-0		<b>11 Serial N°</b> 01, 02, 05, 06
	<b>12 Next Higher Unit Id.</b> PL IF Mechanical Assy		<b>13 Drawing N°.</b> Rev. DPU-EM-200.00-0 Rev. /		<b>14 P.N. / C.I. N°</b> DPU-EM-200.00-0		<b>15 Serial N°.</b> 01, 02, 05, 06
	<b>16 NON CONFORMANCE Detected During:</b> RECEIVING INSP. <input type="checkbox"/> MANUFACT. <input type="checkbox"/> ASSEMBLY/INTEGRATION <input type="checkbox"/> FINAL INSPECTION <input type="checkbox"/> TEST <input checked="" type="checkbox"/> OTHER..... <input type="checkbox"/>						
<b>DESCRIPTION</b>	<b>17 Initiator, Dept., Date, Signature</b> A. Sciortino, Electrical Engineering, 10/03/03,					<b>19 Requirements violated</b>	
	<b>18 Description of NON CONFORMANCE</b>						



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If rising edge of "Gate" signal occurs before falling edge of "Clock" signal during FSDL transmission data, an error reception can occur.

<b>INTERNAL NRB DISPOSITIONS</b>	<b>20 INTERNAL NRB Dispositions:</b> - Modify the FPGA design in order to re-synchronize "Gate" signal at receiver level (see Annex A); - Substitute FPGA (U30) with a new one (S/N 5.1, 5.2, 5.5, 5.6 for board S/N 01, 02, 05, 06 respectively) programmed with file PLI_V3-1.adb based on the new design.										<b>21 Verifications</b>  		
	<b>22 Suspected cause of</b> OPERATOR/PROCEDURE ERROR <input type="checkbox"/> PART <input type="checkbox"/> MATERIAL <input type="checkbox"/> PROCESS <input type="checkbox"/> TEST <input type="checkbox"/> OTHER..... <input type="checkbox"/>										HANDLING <input type="checkbox"/> TRANSPORTATION <input type="checkbox"/> TEST EQUIPMENT <input type="checkbox"/> TOOLS <input type="checkbox"/> SW <input type="checkbox"/> DESIGN <input checked="" type="checkbox"/>		
	<b>23 Classification</b> MINOR <input checked="" type="checkbox"/> MAJOR <input type="checkbox"/>			<b>Corrective/Preventive Actions:</b> For EQM and FM, Designer will take account of this NCR.							<b>24</b>		
	<b>25 REQUEST FOR WAIVER</b> YES <input type="checkbox"/> NO <input checked="" type="checkbox"/> N°					<b>26 Analysis Required</b> YES <input type="checkbox"/> NO <input checked="" type="checkbox"/> N°					<b>27 Other related documents:</b>		
	<b>Department:</b>		<b>28 P.A.</b>		<b>29 Syst. Engineering</b>		<b>30 ENG</b>		<b>30</b>		<b>31 C.C.</b>		
	<b>Name:</b>		E. FRANCINI		A. SCIORTINO		R. GROSSI				L. DI GIOIA		
<b>Signature:</b>													
<b>Date:</b>													
<b>CUSTOMER/HIGHER LEVEL CONTRACTOR NRB DISPOSITIONS</b>	<b>32 CUSTOMER/HIGHER LEVEL CONTRACTOR NRB Dispositions (Class Major Only):</b>  N.A.										<b>21 Verifications</b>  N.A.		
	<b>33 Finally determined Cause of NC</b> N.A.					<b>34 Corrective/Preventive Actions:</b> N.A.							
	<b>35 Customer/HLContractor Approval:</b>										<b>36 CLOSE OUT CERTIFICATION</b>		
	<b>Department:</b>										<b>CGS PA/QA</b>		<b>PA/QA Stamp</b>
<b>Name:</b>													
<b>Signature:</b>													
<b>Date:</b>													



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### 3.3 NCR-DPU-CGS-B-021 REV 1 DATE 10/03/03

2 <b>NCR Title: EEPROM power on reset</b>					
<b>IDENTIFICATION</b>	3 <b>Supplier</b> CGS	4 <b>Purchase Order N°.</b> N.A.	5 <b>Model</b> EM	6 <b>Subsystem</b> N.A.	7 <b>Procedure/Work Item N°</b> N.A.
	8 <b>NC ITEM Identification</b> CPU Mechanical Assy	9 <b>Drawing N°.</b> Rev. DPU-EM 100.00-0 Rev. /	10 <b>P.N. / C.I. N°</b> DPU-EM-100.00-0		11 <b>Serial N°</b> All
	12 <b>Next Higher Unit Id.</b> N.A.	13 <b>Drawing N°.</b> Rev. N.A.	14 <b>P.N. / C.I. N°</b> N.A.		15 <b>Serial N°.</b> N.A.
	16 <b>NON CONFORMANCE Detected During:</b> RECEIVING INSP. <input type="checkbox"/> MANUFACT. <input type="checkbox"/> ASSEMBLY/INTEGRATION <input type="checkbox"/> FINAL INSPECTION <input type="checkbox"/> TEST <input checked="" type="checkbox"/> OTHER..... <input type="checkbox"/>				
<b>DESCRIPTION</b>	17 <b>Initiator, Dept., Date, Signature</b> A. Sciortino, Electrical Engineering, 10/03/2003,				
	18 <b>Description of NON CONFORMANCE</b> EEPROM is not properly reset at start up.			19 <b>Requirements violated</b>	
<b>INTERNAL NRB DISPOSITIONS</b>	20 <b>INTERNAL NRB Dispositions:</b>				21 <b>Verifications</b>
	<ul style="list-style-type: none"> <li>➤ Cut electrical path between pin 47 of U1 and C15 on CPU Piggy Back DPU-EM-120.03-0 (see Annex A);</li> <li>➤ By use of AWG30, connect pin 59 of P2 with pin 47 of U1 on CPU Piggy Back DPU-EM-120.03-0 (see Annex A) and glue with EC2216 B/A;</li> <li>➤ By use of AWG30, connect pin 2 of U5 with pin 59 of P4 on CPU Main Board DPU-EM-110.03-0 and glue with EC2216 B/A (see Annex B).</li> </ul>				
	22 <b>Suspected cause of</b> HANDLING <input type="checkbox"/> TRANSPORTATION <input type="checkbox"/> TEST EQUIPMENT <input type="checkbox"/> TOOLS <input type="checkbox"/> SW <input type="checkbox"/> DESIGN <input checked="" type="checkbox"/> OPERATOR/PROCEDURE ERROR <input type="checkbox"/> PART <input type="checkbox"/> MATERIAL <input type="checkbox"/> PROCESS <input type="checkbox"/> TEST <input type="checkbox"/> OTHER..... <input type="checkbox"/>				
	23 <b>Classification</b> MINOR <input checked="" type="checkbox"/> MAJOR <input type="checkbox"/>		24 <b>Corrective/Preventive Actions:</b> For EQM and FM, Designer will take account of this NCR.		
	25 <b>REQUEST FOR WAIVER</b> YES <input type="checkbox"/> NO <input checked="" type="checkbox"/> N°		26 <b>Analysis Required</b> YES <input type="checkbox"/> NO <input checked="" type="checkbox"/> N°		27 <b>Other related documents:</b> N.A.
<b>Department:</b>	28 <b>P.A.</b>	29 <b>Syst. Engineering</b>	30 <b>ENG</b>	30	31 <b>C.C.</b>
<b>Name:</b>	E. FRANCINI	A. SCIORTINO	R. GROSSI		L. DI GIOIA
<b>Signature:</b>					
<b>Date:</b>					
C D 32 <b>CUSTOMER/HIGHER LEVEL CONTRACTOR NRB Dispositions (Class Major Only):</b>		21 <b>Verifications</b>			

 <b>IFSI CNR</b>	<b>Herschel SPIRE DPU AVM Configuration Status List</b>	<b>Ref.:</b> SPIRE-IFS-DOC-001033 <b>Issue:</b> Issue 1.5 <b>Date:</b> 3/06/2003
	N.A.	
	N.A.	

33 <b>Finally determined Cause of NC</b> N.A.		34 <b>Corrective/Preventive Actions:</b> N.A.		N.A.	
35 <b>Customer/HLContractor Approval:</b>				36 <b>CLOSE OUT CERTIFICATION</b>	
<b>Department:</b>				<b>CGS PA/QA</b>	<b>PA/QA Stamp</b>
<b>Name:</b>					
<b>Signature:</b>					
<b>Date:</b>					

**3.4 NCR-DPU-CGS-B-022 REV 1 DATE 24/03/03**

2 <b>NCR Title: Noise is present on address bus on DPU-EM-120.03-0</b>							
<b>IDENTIFICATION</b>	3 <b>Supplier</b> CGS	4 <b>Purchase Order Nº.</b> N.A.	5 <b>Model</b> EM	6 <b>Subsystem</b> N.A.	7 <b>Procedure/Work Item Nº</b> N.A.		
	8 <b>NC ITEM Identification</b> CPU Piggy Back Electrical Assv	9 <b>Drawing Nº. Rev.</b> DPU-EM-120.00-0 Rev. /	10 <b>P.N. / C.I. Nº</b> DPU-EM-120.00-0	11 <b>Serial Nº</b> All			
	12 <b>Next Higher Unit Id.</b> CPU Board Electrical Assy	13 <b>Drawing Nº. Rev.</b> DPU-EM-110.00-0 Rev. /	14 <b>P.N. / C.I. Nº</b> DPU-EM-110.00-0	15 <b>Serial Nº.</b> All			
	16 <b>NON CONFORMANCE Detected During:</b> RECEIVING INSP. <input type="checkbox"/> MANUFACT. <input type="checkbox"/> ASSEMBLY/INTEGRATION <input type="checkbox"/> FINAL INSPECTION <input type="checkbox"/> TEST <input checked="" type="checkbox"/> OTHER..... <input type="checkbox"/>						
<b>DESCRIPTION</b>	17 <b>Initiator, Dept., Date, Signature</b> A. Sciortino, Electrical Engineering, 24/03/2003,						
	18 <b>Description of NON CONFORMANCE</b> Noise is present on Data Memory Address Bus on CPU Piggy Back Board (DPU-EM-120.00-0 and a wrong reading of EEPROM can occur.			19 <b>Requirements violated</b>			
- 20	<b>INTERNAL NRB Dispositions:</b>				21 <b>Verifications</b>		



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Mount a 68 pF ceramic capacitor between each address line and ground in the position indicated in the Annex A. Then glue wires with EC2216 B/A.

22		<b>Suspected cause of</b>		HANDLING <input type="checkbox"/>	TRANSPORTATION <input type="checkbox"/>	TEST EQUIPMENT <input type="checkbox"/>	TOOLS <input type="checkbox"/>	SW <input type="checkbox"/>	DESIGN <input type="checkbox"/>		
		OPERATOR/PROCEDURE ERROR <input type="checkbox"/>		PART <input type="checkbox"/>	MATERIAL <input type="checkbox"/>	PROCESS <input checked="" type="checkbox"/>	TEST <input type="checkbox"/>	OTHER..... <input type="checkbox"/>			
23		<b>Classification</b>		<b>Corrective/Preventive Actions:</b>						24	
		MINOR <input checked="" type="checkbox"/> MAJOR <input type="checkbox"/>		For EQM and FM, Designer will take account of this NCR.							
25		<b>REQUEST FOR WAIVER</b>		26		<b>Analysis Required</b>		27		<b>Other related documents:</b>	
		YES <input type="checkbox"/> NO <input checked="" type="checkbox"/> N <sup>a</sup>				YES <input type="checkbox"/> NO <input checked="" type="checkbox"/> N <sup>a</sup>				N.A.	
<b>Department:</b>		28	P.A.	29	Syst. Engineering	30	ENG	30		31	C.C.
<b>Name:</b>		E. FRANCINI		A. SCIORTINO		R. GROSSI				L. DI GIOIA	
<b>Signature:</b>											
<b>Date:</b>											
CUSTOMER/HIGHER LEVEL CONTRACTOR NRB DISPOSITIONS	32		<b>CUSTOMER/HIGHER LEVEL CONTRACTOR NRB Dispositions (Class Major Only):</b>						21		<b>Verifications</b>
			N.A.								N.A.
	33		<b>Finally determined Cause of NC</b>		34		<b>Corrective/Preventive Actions:</b>				
		N.A.				N.A.					
35		<b>Customer/HLContractor Approval:</b>						36		<b>CLOSE OUT CERTIFICATION</b>	
<b>Department:</b>										<b>CGS PA/QA</b>	
<b>Name:</b>										<b>PA/QA Stamp</b>	
<b>Signature:</b>											
<b>Date:</b>											

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#### **4 LIST OF REQUESTS FOR WAIVERS (RFW)**

None

#### **5 LIST OF CHANGE REQUESTS**

None