Minutes of meeting.
DDR preparation at CEA 30/10/01

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External contractors now available for scheduling and programme control.

A new schedule will be created.

This activity will be complete by early December.

New planning will take account of the milestones specified by the project.

Information from JPL is lacking.

Parts procurement.

It is a goal to finalise all detector electronics issues at the summit (6/7 Nov), a schedule session should be planned for 6/11/01.

A new administration system is being implemented at CEA in December.

Detailed planning is available for the QM work with JPL, but dates have slipped.

Cryostat required at CEA for testing after the initial tests at JPL.

Open issues from Meeting on 2/3 Oct. + problem areas.

Grounding scheme, CEA are not convinced that this is optimised.

This has impact on the design and procurement of the power supply.

Sub system switching, is this necessary? Can switching be done on input side.

LAM are analysing the effects of switching.

Switching of detectors is required.

This decision is preventing procurement of the power supply.

FMECA has been delivered to CEA.

Redundancy of bias design should be an outcome of the FMECA.

The common parts procurement seems incapable of purchasing other than hi-rel parts.

Cost of parts and who pays is not resolved.

Reliability of system needs to be addressed.

Analysis could be worked from the other end to obtain a reliability of a detector chain given mil spec or hi-rel electronics.

Band pass filters specification is still open.

Offset to allow for limited dynamic range, this needs to be analyses by the system team.

Location and range of temperature probes need defining, and putting in the subsystem interface document. This information should be available from the subsystem DDRs.

Temperature regulation of BDAs is still open.

Electrical interface details of shutter missing.

RAL to supply a list of documents to which the DRCU should be compliant.

Development plan.

Cooler is not included.

RAL will not provide any test harness.

Harness from cooler to filters (F16 and F17) is a CEA responsibility, but not currently planned by CEA. It was proposed that a single manufacturer is used for all harness.

RAL to investigate common procurement of all isothermal harness.

FPU simulator.

SPIRE consider current proposal too complicated, but it suits CEA as it ill be used for development.

Two simulators will be produced by CEA. One with QM1, and one with QM2 which will be compatible with QM2. After delivery of QM1 to ESA, the first simulator will be upgraded to

Mechanical simulator of the SMEC and BSM will be supplied by LAM.

Other subsystem simulator will be manufactured by CEA.

RAL to specify to ESA that the simulator is an integration tool only and will not stay at the contractors for a long period of time.

Flight spare will be supplied as boards only, limited number only.

Spares will be tested as part of the FM programme.

Calibration of instrument will require a DRCU. The QM2 is baselined for this activity.

Mechanical envelope.

Mass breakdown to be available soon.

DDR

Subsystem spec. updated and commented from RAL, some inputs still missing, see above.

Interface doc, CEA have a problem knowing what info is required for analogue interfaces.

Commands doc. close to completion, but incomplete.

Pin out list to be included in the ICD, ECS to check with JD.

CES are unlikely to be ready for the DDR in December,

Detailed design description not ready.

Development plan and schedule needs update.

SCU is contracted to another part of CEA.