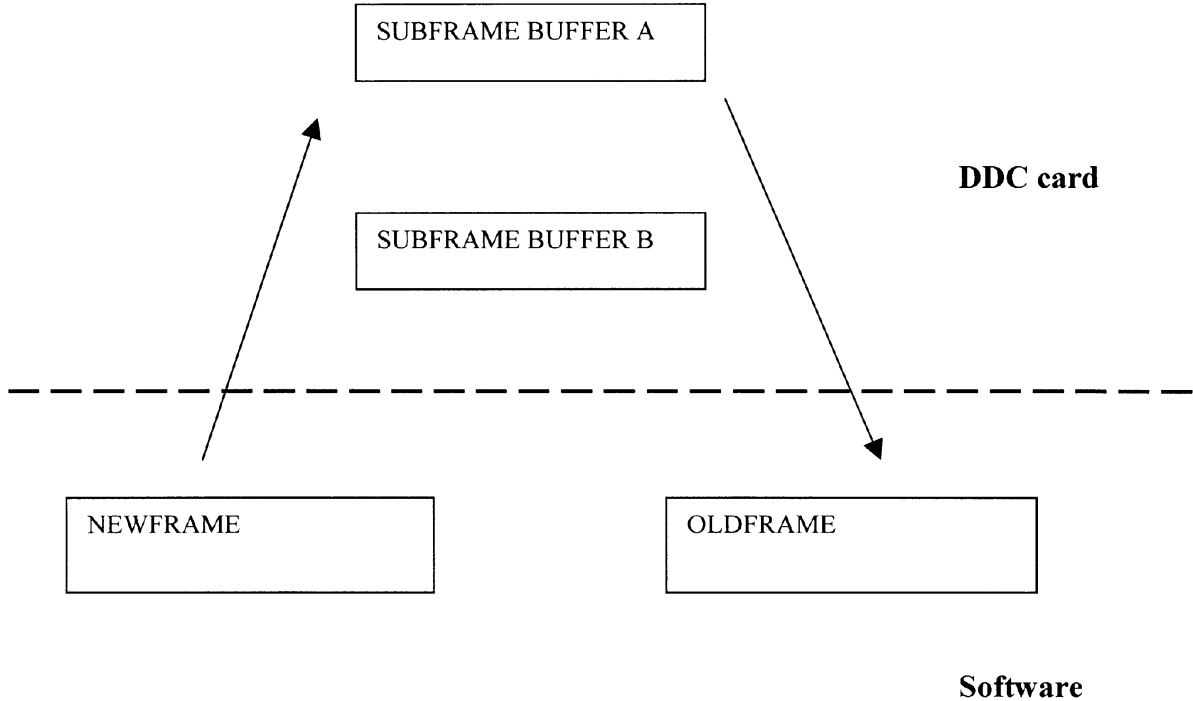


**Packet TM transfers for single RT (as specified in PS-ICD)**

BC	Bus	Subframes	RT
Build TMReq		x	Update TM buffers
			↓ 2mS max
	TMReq	x+1	
Extract TMReq Check it, if valid... Build TMPacket, TMConf		x+2	
Build TMReq	TMPacket TMConf	x+3	
Extract TMPacket	TMReq	x+4	Read TMConf Update TM buffers
Extract TMReq Check it, if valid... Build TMPacket, TMConf		x+5	
Build TMReq	TMPacket TMConf	⋮	
Extract TMPacket	TMReq	⋮	Read TMConf Update TM buffers
Extract TMReq Check it, if valid... Build TMPacket, TMConf		⋮	
		⋮	

⋮

**DDC Card Data Exchange**



**Time** →

