SPIRE (IFSI)

Monthly Report – August 2001

Date: 28/8/2001

Work Package: DPU SPIRE-IFS-REP-000857

1.	Subsystem	Progress	Since	Project	Inception
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2. Subsystem Progress This Month

- Presented and discussed at RAL the OBS architecture.
- Slow speed $\ensuremath{\mathsf{I/F}}$ simulator successfully tested at IFSI.
- Test on high speed I/F simulator in progress
- $2^{\rm nd}$ Progress Meeting held at CGS: activities in schedule.

Action

4. Engineering Activities

5. Design Changes

6. PA/QA Activities

7. Subsystem Management Issues

8. Actions Requiring Immediate Attention

None

9. Status of Previous Actions

10. Activities Yet to be Achieved

11. Mileston	es	Status
28/06/01	DRCU simulator at IFSI	Late - expected completion 30/9/01
15/6/01	CDMS (S/C I/F) at IFSI	Late - Proto SW arrived on 21/8/01
28/6/01	EGSE at IFSI	Late - SCOS 2000 arrived on 20/8/01
1/10/01	DPU-AVM Board at IFSI	Late - new date 24/11/01
2/4/02	DPU-AVM delivery to RAL	Late - new date 16/04/02
15/11/01	QM author. To proceed	Late - new date 15/01/01
22/4/02	DPU-QM Board at IFSI	On schedule
2/1/03	DPU-QM delivery to RAL	On schedule
20/12/02	FM author. To proceed	On schedule
15/9/03	DPU-FM Board at IFSI	On schedule
1/3/04	DPU-FM delivery to RAL	On schedule
24/2/04	FS ready for delivery	On schedule

13. Schedule Changes

None